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Title: Top-Level-Sheet.SchDoc

Datum: 03.05.2023 | Zeit: 15:39:47

Blatt: 1 von: 22 Entwickler:

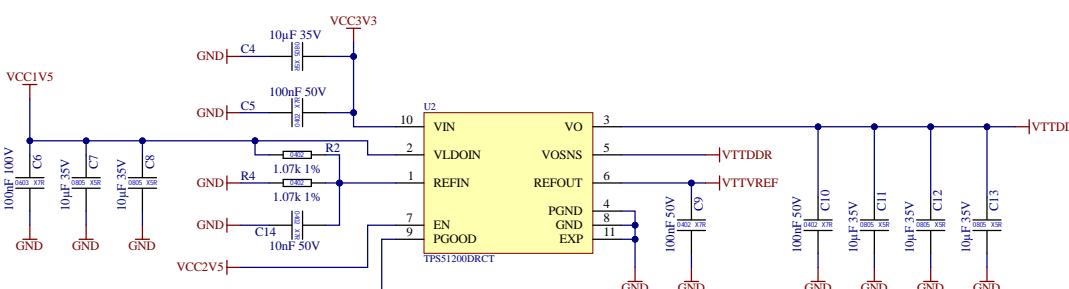
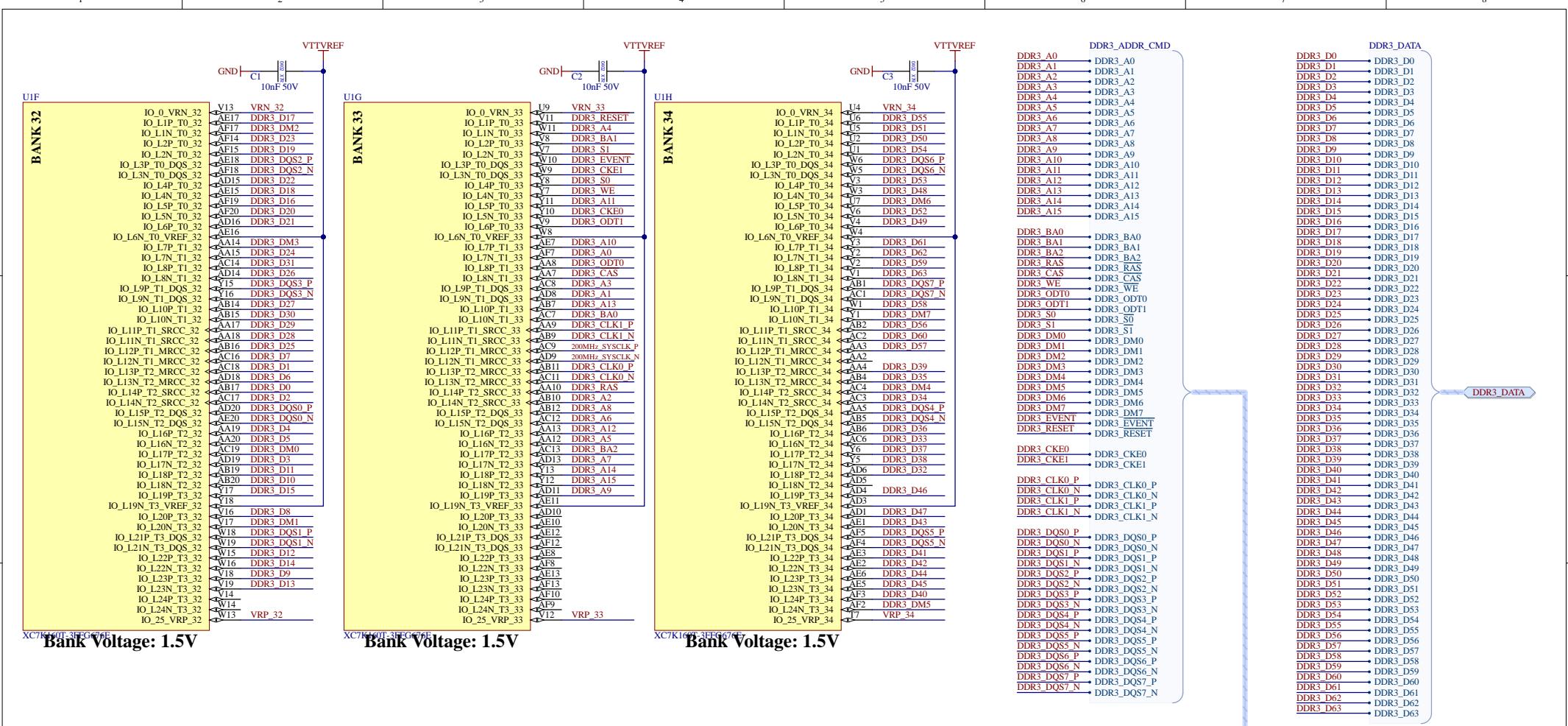
Revision: Checked:

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Ti1 - DDB3 M

Title: DDR3 Memory_Banks 32-33-34.SchD0

Datum: 03.05.2023 Zeit: 15:39:48

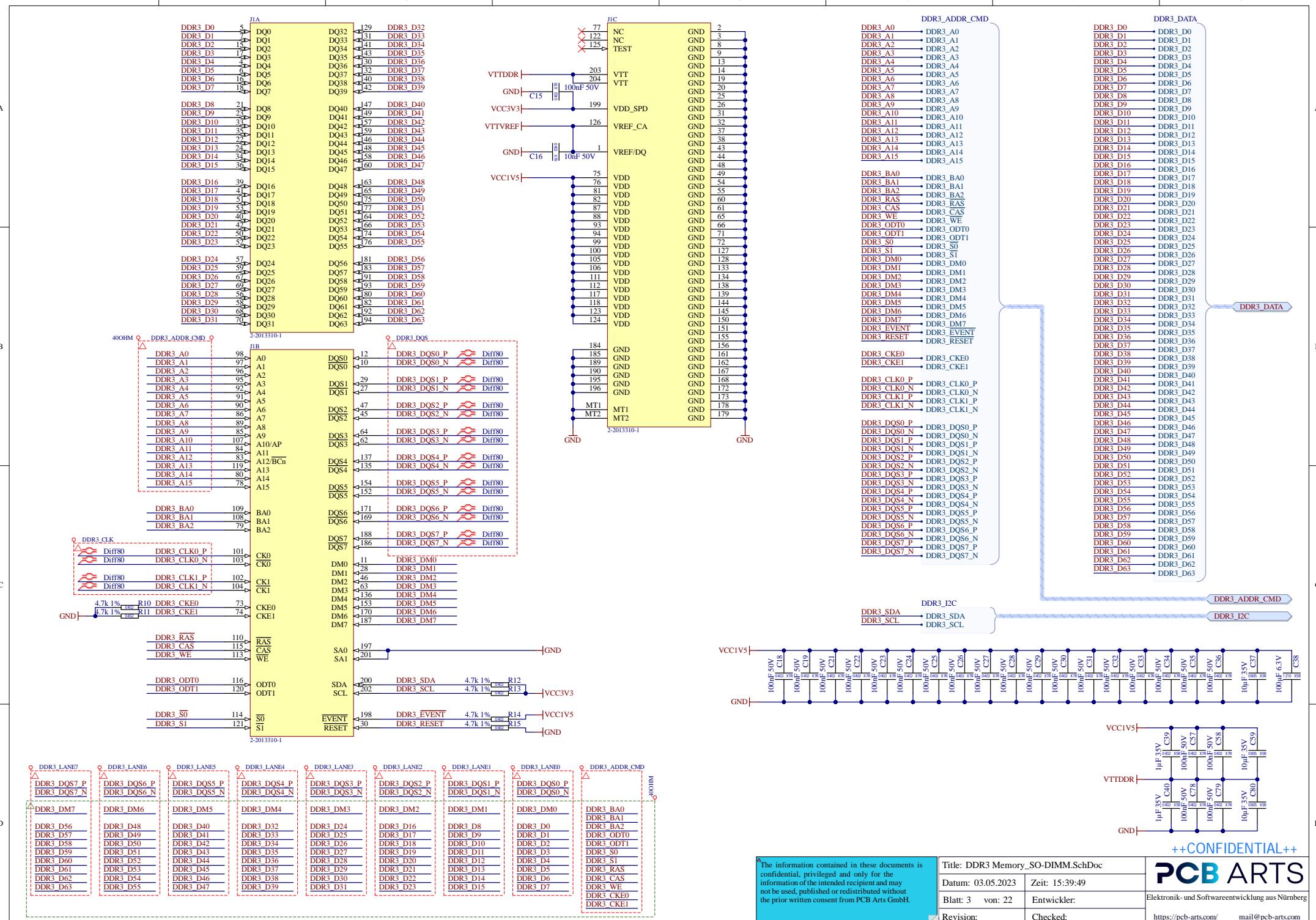
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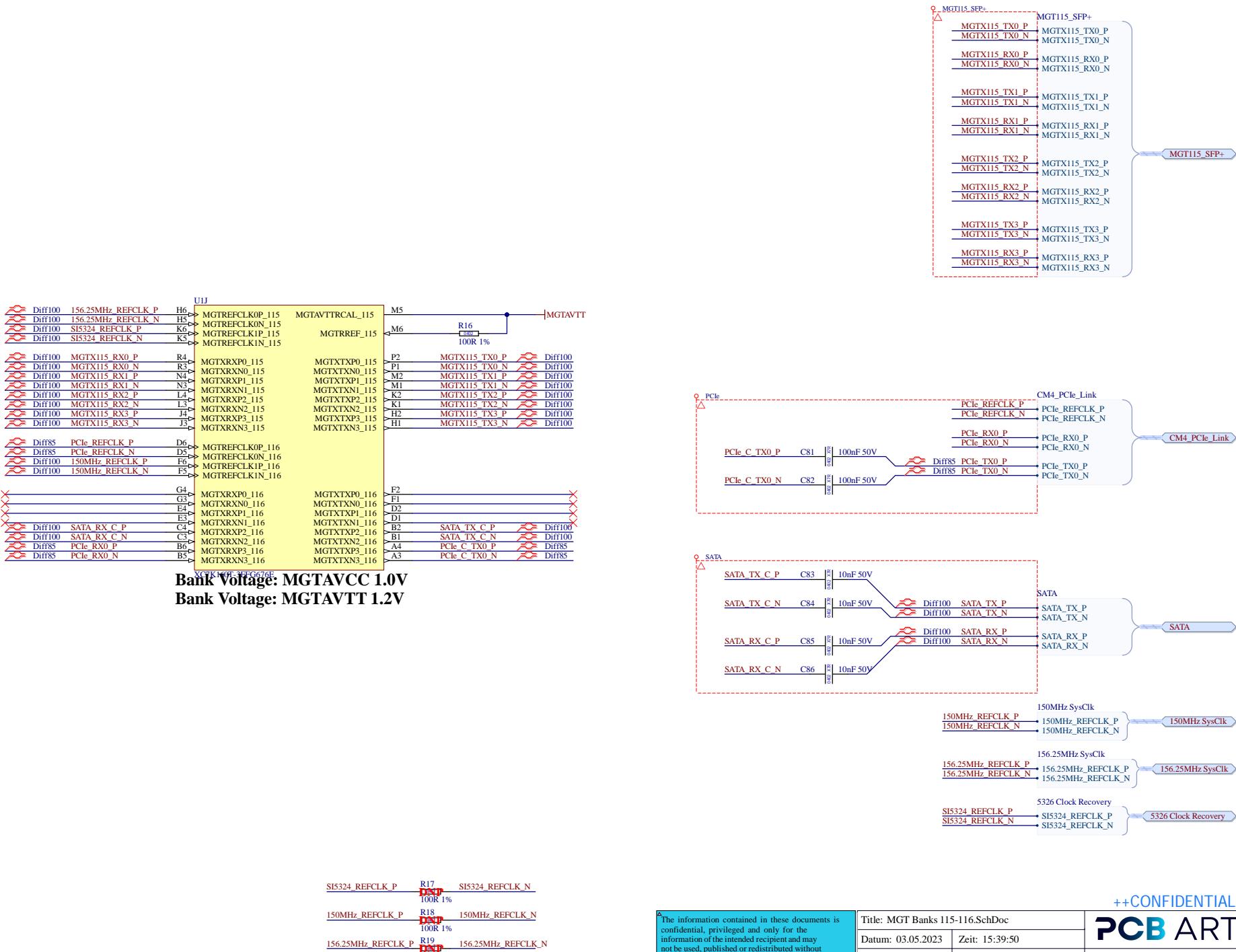
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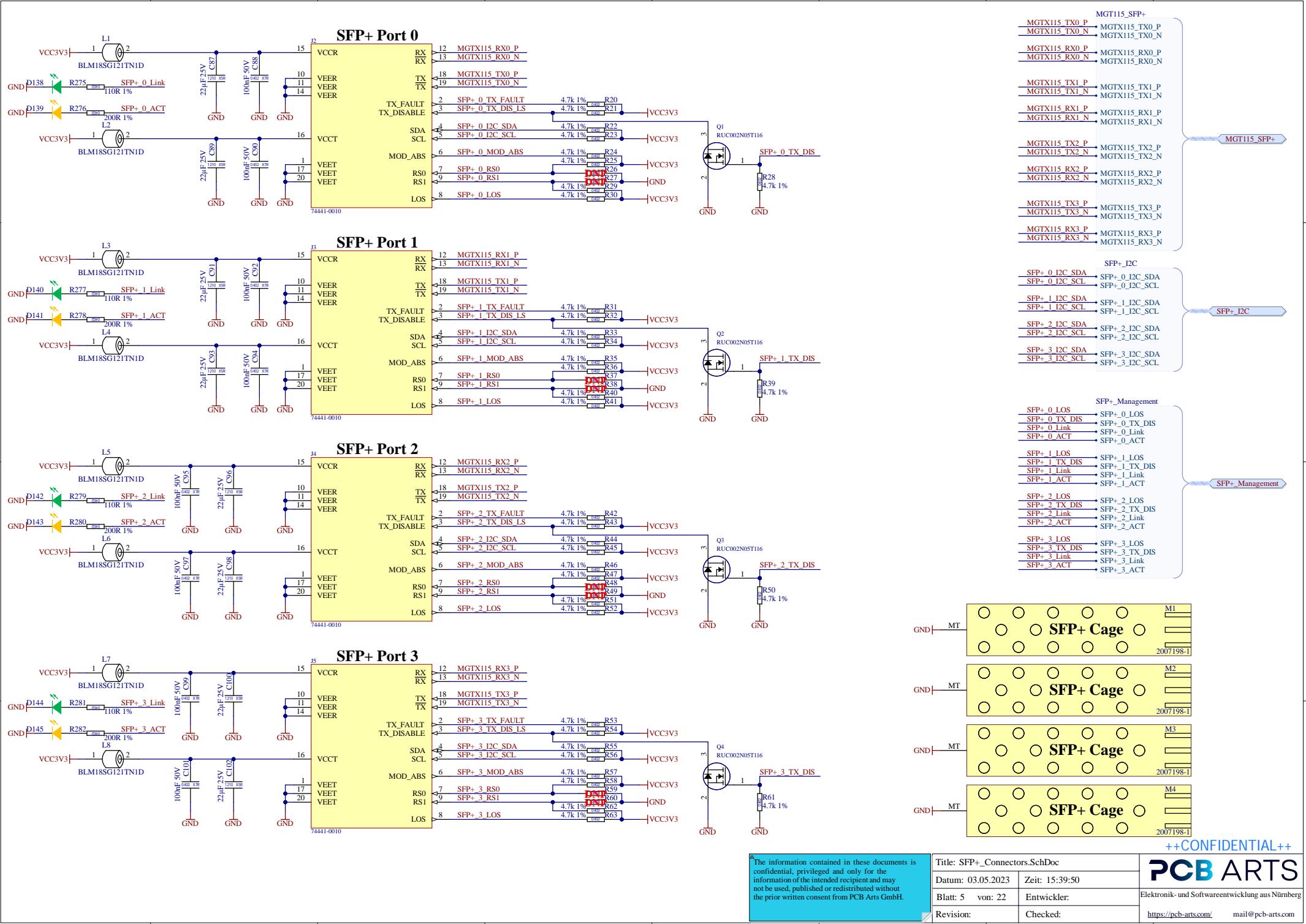


SI5324_Refclk_P R17 SI5324_Refclk_N
100R 1% DNP
150MHz_Refclk_P R18 150MHz_Refclk_N
100R 1% DNP
156.25MHz_Refclk_P R19 156.25MHz_Refclk_N
100R 1% DNP

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Title: MGT Banks 115-116.SchDoc
Datum: 03.05.2023 | Zeit: 15:39:50
Blatt: 4 von: 22 Entwickler:
Revision: Checked: <https://pcb-arts.com/> mail@pcb-arts.com

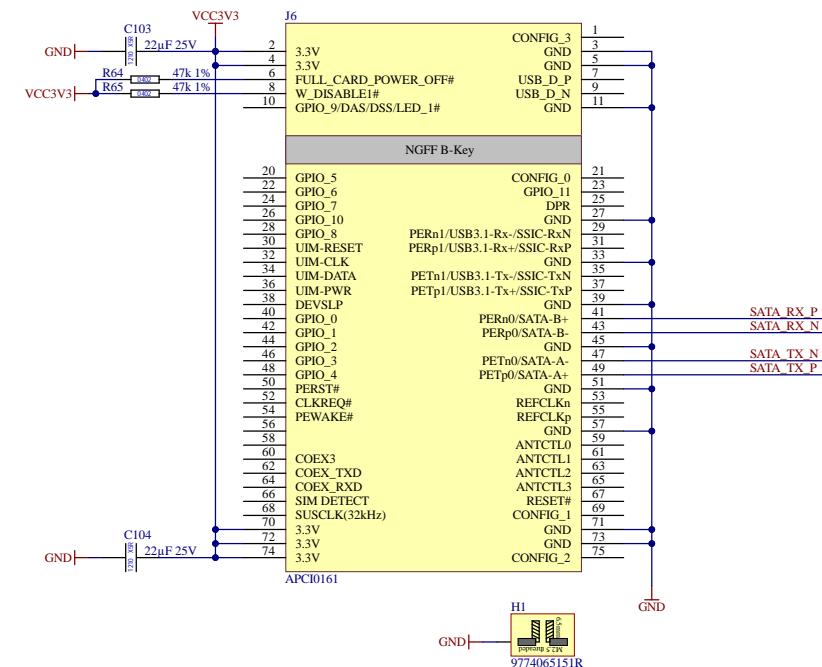
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Title: SSD M.2_B-Key.SchDoc

Datum: 03.05.2023 | Zeit: 15:39:51

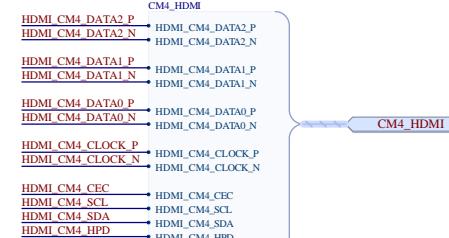
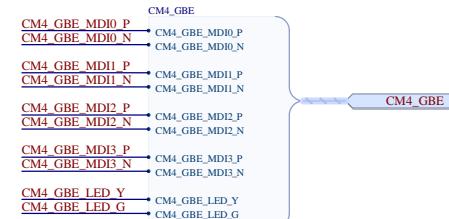
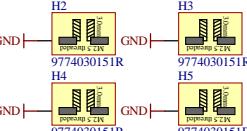
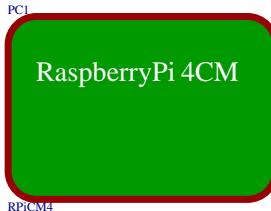
Blatt: 6 von: 22 Entwickler:

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PCIe PERST → PCIe PERST

PI_EXTRST → PI_EXTRST

CM4_PCIE_Link

PCIe_REFCLK_P → PCIe_REFCLK_P
PCIe_REFCLK_N → PCIe_REFCLK_N

PCIe_RX0_P → PCIe_RX0_P
PCIe_RX0_N → PCIe_RX0_N

PCIe_TX0_P → PCIe_TX0_P
PCIe_TX0_N → PCIe_TX0_N

CM4_USB_HOST

CM4_USB_P → CM4_USB_P
CM4_USB_N → CM4_USB_N
CM4_OTG_ID → CM4_OTG_ID

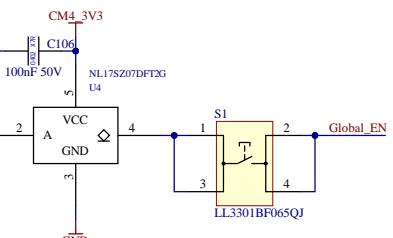
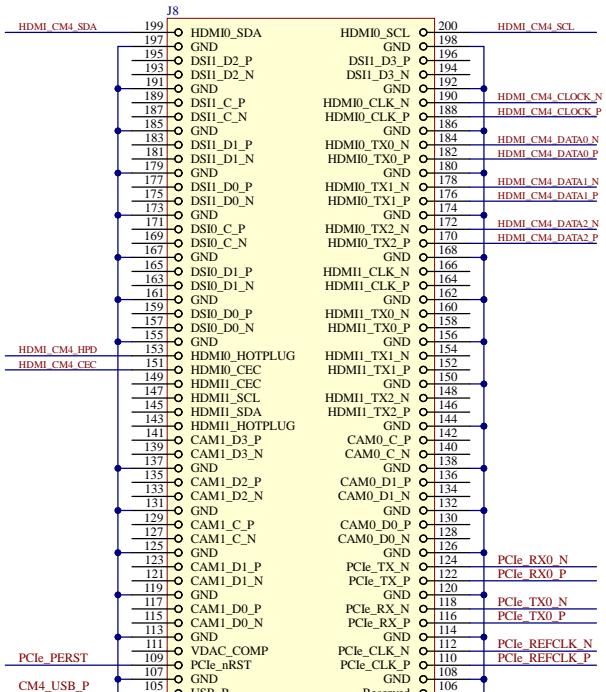
CM4_muSD

CM4_muSD_DATO → CM4_muSD_DATO
CM4_muSD_DAT1 → CM4_muSD_DAT1
CM4_muSD_DAT2 → CM4_muSD_DAT2
CM4_muSD_DAT3 → CM4_muSD_DAT3
CM4_muSD_CLK → CM4_muSD_CLK
CM4_muSD_CMD → CM4_muSD_CMD

CM4_SMI

SMI_SWE_NSRW_N → SMI_SWE_NSRW_N
SMI_SOE_NSE → SMI_SOE_NSE
SMI_SA0 → SMI_SA0
SMI_SA1 → SMI_SA1
SMI_SA2 → SMI_SA2
SMI_SA3 → SMI_SA3
SMI_SA4 → SMI_SA4
SMI_SA5 → SMI_SA5
SMI_SD0 → SMI_SD0
SMI_SD1 → SMI_SD1
SMI_SD2 → SMI_SD2
SMI_SD3 → SMI_SD3
SMI_SD4 → SMI_SD4
SMI_SD5 → SMI_SD5
SMI_SD6 → SMI_SD6
SMI_SD7 → SMI_SD7
SMI_SD8 → SMI_SD8
SMI_SD9 → SMI_SD9
SMI_SD10 → SMI_SD10
SMI_SD11 → SMI_SD11
SMI_SD12 → SMI_SD12
SMI_SD13 → SMI_SD13
SMI_SD14 → SMI_SD14
SMI_SD15 → SMI_SD15
SMI_SD16 → SMI_SD16
SMI_SD17 → SMI_SD17

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Title: Raspberry Pi CM4.SchDoc

Datum: 03.05.2023

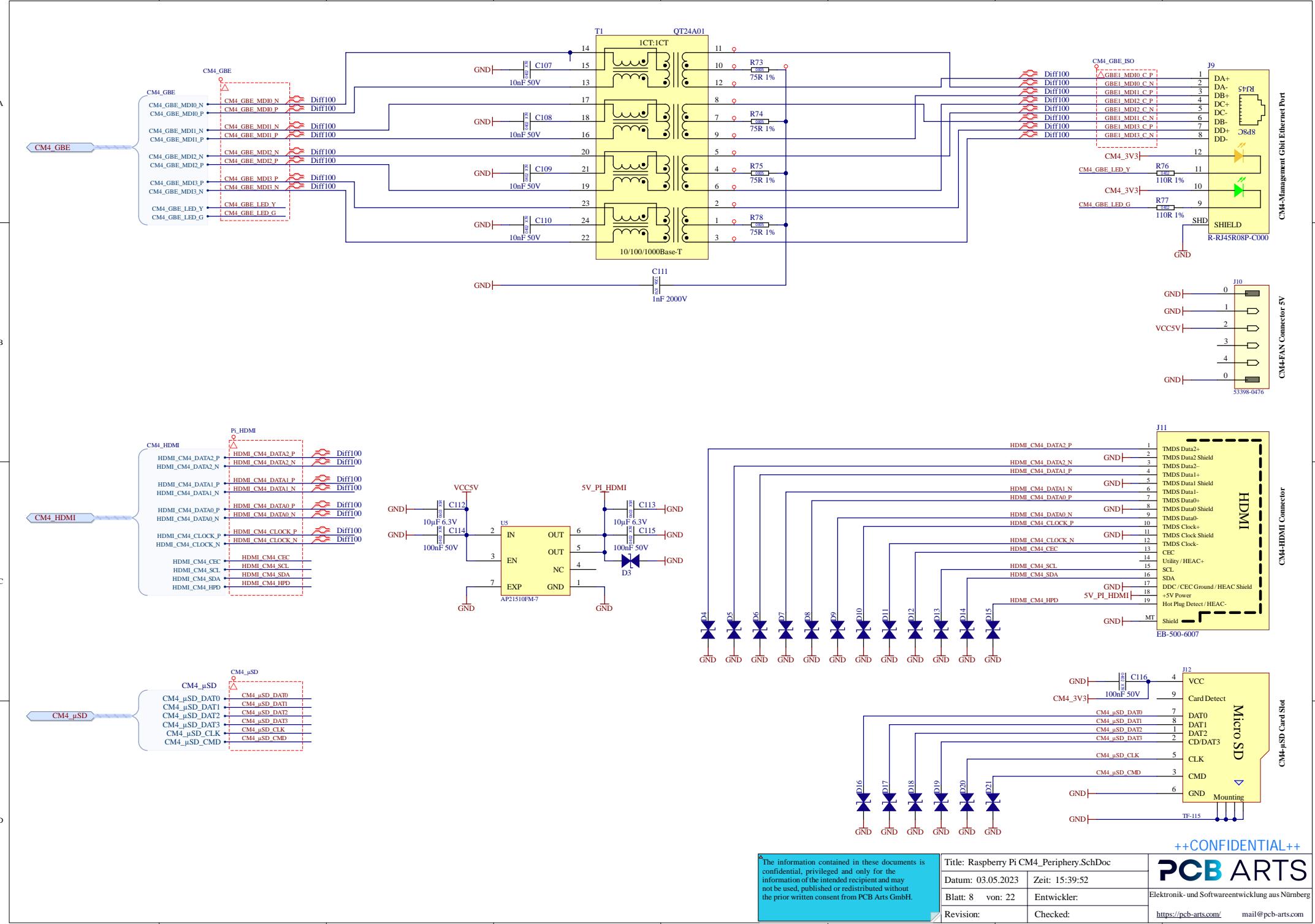
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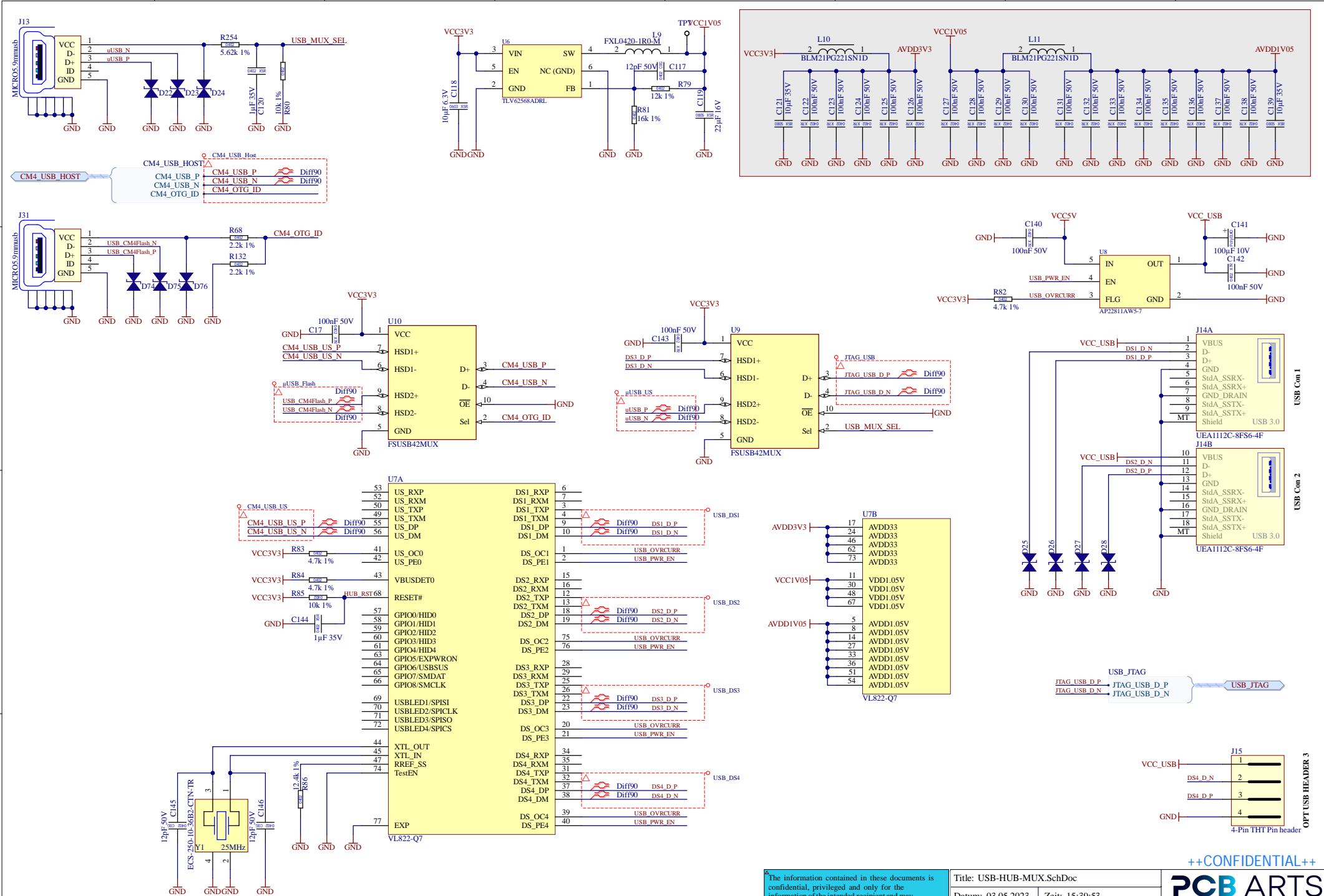
Blatt: 7 von: 22

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Title: USB-HUB-MUX.SchDoc

Datum: 03.05.2023 Zeit: 15:39:53

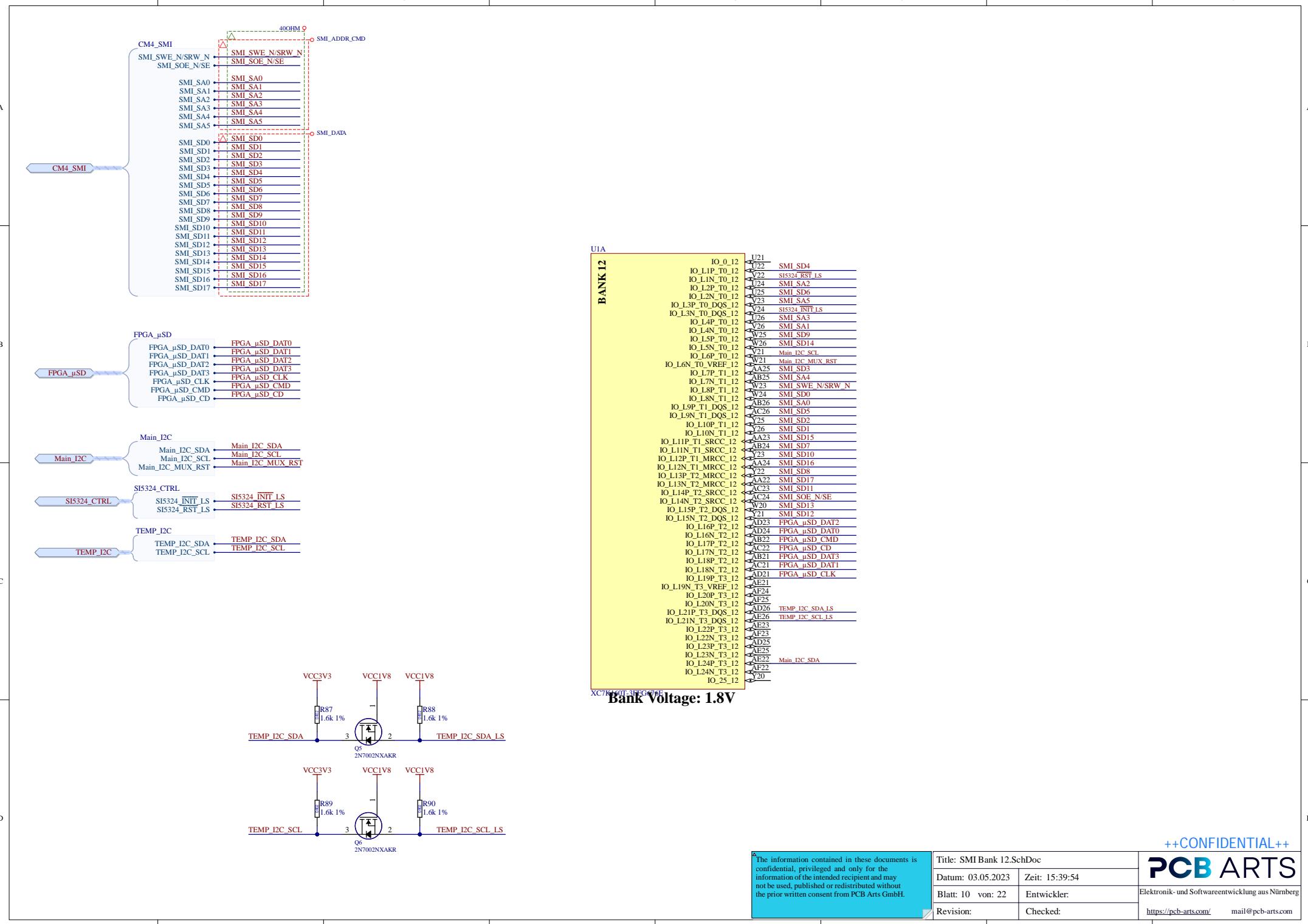
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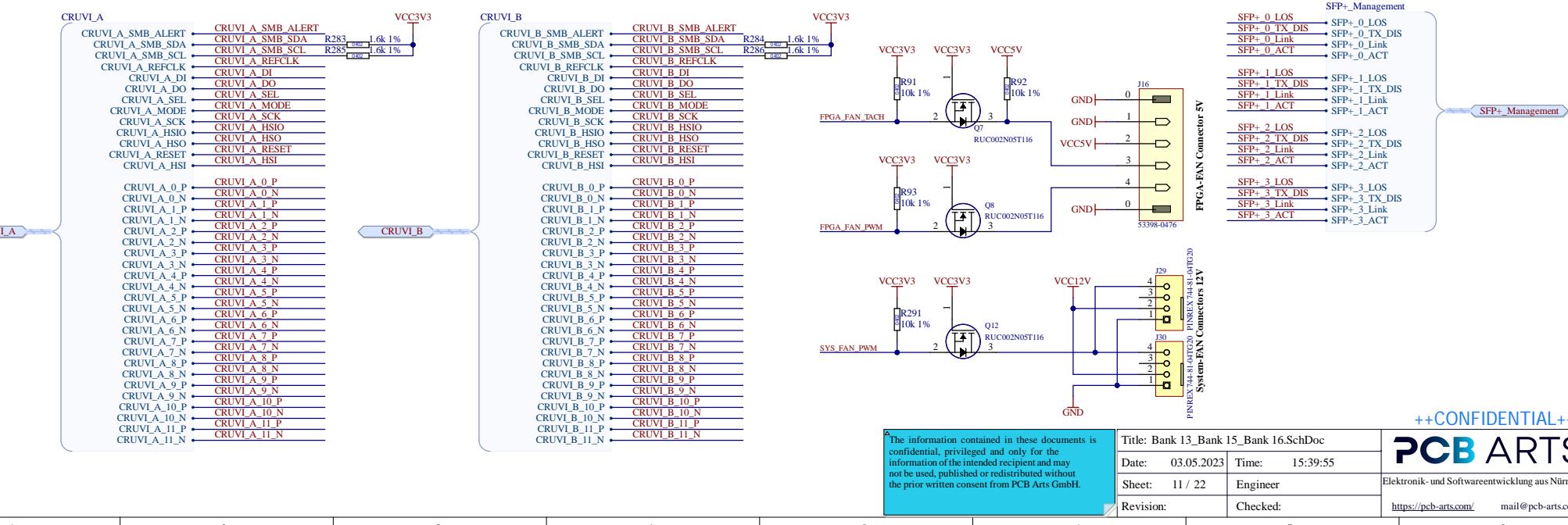
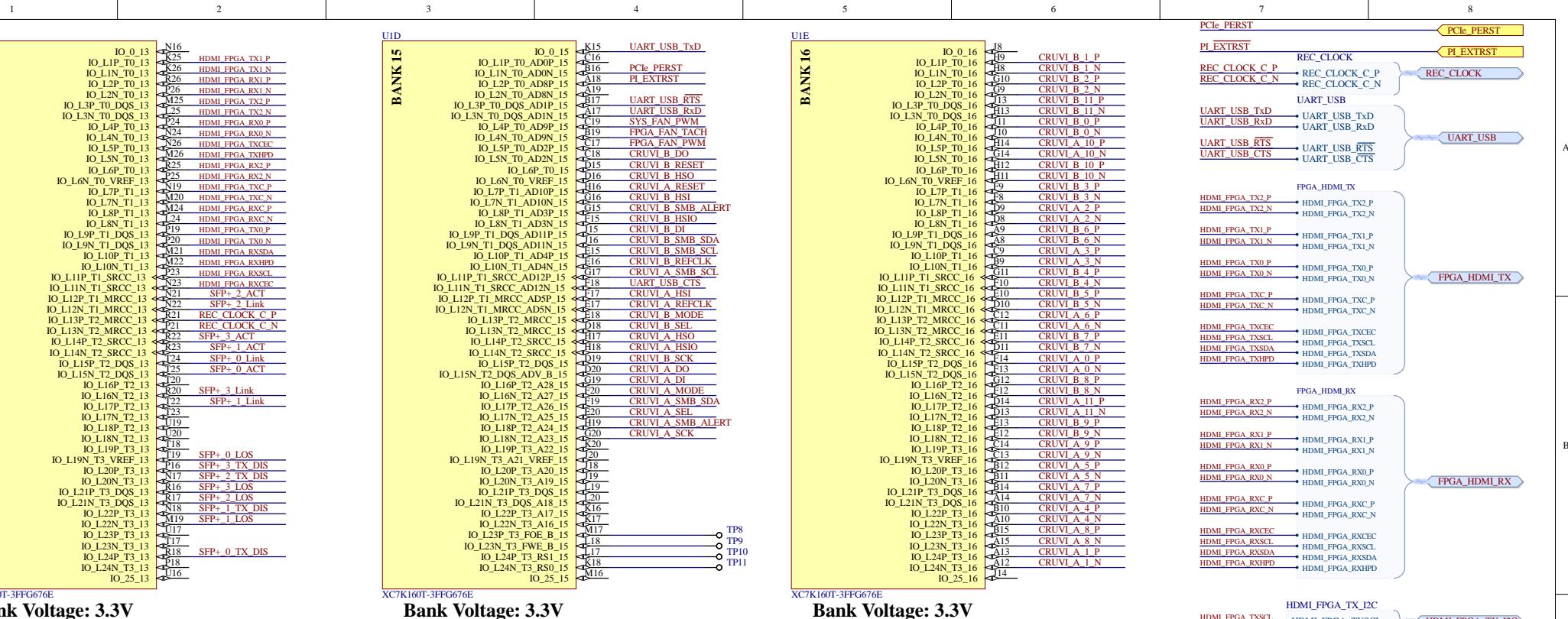
Revision: Checked:

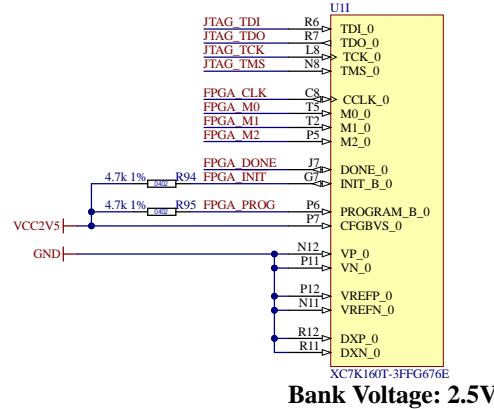
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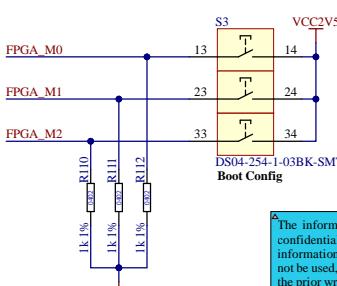
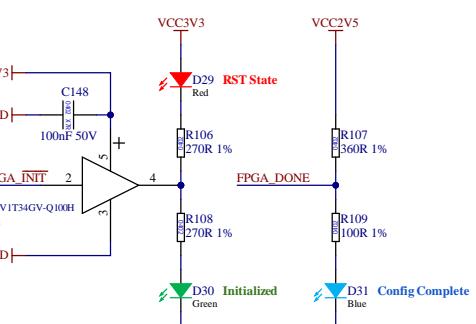
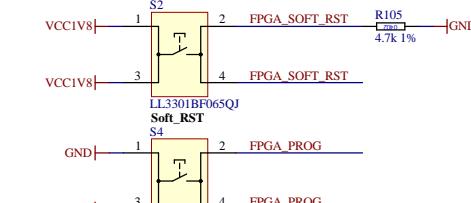
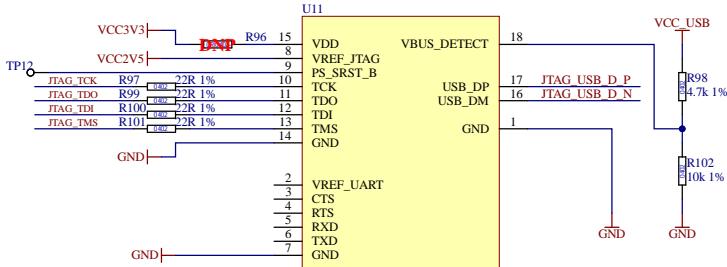
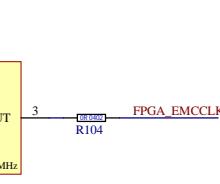
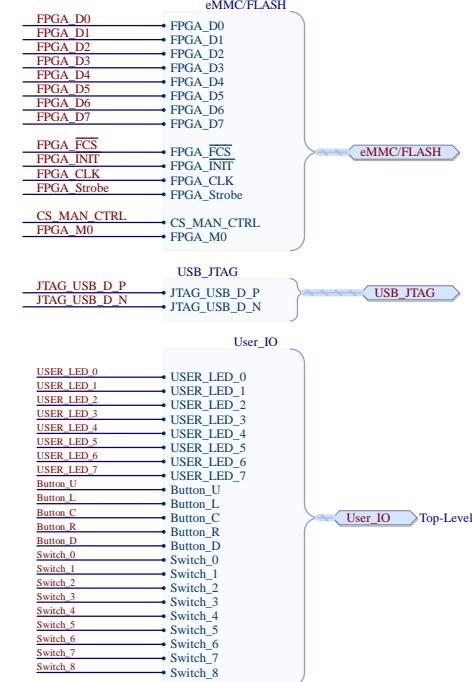
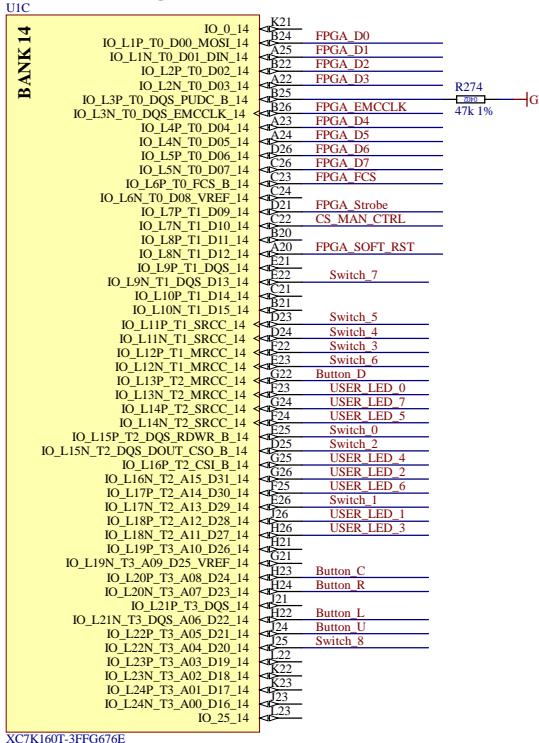
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Bank Voltage: 1.8V



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Title: Bank_0_Bank_14.SchDoc

Datum: 03.05.2023 | Zeit: 15:39:56

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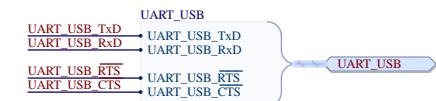
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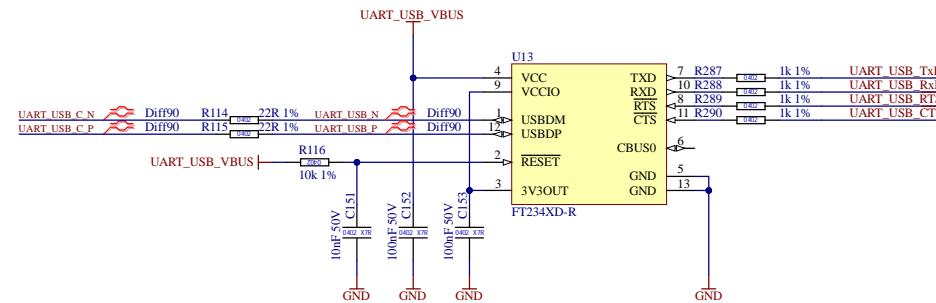
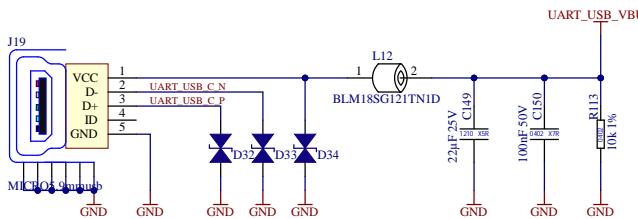
B

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D



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Title: USB-UART-interface.SchDoc

Date: 03.05.2023 Time: 15:39:56

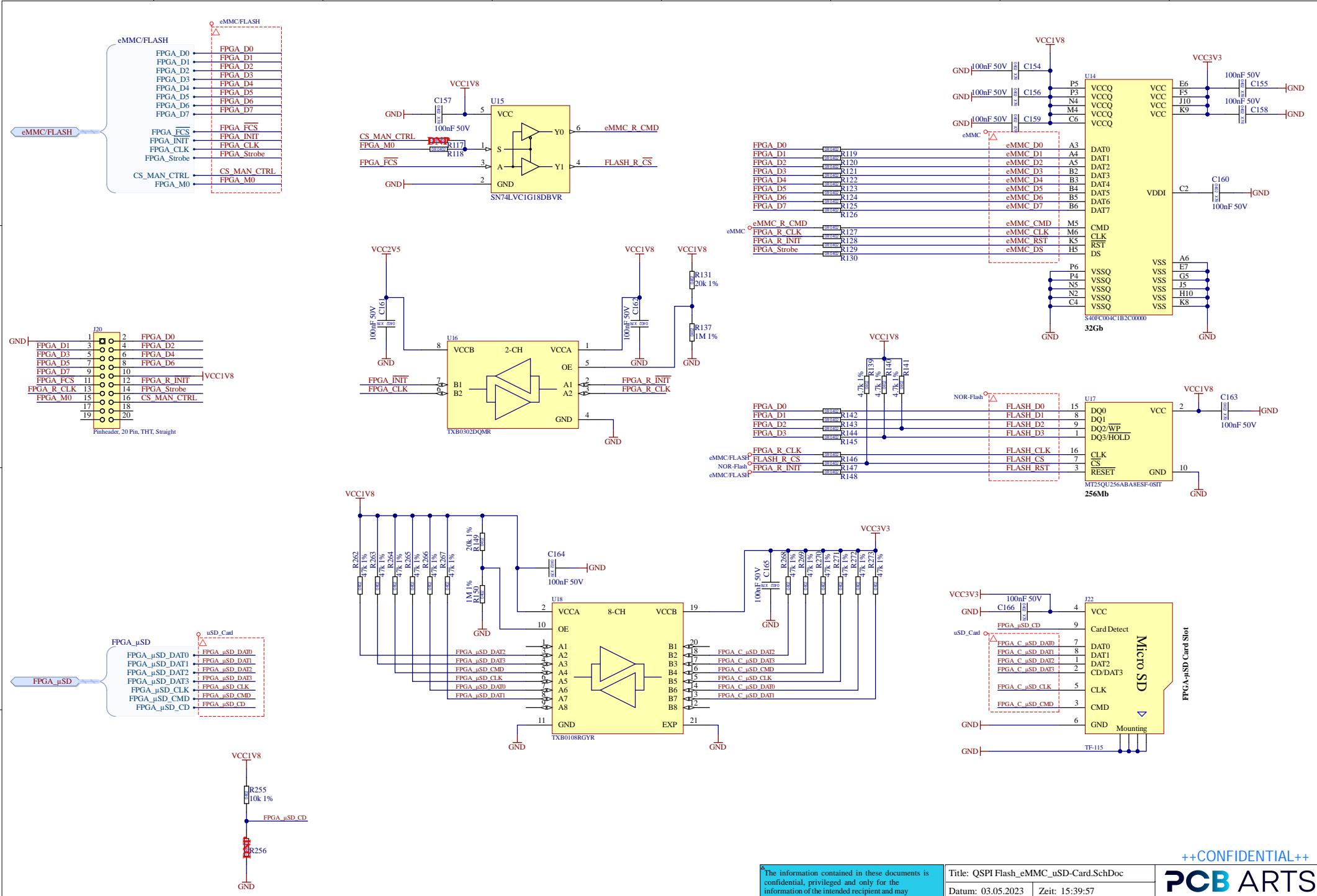
Sheet: 13 / 22 Engineer

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Title: QSPI Flash_eMMC_uSD-Card.SchDoc

Datum: 03.05.2023 | Zeit: 15:39:57

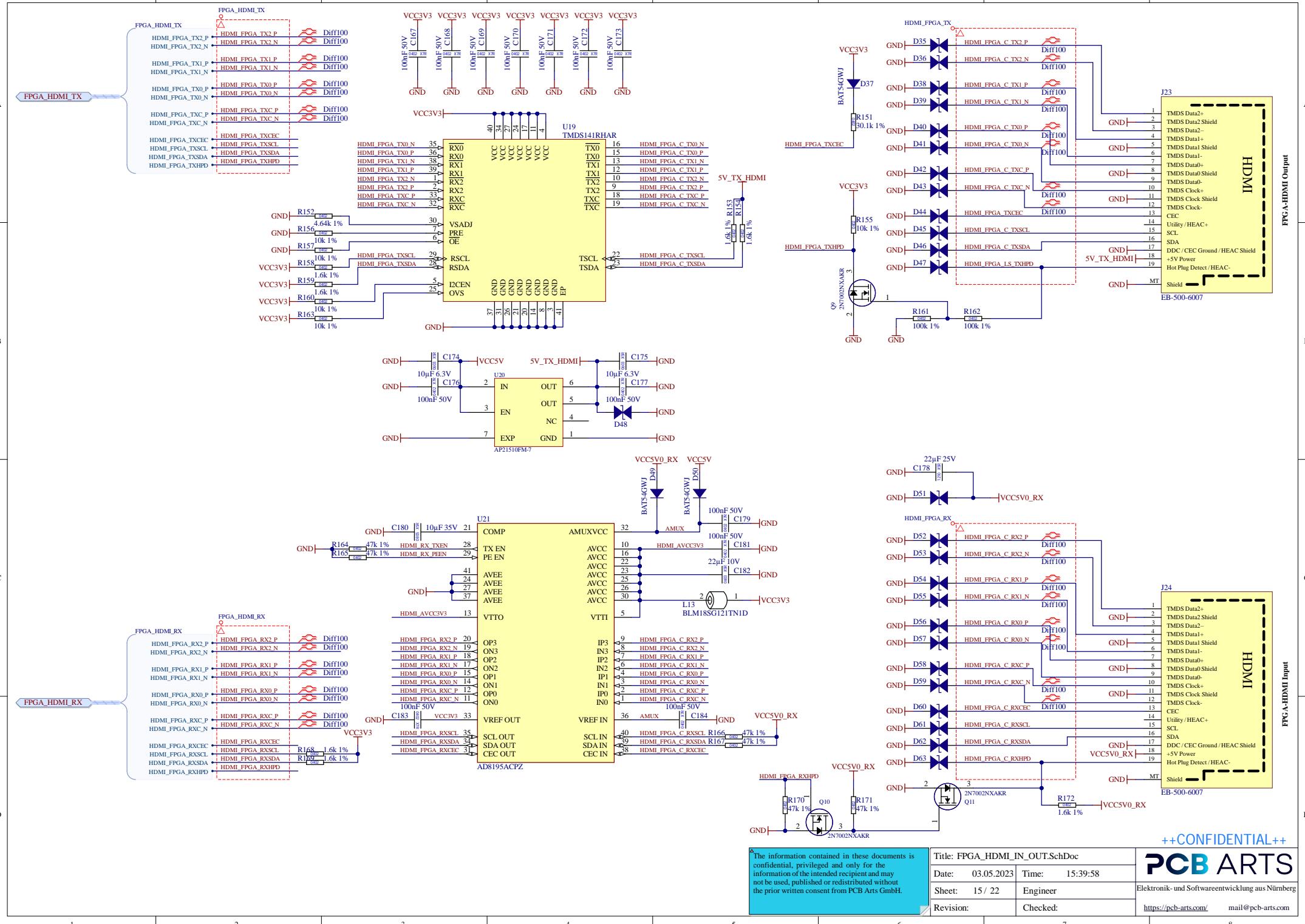
Blatt: 14 von: 22 Entwickler:

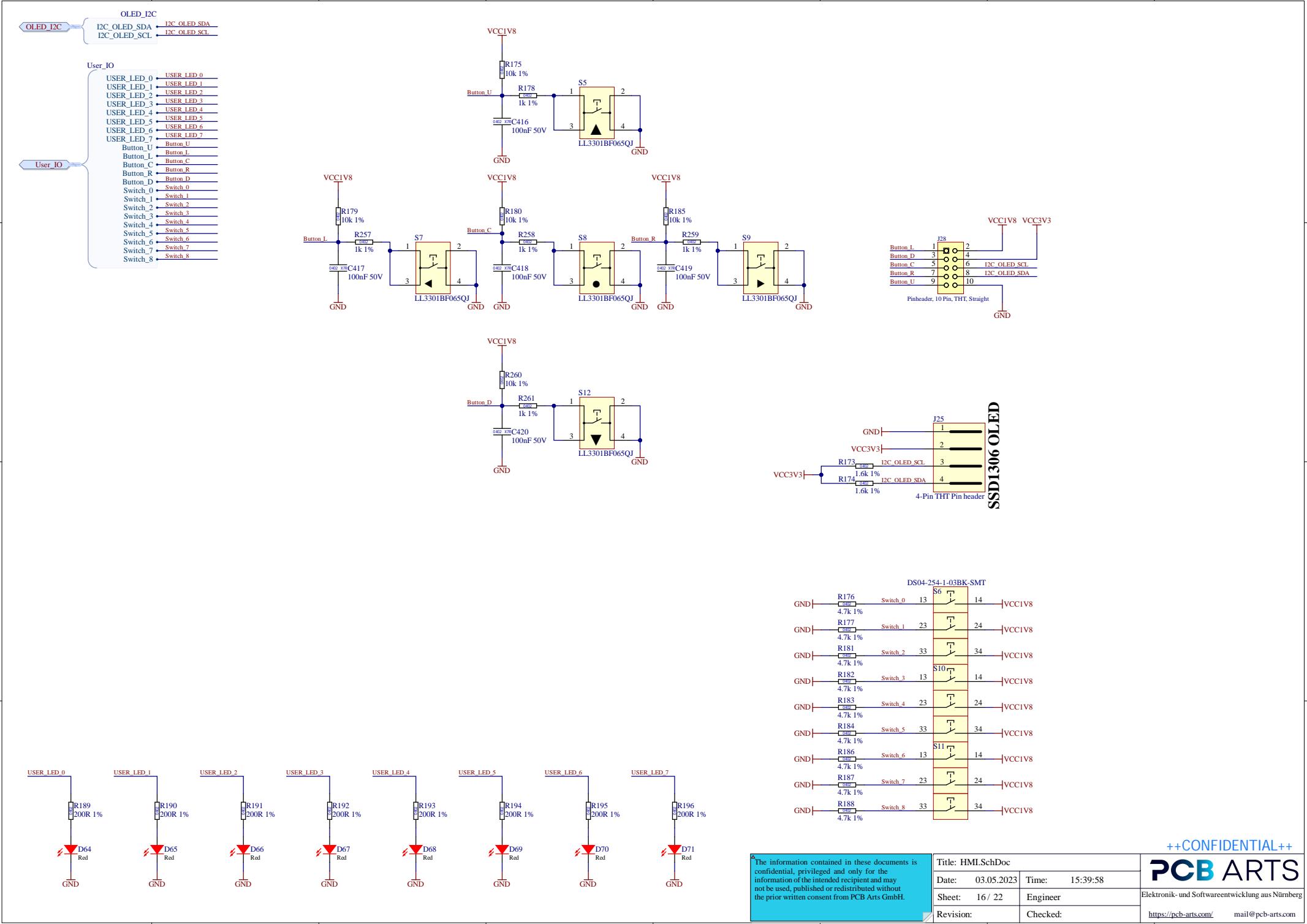
Revision: Checked: https://pcb-arts.com/ mail@pcb-arts.com

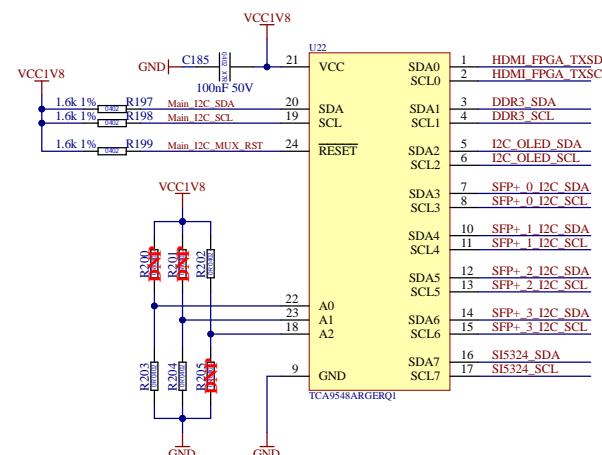
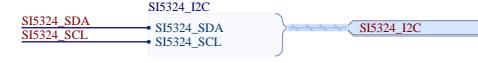
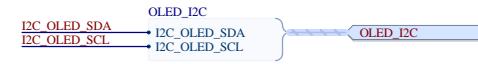
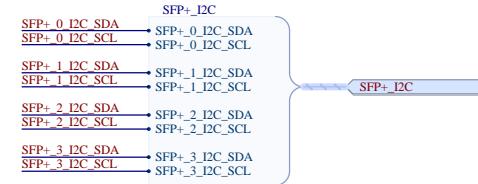
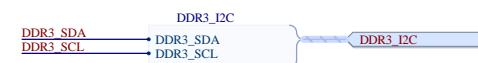
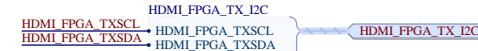
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Title: I2C Multiplexer.SchDoc

Date: 03.05.2023 Time: 15:39:59

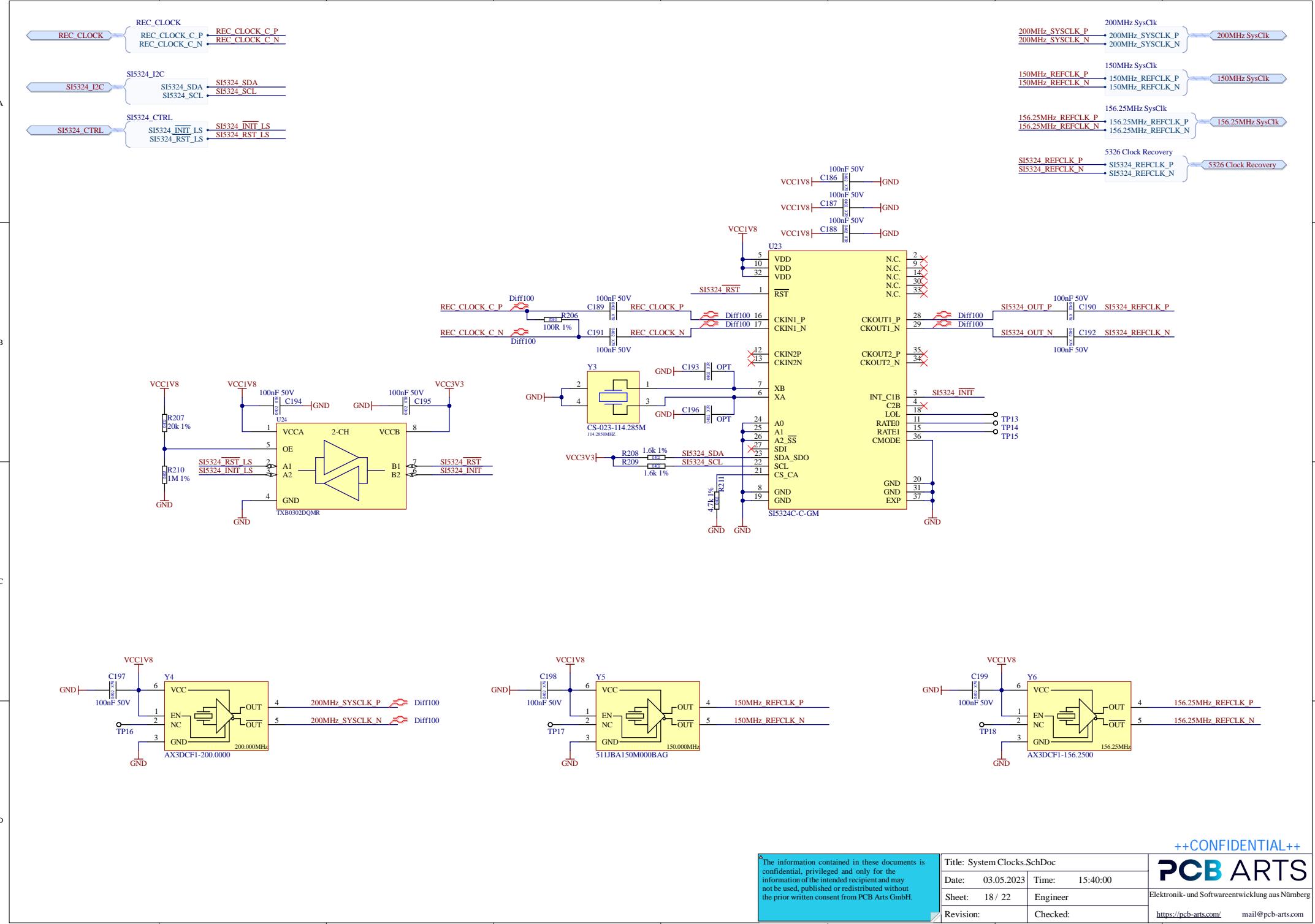
Sheet: 17 / 22 Engineer

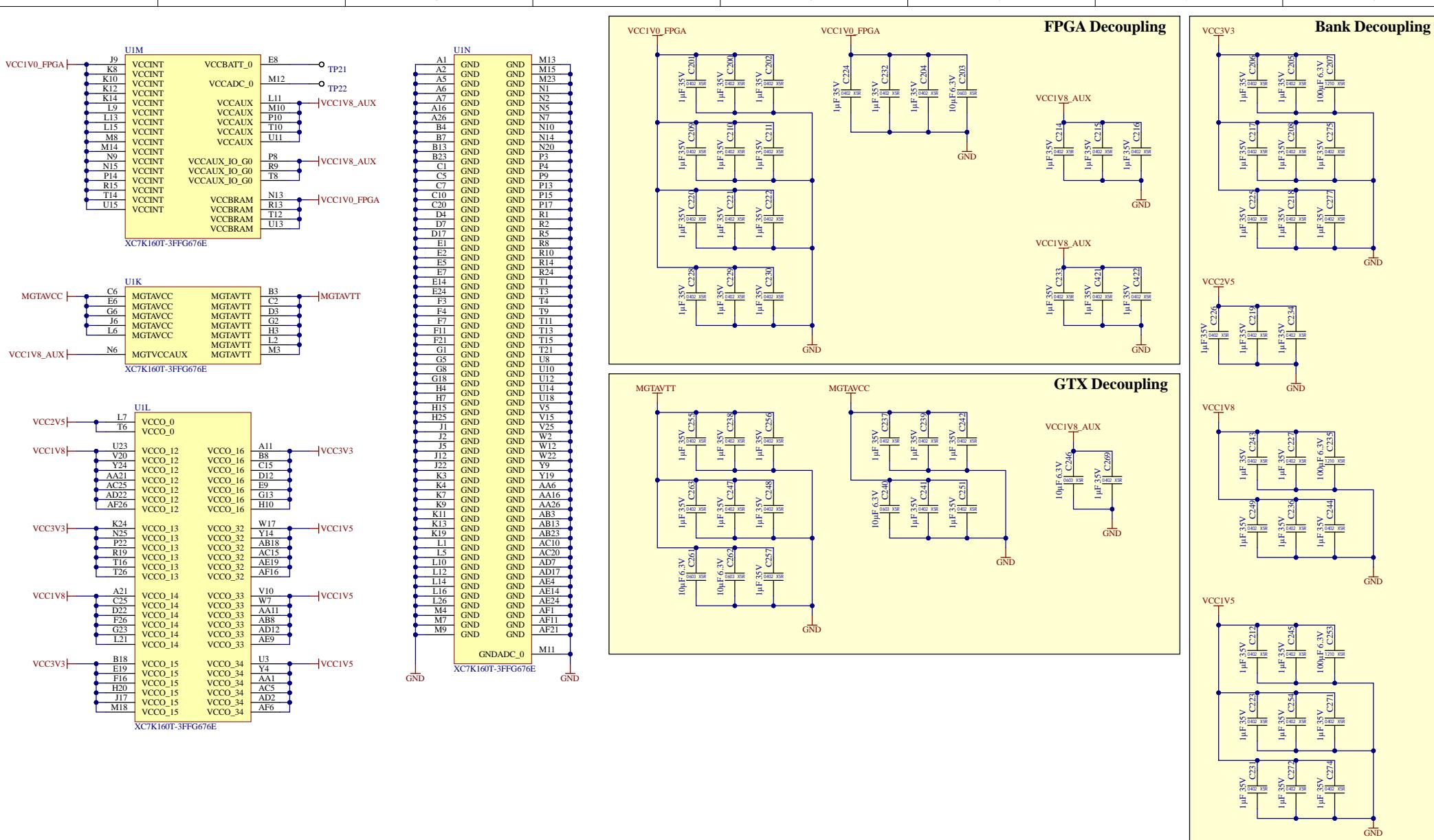
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Title: FPGA Supply Pins.SchDoc

Date: 03.05.2023 Time: 15:40:00

Sheet: 19 / 22 Engineer

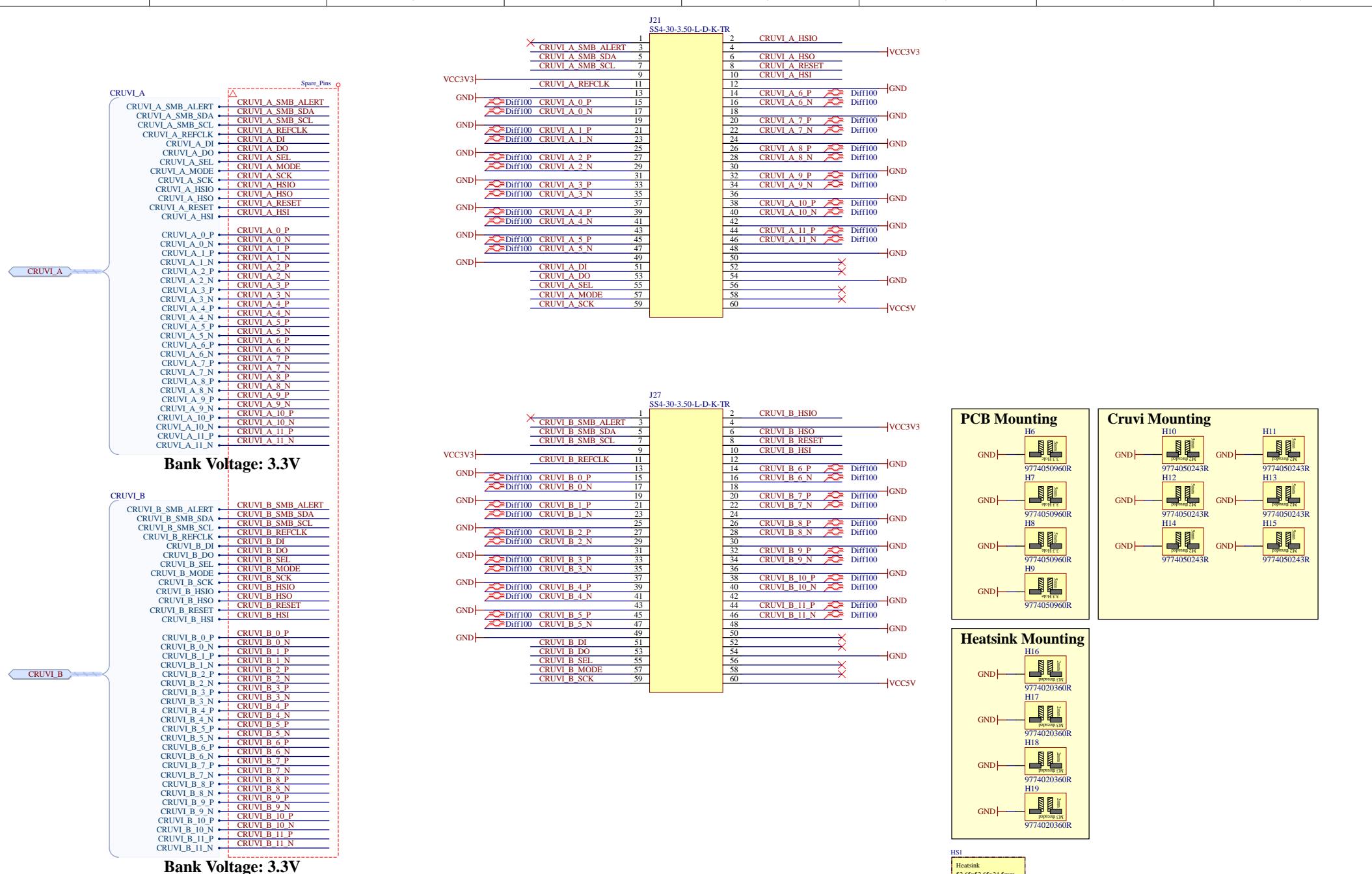
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Title: Spare Pins.SchDoc

Date: 03.05.2023 Time: 15:40:01

Sheet: 20 / 22 Engineer

Revision: Checked:

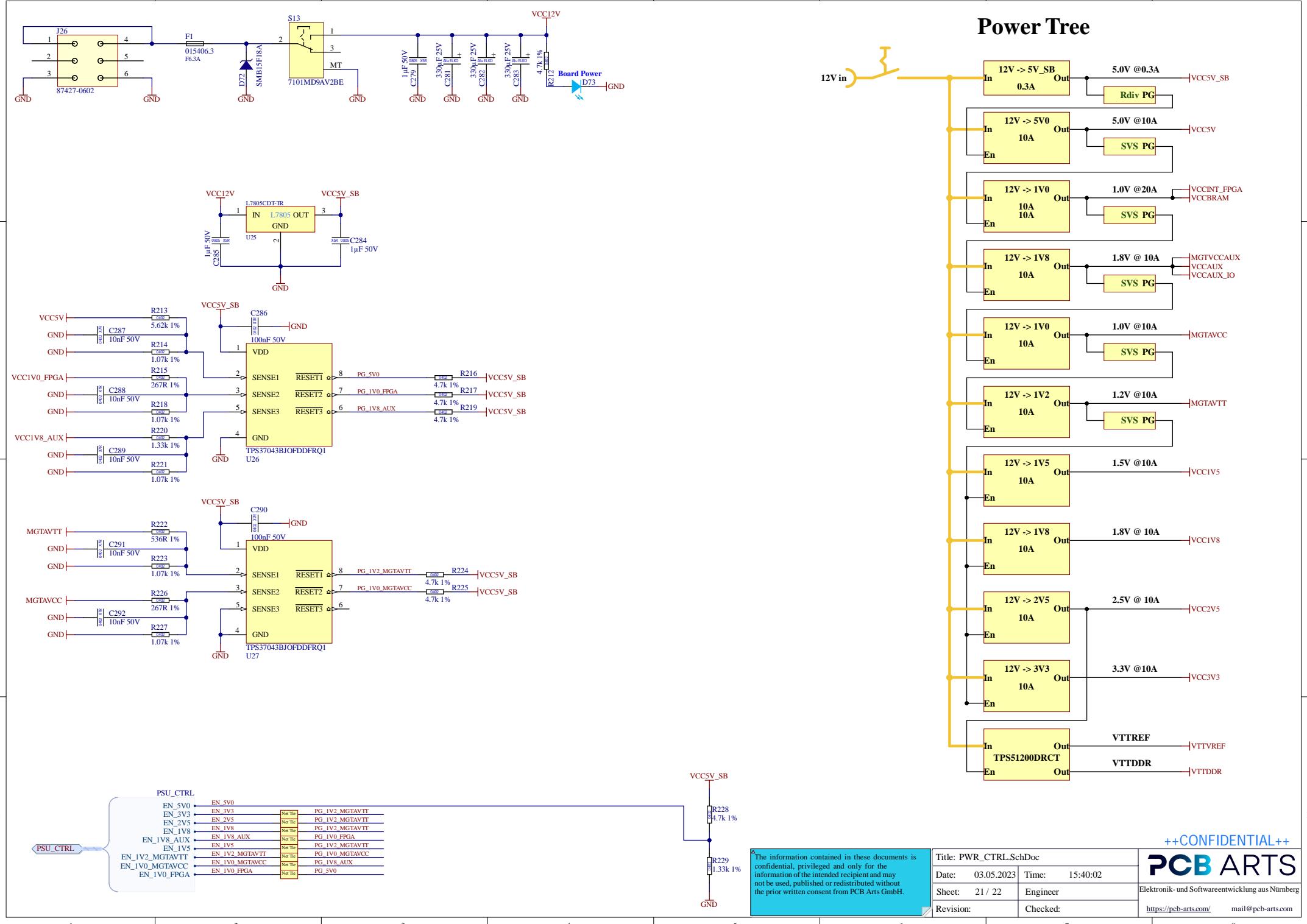
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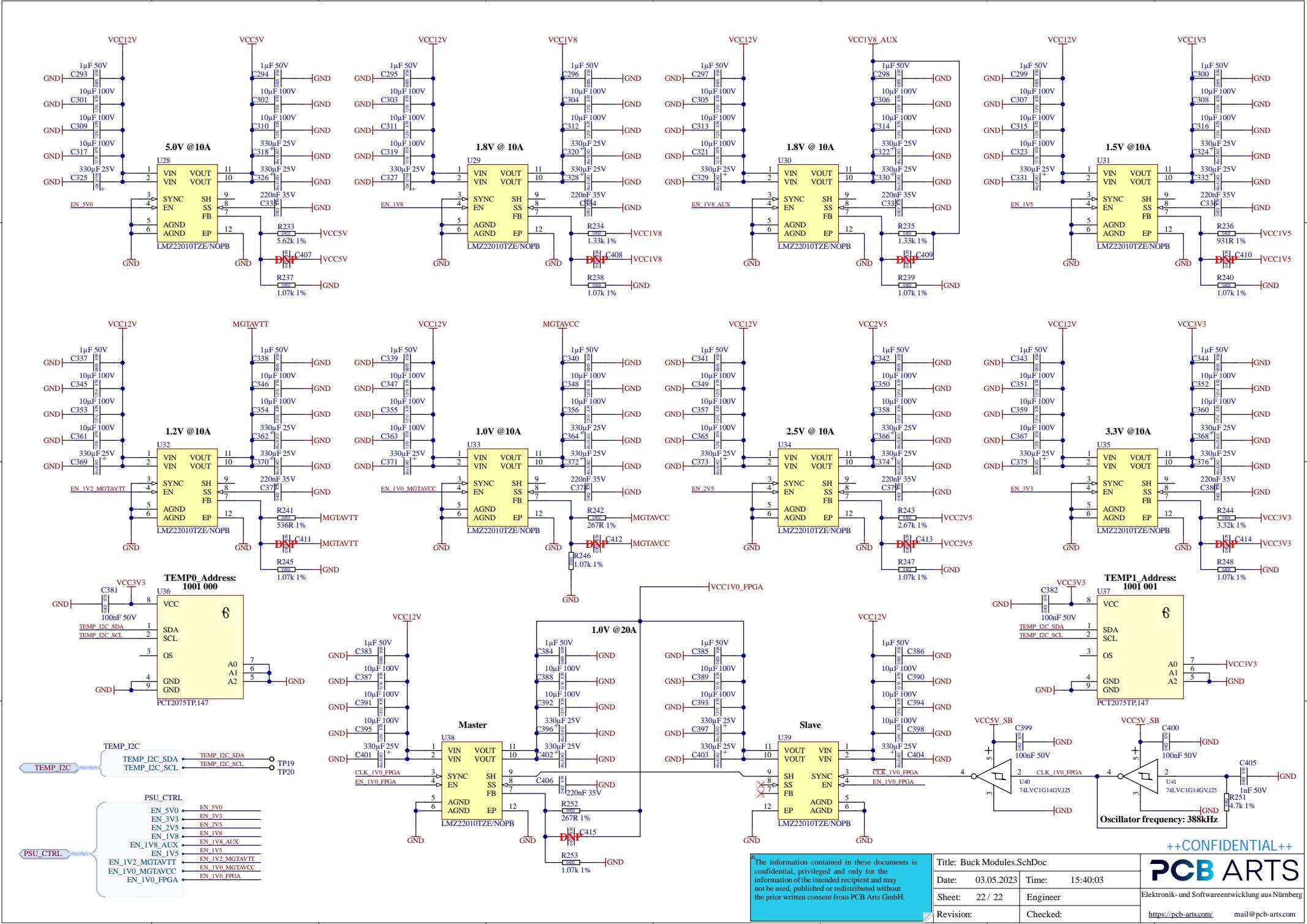
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Power Tree





Bill of Materials

Source Data From:

Project:

Variant:

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<https://pcb-arts.com/>
Sitz: Fürth, Bayern
Registergericht: Amtsgericht Fürth, HRB 166
Geschäftsführer: Sabir Kaygusuz

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