Peter Borenstein

Address: 1214 SW 16th Ave Apt D, Gainesville, Florida
Phone: 904-703-1089
email: PCBORENSTEIN@GMAIL.COM

Work Experience

Current Aug 2013	**Co-Founder and Hardware Lead at Verigo, Gainesville FL** *Designed, developed, debugged, and coded a low power wireless data logger for monitoring supply chains* The devices uses Bluetooth Low Energy to communicate logged temperature and humidity data to smartphones & tablets. Passed FCC and CE certifications. Managed two part time engineers brought on after our seed funding round. Datasheet Raspberry Pis, Python scripts, and the BlueZ stack were used to create automated test equipment for manufacuting.
May 2011-Dec 2011	**Test Technology Intern at Intel, Fort Colins, CO** Created test patterns for server chips pre-Silicon. Modeled scan chains allowing others to run tests. Used Perl to read though lengthy test logs for relevant data
Jun 2010-Nov 2010	**Lifeguard at University of Florida Housing Dept., Gainesville FL** Provided a safe swimming experience for others

Education

December 2013	**Bachelor of Science in Electrical Engineering, University of Florida** Major: Electrical Engineering | *Cum Laude* | Digital Design Specialization GPA: 3.66/4.0 | List of Courses

Skills

Top skills	C and Git
Basic Knowledge	Python, C++, Perl, Linux system programming, Bash, VHDL
IDEs	IAR Workbench, Code Composer Studio, Atmel Studio
PCB layout	Eagle, PCB Artist, Altera
Tools Used	Oscilloscopes, Logic Analyzers, Multimeters, SMU
TI E2E MVP	Recognized for sharing expertise, solutions, and experience to help other community members

UF IEEE Student Chapter Involvement

2011-2012 Academic Year	Secretary (elected)
2010-2011 Academic Year	Advertising Chair
2011	1ST place IEEE South East Conference Engineering Ethics Competition
Various Dates	Introduced local grade students to robotics with Lego Mindstorm

Interests and Activities

Bluetooth, Internet of Things, Open-Source, Vim, Cryptography
Distance Running, League of Legends

Bachelor of Science in Electrical Engineering

Relevant Coursework

Course	Grade	Credit Hrs
Digital Logic	A	4
Digital Computer Architecture	B	4
Digital Design	A-	4
Microprocessor Applications	A	4
Computer Programming for Engineers C++	A	2
Circuits 1	B	4
Circuits 2	A-	4
Electronic Circuits	B+	4
Statistics	A	3
Linear Algebra	A	3
Signals and Systems	A-	3
Audio Engineering	B+	3
Solid State Electronics	B+	3
Electromagnetic Fields	A-	3
Communication Systems and Components	B	3
Linear Control Systems	A	3
Electrical Engineering Design 1	A	4
Electrical Engineering Design 2	C+	4