

# Peter BORENSTEIN

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ADDRESS: 1214 SW 16th Ave Apt D, Gainesville, Florida  
PHONE: 904-703-1089  
EMAIL: [pcborenstein@gmail.com](mailto:pcborenstein@gmail.com)

## WORK EXPERIENCE

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| MAY 2018 - JAN 2015 | Hardware/Firmware Engineer at CRITICAL ALERT SYSTEMS, Jacksonville FL<br><i>Designed, developed, and coded hospital signalling equipment</i><br>The system routes calls triggered by patients to nurses. I created a new hub, a screen device to display calls, audio circuits and code to create new alarm tones to replace chime chips, and firmware for vandal proof room devices. For an ethernet device close to release, I made minor firmware changes and created a python script to place a MAC addresses into the binary programming file and print a label. |
| JAN 2015 - AUG 2013 | Co-Founder and Hardware Lead at VERIGO, Gainesville FL<br><i>Designed, developed, debugged, and coded a low power wireless data logger for monitoring supply chains</i><br>The devices uses Bluetooth Low Energy to communicate logged temperature and humidity data to smartphones & tablets. Passed FCC and CE certifications. Managed two part time engineers brought on after our seed funding round. <a href="#">Datasheet</a><br>Raspberry Pis, Python scripts, and the BlueZ stack were used to create automated test equipment for manufacturing.             |
| MAY 2011-DEC 2011   | Test Technology Intern at INTEL, Fort Collins, CO<br>Created test patterns for server chips pre-Silicon. Modeled scan chains allowing others to run tests. Used Perl to read through lengthy test logs for relevant data  |
| JUN 2010-NOV 2010   | Lifeguard at UNIVERSITY OF FLORIDA HOUSING DEPT., Gainesville FL<br>Provided a safe swimming experience for others  |

## EDUCATION

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| DECEMBER 2013 | Bachelor of Science in ELECTRICAL ENGINEERING, <b>University of Florida</b><br>Major: Electrical Engineering   <i>Cum Laude</i>   Digital Design Specialization<br>GPA: 3.66/4.0 |
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## SKILLS

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| Top skills      | C and Git   |
| Basic Knowledge | Python, C++, Perl, Linux system programming, Bash, VHDL                                     |
| IDEs            | IAR Workbench, Code Composer Studio, Atmel Studio   |
| PCB layout      | Eagle, PCB Artist, Altera   |
| Tools Used      | Oscilloscopes, Logic Analyzers, Multimeters, SMU  |
| TI E2E MVP      | Recognized for sharing expertise, solutions, and experience to help other community members |

## UF IEEE STUDENT CHAPTER INVOLVEMENT

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2011-2012 Academic Year	Secretary (elected)
2010-2011 Academic Year	Advertising Chair
2011	1 <sup>st</sup> place IEEE South East Conference Engineering Ethics Competition
Various Dates	Introduced local grade students to robotics with Lego Mindstorm