

Peter BORENSTEIN

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WORK EXPERIENCE

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| JAN 2016 - MAY 2018 | Hardware Engineer at CRITICAL ALERT SYSTEMS, Jacksonville, FL
<i>Designed, developed, and coded hospital signalling equipment</i>
The system conveys calls triggered by patients to nurses. I created a new hub, a screen device to display calls, replaced chime chips with an audio circuit for new alarms, and created firmware for vandal proof room devices. For an ethernet device close to release, I made minor firmware changes and created a python script to place MAC addresses into the binary programming file and print a label. Used TI-RTOS. |
| AUG 2013 - JAN 2016 | Co-Founder and Hardware Lead at VERIGO, Gainesville, FL
<i>Designed, developed, debugged, and coded a low power wireless data logger for monitoring supply chains</i>
The devices use Bluetooth Low Energy to communicate logged temperature and humidity data to smartphones & tablets. Passed FCC and CE certifications. Managed two part time engineers brought on after our seed funding round.
Raspberry Pi, Python scripts, and the BlueZ stack were used to create automated test equipment for manufacturing. |
| MAY 2011-DEC 2011 | Test Technology Intern at INTEL, Fort Collins, CO
Created test patterns for server chips pre-Silicon. Modeled scan chains allowing others to run tests. Used Perl to read through lengthy test logs for relevant data. |
| JUN 2010-NOV 2010 | Lifeguard at UNIVERSITY OF FLORIDA HOUSING DEPT., Gainesville FL
Provided a safe swimming experience for others. |

EDUCATION

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| DECEMBER 2013 | Bachelor of Science in ELECTRICAL ENGINEERING, University of Florida
Major: Electrical Engineering <i>Magna Cum Laude</i> Digital Design Specialization
GPA: 3.66/4.0 |
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SKILLS

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| Top skills | C and Git |
| Basic Knowledge | Python, C++, Perl, Linux system programming, Bash, VHDL |
| IDEs Used | IAR Workbench, Code Composer Studio, Atmel Studio |
| PCB CAD Used | Eagle, PCB Artist, Altera |
| Tools Used | Oscilloscopes, Logic Analyzers, Multimeters, SMU |
| Barr Training | Attended embedded software boot camp. Only person to finish the final project. |
| Agile and Scrum | Used Jira and TFS to optimize meaningful labor |
| TI E2E MVP | I was recognized for sharing expertise, solutions, and experience to help other community members |

UF IEEE STUDENT CHAPTER INVOLVEMENT

2011-2012 Academic Year	Secretary (elected)
2010-2011 Academic Year	Advertising Chair
2011	1 st place IEEE South East Conference Engineering Ethics Competition
Various Dates	Introduced local grade students to robotics with Lego Mindstorm