Network Notes

February 20, 2022

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The Network Protocol

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1.1 Overview

Loconet is a peer-to-peer distributed network system on which all devices can monitor the data flow. The network is event driven and is not polled by a centralised controller in normal operation. The normal network state is quiet, with no data traffic unless a device has information to send.

The network data is sent in asynchronous format using 1 start bit, 8 data bits and 1 stop bit. The 8 bit data is transmitted least significant bit first. The bit times are 60.0 μ S or 16,660 baud +/- 1.5%. A computer can connect to a USB interface at higher baud rates and the device will make the necessary conversion. Bytes may be transmitted back-to-back, with a start bit immediately following the stop bit of the previous character.

All the network communications are via multi-byte messages. The command station is the device that maintains the refresh stack for DCC packet generation and generates the DCC track data. Refresh of information is typically only performed for a mobile decoder. A stationary decoder is not refreshed and individual immediate commands are sent out to the track as requested.

The command station is only privileged in respect to performing the task of maintaining the locomotive refresh stack and generating DCC packets. In this way other network transactions may occur that the command station does not need to be involved with or understand, as long as they follow the message protocol and timing requirements. i.e. other devices may have a dialog on the network without disturbing or involving the command station. Devices on the network monitor the messages, check for format and data integrity and parse good messages to decode if action is required in the context. Devices such as throttles, input sensors, computer interfaces and control panels may generate the network messages without needing prompting or polling by a central controller.

Devices frequently will be added and removed from an operating the network. The devices and protocol are tolerant of electrical and data transients. The format chosen gives a good degree of data integrity, guaranteed quick network-state synchronisation, high data throughput, good distribution of access to many competing devices and low event latency.

1.2 Message Format

The data bytes on the network are defined as 8 bit data with the most significant bit as an opcode flag bit. If the most significant bit is 1, then the 7 least significant bits are interpreted as an opcode. The opcode may only occur once in a valid message and it is the first byte of a message. The opcode does not necessarily uniquely identify a message type. Sometimes the opcode must be used in combination with other bits or bytes in the message to determine the message signature. All the remaining bytes in the message must have a most significant bit of 0, including the last checksum byte. The checksum is the 1's complement of the byte wise exclusive or of all the bytes in the message, except the checksum itself. To validate data accuracy, all the bytes in a correctly formatted message are exclusive or'ed. If this resulting byte value is 0xFF, then the message data is accepted as good. Any message that has format or framing errors, data errors or is a fragment caused by noise glitches and does not completely follow the message format will be ignored by all receivers, and a new opcode will be scanned for re-synchronisation.

The opcodes may be examined to determine message length and if subsequent response message is required. Data bits d6 and d5 encode the message length. The message length includes the opcode and the checksum bytes. When bit d3 equals 1 a follow-on message or reply is expected. For variable byte messages the byte following the opcode in the message is a 7 bit byte count.

d7	$\underline{d6}$	$\underline{\mathrm{d}5}$	$\underline{d4}$	$\underline{\mathrm{d}3}$	$\underline{\mathrm{d}2}$	$\underline{d1}$	$\underline{d0}$	
1	0	0	${ m E}$	D	\mathbf{C}	В	A	2 byte message
1	0	1	\mathbf{E}	D	\mathbf{C}	В	A	4 byte message
1	1	0	\mathbf{E}	D	\mathbf{C}	В	\mathbf{A}	6 byte message
1	1	1	${ m E}$	D	\mathbf{C}	В	A	Variable length message.

The A,B,C,D,E are bits available to encode 32 opcodes per message length.

There are four main message types: Broadcast, Command, Response, and Report.

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1.2.1 Broadcast

A Broadcast is a message sent by a device to all other devices on the network.

1.2.2 Command

A Command is a message sent to a device to request it to do something. The recipient device may send a Response back to the sender. Technically a Command is a request for action. The Command may not reach the intended recipient or the recipient may ignore the request.

1.2.3 Response

A Response is a message sent in response to a Command.

1.2.4 Report

A Report is a message sent by a device in response to a change in its internal and/or external state.

1.3 Slots

The command station contains an array of read/write slots. There are two classes of slots (locomotive slot and system slot) and two protocols for manipulating the slots. Protocol 1 allows up to 120 locomotive slots and 8 system slots. Each slot contains 10 bytes of data. Digitrax calls these slots standard slots. Protocol 2 allows up to 960 locomotive slots and 64 system slots. Each slot contains 15 bytes of data. Digitrax calls these slots expanded slots. Not all command stations implement both protocols. A command station may also not implement the maximum number of locomotive slots for the protocols it supports. The user should check the Global System Track Status bits in a LocoSlotDataP1 or LocoSlotDataP2 response to determine which protocols are supported. Expanded capability throttles, i.e. those that implement protocol 2, are given the expanded slots first, leaving the standard slots available for legacy throttles. In this document message mnemonics that are suffixed "P1" belong to protocol 1 and those suffixed "P2" belong to protocol 2. Protocol 1 uses a single 7 bit number to identify a slot. Protocol 2 uses a 3 bit number to identify the page or bank of slots and a 7 bit number to identify the slot

within the page or bank. In both protocols slots numbered 0 to 119 (0x00 to 0x77) are locomotive slots and those numbered 120 to 127 (0x78 to 0x7F) are system slots. The slot number is similar to a file handle. System slots are encoded differently from the locomotive slots.

System Slot#	Description
$\overline{123 \text{ (0x7B)}}$	Fast Clock
124 (0x7C)	Programming
127 (0x7F)	Configuration

Locomotive Control

2.1 Introduction

Initially all locomotive slots are empty and are said to be Free. A Free slot does not have a locomotive address loaded and no DCC commands are generated by the command station for it. To control a locomotive a throttle must request a slot from the command station and in the case of an expanded slot take ownership of it.

2.1.1 Slot State

A locomotive slot's slot state is determined by bits d5 and d4 of the Slot Status 1 byte of the applicable LocoSlotDataP1 or LocoSlotDataP2 response and whether the locomotive's address has been loaded. The slot state determines whether DCC commands are generated for it and if throttles can take control of it.

Slot State	d5	$\underline{\mathrm{d}4}$	Address Loaded	Decoder Refreshed	Any Throttle
Free	0	0	No	No	Yes
New	0	0	Yes	No	Yes
Common	0	1	Yes	Yes	Yes
Idle	1	0	Yes	No	Yes
In-Use	1	1	Yes	Yes	No

2.1.2 Throttle ID

The Throttle ID for a physical throttle is derived from the throttle's serial number. Digitrax serial numbers are 16-bit numbers. The Throttle ID is split into two parts consisting of the least significant bits of the low and high bytes of the serial number respectively. For example a physical throttle with the serial number of 0xFFFE would have a Throttle ID of 0x7E 0x7F with 0x7E being the low byte. The low byte of the Throttle ID is required by some of the protocol 2 commands to ensure that only the throttle that has ownership of the locomotive slot is the one that updates the slot. A software throttle should choose a Throttle ID that does not clash with that of a physical throttle.

2.1.3 Protocol 1

- 1. The throttle requests a slot for the locomotive address by sending either a GetLocoSlotDataSAdrP1 or GetLocoSlotDataLAdrP1 Command to the command station. Which one depends on what type of address the locomotive's decoder is programmed to use.
- 2. If a slot has been previously loaded with the locomotive's address, then the command station will return a **LocoSlotDataP1** Response.
- 3. If the locomotive's address is not currently in a slot, then the command station will load the new locomotive address into a Free slot, with speed equal to zero, direction forwards, functions off and default decoder mode, and return a **LocoSlotDataP1** Response. The default decoder mode is determined by the command station's OpSw21-OpSw23 settings.
- 4. If there are no Free slots to load the new locomotive address into, the command station with return a **NoFreeSlotsP1** Response and this procedure is terminated.
- 5. The throttle must then examine the slot data bytes to work out how to process the command station response.
- 6. If the slot state is New, Common or Idle then the throttle requests a "null move" operation by sending the command station a **MoveSlotsP1** Command. The command station returns a **LocoSlotDataP1** Response.
- 7. The **SetLocoSlotDataP1** Command can be used at this time to change the decoder mode from that of the default.
- 8. The throttle will then be able to update speed, direction and function information. Whenever slot information is changed in an active slot, the slot is flagged to be updated as the next DCC packet sent to the track.

2.1.4 Protocol 2

- 1. The throttle requests a slot for the locomotive address by sending either a GetLocoSlotDataSAdrP2 or GetLocoSlotDataLAdrP2 Command to the command station. Which one depends on what type of address the locomotive's decoder is programmed to use.
- 2. If a slot has been previously loaded with the locomotive's address, then the command station will return a **LocoSlotDataP2** Response.
- 3. If the locomotive's address is not currently in a slot, then the command station will load the new locomotive address into a Free slot, with speed equal to zero, direction forwards, functions off and default decoder mode, and return a **LocoSlotDataP2** Response. The default decoder mode is determined by the command station's OpSw21-OpSw23 settings.
- 4. If there are no Free slots to load the new locomotive address into, the command station with return a **NoFreeSlotsP2** Response and this procedure is terminated.
- 5. The throttle must then examine the slot data bytes to work out how to process the command station response.
- 6. If the slot state is New, Common or Idle then the throttle requests a "null move" operation by sending the command station a **MoveSlotsP2** Command. The command station returns a **LocoSlotDataP2** Response.
- 7. If the slot state is In-Use and the slot's Throttle ID does not match that of the throttle then the throttle should ask the user if they wish to "steal?" the slot. If the answer is no then this procedure is terminated.
- 8. The throttle now takes ownership of the slot by updating the slot's Throttle ID to that of the throttle and writing the updated slot data to the command station by sending a **SetLocoSlotDataP2** Command. If the request is successful then the command station will return a **setSlotDataOKP2** Response. The **SetLocoSlotDataP2** can also be used to change the decoder mode from that of the default.
- 9. The throttle will then be able to update speed, direction and function information. Whenever slot information is changed in an active slot, the slot is flagged to be updated as the next DCC packet sent to the track. If the slot was stolen from another throttle then the other throttle will no longer be able to command the locomotive.

Example:

getLocoSlotDataSAdrP2 0xbe 0x00 0x17 0x56

2.1.5 Purging

0xb4 0x6e 0x7f 0x5a

setSlotDataOKP2

If a device disconnects from the network and so does not access or reference a slot within the system purge time, the command station will force the un-accessed slot to Common status so other system devices can use the slot. The typical purge time of a command station is about 200 seconds. A good "ping" or slot update activity is about every 100 seconds, i.e. if a user makes no change to a throttle/slot within 100 seconds, the throttle/device should automatically send another speed update at the current speed to reset the purge timeout for that slot. Purging behaviour can be modified by adjusting the command station's OpSw13-OpSw15 settings.

2.2 Consisting

There are three ways of creating a consist with Digital Command Control. Each consisting method has its advantages and disadvantages. In a Basic Consist all mobile decoders in the consist have the same Primary Address or Extended Address. In an Advanced Consist all mobile decoders in the consist have the same Consist Address stored in CV19, as well as their own Primary Address or Extended Address. In a Universal Consist the command station manages the individual locomotives in the consist.

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2.2.1 Basic Consist

This is the simplest way of creating a consist. Simply program all the locomotives you want to run together with the same address. This can be done with a Primary or Extended address. The locomotives must all be facing the same direction for this to work, as direction of travel is determined by their orientation. If it is planned to operate locomotives as a permanent consist, such as an A-B-A set, the Normal Direction Of Travel must be set accordingly.

It has the advantage of only using one slot in the command station. The command station will see all the locomotives with the same address as a single locomotive, and they will all respond to all commands in unison. This requires reprogramming the decoder to change consists. All locomotives in a basic consist will respond to function commands at the consist address. The command station sees the basic consist as a single locomotive.

If the consists were created with a Primary address (1-127), there may be issues operating as a consist on another layout. There will be conflicts if the primary address is also used by another locomotive which is not part of the consist. An analog (non-decoder equipped) locomotive cannot be part of a Basic Consist. The operator cannot control individual locomotive functions such as the horn, bell, and lighting, as all the decoders will act on the command.

2.2.2 Advanced Consist

Advanced Consisting uses a temporary secondary address (CV19) in the locomotive to group locomotives together without changing the Primary Address. The locomotives keep track of the consisting information. Removing a locomotive from the consist without clearing the consist information will result in an orphan locomotive which responds to the Consist Address.

Advanced Consisting is done by using ops mode programming to change CV19 to set the address and normal direction of movement. Some decoders will allow you to specify what functions will respond to commands addressed at the consist address.

This method was given the name Advanced Consisting to reflect that its possibilities were much more advanced than that of Basic Consisting, as the Primary or Extended addresses were not altered, and individual control of each locomotive in the consist is possible. This implementation of consisting was considered to be a better choice than relying on the command station to create and manage consists.

The command station sees the consist as a single locomotive:

• The consist address is stored onboard the multifunction decoder

- The consist will use a single slot in the command station.
- The operator can control any locomotive's function operation from its specific address
- CV 21 (F1–F8) and 22 (FL, F9–F12) can set the functions available on any locomotive in the consist
- An analog locomotive cannot be part of an advanced consist
- Advanced Consists operate in 28 or 128 DCC speed step modes only

To request a slot for an Advanced Consist send a **GetLocoSlotSAdrP1** or **GetLocoSlot-SAdrP2** Command. The address should be set to the Consist Address instead of the Primary Address. In the slot write-back after the "Null Move" Advanced Consist Bit of Slot Status 1 must be set to 1. The Consist Address is limited to the values 1 through 127.

2.2.3 Universal Consist

All the bookkeeping chores for the consist are performed by the command station. With this method of consisting, no changes to the programming of the decoder are made. Each locomotive in the consist is a separate entity in the command station, so the size of the consist is limited to the capacity of the command station. Analog locomotives may be a part of one consist - all analog locomotives on the track will respond to the same commands, so you can't run them in different consists. Digitrax calls this method UniVersal Consisting (Yes, with a capital "V" in the middle).

Consists may be stacked, making up consists within consists. Locomotive functions can still be individually controlled without affecting other locomotives in the consist. The command station sees the consist as individual locomotives, sending each locomotive its own speed and direction commands.

- The lead locomotive's address is also known as the Top Address.
- The functions are only acted on by the top address, or lead locomotive.
- Functions on the other locomotives can be controlled directly from their address
- Since the command station does the housekeeping for the consist, there will be memory or software-imposed limits on the number of consists.

With Universal Consisting, the consist responds to commands sent to the Top Locomotive (Address). The top locomotive does not have to be the lead engine, nor does it even have to exist. The consist is managed by the command station. An analog locomotive can also be included in the consist. Universal Consisting Allows:

2.2. CONSISTING

- Any address to be used, including Address 00.
- Number of consists is limited by the number of slots.

A slot can be added to a top-member by sending a LinkSlotsP1 or LinkSlotsP2 and removed by sending a UnlinkSlotsP1 or UnlinkSlotsP2.

Switch Control

3.1 Introduction

Detection & Transponding

4.1 Introduction

Programming Configuration Variables (CVs)

5.1 Introduction

The decoders installed in your locomotives provide you with the ability to create a more realistic operating experience through the configuration variables (CVs for short). The network protocol supports configuration of up to 1024 CVs.

It is a good idea to run your decoders with the default CV values that come pre-programmed in your decoders until you get used to the performance characteristic and how they work on your layout. Once you are comfortable with running the trains, then you can begin customizing locomotive characteristics.

Each CV (configuration variable) controls a specific characteristic of the decoder, which in turn controls how the locomotive performs. See your decoder manual for a list of the most commonly used CVs and their meanings. Each decoder comes pre-programmed from the factory with the default settings outlined in your decoder manual. You can change your decoder's performance characteristics by changing the CV values entered in the CVs you want to change. Each of these CVs can be set up when your command station is in the programming mode. The CVs are remembered in the decoder until it is reprogrammed to with a different CV value. Please refer to your mobile decoder manual for a complete listing of the CVs supported by each decoder.

Programming decoder CVs is usually done on an isolated programming track.

There are four programming modes:

- Paged mode
- Physical register mode
- Direct mode
- Operations mode

5.1.1 Paged Mode Programming

5.1.2 Physical Register Programming

Physical Register Mode can only read CV01-CV08. You should not rely on values in the display for CVs above 08 when reading back in physical register mode.

5.1.3 Direct Mode Programming

This is the preferred programming mode.

5.1.4 Operations Mode Programming

Operations mode programming lets you program CVs in locomotives equipped with Extended Packet Format decoders while they are on the mainline. A typical use for Ops mode programming would be to change the acceleration rate (CV03) or the deceleration rate (CV04) of your locomotives to simulate the weight and braking capability of the train to compensate for changing the number of cars or power units on a train.

Operations Mode read back can only be used with decoders that are capable of operations mode read back when there is a device attached to the network that supports operations mode read back. Digitrax transponding decoders and the DCS210 or DCS240 command stations would allow operations mode read back.

5.2 Programming Mobile Decoder Addresses

Be sure that only the loco you want to program is on the programming track. If you are using operations mode programming, the loco you want to program can be anywhere on the layout but it must have a decoder that is capable of operations mode programming installed.

There are two addressing methods - short and long. The short addresses can take a value between 0 and 127, and long addresses a value between 128 and 9983. The bit 5 of mobile decoder's configuration register (CV29) determines what addressing method is used. If bit 5 is set to 1 then long addresses are used, and when bit 5 is 0 then short addresses are used. Short addresses are stored in CV1, and long addresses in CV17 and CV18. The address values stored in CV17 and CV18 are not the high and low bytes of the address value. The CV17 and CV18 values must be calculated from the address value as follows:

```
TEMP = address + 49152
CV18 = TEMP \& 0xFF
CV17 = TEMP >> 8
Example:
address = 4007
TEMP = 49152 + 4007 = 53159 = 0xCFA7
CV18 = 0xA7 = 167
CV17 = 0xCF = 207
read cv
Read CV
unknown
   65830.9ms
<DO> 0xef 0b11101111 <- OPC_PROG</pre>
<D1> 0x0e 0b00001110 <- Message Length</pre>
<D2> 0x7c 0b01111100 <- Special programming slot number
<D3> 0x2b 0b00101011 <- PCMD
d7 0
d6 0 - read
d5 1 - byte mode
d4 0 - TV1
d2 0 - service mode on programming track
d1 1 - unknown
d0 1 - unknown
```

Direct mode byte read on service track

d0 0 - reserved

```
<D4> 0x00 0b00000000 - 0x00
<D5> 0x00 0b00000000 - HOPSA - Ops mode programming - 7 high address bits of Loco to pro
<D6> 0x0e 0b00001110 - LOPSA - Ops Mode programming - 7 low address bits of loco to prog
<D7> 0x00 0b00000000 - TRK - normal track status for command station - this doesn't look
<D8> 0x00 0b00000000 - CVH
<D9> 0x00 0b00000000 - CVL
<D10> 0x0f 0b00001111 - DATA
<D11> 0x6d 0b01101101 - Throttle serial number
<D12> 0x52 0b01010010 - Throttle serial number
<D13> 0x77 0b01110111
response
    1722.5ms
<DO> 0xe7 0b11100111 <- Opcode
<D1> 0x0e 0b00001110 <- length
<D2> 0x7c 0b011111100 <- Programming slot</pre>
<D3> 0x2b 0b00101011 <- PCMD
<D4> 0x00 0b00000000 <- PSTAT - success
<D5> 0x00 0b00000000 <- HOPSA
<D6> 0x02 0b00000010 <- LOPSA should be 0
<D7> 0x47 0b01000111 <- TRK
<D8> 0x02 0b00000010 <- CVH : 0, 0, CV9, CV8, 0, 0, D7, CV7</pre>
<D9> 0x04 0b00000100 <- CVL - CV5</pre>
<D10> 0x16 0b00010110 <- low 7 bits of value</pre>
<D11> 0x6d 0b01101101 <- SN
<D12> 0x52 0b01010010 <- SN
<D13> 0x2b 0b00101011 <- CHK
value displayed is 150 10010110
PCMD
 d7 = 0
 d6 	 1 = write, 0 = read
 d5 1 = \text{byte operation}, 0 = \text{bit operation (if possible)}
 d4 	ext{TV1}
 d3 TV0
 d2 1 = Ops mode on mainlines, 0 = service mode on programming track
 d1 0 - reserved
```

Byte Mode	Ops Mode	$\underline{\text{TV1}}$	$\underline{\text{TV0}}$	Meaning
1	0	0	0	Paged mode byte read/write on service track
1	0	0	1	Direct mode byte read/write on service track
0	0	0	1	Direct mode bit read/write on service track
×	0	1	0	Physical register byte read/write on service track
×	0	1	1	Service track reserved function
1	1	0	0	Ops mode byte program no feedback
1	1	0	1	Ops mode byte program with feedback
0	1	0	0	Ops mode bit program no feedback
0	1	0	1	Ops mode bit program with feedback

Fast Clock

6.1 Summary

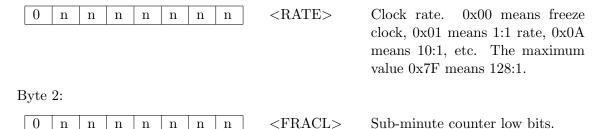
The command station provides a system fast clock and parameters are stored in slot #123 (0x7B). Use OPC_WR_SL_DATA to write new clock information, The current slot information can be read using OPC_RQ_SL_DATA. This will return an OPC_SL_RD_DATA message containing the fast clock information. This message is called the "sync". Other throttles will update to this sync. Note that all attached display devices keep a current clock calculation based on this sync read value, i.e. devices must not continuously poll the clock slot to generate time, but use this merely to restore sync and follow current rate etc. The clock slot is typically "pinged" or read every 70 to 100 seconds, by a single user, so all attached devices can synchronise any phase drifts. Upon seeing a sync read, all devices should reset their local sub-minute phase counter and invalidate the sync update ping generator.

6.2 Slot #123 Encoding

Byte 0:

0	1	1	1	1	0	1	1	0x7B	Slot number

Byte 1:



Byte 3:



The implementation and meaning of FRACL and FRACH depend upon the specific clock generator. These values should not be used externally. These values are reset when a valid sync message is seen.

That said, the following timing was derived for the DCS240.

Get:

$$\label{eq:maxTick} \begin{split} \max & \text{Tick} = 0 \\ \text{xBFF} \\ & \text{ticks} = \max & \text{Tick} \cdot (0 \\ \text{x3FFF} \cdot ((<& \text{FRACL}> \& 0 \\ \text{x7F}) - ((<& \text{FRACH}> \& 0 \\ \text{x7F}) << 7))) \\ & \text{seconds} = 60.0 * \\ & \text{ticks} / \\ & \text{(maxTick} + 1) \\ \\ & \text{Set:} \end{split}$$

temp = ticks - maxTick + 0x3FFF

$$\langle FRACL \rangle = temp \& 0x7F$$

$$\langle FRACH \rangle = (temp >> 7) \& 0x7F$$

Byte 4:



Get:

temp =
$$((255 - \langle MINS \rangle) \& 0x7F) \mod 60$$

minutes = $(60 - \text{temp}) \mod 60$

Set:

$$<$$
MINS $> = (255 - (60 - minutes)) & 0x7F$

Byte 5:

25

0	d6 d5 d4 d3 d2 d1 d0	<trk></trk>	Global system track status.
d6	Reserved. Set to 0.		
d5	Reserved. Set to 0.		
d4	Reserved. Set to 0.		
d3	1 means the programming tra-	-	
d2	1 means this master implement		
1	1.1 capability, 0 means the ma		
d1	0 means the track is paused, l stop.	broadcast an em	ergency
d0	$1~\mathrm{means}$ the DCC packets are	on in the master	e, global
	power up.		
Byte	6:		
0	n n n n n n	<hrs></hrs>	Fast clock hours. This is encoded.
Get:			
temp	$o = ((256 - \langle HRS \rangle) \& 0x7F) \mod$	d 24	
hours	$s = (24 - temp) \mod 24$		
Set:			
<hr< td=""><td>dS > = (256 - (24 - hours)) & 0x7H</td><td>ਜੁ</td><td></td></hr<>	dS > = (256 - (24 - hours)) & 0x7H	ਜੁ	
Byte	7:		
0	n n n n n n	<days></days>	Fast clock days. Number of 24 hour clock rolls.
Byte	8:		
0	d6 0 0 0 0 0 0	<cntrl></cntrl>	The bit d6 indicates valid clock in-
			formation. 1 means good and 0 means ignore.
Byte	9:		
0	n n n n n n	<id1></id1>	Device ID low bits.
Byte	10:		
0	n n n n n n n	<id2></id2>	Device ID high bits.

ID1 and ID2 indicate the device that last set the clock. 0x00, 0x00 means that not set has happened. 0x7F, 0x7X are reserved for computer use.

Updating Firmware

7.1 Bootloader Protocol 1

7.2 Bootloader Protocol 2

- 1. The IPL Setup message is sent twice to initiate the download.
- 2. Each block is sent,
- 3. The IPL End Operation message is sent.

A block consists of an IPL Address message following by 1 or more IPL Data messages.

7.3 Firmware Parameters

<u>PC</u>	<u>Device</u>	$\overline{\mathrm{DT}}$	$\underline{\mathrm{BV}}$	\underline{HV}	$\underline{\mathrm{SV}}$	$\underline{\mathrm{CK}}$	$\underline{\mathrm{DL}}$	<u>OP</u>	<u>PB</u>	$\underline{\mathrm{EB}}$	$\underline{\mathrm{ED}}$	$\overline{\mathrm{DC}}$
0x01	LNRP	09OCT15	1	0	3	64	8	1	64	64	8	0x50
0x04	UT4											
0x06	UT6	05APR21	2	0	1	64	11	2	512	4096	40	0x2E
0x0C	WTL12											
0x14	DB210O	06MAR20	2	1	2	64	7	2	512	4096	25	0x08
0x15	DB210	06MAR20	2	1	2	64	7	2	512	4096	25	0x08
0x16	DB220	06MAR20	2	1	2	64	7	2	512	4096	25	0x08
0x1A	DCS210+											
0x1B	DCS210	06MAR20	2	1	3	64	5	2	256	2048	40	0x2C
0x1C	DCS240	06MAR20	2	1	3	64	5	2	256	2048	40	0x2C
0x23	PR3	12FEB14	1	0	8	64	5	1	-	1024	-	0x14
0x24	PR4	05JAN 18	0	0	0	64	5	2	256	2048	60	0x1C
0x2A	DT402	10OCT16	1	0	17	64	15	1	64	64	15	0x73
0x2A	DT402	05 AUG16	1	0	17	64	15	1	64	64	15	0x4B
0x32	DT500	10OCT16	1	0	1	64	15	1	64	64	15	0x0E
0x33	DCS51	06OCT14	1	0	5	64	12	1	-	-	-	0x0E
0x34	DCS52	17JUN21	2	0	1	64	11	2	512	4096	40	0x2C
0x3E	DT602	15JUL 21	2	0	1	64	11	2	512	4096	40	0x30
0x51	BXPA1	18JUN21	2	0	1	64	6	2	512	4096	41	0x0A
0x58	BXP88	21OCT17	2	0	2	64	50	2	256	2048	100	0x18
0x5C	UR92	07DEC15	0	1	8	64	16	1	64	64	16	0x24
0x5D	UR93	30 AUG21	2	0	0	64	5	2	512	4096	25	0x0A
0x63	LNWI	11MAR21	2	1	2	64	5	2	512	4096	25	0x12

$\overline{\mathrm{EBS}}$	$\underline{\text{LNRP}}$	$\overline{\mathrm{DT402}}$	$\overline{\mathrm{DT500}}$	$\underline{\text{DCS51}}$	$\overline{\text{DCS52}}$
32	0x20	0x65	0x1B	-	0x00
64	0x50	0x73	0x0E	0x0E	0x00
128	0x28	0x3A	0x47	-	0x00
192	0x1B	0x7C	0x05	-	0x2B
256	0x14	0x5D	0x64	-	0x40
1024	0x05	0x18	0x19	-	0x30
2048	0x03	0x0C	0x0D	-	0x58
4096	0x02	0x06	0x07	-	0x2C
8192	0x01	0x03	0x04	-	0x16
16384	-	0x02	0x02	-	0x0B
32768	-	0x01	0x01	-	0x06
65536	-	-	-	-	0x03

DMF File Format 7.4

The manufacturer's DMF file format is a modified form of the Intel Hexadecimal Object File Format. Character encoding is ASCII. The file consists of multiple "records" each terminated by an ASCII linefeed character (0x0A). There are four types of record and they appear in the file in the following order:

- 1. Sync records
- 2. Parameter records
- 3. Data records
- 4. End of File record

Sync Records 7.4.1

Sync records are used to identify the file as a DMF file. There are six sync records each consisting of a single ASCII # character (0x23).

Example:

#

7.4.2 Parameter Records

PARAM MARK	PARAMETER NAME	NAME TERMINATOR	VALUE
! (0x21) < SPC > (0x20)	ASCII text	: (0x3A) < SPC > (0x20)	decimal value
2 bytes	variable bytes	2 byte	variable bytes

PARAMETER RECORD FORMAT

Parameter records start with a two byte PARAM MARK. This consists of the ASCII code for the exclamation mark (0x21) followed by the ASCII code for the space character (0x20). The PARAMETER NAME is next. The PARAMETER NAME is ASCII encoded text identifying the parameter. The PARAMETER NAME may include spaces. It is terminated by the NAME TERMINATOR which consists of the ASCII code for a colon (0x3A) followed by the ASCII code for a space character (0x20). The last field is the VALUE field. This is a decimal numeric value encoded as ASCII text.

The following parameters have been identified and they are presented in the following order (it is not known if the order is significant):

- 1. Bootloader Version
- 2. Manufacturer Code
- 3. Product Code
- 4. Hardware Version
- 5. Software Version
- 6. Chunk Size
- 7. Delay
- 8. Options
- 9. First Address
- 10. Last Address
- 11. Prog Blk Size
- 12. Erase Blk Size
- 13. Erase Dly

Example:

- ! Bootloader Version: 2
- ! Manufacturer Code: 0
- ! Product Code: 88
- ! Hardware Version: 0
- ! Software Version: 2
- ! Chunk Size: 64
- ! Delay: 50
- ! Options: 2
- ! First Address: 24576 ! Last Address: 73728
- ! Prog Blk Size: 256
- ! Erase Blk Size: 2048
- ! Erase Dly: 100

7.4.3 Data Records

RECORD MARK	RECLEN	LOAD OFFSET	RECTYP	DATA	CHKSUM
(:)					
1 byte	2 bytes	6 bytes	2 bytes	n bytes	2 bytes

DATA RECORD FORMAT

The data record provides a set of hexadecimal digits that represent the ASCII code for data bytes that make up a portion of a memory image.

Each data record begins with a RECORD MARK field containing the ASCII code for the colon (:) character (0x3A).

Each record has a RECLEN field which specifies the number of bytes of data which follows the RECTYP field of the record. Note that one data byte is represented by two ASCII characters. The maximum value of the RECLEN field is hexadecimal "FF" or 255. Although the maximum is 255, the manufacturer seems to prefer 64.

Each record has a LOAD OFFSET field which specifies the 24-bit starting load offset of the data bytes.

The RECTYP field for data records is "00".

Each record has a variable length DATA field, it consists of zero or more bytes encoded as pairs of hexadecimal digits.

Each record ends with a CHKSUM field that contains the ASCII hexadecimal representation of the two's complement of the 8-bit bytes that result from converting each pair of ASCII hexadecimal digits to one byte of binary, from and including the RECLEN field to and including the last byte of the DATA field. Therefore, the sum of all the ASCII pairs in a record after converting to binary, form the RECLEN field up to and including the CHKSUM field, is zero.

The contents of the individual fields within the record are:

RECORD MARK This field contains 0x3A, the encoding of the ASCII colon

(:) character.

RECLEN The field contains two ASCII hexadecimal digits that specify

the number of data bytes in the record. The maximum value

is "FF" or 0x4646 (255 decimal).

LOAD OFFSET This field contains six ASCII hexadecimal digits representing

the address at which the first byte of the data is to be placed.

Most significant digit is presented first.

RECTYP This field contains 0x3030, the hexadecimal encoding of the

ASCII characters "00", which specifies the record type to be

a data record.

DATA This field contains pairs of ASCII hexadecimal digits, one

pair for each data byte.

CHKSUM This field contains the check sum on the RECLEN, LOAD

OFFSET, RECTYP, and DATA fields.

Example:

:400060000057AAC3880FAAC388559AC38855AAC388553AC38855AAC38855AAC3884AO 0C38855AAC38855AAC3882DFCC38861B8C3882DFCC38861B8C3886D

7.4.4 End of File Record

RECORD MARK	RECLEN	LOAD OFFSET	RECTYP	CHKSUM
(:)	"00"	"000000"	"01"	"FF"
1 byte	2 bytes	6 bytes	2 bytes	2 bytes

END OF FILE RECORD FORMAT

The End of File Record specifies the end of the file.

The contents of the individual fields within the record are:

RECORD MARK This field contains 0x3A, the encoding of the ASCII colon

(:) character.

RECLEN The field contains 0x3030, the hexadecimal encoding of the

ASCII characters "00". Since this record does not contain

any DATA bytes, the length is zero.

LOAD OFFSET This field contains 303030303030H, the hexadecimal encod-

ing of the ASCII characters "000000", since this field is not

used for this record.

RECTYP This field contains 0x3031, the hexadecimal encoding of the

ASCII characters "01", which specifies the record type to be

an End of File Record.

CHKSUM This field contains the check sum on the RECLEN, LOAD

OFFSET, and RECTYP fields. Since all the fields are static, the check sum can also be calculated statically, and the value is 4646H, the hexadecimal encoding of the ASCII characters

"FF".

Example:

:000000001FF

Chapter 8

Statistics

8.1 Introduction

Chapter 9

Message Reference

9.1 Introduction

The following information is provided for each of the messages:

Description:

Description of the message's function.

Protocol:

Which protocol the message belongs to. Only messages that relate to refresh slots belong to a protocol.

Group:

Which message size group the message belongs to.

Opcode:

The opcode mnemonic. This is the Digitrax assigned mnemonic when known.

Type:

The message type - Broadcast, Command, Response, or Report.

Encoding:

How the message is encoded byte by byte.

Response:

The response expected from a command message, if applicable.

Signature:

The bits and bytes that must be tested to determine the message's unique type.

Notes:

Any notes.

9.2. ACK 39

9.2 Ack

Description:

This message provides a response code from a Command. This is the generic form of this message type.

Group:

4-Byte Message

Opcode:

OPC_LONG_ACK

Type:

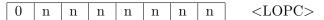
Response

Encoding:

Byte 0:

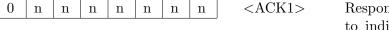
1	0	1	1	0	1	0	0	0xB4	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:



Opcode of the Command that this message is a response to with the most significant bit set to 0.

Byte 2:



Response code. This is usually 0 to indicate the Command failed, and 127 (0x7F) if it was successful. Other values are possible to indicate other conditions or states.

Byte 3:

0	n	n	n	n	n	n	n	<chk></chk>	Checksum

Response:

None.

Signature:

Byte 0:

9.3. BUSY 41

9.3 Busy

Description:

The **Busy** Broadcast allows the command station to keep the network active whilst it is performing a task that requires a response, and entails a significant processing delay, i.e. it can ensure no new requests are started until it has responded to the last message. The **Busy** Broadcast should be simply stripped and ignored.

Group:		
2-Byte Message		
Opcode:		
OPC_BUSY		
Type:		
Broadcast		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x81	Opcode.
Byte 1:		
$egin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7E	Checksum.
Response:		
None		
Signature:		
Byte 0:		
	0x81	
Notes:		
None.		

9.4 CfgSlotDataP1

Description:

This Response provides the current command station configuration slot data. It is returned by the command station in response to the **GetCfgSlotDataP1** Command.

Protocol:

1

Group:

Variable-Byte Message

Opcode:

OPC_SL_RD_DATA

Type:

Response

Encoding:

Byte 0:

		1	1	1	0	0	1	1	1	0xE7	Opcode.
--	--	---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	0	0	0	1	1	1	0	0x0E	Message length (14 bytes).
								=	

OST1 to OST8 encode the command station's option switch table. A bit value of 1 means that the switch is closed and a value of 0 means that a switch is thrown. OpSw 8, OpSw 16, OpSw 24, OpSw 32, OpSw 40, OpSw 48, OpSw 56 and OpSw 64 cannot be read due to bit 7 being cleared in the message format.

Byte 2:

0 1 1 1 1 1 1 1 0x7F Cor

Byte 3:

0	d6	d5	d4	d3	d2	d1	d0	$\langle OST1 \rangle$	Option switch table byte 1

$\underline{\mathrm{Bit}}$	$\overline{\text{OpSw}}$
d6	OpSw 7
d5	OpSw 6
d4	OpSw 5
d3	OpSw 4
d2	OpSw 3
d1	OpSw 2
d0	${\rm OpSw}\ 1$

Byte 4:

0	d6 d5	d4	$d3 \mid d2 \mid$	d1	O $OST2>$	Option switch table byte 2.
$\underline{\mathrm{Bit}}$	OpSw					
d6	$\overline{\mathrm{OpSw}}$	15				
d5	OpSw	14				
d4	OpSw	13				
d3	OpSw	12				
d2	OpSw	11				
d1	OpSw	10				
d0	OpSw	9				

Byte 5:

0	d6 d5 d4 d3 d2 d1 d0	<ost3></ost3>	Option switch table byte 3.
Bit d6 d5 d4 d3 d2 d1	OpSw OpSw 23 OpSw 22 OpSw 21 OpSw 20 OpSw 19 OpSw 18		
d0	OpSw 17		

Byte 6:

0	d6 d5	d4	d3	d2	d1	d0	$\langle OST4 \rangle$	Option switch table byte 4.
---	-------	----	----	----	----	----	------------------------	-----------------------------

$\underline{\mathrm{Bit}}$	$\overline{\mathrm{OpSw}}$
d6	OpSw 31
d5	OpSw~30
d4	OpSw 29
d3	OpSw 28
d2	OpSw 27
d1	OpSw 26

OpSw 25

Byte 7:

d0

ĺ	Ω	46	0	0	43	42	41	d0	<TRK $>$	Global System	Track Status
	U	l ao	U	U	l ao	uz	l ar	au	<1nn>	Giodai System	Track Status.

- d6 1 means this command station implements protocol 2 commands.
- d3 1 means the programming track is busy.
- d2 1 means this master implements protocol 1 commands, 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on and global power is up.

Byte 8:

```
0
     d6 d5 d4 d3 d2 d1 d0
                                                         Option switch table byte 5.
                                        \langle OST5 \rangle
       OpSw
Bit
       \overline{\mathrm{OpSw}} 39
d6
d5
       OpSw 38
d4
       OpSw 37
d3
       OpSw 36
       OpSw 35
d2
       OpSw 34
d1
d0
       OpSw 33
```

Byte 9:

0	d6 d5 d4 d3 d2 d1 d0	<ost6></ost6>	Option switch table byte 6.
$\underline{\mathrm{Bit}}$	OpSw		
d6	$\overline{\mathrm{OpSw}}$ 47		
d5	OpSw 46		
d4	OpSw 45		
d3	OpSw 44		
d2	OpSw 43		
d1	OpSw 42		
d0	OpSw 41		

Byte 10:

0	d6	d5	d4	d3	d2	d1	d0	$\langle OST7 \rangle$	Option switch table byte 7.
$\underline{\mathrm{Bit}}$	O	pSw							
d6	$\overline{\mathrm{O}}_{\mathrm{I}}$	\overline{pSw}	55						
d5	O_{J}	pSw	54						
d4	O_{J}	pSw	53						
d3	O_{J}	pSw	52						
d2	O_{J}	pSw	51						
d1	O_{J}	pSw	50						
d0	O_{I}	pSw	49						
Burto	11.								

Byte 11:

0	$d6 \mid d5 \mid d4 \mid d3 \mid d2 \mid d1 \mid d0$	<ost8></ost8>	Option switch table byte 8.
$\underline{\mathrm{Bit}}$	OpSw		
d6	$\overline{\mathrm{OpSw}}$ 63		
d5	OpSw 62		
d4	OpSw 61		
d3	OpSw 60		
d2	OpSw 59		
d1	OpSw 58		
d0	OpSw 57		

Byte 12:

0 d6 d5 d4 d3 d2 d1	. d0	nknown.
d6		
d5		
d4		
d3 $d2$		
d2		
d1		
d0		

Byte 13:

0	n	n	n	n	n	n	n	<CHK $>$	Checksu

Response:

None.

Signature:

Ву	te	0:							
	1	1	1	0	0	1	1	1	0xE7
Ву	te	1:							
	0	0	0	0	1	1	1	0	0x0E

Byte 2:

|--|

Byte 7:

0×0	0 ×	XX	×
--------------	-----	----	---

Notes:

None.

9.5 CfgSlotDataP2

Description:

This Response provides the current command station configuration slot data. It is returned by the command station in response to the **GetCfgSlotDataP2** Command.

Protocol:

2

Group:

Variable-Byte Message

Opcode:

OPC_SL_RD_DATA_P2 (unofficial mnemonic)

Type:

Response

Encoding:

Byte 0:

1	1	1	0	0	1	1	0	0xE6	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	0	0	1	0	1	0	1	0x15	Message length (21 bytes).

OST1 to OST14 encode the command station's option switch table. A bit value of 1 means that the switch is closed and a value of 0 means that a switch is thrown. OpSw 8, OpSw 16, OpSw 24, OpSw 32, OpSw 40, OpSw 48, OpSw 56, OpSw 64, OpSw 72, OpSw 80, OpSw 88, OpSw 96, OpSw104, and OpSw 112 cannot be read due to bit 7 being cleared in the message format.

Byte 2:

	0	0	0	0	0	0	0	0	0×00	Configuration slot page number.
В	yte	3:								
	0	1	1	1	1	1	1	1	0x7F	Configuration slot number.

Byte 4:

0	d6 d5 d4 d3 d2 d1 d0	<ost1></ost1>	Option switch table byte 1.
Bit	OpSw		
$\frac{\mathbf{D}\mathbf{R}}{\mathbf{d}6}$	$\frac{\text{OpSw}}{\text{OpSw}}$ 7		
d5	OpSw 6		
d4	OpSw 5		
d3	OpSw 4		
d2	OpSw 3		
d1	OpSw 2		
d0	OpSw 1		
Byte	5:		
0	d6 d5 d4 d3 d2 d1 d0	<ost2></ost2>	Option switch table byte 2.
Bit	OpSw		
$\overline{d6}$	$\frac{\text{OpSw}}{\text{OpSw}}$ 15		
d5	OpSw 14		
d4	OpSw 13		
d3	OpSw 12		
d2	OpSw 11		
d1	OpSw 10		
d0	OpSw 9		
Byte	6:		
0	d6 d5 d4 d3 d2 d1 d0	<ost3></ost3>	Option switch table byte 3.
Bit	OpSw		
$\overline{d6}$	$\overline{\text{OpSw}}$ 23		
d5	OpSw 22		
d4	OpSw 21		
d3	OpSw 20		
d2	OpSw 19		
d1	OpSw 18		
d0	OpSw 17		
Byte	7:		
0	d6 d5 d4 d3 d2 d1 d0	<ost4></ost4>	Option switch table byte 4.

$\underline{\mathrm{Bit}}$	OpSw
d6	$\overline{\mathrm{OpSw}}$ 31
d5	OpSw~30
d4	OpSw 29
d3	OpSw 28
d2	OpSw 27
d1	OpSw 26
d0	OpSw 25

Byte 8:

0	$d6 \mid d5 \mid$	d4	d3	d2	d1	d0	$\langle OST5 \rangle$	>	Option switch table byte 5.
<u>Bit</u>	OpSw								
d6	$\overline{\mathrm{OpSw}}$	39							
d5	OpSw	38							
d4	OpSw	37							
d3	OpSw	36							
d2	OpSw	35							
d1	OpSw	34							
d0	OpSw	33							

Byte 9:

0	d6 d5 d4	d3 d2 d1 d0	<ost6></ost6>	Option switch table byte 6.
Bit d6 d5 d4 d3 d2 d1	OpSw 47 OpSw 46 OpSw 45 OpSw 44 OpSw 43 OpSw 42			
d0	OpSw 41			

Byte 10:

0	d6 d5	d4	d3	d2	d1	d0	$\langle OST7 \rangle$	Option	switch	table	byte 7
---	-------	----	----	----	----	----	------------------------	--------	--------	-------	--------

```
OpSw
\underline{\mathrm{Bit}}
         \overline{\mathrm{OpSw}} 55
d6
d5
         OpSw 54
         {
m OpSw}~53
d4
d3
         {\rm OpSw}~52
         OpSw 51
d2
         OpSw~50
d1
         OpSw 49
d0
```

Byte 11:

0	d6 d5 d4 d3 d2 d1 d0	<ost8></ost8>	Option switch table byte 8.
Bit d6 d5 d4 d3 d2 d1 d0	OpSw OpSw 63 OpSw 62 OpSw 61 OpSw 60 OpSw 59 OpSw 58 OpSw 57		

Byte 12:

0	$d6 \mid d5 \mid d4 \mid d3 \mid d2 \mid d1 \mid d0$	$\langle OST9 \rangle$	Option switch table byte 9.
Bit d6 d5 d4	OpSw OpSw 71 OpSw 70 OpSw 69	(00-0)	o p
d3	OpSw 68		
d2	OpSw 67		
d1	OpSw 66		
d0	OpSw 65		

Byte 13:

0	d6 d	5 d4	d3	d2	d1	d0	$\langle OST10 \rangle$	Option switch table byte 10	0.
---	------	------	----	----	----	----	-------------------------	-----------------------------	----

$\underline{\mathrm{Bit}}$	$\overline{\text{OpSw}}$
d6	OpSw 79
d5	OpSw 78
d4	OpSw 77
d3	OpSw 76
d2	OpSw 75
d1	OpSw 74
d0	OpSw 73

Byte 14:

0	$d6 \mid d5 \mid$	d4	d3	d2	d1	d0	<ost11></ost11>	Option switch table byte 11.
$\underline{\mathrm{Bit}}$	OpSw							
d6	$\overline{\mathrm{OpSw}}$	87						
d5	OpSw	86						
d4	OpSw	85						
d3	OpSw	84						
d2	OpSw	83						
d1	OpSw	82						
d0	OpSw	81						

Byte 15:

0	d6 d5 d4 d3 d2 d1 d0	<ost12></ost12>	Option switch table byte 12.
$\underline{\mathrm{Bit}}$	OpSw		
d6	$\overline{\mathrm{OpSw}}$ 95		
d5	OpSw 94		
d4	OpSw 93		
d3	OpSw 92		
d2	OpSw 91		
d1	OpSw 90		
d0	OpSw 89		
	-		

Byte 16:

0	d6 d5	d4	d3	d2	d1	d0	<ost13></ost13>	Option switch table byte 13	3.
---	-------	----	----	----	----	----	-----------------	-----------------------------	----

```
OpSw
\underline{\mathrm{Bit}}
         \overline{\mathrm{OpSw}} 103
d6
d5
          OpSw 102
         {
m OpSw}~101
d4
d3
         OpSw 100
         {\rm OpSw} 99
d2
         {
m OpSw} 98
d1
         {
m OpSw} 97
d0
```

Byte 17:

0	d6 d5 d4 d3 d2 d1 d0	<ost14></ost14>	Option switch table byte 14.
$\underline{\mathrm{Bit}}$	OpSw		
d6	$\overline{\mathrm{OpSw}}$ 111		
d5	OpSw 110		
d4	OpSw 109		
d3	OpSw 108		
d2	OpSw 107		
d1	OpSw 106		
d0	OpSw 105		

Byte 18:

0	d6 d5	d4 d3	$d2 \mid d1$	d0	Unknown.
$\underline{\mathrm{Bit}}$	Funct	$\underline{\mathrm{ion}}$			
d6					
d5					
d4					
d3					
d2					
d1					
d0					
Byte	19:				

Unknown.

d6 d5 d4 d3 d2 d1 d0

Bit Function d6 d5 d4 d3		
d2 $d1$		
d0		
Byte 20:		
$oxed{0} oxed{n} oxed{n}$	<chk></chk>	Checksum.
Response:		
None.		
Signature:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xE6	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x15	
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	
Notes:		
None.		

9.6 ConsistDirF0F4

Description:

This Command sets the consist element's direction and function F0 to F4 states.

Protocol:

1

Group:

4-Byte Message

Opcode:

OPC_CONSIST_FUNC

Type:

Command

Encoding:

Byte 0:

	1	0	1	1	0	1	1	0	0xB6	Opcode.
--	---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	n	n	n	n	n	n	n	<SLOT $#$ $>$	Slot number in the range $0x00$ to
									0v 77

Byte 2:

0	0	d5	d4	d3	d2	d1	d0	<dirf></dirf>	Consist	element's	direction	and
									function	F0 to F4 s	states.	

- d5 Direction: 1 means forward and 0 means backwards.
- d4 F0 state: 1 means on and 0 means off.
- d3 F4 state: 1 means on and 0 means off.
- d2 F3 state: 1 means on and 0 means off.
- d1 F2 state: 1 means on and 0 means off.
- d0 F1 state: 1 means on and 0 means off.

Byte 3:

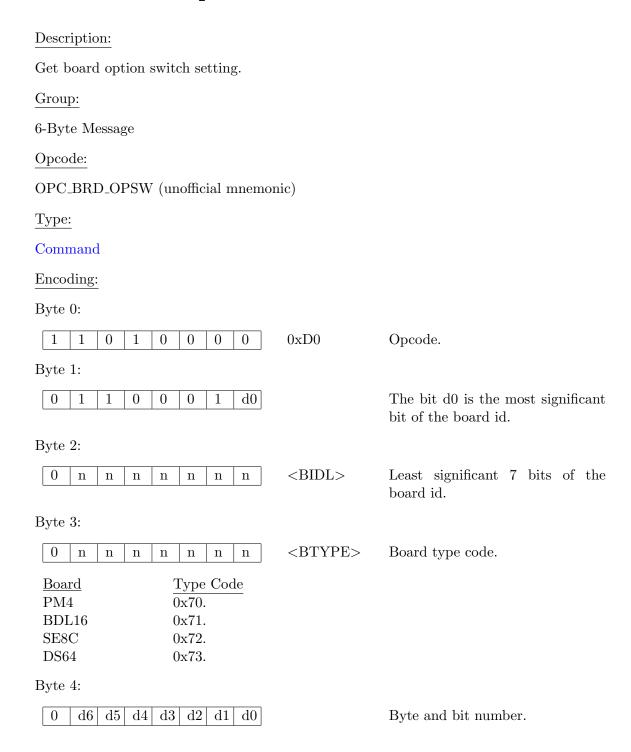
0	Chec
---	------

Response:

None.

None.							
Signature	<u>:</u>						
Byte 0:							
$\begin{bmatrix} 1 & 0 \end{bmatrix}$	1	1	0	1	1	0	0xB6
Byte 1:							
0 n	n	n	n	n	n	n	less than $0x78$
Byte 2:							
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	×	×	×	×	×	×	
Notes:							

9.7 GetBrdOpSw



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The high nibble encodes the byte number, and the low nibble the bit number. The byte number is calculated as (OpSw# - 1) >> 3 and the bit number is (OpSw# - 1) - byte number \times 8.

Byte 5:

				_			
0	n	n	n	n	n	n	n

Response:

 \mathbf{Ack} ;- *** SHOULD NOT BE A RESPONSE ***

Signature:

Byte 0:

-		_	_	_	_	_		0.00
1	1	0	1	0	0	0	0	$0 \times D0$

Byte 1:

0	1	1	0	0	0	1	×

Notes:

*** THIS HAS NOT BEEN TESTED ***

1 0

Byte 1:

9.8 GetCfgSlotDataP1

Description: This Command requests the configuration slot data. The command station returns a CfgSlotDataP1 Response. Protocol: 1 Group: 4-Byte Message Opcode: OPC_RQ_SL_DATA Type: Command Encoding: Byte 0: 1 0xBBOpcode. 0 1 1 1 0 1 Byte 1: 0 1 1 1 1 1 1 1 0x7FByte 2: 0 0 0 0 0 0 0 0 0x00Byte 3: 0 \mathbf{n} \mathbf{n} \mathbf{n} n n \mathbf{n} \mathbf{n} <CHK>Checksum. Response: CfgSlotDataP1 Signature: Byte 0:

0

1

1

1

1

0xBB

0 1 1 1 1 1 1 1	0x7F
Byte 2:	
0 0 0 0 0 0 0 0	0x00
Notes:	
None.	

 $1 \quad 0$

Byte 1:

$9.9 \quad GetCfgSlotDataP2$

Description:				
This Command requests the configure CfgSlotDataP2 Response.	ation slot data.	The command	station	returns a
Protocol:				
2				
Group:				
4-Byte Message				
Opcode:				
OPC_RQ_SL_DATA				
Type:				
Command				
Encoding:				
Byte 0:				
	0xBB	Opcode.		
Byte 1:				
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F			
Byte 2:				
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x40			
Byte 3:				
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.		
Response:				
CfgSlotDataP2				
Signature:				
Ryte 0:				

0xBB

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F
Byte 2:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x40
Notes:	
None.	

9.10 GetInterfaceData

Description:					
This Command is sent by a compute attached network interface device.	er to request	an InterfaceData	Response	from	the
Group:					
2-Byte Message					
Opcode:					
OPC_BUSY					
Type:					
Command					
Applicable Hardware:					
Digitrax PR4 and DCS240.					
Encoding:					
Byte 0:					
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x81	Opcode.			
Byte 1:					
$egin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7E	Checksum.			
Response:					
Interface device returns an Interface	Data respons	se.			
Signature:					
Notes:					
None.					

9.11 GetLocoSlotDataLAdrP1

Description:

This Command requests a slot for the selected locomotive address. If the locomotive address is found in the slot table then the command station returns an LocoSlotDataP1 Response with the slot information. If it is not found then the command station will put the locomotive address into a free slot and then return an LocoSlotDataP1 Response with the slot information. If there are no free slots then the command station returns a NoFreeSlotsP1 Response.

The command station will generate NMRA 14 bit or long address packets for the locomotive. The address must be in the range 128 to 9983.

Protocol: 1 Group: 4-Byte Message Opcode: OPC_LOCO_ADR Type: Command Encoding: Byte 0: 1 0 1 1 1 0xBFOpcode. Byte 1: n n n n n n <ADR2>Address high 7 bits. Byte 2: 0 Address low 7 bits. n \mathbf{n} n <ADR>n n Byte 3: 0 n n n \mathbf{n} n n <CHK>Checksum. Response:

${\bf LocoSlotDataP1} \ {\bf if} \ {\bf success}, \ {\bf otherwise} \ {\bf NoFreeSlotsP1}$

Signa	1
Signa	filro.
Digita	ourc.
0	

Byte 0:

|--|

Byte 1:

0	n	n	n	n	n	n	n	not equal to 0
---	---	---	---	---	---	---	---	----------------

Notes:

This command is not supported by the Digitrax DT200 command station.

9.12 GetLocoSlotDataLAdrP2

Description:

This Command requests a slot for the selected locomotive address. If the locomotive address is found in the slot table then the command station returns a **LocoSlotDataP2** Response with the slot information. If it is not found then the command station will put the locomotive address into a Free slot and then return an **LocoSlotDataP2** Response with the slot information. If there are no free slots then the command station returns a **NoFreeSlotsP2** Response.

The command station will generate NMRA 14 bit or long address packets for the locomotive. The address must be in the range 128 to 9983.

Protocol: 2 Group: 4-Byte Message Opcode: OPC_LOCO_ADR_P2 (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 0 1 0 0xBEOpcode. 1 Byte 1: n \mathbf{n} n n n n n <ADR2>Address high 7 bits. Byte 2: 0 Address low 7 bits. n \mathbf{n} n <ADR>n n n Byte 3: 0 n n n n n n <CHK>Checksum. Response:

${\bf LocoSlotDataP2} \ {\bf if} \ {\bf success}, \ {\bf otherwise} \ {\bf NoFreeSlotsP2}.$

Signature:

Byte 0:

|--|

Byte 1:

0	n	n	n	n	n	n	n	not equal to 0
---	---	---	---	---	---	---	---	----------------

 $\underline{\text{Notes:}}$

This Command can be disabled by the command station OpSw66.

9.13 GetLocoSlotDataP1

Description:

This Command requests the locomotive slot data for the specified slot. The command station responds with a LocoSlotDataP1 Response.

Protocol: 1 Group: 4-Byte Message Opcode: OPC_RQ_SL_DATA Type: Command Encoding: Byte 0: 1 0 1 1 1 0 1 1 0xBBOpcode. Byte 1: <SLOT#>0 \mathbf{n} \mathbf{n} n \mathbf{n} \mathbf{n} n n Slot number in the range 0x00 to 0x77.Byte 2: 0 0 0 0 0x000 0 0 d0Byte 3: 0 n <CHK> Checksum. n n n \mathbf{n} n n

Response:

LocoSlotDataP1 or SlotNotImplemented

Signature:

Byte 0:

1	0	1	1	1	0	1	1	0xBB
---	---	---	---	---	---	---	---	------

В	yte	1:							
	0	n	n	n	n	n	n	n	less than 0x78
В	lvte	2:							

0 0

d0

0x00

Notes:

 $0 \quad 0$

0

0 0

None.

9.14 GetLocoSlotDataP2

Description:

This Command requests the locomotive slot data for the specified slot. The command station responds with a LocoSlotDataP2 Response.

Protocol:

2

Group:

4-Byte Message

Opcode:

OPC_RQ_SL_DATA

Type:

Command

Encoding:

Byte 0:

1	0	1	1	1	0	1	1	0xBB	Opcode.

Byte 1:

0	n	n	n	n	n	n	n	<SLOT $#>$	Slot number i	in the	range	0x00	to
									0x77.				

Byte 2:

0	1	0	0	d3	d2	d1	d0	$\langle SLOTP \rangle$	Bits d2 to d0 contain the slot page
	•								number in the range $0x0$ to $0x7$.

Byte 3:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum

Response:

LocoSlotDataP2 or SlotNotImplemented

Signature:

Byte 0:

1		0	1	1	1	0	1	1	0xBB
---	--	---	---	---	---	---	---	---	------

D .	-1
Rvto	١.
\mathbf{D}	т.

Byte 2:

 $\underline{\text{Notes:}}$

None.

9.15 GetLocoSlotDataSAdrP1

Description:

This Command requests a slot for the selected locomotive address. If the locomotive address is found in the slot table then the command station returns a LocoSlotDataP1 Response with the slot information. If it is not found then the command station will put the locomotive address into a Free slot and then return a LocoSlotDataP1 Response with the slot information. If there are no free slots then the command station returns a NoFreeSlotsP1 Response.

The command station will generate NMRA 7 bit or short address packets for the locomotive. The address has the range 0 to 127. The analog locomotive is selected with address 0.

Protocol:		
1		
Group:		
4-Byte Message		
Opcode:		
OPC_LOCO_ADR		
Type:		
Command		
Encoding:		
Byte 0:		
1 0 1 1 1 1 1 1	0xBF	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 2:		
	<adr></adr>	Short address in the range 0 to 127.
Byte 3:		

0	n	n	n	n	n	n	n	<chk></chk>		Checksum.	
Respo	Response:										
${\bf LocoSlotDataP1} \ {\bf if} \ {\bf success}, \ {\bf otherwise} \ {\bf NoFreeSlotsP1}$											
Signat	ture	<u>:</u>									
Byte	0:										
1	0	1	1	1	1	1	1	0xBF			
Byte	1:										
0	0	0	0	0	0	0	0	0x00			
Notes	<u>:</u>										
None.											

9.16 GetLocoSlotDataSAdrP2

Description:

This Command requests a slot for the selected locomotive address. If the locomotive address is found in the slot table then the command station returns a **LocoSlotDataP2** Response with the slot information. If it is not found then the command station will put the locomotive address into a Free slot and then return a **LocoSlotDataP2** Response with the slot information. If there are no free slots then the command station returns a **NoFreeSlotsP2** Response.

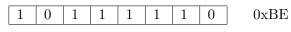
The command station will generate NMRA 7 bit or short address packets for the locomotive. The address has the range 0 to 127. The analog locomotive is selected with address 0.

··		
Protocol:		
2		
Group:		
4-Byte Message		
Opcode:		
OPC_LOCO_ADR_P2 (unofficial mner	monic)	
Type:		
Command		
Encoding:		
Byte 0:		
1 0 1 1 1 1 1 0	0xBE	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<adr></adr>	Short address in the range 0 to 127.
Byte 3:		

	0	n	n	n	n	n	n	n	<CHK $>$	Checksum.	
F	Response:										
I	LocoSlotDataP2 if success, otherwise NoFreeSlotsP2										

Signature:

Byte 0:



Byte 1:

0	0	0	0	0	0	0	0	0x00

Notes:

This Command can be disabled by the command station's OpSw66.

9.17 GetSwState

Description:

This Command requests the state of a switch. The device responds with a SwState Response.

 $\underline{\text{Group:}}$

4-Byte Message

Opcode:

 OPC_SW_STATE

Type:

Command

Encoding:

Byte 0:

1	0	1	1	1	1	0	0	0xBC	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	n	n	n	n	n	n	n	<sw1></sw1>	Switch address A6 to A6
0	11	11	11	11	11	11	11	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	DWITCH address At to A

Byte 2:

0	0	d5	d4	d3	d2	d1	d0	$\langle SW2 \rangle$	Switch address A10 to A7	7.
---	---	----	----	----	----	----	----	-----------------------	--------------------------	----

- d3 A10.
- d2 A9.
- d1 A8.
- d0 A7.

Byte 3:

		0	n	n	n	n	n	n	n	<chk></chk>	Checksum
--	--	---	---	---	---	---	---	---	---	-------------	----------

Response:

SwState

Signature:

Byte 0:

1	0	1	1	1	1	0	0	0xBC
---	---	---	---	---	---	---	---	------

Desta	o.
Dyte	<i>Z</i> :

0	0	0	0	×	×	×	X
---	---	---	---	---	---	---	---

Notes:

None.

9.18 IllegalMoveP1

Description:

Byte 0:

The IllegalMoveP1 Response means that the slot move request was not successful. This could be because the slot combination is invalid or that the slot in question was In-Use.

OBC.									
Proto	col:								
1									
Group):								
4-Byte	е Ме	essag	ge						
Opco	de:								
OPC_	LON	NG_A	ACK						
Type:									
Respo	onse								
Encoc	ling:								
Byte	0:								
1	0	1	1	0	1	0	0	0xB4	Opcode.
Byte	1:								
0	0	1	1	1	0	1	0	0x3A	
Byte :	2:								
0	0	0	0	0	0	0	0	0x00	
Byte :	3:								
0	1	1	1	0	0	0	1	0x71	Checksum
Respo	nse:								
None									
Signat	ture:								

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xB4
Byte 1:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x3A
Byte 2:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x 0 0
Byte 3:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x71
Notes:	
None.	

9.19 IllegalMoveP2

Description:

Byte 0:

The IllegalMoveP2 Response means that the slot move request was not successful. This could be because the slot combination is invalid or that the slot in question was In-Use.

OBC.									
Prote	ocol:								
2									
Grou	p:								
4-By	te M	essag	ge						
Opco	de:								
OPC	_LOI	NG_A	ACK						
Туре	<u>:</u>								
Resp	onse								
Enco	ding	<u>.</u>							
Byte	0:								
1	0	1	1	0	1	0	0	0xB4	Opcode.
Byte	1:								
0	1	0	1	0	1	0	0	0x54	
Byte	2:								
0	0	0	0	0	0	0	0	0x00	
Byte	3:								
0	0	0	1	1	1	1	1	0x1F	Checksum.
Resp	onse:	-							
None									
Signa	ture	:							

1 0 1 1 0 1 0 0	0xB4
Byte 1:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x54
Byte 2:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00
Byte 3:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x1F
Notes:	
None.	

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9.20 IMMPacket

This Command sends an n-byte DCC immediate packet.

Group:

Variable-Byte Message

Opcode:

OPC_IMM_PACKET

Type:

Command

Encoding:

Byte 0:

1	1	1	0	1	1	0	1	0xED	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:

	0	0	0	1	0	0	0	0	0x0B	Message length (11 bytes).
--	---	---	---	---	---	---	---	---	------	----------------------------

Byte 2:

0	1	1	1	1	1	1	1	0x7F

Byte 3:

0	d6	d5	d4	0	d2	d1	d0	<REPS $>$	Number	of	immediate	bytes	and
									repeat co	oun	t.		

- d6 N2. Number of immediate bytes.
- d5 N1. Number of immediate bytes.
- d4 No. Number of immediate bytes.
- d2 R2. Repeat count.
- d1 R1. Repeat count.
- d0 R0. Repeat count.

Byte 4:

0	0	1	d4	d3	d2	d1	d0	<dhii></dhii>	High bits of IM1 to IM5.
---	---	---	----	----	----	----	----	---------------	--------------------------

 d4 IM5.7. High bit. d3 IM4.7. High bit. d2 IM3.7. High bit. d1 IM2.7. High bit. d0 IM1.7. High bit. 		
Byte 5:		
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	<im1></im1>	Data item 1 low 7 bits.
Byte 6:		
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	<IM2 $>$	Data item 2 low 7 bits.
Byte 7:		
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	<im3></im3>	Data item 3 low 7 bits.
Byte 8:		
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	<im4></im4>	Data item 4 low 7 bits.
Byte 9:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\langle IM5 \rangle$	Data item 5 low 7 bits.
Byte 10:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		
Ack.		
0x7E < lim address> Comm	ing nand OK, if com nand OK, if limi nal buffer busy o	ted master.
Signature:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xED	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x0B	
Byte 2:		

0x7F

1 1 1 1

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Byte 3:

Byte 4:

0	0	1	×	×	×	×	×

Notes:

None.

9.21 InterfaceData

Description:		
This Response is returned by an int Command.	terface device in	response to a getInterfaceData
Group:		
Variable-Byte Message		
Opcode:		
OPC_PEER_XFER		
Type:		
Response		
Applicable Hardware:		
Digitrax PR4 and DCS240.		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xE5	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x10	Message length (16 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x22	
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x22	
Byte 4:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x01	
Byte 5:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 6:		

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d1></d1>	Serial Number low byte low 7 bits.
Byte 7:		
	<d2></d2>	Serial Number high byte low 7 bits.
Byte 8:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d3></d3>	It contains a value but the meaning is unknown.
Byte 9:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d4></d4>	Unknown - set to zero for PR4 and DCS240.
Byte 10:		
0 n n n n n n	<pxct2></pxct2>	Unknown - set to zero for PR4 and DCS240.
Byte 11:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d5></d5>	Maybe hardware version.
Byte 12:		
	<d6></d6>	Software version.
Byte 13:		
	$\langle D7 \rangle$	Maybe hardware version.
Byte 14:		
0 n n n n n n n	<d8></d8>	Product Code.
Byte 15:		
	<chk></chk>	Checksum.
Response:		
None		
Signature:		
Byte 0:		
1 1 1 0 0 1 0 1	0xE5	
Byte 1:		

0 0 0 1 0 0 0 0	x10
Byte 2:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	x22
Byte 3:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	x22
Byte 4:	
0 0 0 0 0 0 0 0 0	x01
Byte 5:	
0 0 0 0 0 0 0 0	x00
Byte 10:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	x00
Notes:	
PR4 #1	
<do> Oxe5 OPCODE</do>	
<d1> 0x10 LENGTH</d1>	
<d2> 0x22 SRC <d3> 0x22 DSTL</d3></d2>	
<d3> 0x22 D3TL <d4> 0x01 DSTH</d4></d3>	
<d5> 0x00 PXCT1 <- I would have exp</d5>	pected b4 = 1
<d6> 0x08 Serial Number Low Byte</d6>	
<pre><d7> 0x07 Serial Number High Byte - <d8> 0x16</d8></d7></pre>	- Actual serial number 0x0788
<d9> 0x10 <d9> 0x00</d9></d9>	
<d10> 0x00 PXCT2</d10>	
<d11> 0x00</d11>	
<d12> 0x00</d12>	
<d13> 0x00 <d14> 0x24 Product Code for PR4</d14></d13>	
<d15> 0x36 CHSUM</d15>	
PR4 #2	
<pre><do> Oxe5 OPCODE OPC_PEER_XFER</do></pre>	
<d1> 0x10 LENGTH</d1>	

```
<D2> 0x22 SRC
<D3> 0x22 DSTL
<D4> 0x01 DSTH
<D5> 0x00 PXCT1
<D6> 0x57 Serial Number Low Byte
<D7> 0x13 Serial Number High Byte - Actual serial number 0x1357
<D8> 0x16
<D9> 0x00
<D10> 0x00 PXCT2
<D11> 0x00
<D12> 0x00
<D13> 0x00
<D14> 0x24 Product Code for PR4
<D15> 0x7d CHKSUM
DCS240
<DO> 0xe5 OPCODE
<D1> 0x10 Length
<D2> 0x22 SRC
<D3> 0x22 DSTL
<D4> 0x01 DSTH
<D5> 0x00 PXCT1 <- I would have expected b4 to be 1
<D6> 0x2b Serial Number Low Byte
<D7> 0x0a Serial Number High Byte - Actual serial number 0x0aab
<D8> 0x14
<D9> 0x00
<D10> 0x00 PXCT2
<D11> 0x01 Hardware Version?
<D12> 0x03 Software Version
<D13> 0x01 Hardware Version?
<D14> 0x1c Product Code for DCS240
<D15> 0x21
```

9.22 IPLDataLoad

Description:

This Command loads firmware data into a device that supports IPL. D1 is the lowest addressed byte and D8 is the highest addressed byte.

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Command

Encoding:

Byte 0:

<i>u</i>		
	0xE5	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x10	Message length (16 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Broadcast id.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Broadcast id.
Byte 4:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Broadcast id.
Byte 5:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<pxct1></pxct1>	Download code 0x40 and high bits

of D1 to D4.

 d3 D4.7. High bit d2 D3.7. High bit d1 D2.7. High bit d0 D1.7. High bit 		
Byte 6:		
	<d1></d1>	Data Byte 1. Low 7 bits.
Byte 7:		
	<d2></d2>	Data Byte 2. Low 7 bits.
Byte 8:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d3></d3>	Data Byte 3. Low 7 bits.
Byte 9:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d4></d4>	Data Byte 4. Low 7 bits.
Byte 10:		
0 0 1 0 n n n n	<pxct2></pxct2>	Data type code 0x20 and high bits for D5 to D8.
 d3 D8.7. High bit d2 D7.7. High bit d1 D6.7. High bit d0 D5.7. High bit 		
Byte 11:		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	<d5></d5>	Data Byte 5. Low 7 bits.
Byte 12:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d6></d6>	Data Byte 6. Low 7 bits.
Byte 13:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d7></d7>	Data Byte 7. Low 7 bits.
Byte 14:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d8></d8>	Data Byte 8. Low 7 bits.
Byte 15:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		

None	
Signature:	
Byte 0:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xE5
Byte 1:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x10
Byte 2:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F
Byte 3:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F
Byte 4:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F
Byte 5:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	
Byte 10:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	
Notes:	
None.	

9.23. IPLDEVDATA 91

9.23 IPLDevData

Description:

An IPL capable device returns this Response in response to an IPLDiscover Command.

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Response

Encoding:

Byte 0:

	0xE5	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x14	Message length (20 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x0F	
Byte 3:		

0x10

0 0 Byte 4:

0	0	0	0	d3	d2	d1	d0	<pxct1></pxct1>	High bits of D1 to D4

0

- d3 D4.7. High bit
- d2 D3.7. High bit
- d1 D2.7. High bit
- d0 D1.7. High bit

Byte 5:

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1> Product Code	1> Product Code :	1> Product Code low	1> Product Code low	1> Product Code low 7	1> Product Code low 7	1> Product Code low 7	1> Product Code low 7 l	1> Product Code low 7 b	1> Product Code low 7 b	1> Product Code low 7 b	1> Product Code low 7 b	1>	1>	1>	1>	1>
Product Code	Product Code 1	Product Code low	Product Code low	Product Code low 7	Product Code low 7	Product Code low 7	Product Code low 7 l	Product Code low 7 b	Product Code low 7 b	Product Code low 7 b	Product Code low 7 b	1>)1>)1>)1>)1>
D1> Product Code	D1> Product Code I	D1> Product Code low	D1> Product Code low	D1> Product Code low 7	D1> Product Code low 7	Product Code low 7	D1> Product Code low 7 k	Product Code low 7 b	D1> Product Code low 7 b	D1> Product Code low 7 b	Product Code low 7 b)1>)1>)1>)1>)1>

Byte 6:

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d2></d2>	Hardware version 2 low 7 bits.
$\begin{array}{cc} \underline{D2} & \underline{Meaning} \\ 0x00 & \underline{Slave all} \\ 0x18 & \underline{Slave RF24} \end{array}$		
Byte 7:		
$oxed{0} oxed{n} oxed{n} oxed{n} oxed{n} oxed{n} oxed{n} oxed{n} oxed{n}$	<d3></d3>	Data item 3. Low 7 bits.
Byte 8:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d4></d4>	Software Version Number low 7 bits.
d6 version number bit 3 d5 version number bit 2. d4 version number bit 1 d3 version number bit 0 d2 subversion number bit 2 d1 subversion number bit 1 d0 subversion number bit 0 e.g. 0x09 decodes as version 1.1.		
Byte 9:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<pxct2></pxct2>	High bits of D5 to D8.
 d3 D8.7. High bit d2 D7.7. High bit d1 D6.7. High bit d0 D5.7. High bit 		
Byte 10:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d5></d5>	Data item 5. Low 7 bits.
Byte 11:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d6></d6>	Serial number low byte low 7 bits.
Byte 12:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d7></d7>	Serial number high byte low 7 bits.
Byte 13:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d8></d8>	Data item 8. Low 7 bits.

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Byte 14:

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<pxct3></pxct3>	High bits for D9 to D12.
 d3 D12.7. High bit d2 D11.7. High bit d1 D10.7. High bit d0 D9.7. High bit 		
Byte 15:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d9></d9>	Data item 9. Low 7 bits.
Byte 16:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d10></d10>	Data item 10. Low 7 bits.
Byte 17:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d11></d11>	Data item 11. Low 7 bits.
Byte 18:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d12></d12>	Data item 8. Low 12 bits.
Byte 19:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.

Response:

None.

Notes:

These came from DigiPLII:

 $e5\ 14\ 0f\ 10\ 00\ 24\ 00\ 00\ 00\ 02\ 00\ 08\ 07\ 00\ 00\ 00\ 00\ 00\ 00\ 38$

 $e5\ 14\ 0f\ 10\ 00\ 24\ 00\ 00\ 00\ 00\ 00\ 57\ 13\ 00\ 00\ 00\ 00\ 00\ 00\ 71$

 $e5\ 14\ 0f\ 10\ 00\ 1b\ 00\ 00\ 03\ 02\ 00\ 54\ 10\ 00\ 00\ 00\ 00\ 00\ 00\ 4f$

PR4 with serial number 0x0788 ver 0

PR4 with serial 0x1357 ver 0

DCS210 with SN 0x10D4 ver 0.3

DCS240 with SN 0x0AAB ver 0.3

9.24. IPLDISCOVER 95

9.24 IPLDiscover

Description:

This Command requests IPL capable devices to report their IPL information. The devices each respond with a IPLDevData Response.

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Command

Encoding:

Byte 0:

0 0

Byte 7:

 $0 \quad 0$

0

0

0 0

0

0

0

0 0

0

0

0

J		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xE5	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x14	Message length (20 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x0F	
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x08	
Byte 4:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 5:		
0 0 0 0 0 0 0 0	0x00	
Byte 6:		

0x00

0x00

Byte 8:		
	0x00	
Byte 9:		
0 0 0 0 0 0 0 0	0x00	
Byte 10:		
	0x00	
Byte 11:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x01	
Byte 12:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 13:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 14:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 15:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 16:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 17:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 18:		
	0x00	
Byte 19:		
	<chk></chk>	Checksum.
Response:		

Response:

${\bf IPLDevData}$

Signature:

97

Byte 0:				
1 1 1 0	0 0	1	0 1	0xE5
Byte 1:				
0 0 0	1 0	1	0 0	0x14
Byte 2:				
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}$	0 1	1	1 1	0x0F
Byte 3:				
0 0 0	0 1	0	0 0	0x08
Byte 4:				
0 0 0	0 0	0	0 0	0x00
Byte 5:				
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}$	0 0	0	0 0	0x00
Byte 6:				
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}$	0 0	0	0 0	0x00
Byte 7:				
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}$	0 0	0	0 0	0x00
Byte 8:				
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}$	0 0	0	0 0	0x00
Byte 9:				
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}$	0 0	0	0 0	0x00
Byte 10:				
0 0 0	0 0	0	0 0	0x00
Byte 11:				
0 0 0	0 0	0	0 1	0x01
Byte 12:				
0 0 0	0 0	0	0 0	0x00
Byte 13:				

0 0 0 0 0 0 0 0	0x00
Byte 14:	
	0x00
Byte 15:	
0 0 0 0 0 0 0 0	0x 0 0
Byte 16:	
0 0 0 0 0 0 0 0	0x00
Byte 17:	
0 0 0 0 0 0 0 0	0x00
Byte 18:	
	0x00
Notes:	
None.	

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9.25 IPLEndLoad

Description:					
This Command ends a device firmware update.					
Group:					
Variable-Byte Message					
Opcode:					
OPC_PEER_XFER					
Type:					
Command					
Encoding:					
Byte 0:					
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xE5	Opcode.			
Byte 1:					
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x10	Message length (16 bytes).			
Byte 2:					
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Broadcast id.			
Byte 3:					
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Broadcast id.			
Byte 4:					
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Broadcast id.			
Byte 5:					
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x40	Download code.			
Byte 6:					
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00				
Byte 7:					
0 0 0 0 0 0 0 0	0x00				

Byte 8:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 9:		
0 0 0 0 0 0 0 0	0x00	
Byte 10:		
0 1 0 0 n n n n	0x40	End load type code.
Byte 11:		
	0x00	
Byte 12:		
	0x00	
Byte 13:		
	0x00	
Byte 14:		
	0x00	
Byte 15:		
	<chk></chk>	Checksum.
Response:		
None		
Signature:		
Byte 0:		
1 1 1 0 0 1 0 1	0xE5	
Byte 1:		
0 0 0 1 0 0 0	0x10	
Byte 2:		
0 1 1 1 1 1 1 1	0x7F	
Byte 3:		
0 1 1 1 1 1 1 1	0x7F	

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Byte 4:						
0 1	1	1	1	1	1 1	0x7F
Byte 5:						
0 1	0	0	0	0	0 0	0x40
Byte 6:						
0 0	0	0	0	0	0 0	0x00
Byte 7:						
0 0	0	0	0	0	0 0	0x00
Byte 8:						
0 0	0	0	0	0	0 0	0x00
Byte 9:						
0 0	0	0	0	0	0 0	0x00
Byte 10:		•	'			
0 1	0	0	n	n	n n	0x40
Byte 11:						
0 0	0	0	0	0	0 0	0x00
Byte 12:			l			
0 0	0	0	0	0	0 0	0x00
Byte 13:	<u> </u>		l			
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	0	0	0	0	0 0	0x00
Byte 14:						01200
$\begin{array}{c c} D_{j} & 0 & 1 \\ \hline 0 & 0 & \end{array}$	0	0	0	0	0 0	0x00
Notes:						0100
None.						
MOHE.						

Byte 5:

1

d3 d2 d1 d0

9.26 IPLSetAddr

Description:		
This Command sets the memory addata.	ddress of where	e to load the next block of firmware
Group:		
Variable-Byte Message		
Opcode:		
OPC_PEER_XFER		
Type:		
Command		
Encoding:		
Byte 0:		
1 1 1 0 0 1 0 1	0xE5	OPC_PEER_XFER opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x10	Message length (16 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Broadcast id.
Byte 3:		
0 1 1 1 1 1 1 1	0x7F	Broadcast id.
Byte 4:		
0 1 1 1 1 1 1 1	0x7F	Broadcast id.

<PXCT1>

Download code 0x40 and high bits

of D1 to D4.

 d3 D4.7. High bit d2 D3.7. High bit d1 D2.7. High bit d0 D1.7. High bit 		
Byte 6:		
	<d1></d1>	Address High Byte. Low 7 bits.
Byte 7:		
	<d2></d2>	Address Mid Byte. Low 7 bits.
Byte 8:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d3></d3>	Address Low Byte. Low 7 bits.
Byte 9:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d4></d4>	Reserved always 0x00. Low 7 bits.
Byte 10:		
0 0 1 n n n n	<pxct2></pxct2>	Address type code 0x10 and high bits for D5 to D8.
d3 D8.7. High bit d2 D7.7. High bit d1 D6.7. High bit d0 D5.7. High bit		
Byte 11:		
	<d5></d5>	Reserved always 0x00. Low 7 bits.
Byte 12:		
	<d6></d6>	Reserved always 0x00. Low 7 bits.
Byte 13:		
	<d7></d7>	Reserved always 0x00. Low 7 bits.
Byte 14:		
	<d8></d8>	Reserved always $0x00$. Low 7 bits.
Byte 15:		
	<chk></chk>	Checksum.
Response:		

None						
Signature	<u>:</u>					
Byte 0:						
1 1	1	0	0	1	0 1	0xE5
Byte 1:						
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	0	1	0	0	0 0	0x10
Byte 2:						
$0 \mid 1$	1	1	1	1	1 1	0x7F
Byte 3:						
$0 \mid 1$	1	1	1	1	1 1	0x7F
Byte 4:						
$0 \mid 1$	1	1	1	1	1 1	0x7F
Byte 5:						
0 1	0	0	X	×	× ×	
Byte 9:						
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	0	0	0	0	0 0	0x00
Byte 10:						
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	0	1	X	X	× ×	
Byte 11:						
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	0	0	0	0	0 0	0x00
Byte 12:						
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	0	0	0	0	0 0	0x00
Byte 13:						
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	0	0	0	0	0 0	0x00
Byte 14:						
$\begin{bmatrix} 0 & 0 \end{bmatrix}$	0	0	0	0	0 0	0x00
Notes:						

None.

9.27 IPLSetupBL2

Description:

This Command initiates a firmware update for a device that supports the IPL Bootloader 2 protocol.

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Command

Encoding:

Byte 0:

0

1

0

0

1 1 1 0 0 1 0 1	0xE5	Opcode.
Byte 1:		
	0x10	Message length (16 bytes).
Byte 2:		
0 1 1 1 1 1 1 1	0x7F	Broadcast id.
Byte 3:		
0 1 1 1 1 1 1 1	0x7F	Broadcast id.
Byte 4:		
0 1 1 1 1 1 1 1	0x7F	Broadcast id.
Byte 5:		

<PXCT1>

Download code 0x40 and high bits

of D1 to D4.

d3 d2 d1 d0

0 0 0 0 0 0 0 0 0 0 0 0				r						
10 10 10 10 10 10 10 10 10 10 10 Reserved always 0x00. Low 100	\cap	Ω		\cap	\cap	\cap	1.0	\cap	∠D6>	Percent always 0x00 I ow 7 hits
	U	U	U	U	l U	U	U	U	<d0></d0>	reserved arways 0x00. Low 7 bits.

Byte 13:

0	n	n	n	n	n	n	n	<d7></d7>	Number of blocks to erase 7. Low
									7 bits.

This is calculated as INT(0.5 + (Last Address - First Address) / Erase Blk Size).

0 0 0 0 0 0 0 0	<d8></d8>	Reserved always 0x00. Low 7 bits.
Byte 15:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		
None		
Signature:		
Byte 0:		
1 1 1 0 0 1 0 1	0xE5	
Byte 1:		
0 0 0 1 0 0 0	0x10	
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	
Byte 3:		
0 1 1 1 1 1 1 1	0x7F	
Byte 4:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	
Byte 5:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 10:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 12:		
0 0 0 0 0 0 0 0	0x00	
Byte 14:		
	0x00	
Notes:		
None.		

9.28. LINKSLOTSP1 109

9.28 LinkSlotsP1

Description:

Protocol:

This Command requests the command station to link sub-member or mid-consist slot SL1 to top-member slot SL2. If the Command was successful then a **LocoSlotDataP1** Response will be returned for the sub-member or mid-consist. An invalid link will return a **InvalidLinkP1** Response. The top-member slot does not have to have a locomotive address loaded.

1		
Group:		
4-Byte Message		
Opcode:		
OPC_LINK_SLOTS		
Type:		
Command		
Encoding:		
Byte 0:		
1 0 1 1 1 0 0 1	0xB9	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<sl1></sl1>	Sub-member or mid-consist slot number in the range 0x01 to 0x77.
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<sl2></sl2>	Top-member slot number in the range $0x01$ to $0x77$.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.

Response:

LocoSlotDataP1 if successful, InvalidLinkP1 otherwise.

Signature:

В	Byte	0:							
	1	0	1	1	1	0	0	1	

0xB9

Byte 1:

 in the range 0x01 to 0x77.

Byte 2:

 $oxed{0 \ \ n \ \ n \ \ n \ \ n \ \ n \ \ n}$

in the range 0x01 to 0x77.

 $\underline{\text{Notes:}}$

None.

9.29 LinkSlotsP2

Description:

This Command requests the command station to link sub-member or mid-consist slot SL1 to top-member slot SL2. If the Command was successful then a **LocoSlotDataP2** Response will be returned for the sub-member or mid-consist. An invalid link will return a **InvalidLinkP2** Response. The top-member slot does not have to have a locomotive address loaded.

Protocol: 2 Group: 6-Byte Message Opcode: OPC_D4_GROUP (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 0 0 Opcode. 1 0 1 0xD4Byte 1: 0 0 1 1 d2d1d0 $\langle SL1P \rangle$ Bits d2 to d0 contain the submember or mid-consist slot page number in the range 0x0 to 0x7. Byte 2: 0 $\langle SL1\# \rangle$ Sub-member or mid-consist slot n n n n n n n number in the range 0x00 to 0x77. Byte 3: $\langle SL2P \rangle$ 0 0 0 0 d2 | d1 | d0 |Bits d2 to d0 contain the top-1 member slot page number in the

range 0x0 to 0x7.

Byte 4:

	<sl2#></sl2#>	Top-member slot number in the range $0x00$ to $0x77$.
Byte 5:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		
${\bf LocoSlotDataP2} \ {\rm or} \ {\bf InvalidSlotP2}$		
Signature:		
Byte 0:		
1 1 0 1 0 1 0 0	0xD4	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	less than $0x78$	
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 4:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	less than 0x78	
Notes:		
None.		

9.30 LocoBinStateP2

Description:

This Command sets the locomotive's binary states with addresses in the range 1 to 32767. The address of 0 is a broadcast command and will set or reset all binary states.

Protocol:

2?

Group:

6-Byte Message

Opcode:

OPC_D4_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

	1	1	0	1	0	1	0	0	0xD4	Opcode.
--	---	---	---	---	---	---	---	---	------	---------

Byte 1:

O 0 0 d4 d3 d2 d1 d0
SLOTP> Bits d2 to d0 contain the slot page number in the range 0x0 to 0x7. The bit d4 contains the function state where 1 means on and 0 means off. The bit d3 contains the high bit of the binary state address

(bit 14).

Byte 2:

0	n	n	n	n	n	n	n	<SLOT $#$ $>$	Slot number.
---	---	---	---	---	---	---	---	---------------	--------------

Byte 3:

	address	Binary state	<bsa0></bsa0>	n	n	n	
n n n <bsa0> Binary state address</bsa0>	n n n <bsa0> Binary state</bsa0>	n n n <bsa0></bsa0>	n n n	n n	n		0 n n n
n n n n <bsa0> Binary state address</bsa0>	n n n n <bsa0> Binary state</bsa0>	n n n n <bsa0></bsa0>	n n n n	n n n	n n	n	0 n n
n n n n n <bsa0> Binary state address</bsa0>	n n n n n <bsa0> Binary state</bsa0>	n n n n n <bsa0></bsa0>	n n n n	n n n n	n n n	n n	0 n
n n n n n n SSA0> Binary state address	n n n n n n SSA0> Binary state	n n n n n <bsa0></bsa0>	n n n n n n	n n n n	n n n n	n n n	0
n n n n n n n SSA0> Binary state address	n n n n n n n SSA0> Binary state	n n n n n n n <bsa0></bsa0>	n n n n n n n	n n n n n n	n n n n	n n n n	

0	n	n	n	n	n	n	n	<BSA1 $>$	Binary state	${\rm address}$	bits '	7 to	13
---	---	---	---	---	---	---	---	-----------	--------------	-----------------	--------	------	----

Byte 5:

Response:

None.

Signature:

Byte 0:

1 1 0 1 0 1 0 0 0xD4

Byte 1:

Notes:

*** THIS HAS NOT BEEN TESTED ***

9.31 LocoDirF0F4P1

Description:

This Command requests the command station to set the locomotive's direction and function F0 to F4 states.

Protocol:

1

Group:

4-Byte Message

Opcode:

OPC_LOCO_DIRF

Type:

Command

Encoding:

Byte 0:

1	0	1	0	0	0	0	1	0xA1	Opcode

Byte 1:

0	n	n	n	n	n	n	n	<SLOT $#$ $>$	Slot number in the range 0x00 to
			•						0x77.

Byte 2:

0	0	d5	d4	d3	d2	d1	d0	$\langle \text{DIRF} \rangle$	Locomotive's	${\rm direction}$	and	state
									of functions F	'0 to F4.		

- d5 Direction: 1 means forward and 0 means backwards.
- d4 F0 state: 1 means on and 0 means off.
- d3 F4 state: 1 means on and 0 means off.
- d2 F3 state: 1 means on and 0 means off.
- d1 F2 state: 1 means on and 0 means off.
- d0 F1 state: 1 means on and 0 means off.

Byte 3:

	0	n	n	n	n	n	n	n	<chk></chk>	Checksum.
--	---	---	---	---	---	---	---	---	-------------	-----------

Response:	
None.	
Signature:	
Byte 0:	
1 0 1 0 0 0 0 1	0xA1
Byte 1:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	less than 0x78
Byte 2:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	
27	
Notes:	
None.	

9.32 LocoDirF0F4P2

Description:

Byte 4:

 $0 \quad 0$

d5 d4 d3 d2 d1 d0

This Command requests the command station to set the locomotive's direction and function F0 to F4 states

Protocol: 2 Group: 6-Byte Message Opcode: OPC_D4_GROUP (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 1 0 1 0 1 0 0 0xD4Opcode. Byte 1: 0 0 1 0 0 $d2 \mid d1$ d0 $\langle SLOTP \rangle$ Bits d2 to d0 contain the slot page number in the range 0x0 to 0x7. Byte 2: 0 n n n <SLOT#>Slot number. \mathbf{n} n \mathbf{n} n Byte 3: 0 0 0 0 0x06Subcode. 0 1 1 0

Direction and function states.

d5	D	Direction: 1 means forward and 0 means backwards.												
d4	F	0 sta	ate:	$1~\mathrm{m}\epsilon$	eans	on a	nd 0	means off.						
d3	F	$4 \mathrm{sta}$	ate:	$1~\mathrm{m}\epsilon$	eans	on a	nd 0	means off.						
d2	F	3 sta	ate:	1 me	eans	on a	and 0	means off.						
d1	F2 state: 1 means on and 0 means off.													
d0	d0 F1 state: 1 means on and 0 means off.													
Byte	5:													
0	n	n	n	n	n	n	n	<chk></chk>	Checksum.					
Resp	onse:	<u>:</u>												
None).													
Signa	ature	<u>:</u>												
Byte	0:													
1	1	0	1	0	1	0	0	0xD4						
Byte	1:													
0	0	1	0	0	×	×	×							
Byte	3:													
0	0	0	0	0	1	1	0	0x06						
Byte	4:													
0	0	×	X	×	X	×	×							

This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

9.33. LOCOF0F6P2 119

9.33 LocoF0F6P2

11	OCOPI	nt	100	٠.
17	escri	DU	ш	Ι.
		L		

This Command requests the command station to set the locomotive's function F0 to F6 states.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D5_GROUP (Unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

1	1	0	1	0	1	0	1	0xD5	Opcode.

Byte 1:

0	0	0	1	0	d2	d1	d0	$\langle SLOTP \rangle$	Bits d2 to d0 contain the slot page
	•		•						number in the range $0x0$ to $0x7$.

Byte 2:



Byte 3:



0	d6 d5	d4 d3	d2	d1	d0	Function states.
---	-------	-------	----	----	----	------------------

d6	F6 st	ate: 1	means	on and 0	mea	ans off.							
d5	F5 sts	ate: 1	means	on and 0	mea	ans off.							
d4													
d3													
d2													
d1													
d0	F1 st	ate: 1	means	on and 0	mea	ans off.							
Byte	5:												
0	n n	n	n n	n n	<	<chk></chk>	Checksum.						
Resp	onse:												
None).												
Signa	ature:												
Byte	0:												
1	1 0	1 (0 1	0 1	(0xD5							
Byte	1:												
0	0 0	1 (× 0	XX									
Byte	2:												
0	n n	n	n n	n n	1	ess than 0x78							
Notes	<u>s:</u>												
None).												

9.34. LOCOF5F8P1 121

9.34 LocoF5F8P1

Description:

This Command requests the command station to set the locomotive's function F5 to F8 states.

Protocol:

1

Group:

4-Byte Message

Opcode:

OPC_LOCO_SND

Type:

Command

Encoding:

Byte 0:

-		-	_		_	-	_	0.40	0 1
1	0	1	U	0	U	1	0	0xA2	Opcode.

Byte 1:

0	n	n	n	n	n	n	n	<SLOT $#>$	Slot number	in the	range	0x00	to
									0x77.				

Byte 2:

0	0	0	0	d3	d2	d1	d0	$\langle SND \rangle$	$Locomotive \lq s$	function	F5	to	F8
									states.				

- d3 F8 state: 1 means on and 0 means off.
- d2 F7 state: 1 means on and 0 means off.
- d1 F6 state: 1 means on and 0 means off.
- d0 F5 state: 1 means on and 0 means off.

Byte 3:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum.

Response:

None.

Signature:				
Byte 0:				
$\begin{bmatrix} 1 & 0 \end{bmatrix}$	1 0	0 0	1 0	0xA2
Byte 1:				
0 n n	n n	n n	n n	less than $0x78$
Byte 2:				
0 0 0	0 0	×	××	

None.

9.35. LOCOF7F13P2 123

9.35 LocoF7F13P2

Description:

This Command requests the command station to set the locomotive's function F7 to F13 states.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D5_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

1	1	0	1	0	1	0	1	0xD5	Opcode.

Byte 1:

0	0	0	1	1	d2	d1	d0	$\langle SLOTP \rangle$	Bits d2 to d0 contain the slot page
			•						number in the range $0x0$ to $0x7$.

Byte 2:



Byte 3:

0	n	n	n	n	n	n	n	Low byte of the Throttle ID.
---	---	---	---	---	---	---	---	------------------------------

0	d6 d5 d4	d d d d d d d d d d	Function state
---	--------------	---------------------------------------	----------------

d6	F13 state: 1 means on and 0 means off.
d5	F12 state: 1 means on and 0 means off.
d4	F11 state: 1 means on and 0 means off.
d3	F10 state: 1 means on and 0 means off.
d2	F9 state: 1 means on and 0 means off.
d1	F8 state: 1 means on and 0 means off.
d0	F7 state: 1 means on and 0 means off.
Byte 5:	
0 r	$\begin{array}{c c c c c c c c c c c c c c c c c c c $
Respon	se:
None.	
Signatu	re:
Byte 0:	
1 1	$egin{array}{ c c c c c c c c c c c c c c c c c c c$
Byte 1:	
0 ($0 \mid 0 \mid 1 \mid 1 \mid imes \mid imes \mid imes$
Byte 2:	
0 r	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
Notes:	
None.	

9.36. LOCOF5F11P2 125

9.36 LocoF5F11P2

Description:

This Command requests the command station to set the locomotive's function F5 to F11 states.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D4_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

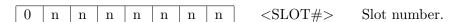
Byte 0:

1	1	0	1	0	1	0	0	0xD4	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:



Byte 2:



Byte 3:

0	0	0	0	0	1	1	1	0x07	Subcode.

|--|

```
d6
        F11 state: 1 means on and 0 means off.
        F10 state: 1 means on and 0 means off.
 d5
 d4
        F9 state: 1 means on and 0 means off.
 d3
        F8 state: 1 means on and 0 means off.
 d2
        F7 state: 1 means on and 0 means off.
 d1
        F6 state: 1 means on and 0 means off.
        F5 state: 1 means on and 0 means off.
 d0
Byte 5:
  0
                                       <CHK>
                                                      Checksum.
      n
          n
              n
                           n
                               n
                  n
                       n
Response:
None.
Signature:
Byte 0:
  1
     1
          0
              1
                   0
                       1
                           0
                               0
                                      0xD4
Byte 1:
      0
          1
              0
                   0
                       \times
                               X
Byte 3:
  0
      0
          0
              0
                   0
                       1
                           1
                               1
                                      0x07
```

This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

9.37 LocoF12F20F28P2

Description:

This Command requests the command station to set the locomotive's function F12, F20, and F28 states.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D4_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

1	1	0	1	0	1	0	0	0xD4	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:

Byte 2:

Byte 3:

	0	0	0	0	0	1	0	1	0x05	Subcode.
--	---	---	---	---	---	---	---	---	------	----------

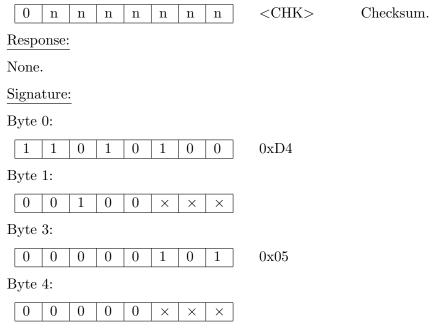
Byte 4:

0 0 0 0 d2 d1 d0
(

d2 F28 state: 1 means on and 0 means off.

d1 F20 state: 1 means on and 0 means off.
 d0 F12 state: 1 means on and 0 means off.

Byte 5:



This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

9.38 LocoF13F19P2

Description:

This $\overline{\text{Command}}$ requests the command station to set the locomotive's function F13 to F19 states.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D4_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

1	1	0	1	0	1	0	0	0xD4	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:



Byte 2:



Byte 3:

0	0	0	0	1	0	0	0	0x08	Subcode.

```
d6
        F19 state: 1 means on and 0 means off.
        F18 state: 1 means on and 0 means off.
 d5
 d4
        F17 state: 1 means on and 0 means off.
 d3
        F16 state: 1 means on and 0 means off.
 d2
        F15 state: 1 means on and 0 means off.
 d1
        F14 state: 1 means on and 0 means off.
 d0
        F13 state: 1 means on and 0 means off.
Byte 5:
  0
                                       <CHK>
                                                      Checksum.
      n
          n
              n
                           n
                               n
                  n
                       n
Response:
None.
Signature:
Byte 0:
  1
      1
          0
              1
                   0
                       1
                           0
                               0
                                      0xD4
Byte 1:
      0
          1
              0
                   0
                       \times
                               X
Byte 3:
  0
      0
          0
              0
                   1
                       0
                           0
                               0
                                      0x08
```

This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

Byte 4:

| d6 | d5 | d4 | d3 | d2 | d1 | d0 |

9.39 LocoF14F20P2

Description:		
This Command requests the command states.	station to set th	ne locomotive's function F14 to F20
Protocol:		
2		
Group:		
6-Byte Message		
Opcode:		
OPC_D5_GROUP (unofficial mnemoni	c)	
Type:		
Command		
Encoding:		
Byte 0:		
1 1 0 1 0 1 0 1	0xD5	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<slotp></slotp>	Bits d2 to d0 contain the slot page number in the range 0x0 to 0x7.
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<slot#></slot#>	Slot number in the range $0x00$ to $0x77$.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		Low byte of the Throttle ID.

Function states.

d6	F20	state:	1 m	eans	s on	and	0 me	eans off.	
d5	F19	state:	1 m	eans	s on	and	0 me	eans off.	
d4	F18	state:	1 m	eans	s on	and	0 me	eans off.	
d3	F17	state:	1 m	eans	s on	and	0 me	eans off.	
d2	F16	state:	1 m	eans	s on	and	0 me	eans off.	
d1	F15	state:	1 m	eans	s on	and	0 me	eans off.	
d0	F14	state:	1 m	eans	s on	and	0 me	eans off.	
Byte	5:								
0	n r	n n	n	n	n	n	<	<chk></chk>	Checksum.
Resp	onse:								
None									
Signa	ture:								
Byte	0:								
1	1 (1	0	1	0	1	()xD5	
Byte	1:								
0	0 1	. 0	0	×	X	×			
Byte	2:								
0	n r	n n	n	n	n	n	1	ess than 0x78	
Notes	<u>s:</u>								
None									

9.40 LocoF21F27P2

Description:

This Command requests the command station to set the locomotive's function F21 to F27 states.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D4_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

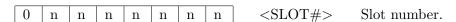
Byte 0:

1	1	0	1	0	1	0	0	0xD4	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:



Byte 2:



Byte 3:

0	0	0	0	0	1	0	1	0x09	Subcode.

```
d6
        F27 state: 1 means on and 0 means off.
        F26 state: 1 means on and 0 means off.
 d5
 d4
        F25 state: 1 means on and 0 means off.
 d3
        F24 state: 1 means on and 0 means off.
 d2
        F23 state: 1 means on and 0 means off.
 d1
        F22 state: 1 means on and 0 means off.
 d0
        F21 state: 1 means on and 0 means off.
Byte 5:
  0
                                        <CHK>
                                                        Checksum.
      n
           \mathbf{n}
                            n
                                n
               n
                   n
                        \mathbf{n}
Response:
None.
Signature:
Byte 0:
  1
      1
           0
               1
                   0
                        1
                            0
                                0
                                        0xD4
Byte 1:
      0
           1
               0
                   0
                        \times
                                 X
Byte 3:
  0
      0
           0
               0
                    1
                        0
                            0
                                 1
                                        0x09
```

This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

9.41 LocoF21F28P2

Description:

This Command requests the command station to set the locomotive's function F21 to F28 states.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D5_GROUP (unofficial mnemonic)

Type:

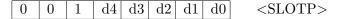
Command

Encoding:

Byte 0:

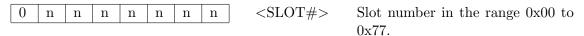
		1	1	0	1	0	1	0	1	0xD5	Opcod
--	--	---	---	---	---	---	---	---	---	------	-------

Byte 1:



Bits d2 to d0 contain the slot page number in the range 0x0 to 0x7. d4 and d3 encode the F28 state where 0b10 means on and 0b01 means off.

Byte 2:



Byte 3:

	Low byte of Throttle	Low byte of Throttle II
n n n n n n	$egin{array}{ c c c c c c c c c c c c c c c c c c c$	$egin{array}{ c c c c c c c c c c c c c c c c c c c$

d6	F	27	state:	1 m	neans	s on	and	0	means off.			
d5	F	26	state:	1 m	neans	s on	and	0	means off.			
d4	F	25	state:	1 m	neans	s on	and	0	means off.			
d3	F	24	state:	1 m	neans	s on	and	0	means off.			
d2	F	23	state:	1 m	neans	s on	and	0	means off.			
d1	F	22	state:	1 m	neans	s on	and	0	means off.			
d0	F	21	state:	1 m	neans	s on	and	0	means off.			
Byte	5:											
0	n	n	n	n	n	n	n		<chk></chk>		Checksum.	
Resp	onse:											
None	·.											
Signa	ature	<u>:</u>										
Byte	0:											
1	1	0	1	0	1	0	1		0xD5			
Byte	1:											
0	0	1	d4	d3	×	×	×		d4 and $d3$	can	be 0b10 or 0b01	
Byte	2:							-				
0	n	n	n	n	n	n	n		less than 02	x78		
Note	s:							-				
None).											

9.42 LocoSlotDataP1

Description:

This response provides the data for a specific locomotive slot.

Protocol:

1

Group:

Variable-Byte Message

Opcode:

OPC_SL_RD_DATA

Type:

Response

Encoding:

Byte 0:

		1	1	1	0	0	1	1	1	0xE7	Opcode.
--	--	---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	0	0	0	1	1	1	0	0x0E	Message length (14 bytes).
0	0	0	U	1		1	0	OAOL	Michael Chien ((II Dy CCS).

Byte 2:

0	n	n	n	n	n	n	n	<slot#> Slot</slot#>	t number in the range 0x00 to
								0x7	77. Slot $0x00$ is the dispatch
								spec	ecial slot.

Byte 3:

d7 d6	$d5 \mid d4 \mid c$	$d3 \mid d2 \mid d1 \mid d0$	$\langle STAT1 \rangle$	Slot Status 1.
<u>d3</u>	$\underline{d6}$			
0	0	No consist indirect	ction/linking.	
0	1	Consist sub-member	ber, uplinked.	
1	0	Consist top-memb	oer, downlink	ed.
1	1	Consist mid-consi	st member, u	plinked and downlinked.

The consist top-member is the locomotive slot which controls the speed, direction and status of all members of a Universal Consist. When a slot is uplinked the <SPD> byte contains the slot number of the top-member. A mid-consist is a slot that is the top-member of another consist, i.e. a consist of consists. A downlinked slot is one which has another slot uplinked to it.

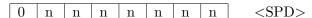
$\underline{\mathrm{d}5}$	$\underline{\mathrm{d}4}$	
0	0	Free slot, no valid data. Not refreshed.
0	1	Common. Locomotive address in this slot. Refreshed.
1	0	Idle. Locomotive address in this slot. Not refreshed.
1	1	In Use. Locomotive address in this slot. Refreshed.
$\underline{d2}$		Advanced Consist Bit
0		Slot does not control an Advanced Consist.
1		Slot controls an Advanced Consist.
<u>d1</u>	<u>d0</u>	
0	0	28 step decoder. 3-byte packet regular mode
0	1	28 step decoder. Generate Motorola trinary packets for this
		mobile address
1	0	14 step decoder.
1	1	128 step decoder.

Byte 4:

$\mid 0 \mid n \mid $ $\prec ADR$

If <ADR2> is 0 then this contains the NMRA Primary Address or if this slot controls an Advanced Consist it contains the NMRA Consist Address (CV19). If <ADR2> is greater than 0 then this contains the low 7 bits of the NMRA Extended Address.

Byte 5:



Speed in the range 0x00 to 0x7F. 0x00 means inertial stop and 0x01 means emergency stop. Other values mean increasing speed. If the slot is an uplinked slot in a consist then this byte will contain the slot number of the consist top-member.

Byte 6:

0	0	d5	d4	d3	d2	d1	d0	<dirf></dirf>	Locomotive	${\rm direction}$	and	state
									of functions	F0 to F4.		

- d5 Direction: 1 means forward and 0 means backwards.
- d4 F0 state: 1 means on and 0 means off.
- d3 F4 state: 1 means on and 0 means off.
- d2 F3 state: 1 means on and 0 means off.
- d1 F2 state: 1 means on and 0 means off.
- d0 F1 state: 1 means on and 0 means off.

Byte 7:

0	10		0	10	10	11	10	<TRK $>$	
()	ab	l ()	1 ()	∟ a.₃	azı	aı	เสบ	< 1 K.N.>	Global System Track Status.
	~~				~-	~-	~~	(Grober System Fracti Status.

- d6 1 means this command station implements protocol 2 commands.
- d3 1 means the programming track is busy.
- d2 1 means this command station implements protocol 1 commands, 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on and global power is up.

Byte 8:

0	0	0	0	d3	d2	0	d0	$\langle SS2 \rangle$	Slot status 2.
---	---	---	---	----	----	---	----	-----------------------	----------------

- d3 1 means expansion in ID1/2, 0 means encoded alias.
- d2 1 means expansion ID1/2 is not ID usage.
- d0 1 means this slot has suppressed advanced consist.

Byte 9:

0	n	n	n	n	n	n	n	<ADR2 $>$	If $\langle ADR2 \rangle$ is greater than 0 then
	•								this contains the high 7 bits of the
									NMRA Extended Address.

Byte 10:

0	0	0	0	d3	d2	d1	d0	$\langle SND \rangle$	Function F5 to F8 states.
10	-			_			1.0	œ	

- d3 F8 state: 1 means on and 0 means off.
- d2 F7 state: 1 means on and 0 means off.
- d1 F6 state: 1 means on and 0 means off.
- d0 F5 state: 1 means on and 0 means off.

Byte 11:

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<id1></id1>	7-bit ls ID code written by throt- tle when $STAT2.4 = 1$.
Byte 12:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<id2></id2>	7-bit ms ID code written by throt- tle when $STAT2.4 = 1$.
Byte 13:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		
None.		
Signature:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xE7	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x0E	
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	less than 0x78	
Byte 6:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 7:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 8:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 10:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Notes:		

 $\underline{\mathrm{d}3}$

0

0

1

1

 $\underline{d6}$

0

1

0

1

9.43 LocoSlotDataP2

Description:		
This response provides data for a spec	eific locomotive s	slot.
Protocol:		
2		
Group:		
Variable-Byte Message		
Opcode:		
OPC_SL_RD_DATA_P2 (unofficial mn	nemonic)	
Type:		
Response		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xE6	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x15	Message length (21 bytes).
Byte 2:		
0 0 0 0 0 d2 d1 d0	<slotp#></slotp#>	Slot page number in the range $0x0$ to $0x7$.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<slot#></slot#>	Slot number in the range $0x00$ to $0x77$.
Byte 4:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<stat1></stat1>	Slot Status 1.

No consist indirection/linking.

Consist sub-member, uplinked.

Consist top-member, downlinked.

Consist mid-consist member, uplinked and downlinked.

The consist top-member is the locomotive slot which controls the speed, direction and status of all members of a Universal Consist. When a slot is uplinked the <SPD> byte contains the slot number of the top-member and the bits d0-d2 of Byte 9 contain the slot page number of the top-member. The top-member slot numbers are also stored in bytes 16 and 17. Why they are duplicated is unknown. A mid-consist is a slot that is the top-member of another consist, i.e. a consist of consists. A downlinked slot is one which has another slot uplinked to it.

$ \begin{array}{c} \underline{d5} \\ 0 \\ 0 \\ 1 \\ 1 \end{array} $	$\begin{array}{c} \underline{d4} \\ 0 \\ 1 \\ 0 \\ 1 \end{array}$	Free slot, no valid data. Not refreshed. Common. Locomotive address in this slot. Refreshed. Idle. Locomotive address in this slot. Not refreshed. In Use. Locomotive address in this slot. Refreshed.
$\frac{d2}{0}$		Advanced Consist Bit Slot does not control an Advanced Consist. Slot controls an Advanced Consist.
$\begin{array}{c} \underline{d1} \\ 0 \\ 0 \end{array}$	<u>d0</u> 0 1	28 step decoder. 3-byte packet regular mode 28 step decoder. Generate Motorola trinary packets for this mobile address
1 1	0 1	14 step decoder. 128 step decoder.

Byte 5:

If <ADR2> is 0 then this contains the NMRA Primary Address or if this slot controls an Advanced Consist it contains the NMRA Consist Address (CV19). If <ADR2> is greater than 0 then this contains the low 7 bits of the NMRA Extended Address.

Byte 6:

0	n	n	n	n	n	n	n	<ADR2 $>$	If $\langle ADR2 \rangle$ is greater than 0 then
									this contains the high 7 bits of the
									NMRA Extended Address.

Byte 7:

0	d6	d4	d3	d2	d1	d0	<TRK $>$	Global system track status.
---	----	----	----	----	----	----	----------	-----------------------------

- d6 1 means this command station implements protocol 2 messages.
- d5 Reserved. Set to 0.
- d4 Reserved. Set to 0.
- d3 1 means the programming track is busy.
- d2 1 means this command station implements protocol 1 messages. 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on in the command station and track power is on.

Byte 8:

Speed in the range 0x00 to 0x7F. 0x00 means inertial stop and 0x01 means emergency stop. Other values mean increasing speed. If the slot is an uplinked slot in a consist then this byte will contain the slot number of the consist top-member.

Byte 9:

0	d6	d5	d4	d3	d2	d1	d0
---	----	----	----	----	----	----	----

Functions.

- d6 F28 state: 1 means on and 0 means off
- d5 F20 state: 1 means on and 0 means off
- d4 F12 state: 1 means on and 0 means off

d3

- d2 Consist top-member slot page number bit 2 if slot uplinked.
- d1 Consist top-member slot page number bit 1 if slot uplinked.
- d0 Consist top-member slot page number bit 0 if slot uplinked.

Byte 10:

0	d6	d5	d4	d3	d2	d1	d0

Direction and Functions.

d6

- d5 Direction: 1 means forwards and 0 means backwards
- d4 F0 state: 1 means on and 0 means off
- d3 F4 state: 1 means on and 0 means off
- d2 F3 state: 1 means on and 0 means off
- d1 F2 state: 1 means on and 0 means off
- d0 F1 state: 1 means on and 0 means off

Byte 11:

0	d6	d5	d4	d3	d2	d1	d0

Functions.

 $\frac{d1}{d0}$

Byte 15:

```
d6
       F11 state: 1 means on and 0 means off
 d5
       F10 state: 1 means on and 0 means off
 d4
       F9 state: 1 means on and 0 means off
 d3
       F8 state: 1 means on and 0 means off
 d2
       F7 state: 1 means on and 0 means off
 d1
       F6 state: 1 means on and 0 means off
 d0
       F5 state: 1 means on and 0 means off
Byte 12:
  0
      d6
          d5 d4
                  d3 d2
                          d1 d0
                                                     Functions.
 d6
       F19 state: 1 means on and 0 means off
 d5
       F18 state: 1 means on and 0 means off
 d4
       F17 state: 1 means on and 0 means off
 d3
       F16 state: 1 means on and 0 means off
       F15 state: 1 means on and 0 means off
 d2
       F14 state: 1 means on and 0 means off
 d1
 d0
       F13 state: 1 means on and 0 means off
Byte 13:
  0
      d6
          d5
              d4
                  d3
                      d2
                          d1
                              d0
                                                     Functions.
 d6
       F27 state: 1 means on and 0 means off
 d5
       F26 state: 1 means on and 0 means off
 d4
       F25 state: 1 means on and 0 means off
 d3
       F24 state: 1 means on and 0 means off
 d2
       F23 state: 1 means on and 0 means off
 d1
       F22 state: 1 means on and 0 means off
 d0
       F21 state: 1 means on and 0 means off
Byte 14:
  0
                                                     Unknown.
      d6
          d5
              d4
                  d3 \mid d2
                          d1
                              d0
 d6
 d5
 d4
 d3
 d2
```

$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Unknown.
d6	
d5	
d4	
d3	
d2 $d1$	
d0	
Byte 16:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	Consist top-member slot number in the range 0x00 to 0x77 if slot uplinked.
Byte 17:	
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Consist top-member slot page number if slot uplinked.
10	•
m d6 $ m d5$	
d4	
d3	
d2 Consist top-member slot page number bit 2 if	slot up-
linked.	_
d1 Consist top-member slot page number bit 1 if	slot up-
linked.	
d0 Consist top-member slot page number bit 0 if linked.	slot up-
Byte 18:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	Throttle id low bits.
Byte 19:	
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Throttle id high bits.
Byte 20:	
	Charlesses
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	Checksum.
Response:	

Notes:

Signature:					
Byte 0:					
1 1 1	0	0	1	1 0	0xE6
Byte 1:					
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}$	1	0	1	0 1	0x15
Byte 2:					
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix}$	0	0	×	××	
Byte 7:					
0×0	0	×	×	× ×	

9.44. LOCOSPDP1 147

9.44 LocoSpdP1

Description:

1 0

0

0

 $0 \mid 0$

0

0xA0

This Command sets the locomotive's speed in the range 0 to 127. 0 means inertial stop and 1 means emergency stop. Other values mean increasing speed.

Protocol: 1 Group: 4-Byte Message Opcode: OPC_LOCO_SPD Type: Command Encoding: Byte 0: 1 0 0 0 0 0xA0Opcode. Byte 1: 0 <SLOT#>n n n n n n n Slot number in the range 0x00 to 0x77.Byte 2: 0 n $\langle SPD \rangle$ Locomotive speed in the range 0 n \mathbf{n} \mathbf{n} n \mathbf{n} \mathbf{n} to 127. Byte 3: 0 Checksum. <CHK> \mathbf{n} \mathbf{n} n Response: None. Signature: Byte 0:

D .	-1
Rvto	١.
\mathbf{D}	т.

0	n n	n	n	n	n	n	less than 0x78
---	-----	---	---	---	---	---	----------------

Notes:

9.45. LOCOSPDP2 149

9.45 LocoSpdP2

Description:

0

n

n

n | n

n

n

 \mathbf{n}

This Command sets the locomotive's speed in the range 0 to 127. 0 means inertial stop and 1 means emergency stop. Other values mean increasing speed.

Protocol: 2 Group: 6-Byte Message Opcode: OPC_D4_GROUP (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 1 0 1 0 0 0 0xD4Opcode. 1 Byte 1: 0 0 1 0 0 d2 d1d0<SLOTP> Bits d2 to d0 contain the slot page number in the range 0x0 to 0x7. Byte 2: 0 <SLOT#>Slot number in the range 0x00 to n \mathbf{n} \mathbf{n} n \mathbf{n} n \mathbf{n} 0x77.Byte 3: 0 0 0 0 0 0 0x04Subcode. 1 0 Byte 4: 0 $\langle SPD \rangle$ Locomotive speed in the range n \mathbf{n} n n \mathbf{n} n n 0x00 to 0x7F. Byte 5:

<CHK>

Checksum.

Response:	
None.	
Signature:	
Byte 0:	
	0 0 0
Byte 1:	
	× × ×
Byte 3:	
0 0 0 0 0 1	0 0 0

Notes:

This Command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

9.46 LocoSpdDirP2

Description:

n

n

n

n

n

n

 \mathbf{n}

This Command sets the locomotive's speed in the range 0 to 127 and direction. 0 means inertial stop and 1 means emergency stop. Other values mean increasing speed.

Protocol: 2 Group: 6-Byte Message Opcode: OPC_D5_GROUP (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 1 0 1 0 1 0 1 0xD5Opcode. Byte 1: 0 0 0 0 d3 $d2 \mid d1$ d0<SLOTP> Bits d2 to d0 contain the slot page number in the range 0x0 to 0x7. Bit d3 contains the direction where 1 means forwards and 0 means backwards. Byte 2: Slot number in the range 0x00 to 0 <SLOT#>n \mathbf{n} n n \mathbf{n} \mathbf{n} n 0x77.Byte 3: 0 n n \mathbf{n} \mathbf{n} \mathbf{n} n \mathbf{n} Low byte of the Throttle ID. Byte 4:

<SPD>

Locomotive speed in the range

0x00 to 0x7F.

Byte 5:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		
None.		
Signature:		
Byte 0:		
1 1 0 1 0 1 0 1	0xD5	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	less than 0x78	3
Notes:		
None.		

9.47 NoFreeSlotsP1

Description:		
The $NoFreeSlotsP1$ Response means	that there are n	o Free slots available.
Protocol:		
1		
Group:		
4-Byte Message		
Opcode:		
OPC_LONG_ACK		
Type:		
Response		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xB4	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x3F	
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x74	Checksum.
Response:		
None		
Byte 0:		
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	0xB4	
Byte 1:		
0 0 1 1 1 1 1 1	0x3F	

В	yte	2:								
	0	0	0	0	0	0	0	0	0x 0 0	

Byte 3:

154

0 1 1 1 0 1 0 0 0x74

 $\underline{\text{Notes:}}$

9.48 NoFreeSlotsP2

Description:		
The $NoFreeSlotsP2$ Response means	that there are n	o Free slots available.
Protocol:		
2		
Group:		
4-Byte Message		
Opcode:		
OPC_LONG_ACK		
Type:		
Response		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xB4	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x3E	
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x75	Checksum.
Response:		
None		
Byte 0:		
1 0 1 1 0 1 0 0	0xB4	
Byte 1:		
0 0 1 1 1 1 1 0	0x3E	

Byte 2:	
0 0 0 0 0 0 0 0	0x00 .
Byte 3:	
0 1 1 1 0 1 0 1	0x75
Notes:	
None.	

9.49 MoveSlotsP1

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Descr	10110	m.
- CDCI	The ore	,,,,,

Move slots.

$\underline{\mathrm{SRC}}$	$\overline{\mathrm{DEST}}$	$\underline{\operatorname{Action}}$
0	×	Dispatch get. Return LocoSlotDataP1 of dispatch slot.
SRC	0	Dispatch put. Mark slot as dispatch.
SRC	SRC	Null move. SRC is set to In-Use.
SRC	DEST	Move slot data from SRC to DEST if not In-Use. Clear SRC.

Protocol:

1

Group:

4-Byte Message

Opcode:

OPC_MOVE_SLOTS

1 0 1 1 1 0 1 0

Type:

Command

Encoding:

Byte 0:

Е	$_{ m yte}$	1:								
	0	n	n	n	n	n	n	n	$\langle SRC \rangle$	Source slot number in the range
										0x00 to 0x77.

0xBA

Opcode.

Byte 2:

0	n	n	n	n	n	n	n	$\langle \text{DEST} \rangle$	Destination	slot	number	in	the
						•			range 0x00 t	$o 0x^7$	77.		

Byte 3:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum
---	---	---	---	---	---	---	---	----------	----------

Response:

${\bf LocoSlotDataP1} \ {\bf if} \ {\bf successful}, \ {\bf otherwise} \ {\bf IllegalMoveP1}$

Byte 0:

1	0	1	1	1	0	1	0	0xBA

Byte 1:

n	n	n	n	n	n	n	n	less than $0x78$
0	11	11	11	11	11	11	11	ICSS UITAII UATO

Byte 2:

0	n	n	n	n	n	n	n	less than $0x78$
_	ı		l				1	

Notes:

9.50 MoveSlotsP2

T .	•	. •
1)00	crin	tion:
\mathbf{D}_{CO}	CLIP	orom.

Move slots.

$\underline{\operatorname{SRC}}$	$\overline{\mathrm{DEST}}$	Action
0	×	Dispatch get. Return LocoSlotDataP2 of dispatch slot.
SRC	0	Dispatch put. Mark slot as dispatch.
SRC	SRC	Null move. SRC is set to In-Use.
SRC	DEST	Move slot data from SRC to DEST if not In-Use. Clear SRC.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D4_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xD4	Opcode.
Byte 1:		
0 0 1 1 1 d2 d1 d0	<srcp></srcp>	Bits d2 to d0 contain the source slot page number in the range $0x0$ to $0x7$.
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\langle SRC \rangle$	Source slot number.

Byte 3:

Byte 4:

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<DEST $>$	Destination slot number.
Byte 5:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		
LocoSlotDataP2 if successful, otherw	wise IllegalMov	veP2.
Signature:		
Byte 0:		
1 1 0 1 0 1 0 0	0xD4	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Notes:		
None.		

9.51. PEERXFER16 161

9.51 PeerXfer16

Description:

This Command sends the 8 bytes of data from one device to another peer to peer. This Command takes many forms and so what is presented here is a generic description. The specific forms are included elsewhere as detailed messages in their own right.

$\underline{\operatorname{SRC}}$	$\overline{\mathrm{DSTL}}$	$\overline{\mathrm{DSTH}}$	Comments
0x00			Source is command station.
Don't Care	0x00	0x00	Broadcast Message.
0x70 to $0x7E$			Reserved.
0x7F	0x00	0x00	Broadcast throttle message transfer.
0x7F	ID1	ID2	Throttle message transfer. ID1 and ID2 en-
			code ID.

Protocol:

1

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Command

Encoding:

Byte 0:

1 1 1 0 0 1 0 1	0xE5	Opcode.
Byte 1:		
	0x10	Message length (16 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<src></src>	Source id in the range $0x00$ to $0x7F$.

Byte 3:

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\langle DSTL \rangle$	Destination id low in the range $0x00$ to $0x7F$.				
Byte 4:						
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<dsth></dsth>	Destination id high in the range $0x00$ to $0x7F$.				
Byte 5:						
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	<pxct1></pxct1>	Address type code and high bits of D1 to D4.				
d6 XC2. Address type code.						
d5 XC1. Address type code.						
d4 XC0. Address type code.						
d3 D4.7. High bit						
d2 D3.7. High bit						
d1 D2.7. High bit						
d0 D1.7. High bit						
$\underline{XC2}$ $\underline{XC1}$ $\underline{XC0}$ Mea	aning					
$0 0 \overline{7 b}$	it peer to peer a	addresses.				
	erved.					
0 1 0 rese	erved.					
0 1 1 rese	erved.					
1 0 0 IPI	download.					
	erved.	rved.				
	erved.					
1 1 1 rese	erved.					
Byte 6:						
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d1></d1>	Data item 1. Low 7 bits.				
Byte 7:						
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d2></d2>	Data item 2. Low 7 bits.				
Byte 8:						
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d3></d3>	Data item 3. Low 7 bits.				
Byte 9:						
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d4></d4>	Data item 4. Low 7 bits.				
Byte 10:						

9.51. PEERXFER16 163

0	n	n	n	n	n	n	n] <pxct2></pxct2>	Data type code and high bits for D5 to D8.					
d6 d5 d4 d3 d2 d1 d0	X X I I I	C4. C3. 08.7. 07.7. 06.7.	Data Data High High High	a typa typa typh bith bith bith	pe co	ode.								
XC!	<u>5</u>	<u>></u>	<u>KC4</u>		<u>XC</u>	<u>23</u>		Meaning						
0														
-	0 0 1 IPL download address subcode. 0 1 0 IPL download send data subcode.													
-	0 1 0 IPL download send data subcode. 0 1 1 IPL download verify data subcode.													
1														
1		0)	1 reserved.										
1		1			0			reserved.						
1		1			1		reserved.							
Optio	Options flags													
_	<pre>private static final int DO_NOT_CHECK_SOFTWARE_VERSION = 0x00; private static final int CHECK_SOFTWARE_VERSION_LESS = 0x04;</pre>													
]	<pre>private static final int DO_NOT_CHECK_HARDWARE_VERSION = 0x00; private static final int REQUIRE_HARDWARE_VERSION_EXACT_MATCH = 0x01; private static final int ACCEPT_LATER_HARDWARE_VERSIONS = 0x03;</pre>													
Byte	11:													
0	n	n	n	n	n	n	n] <d5></d5>	Data item 5. Low 7 bits.					
Byte	12:													
0	n	n	n	n	n	n	n] <d6></d6>	Data item 6. Low 7 bits.					
Byte	13:													
0	n	n	n	n	n	n	n] <d7></d7>	Data item 7. Low 7 bits.					
Byte	14:													
0	n	n	n	n	n	n	n] <d8></d8>	Data item 8. Low 7 bits.					
Byte	15:													

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk> Checksum.</chk>
Response:	
None	
Signature:	
Byte 0:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xE5
Byte 1:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x10
Notes:	
None.	

9.52. *PROGCV* 165

9.52 ProgCV

Description:

The **ProgCV** Command is used to read and write a locomotive's mobile decoder configuration variables.

Group:

Variable-Byte Message

Opcode:

OPC_WR_SL_DATA

Type:

Command

Encoding:

Byte 0:

1	1	1	0	1	1	1	1	0xEF	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	0	0	0	1	1	1	0	0x0E	Message lengtl
	ı						1		0 0

Byte 2:

		_				_			
0	1	1	1	1	1		0	070	D
L ()		I	l I	1 1	1 1	I ()	1 ()	UX/C	Programming slot number.
_	_	_	_	_	_		_	0	0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 -

Byte 3:

	0	d6	d5	d4	d3	d2	d1	d0	<pcmd></pcmd>	Programming command
--	---	----	----	----	----	----	----	----	---------------	---------------------

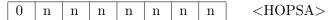
- d6 0 means read and 1 means write
- d5 $\,$ 1 means byte mode and 0 means bit mode
- d4 TY1
- d3 TY0
- d2 0 means service mode on programming track, 1 means operations mode on mainline.
- d1 1 unknown
- d0 1 unknown

$\underline{\mathrm{d}5}$	$\underline{d4}$	$\underline{d3}$	$\underline{d2}$	$\underline{d1}$	$\underline{d0}$	Programming Mode
1	0	0	0	X	×	Paged Mode byte read/write on the Program-
						ming Track
1	0	1	0	×	×	Direct Mode byte read/write on the Program-
						ming Track
0	0	1	0	×	×	Direct Mode bit read/write on the Program-
						ming Track
\times	1	0	0	×	×	Physical Register Mode byte read/write on the
						Programming Track
×	1	1	0	×	×	Service track reserved function
1	0	0	1	×	×	Operations Mode byte program on mainline no
						feedback
1	0	1	1	×	×	Operations Mode byte program on mainline
						with feedback
0	0	0	1	×	×	Operations Mode bit program on mainline no
						feedback
0	0	1	1	×	×	Operations Mode program on mainline with
						feedback

Byte 4:

0 0 0 0 0 0 0 0 0 0	ſ
---------------------------------------	---

Byte 5:



In Operations Mode programming this contains the 7 high address bits of the locomotive to program. 0x00 if service mode.

Byte 6:

0	n	n	n	n	n	n	n

In Operations Mode programming this contains the 7 low address bits of the locomotive to program. 0x00 if service mode.

Byte 7:

0	0	0	0	0	0	0	0	0x00
---	---	---	---	---	---	---	---	------

Byte 8:

0	0	d5	d4	0	0	d1	d0	<cvh></cvh>

Configuration Variable number high 3 bits and most significant bit of data byte. Byte 8:

0 0 ×	× (0	×	×	1
-------	-----	---	---	---	---

 $\underline{\text{Notes:}}$

9.53 ProgSlotDataP1

Description:

This Response provides data for the programming slot.

Group:

Variable-Byte Message

Opcode:

OPC_SL_RD_DATA

Type:

Response

Encoding:

Byte 0:

Byte 1:

Byte 2:

0	1	1	1	1	1	0	0	0x7C	Programming slot number.

Byte 3:

0	d6	d5	d4	d3	d2	d1	d0	<PCMD $>$	Last :	programming	command.
---	----	----	----	----	----	----	----	-----------	--------	-------------	----------

- d6 0 means read and 1 means write
- d5 1 means byte mode and 0 means bit mode
- d4 TY1
- d3 TY0
- d2 0 means service mode on programming track, 1 means operations mode on mainline.
- d1 1 unknown
- d0 1 unknown

$\underline{\mathrm{d}5}$	$\underline{d4}$	$\underline{d3}$	$\underline{d2}$	$\underline{d1}$	$\underline{d0}$	Programming Mode
1	0	0	0	×	×	Paged Mode byte read/write on the Program-
						ming Track
1	0	0	1	×	×	Direct Mode byte read/write on the Program-
						ming Track
0	0	0	1	×	×	Direct Mode bit read/write on the Program-
						ming Track
×	0	1	0	×	×	Physical Register Mode byte read/write on the
						Programming Track
×	0	1	1	×	×	Service track reserved function
1	0	0	1	×	×	Operations Mode byte program no feedback
1	0	1	1	×	×	Operations Mode byte program with feedback
0	0	0	1	×	×	Operations Mode bit program no feedback
0	0	1	1	×	×	Operations Mode bit program with feedback

Byte 4:

0	0	0	0	d3	d2	d1	d0	<pstat></pstat>

- d3 1 means user aborted the previous command
- d2 1 means failed to detect read compare acknowledge from decoder
- d1 1 means no write acknowledge response from decoder
- d0 1 means service mode programming track is empty no decoder detected

Byte 5:

0	n	n	n	n	n	n	n	<hopsa> In Operations Mode programming</hopsa>
	•							this contains the 7 high address
								bits of the locomotive to program.
								0x00 if service mode.

Byte 6:

0	n	n	n	n	n	n	n	<lopsa> In</lopsa>	n Operations Mode programming
								b	his contains the 7 low address its of the locomotive to program. x00 if service mode.

Byte 7:

0	d6 0	0	d3	d2	d1	d0	<TRK $>$	${\bf Global\ System}$	Track Status.
---	------	---	----	----	----	----	----------	------------------------	---------------

Byte 0:

 $1 \mid 1$

 $0 \mid 0$

1

1

1

0xE7

d61 means this command station implements version 2 slot commands. d31 means the programming track is busy. d21 means this master implements Loconet version 1.1 capability, 0 means the master is a DT200. d10 means the track is paused, broadcast an emergency stop. 1 means the DCC packets are on in the command station and global power is up. d0Byte 8: 0 0 <CVH> d5 $d4 \mid 0$ 0 $d1 \mid d0 \mid$ Configuration Variable number high 3 bits and most significant bit of data byte. d5CV9 CV8 d4d1DATA7 d0CV7Byte 9: 0 n \mathbf{n} \mathbf{n} n n \mathbf{n} <CVH> Configuration Variable number \mathbf{n} low 7 bits. CV1 is 0x0000, CV2 is 0x0001 etc. Byte 10: 0 n <DATA>Data value low 7 bits. \mathbf{n} n n n \mathbf{n} \mathbf{n} Byte 11: 0 n \mathbf{n} $\langle SNL \rangle$ Throttle ID low 7 bits of low byte. \mathbf{n} \mathbf{n} \mathbf{n} n n Byte 12: 0 \mathbf{n} $\langle SNH \rangle$ Throttle ID low 7 bits of high byte. n n n n \mathbf{n} n Byte 13: 0 1 0x7EChecksum. 1 1 1 1 1 0 Response: None. Signature:

D (-1
Ryte	- 1
$\mathbf{p}_{\mathbf{v}}$	

0 0 0 0 1 1 1 0 0:	x0E
--------------------	-----

Byte 2:

0	1	1	1	1	1	0	0	0x7C

Byte 4:

0	0	0	0	×	×	×	×

Byte 7:

0 ×	0	0	×	×	×	×
-----	---	---	---	---	---	---

Byte 8:

0	0	×	×	0	0	×	×

Notes:

9.54. PWROFF 173

9.54 PwrOff

Description:		
This Command turns the track power	off.	
Group:		
2-Byte Message		
Opcode:		
OPC_GPOFF		
Type:		
Command		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x82	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7D	Checksum.
Response:		
None.		
Signature:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x82	
Notes:		
None.		

9.55 PwrOn

Description:

This Command turns the track power on.

Group:

2-Byte Message

Opcode:

OPC_GPON

Type:

Command

Encoding:

Byte 0:

1	0	0	0	0	0	1	1	0x83	Opcode
---	---	---	---	---	---	---	---	------	--------

Byte 1:

	0	1	1	1	1	1	0	0	0x7C	Checksum.
--	---	---	---	---	---	---	---	---	------	-----------

Response:

After power on the command station sends an **getCfgSlotDataP1** message. It also sends a sequence of OPC_SW_REQ messages with the following values of SW1 and SW2:

$\underline{\mathrm{SW1}}$	$\underline{\mathrm{SW2}}$	Purpose
0x78	0x27	
0x79	0x27	
0x7A	0x27	
0x7B	0x27	
0x78	0x07	Interrogate all PM4 inputs?
0x79	0x07	Interrogate all BDL16 input reports?
0x7A	0x07	Interrogate all SE8 input reports?
0x7B	0x07	Interrogate all DS64 input reports.

Signature:

Byte 0:

1	0	0	0	0	0	1	1	0x83
---	---	---	---	---	---	---	---	------

9.55. PWRON	175
Notes:	
None.	

9.56 Reset

T .			
Des	orin	tion	١.
レしい		UIUI	т.

This broadcast message is sent by a command station when its "Loco Reset" button has been pressed. Software should reload any locally cached slot data from the command station.

station.							
Group:							
2-Byte Message							
Opcode:							
OPC_LOCO_RESET							
Type:							
Broadcast							
Encoding:							
Byte 0:							
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x8A	Opcode.					
Byte 1:							
$ \begin{bmatrix} 0 & 1 & 1 & 1 & 0 & 1 & 0 & 1 \end{bmatrix} $	0x75	Checksum.					
Response:							
None.							
Signature:							
Byte 0:							
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x8A						
Notes:							
None.							

d3

d2

d1

d0

A11.

A10.

A9.

A8.

9.57 SensRepGenIn

Description: General sensor input Report. Group: 4-Byte Message Opcode: OPC_INPUT_REP Type: Report Encoding: Byte 0: Opcode. 0 1 1 0 0 0 0xB2Byte 1: d4 d3 d2 d1 d0 Sensor address A7 to A1. $d6 \mid d5 \mid$ <IN1> A7. d6d5A6. d4A5. d3A4. d2A3. d1A2. d0A1. Byte 2: 0 d5d4 d3 d2 d1 d0 <IN2> Sensor address A11 to A8, A0 and sensor input state. d5A0. Input state: 1 means sensor input >= 6V, and 0 means d4sensor input = 0V.

Byrt	0.3	
BVU	e o:	

Response:

None.

Signature:

Byte 0:

1 0 1 1 0 0 1 0 0xB2

Byte 2:

 $0 \mid 1 \mid \times \mid \times \mid \times \mid \times \mid \times \mid \times$

Notes:

None.

9.58 SensRepTurnIn

Description:

Turnout sensor input Report.

Group:

4-Byte Message

Opcode:

 OPC_SW_REP

Type:

Report

Encoding:

Byte 0:

		1	0	1	1	0	0	0	1	0xB1	Opcode
--	--	---	---	---	---	---	---	---	---	------	--------

Byte 1:

 $\boxed{ 0 \quad | \ d6 \mid d5 \mid d4 \mid d3 \mid d2 \mid d1 \mid d0 }$ < SN1> Sensor address A7 to A1.

- d6 A7.
- d5 A6.
- d4 A5.
- d3 A4.
- d2 A3.
- d1 A2.
- d0 A1.

Byte 2:

- d5 A0.
- d4 Input sensor state, 1 means sensor >= 6V, 0 means sensor = 0V.
- d3 A11.
- d2 A10.
- d1 A9.
- d0 A8.

Byte 3:

Response:

None.

Signature:

Byte 0:

1 0 1 1 0 0 0 1 0xB1

Byte 2:

 $0 \mid 1 \mid \times \mid \times \mid \times \mid \times \mid \times \mid \times$

Notes:

None.

d0

Byte 3:

A7.

$9.59 \quad Sens Rep Turn Out$

$\overline{\mathrm{Desc}}$	ription:		
Turn	out sensor output Report.		
Grou	ıp:		
4-By	te Message		
Opco	ode:		
	 C_SW_REP		
Туре			
_	_		
Repo	ort		
Enco	oding:		
Byte	0:		
1	0 1 1 0 0 1	0xB1	Opcode.
Byte	1:		
0	d6 d5 d4 d3 d2 d1 d0	<sn1></sn1>	Sensor address A6 to A0.
d6	A6.		
d5	A5.		
d4	A4.		
d3	A3.		
d2	A2.		
d1	A1.		
d0	A0.		
Byte	2:		
0	0 d5 d4 d3 d2 d1 d0	<sn2></sn2>	Sensor address A10 to A7 and sensor state.
d5	0 means closed output line is	off and 1 means	the closed output line is on
d4	_		s the thrown output line is on.
d3	A10.		1
d2	A9.		
d1	A8.		
• •			

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		
None.		
Signature:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xB1	
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Notes:		
None.		

Byte 4:

0

 $d6 \mid d5 \mid$

d4 d3 d2 d1 d0

9.60 SetBrdOpSw

Description: Set board option switch. Group: 6-Byte Message Opcode: OPC_BRD_OPSW (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 0 1 0 0 0 0 Opcode. 1 0xD0Byte 1: 0 0 d0The bit d0 is the most significant 1 1 1 0 1 bit of the board id. Byte 2: 0 <BIDL> Least significant 7 bits of the n n \mathbf{n} \mathbf{n} n n n board id. Byte 3: 0 <BTYPE> Board type code. n \mathbf{n} n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} $\underline{\text{Board}}$ Type Code PM40x70.BDL16 0x71.SE8C 0x72.**DS64** 0x73.

Byte and bit number.

Notes: None.

The high nibble encodes the byte number, and the low nibble the bit number. The byte number is calculated as (OpSw# - 1) >> 3 and the bit number is (OpSw# - 1) - byte number \times 8.

Byte 5: 0 <CHK> n \mathbf{n} n \mathbf{n} \mathbf{n} n n Checksum. Response: \mathbf{Ack} Signature: Byte 0: 1 0xD01 0 1 0 0 0 0 Byte 1: 0 1 1 0 0 1 ×

9.61 SetIdleState

$\underline{\text{Description:}}$

This Command sets the network to Idle state and the command station broadcasts an emergency stop.

Group:

2-Byte Message

Opcode:

OPC_IDLE

Type:

Command

Encoding:

Byte 0:

		1	0	0	0	0	1	0	1	0x85	Opcode
--	--	---	---	---	---	---	---	---	---	------	--------

Byte 1:

Ω	1	1	1	1	Ω	1	0	0357 A	Chookaum
U	1	1	1	1	U	1	U	UXIA	Checksum.

Response:

None.

Signature:

Byte 0:

1	0	0	0	0	1	0	1	0x85
					1	ı	l	

Notes:

This doesn't seem to work.

$9.62 \quad SetCfgSlotDataP1$

Opcode.
Message length (14 bytes).
option switch table. A bit value of 1 means s that a switch is thrown. OpSw 8, OpSw 0pSw 56 and OpSw 64 cannot be read due
Configuration slot number.
> Option switch table byte 1.

7

$\underline{\mathrm{Bit}}$	OpSw
d6	$\overline{\mathrm{OpSw}}$

d5 OpSw 6

d4 OpSw 5

d3 OpSw 4

d2 OpSw 3

d1 OpSw 2

d0 OpSw 1

Byte 4:

 $\boxed{0 \quad \text{d6} \quad \text{d5} \quad \text{d4} \quad \text{d3} \quad \text{d2} \quad \text{d1} \quad \text{d0}}$ <0ST2> Option switch table byte 2.

 $\underline{\mathrm{Bit}} \qquad \mathrm{OpSw}$

 $d6 \quad \overline{OpSw} \ 15$

d5 OpSw 14

d4 OpSw 13

d3 OpSw 12

d2 OpSw 11

d1 OpSw 10

d0 OpSw 9

Byte 5:

<u>Bit</u> OpSw

 $d6 \qquad \overline{OpSw} \ 23$

d5 OpSw 22

d4 OpSw 21

 $\mathrm{d}3 \qquad \mathrm{OpSw}\ 20$

d2 OpSw 19

 $\mathrm{d}1 \qquad \mathrm{OpSw}\ 18$

d0 OpSw 17

Byte 6:

0 d6 d5 d4 d3 d2 d1 d0 <OST4> Option switch table byte 4.

$\underline{\mathrm{Bit}}$	OpSw
d6	$\overline{\mathrm{OpSw}}$ 31
1 -	0.0

- d5 OpSw 30
- d4 OpSw 29
- d3 OpSw 28
- d2 OpSw 27
- d1 OpSw 26
- d0 OpSw 25

Byte 7:

0	d6 0	0	d3	d2	d1	d0	<TRK $>$	Global System	Track Status.
---	------	---	----	----	----	----	----------	---------------	---------------

- d6 1 means this command station implements protocol 2 commands.
- d3 1 means the programming track is busy.
- d2 1 means this master implements protocol 1 commands, 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on and global power is up.

Byte 8:

```
d6 d5 d4 d3 d2 d1 d0
0
                                    \langle OST5 \rangle
                                                   Option switch table byte 5.
      OpSw
Bit
d6
      OpSw 39
d5
      OpSw 38
d4
      OpSw 37
      OpSw 36
d3
d2
      OpSw 35
d1
      OpSw 34
      OpSw 33
d0
```

Byte 9:

0	d6 d5	d4	d3	d2	d1	d0	$\langle OST6 \rangle$	Option switch table byte 6.
Rit	OpSu	7						

- $\underline{\text{Bit}}$ $\underline{\text{OpSw}}$
- d6 OpSw 47
- d5 OpSw 46
- d4 OpSw 45
- d3 OpSw 44
- d2 OpSw 43
- d1 OpSw 42
- d0 OpSw 41

Byte 10:

0	d6	d5	d4	d3	d2	d1	d0	$\langle OST7 \rangle$	Option switch table byte 7.
Bit	0	pSw							
d6	_	$\frac{\mathbf{p} \mathbf{S} \mathbf{w}}{\mathbf{p} \mathbf{S} \mathbf{w}}$							
d5	O	pSw	54						
d4	О	pSw	53						
d3	Ο	pSw	52						
d2	О	pSw	51						
d1	О	pSw	50						
d0	О	pSw	49						
Byrto	11.								

Byte 11:

0	d6 d5 d4 d3 d2 d1 d0	<ost8></ost8>	Option switch table byte 8.
$\underline{\mathrm{Bit}}$	OpSw		
d6	$\overline{\mathrm{OpSw}}$ 63		
d5	OpSw 62		
d4	OpSw 61		
d3	OpSw 60		
d2	OpSw 59		
d1	OpSw 58		
d0	OpSw 57		

Byte 12:

0	d6 d5	d4 d3	d2 d1	d0		Unknown.
d6						
d6 $d5$						
d4						
$\frac{d3}{d2}$						
d2						
d1						
d0						

Byte 13:

0	n	n	n	n	n	n	n	<chk></chk>	Checksum.

Response:

\mathbf{Ack}

Signature:

Byte 0:	
1 1 1 0 1 1 1 1	0xEF
Byte 1:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x0E
Byte 2:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F
Notes:	
None.	

Byte 4:

0

d6 d5 d4 d3 d2 d1 d0

$9.63 \quad SetCfgSlotDataP2$

<u>Description:</u>		
This Command sets the configuration s	lot data.	
Protocol:		
2		
Group:		
Variable-Byte Message		
Opcode:		
OPC_WR_SL_DATA_P2 (unofficial mne	emonic)	
Type:		
Command		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xEE	Opcode.
Byte 1:		
0 0 1 0 1 0 1	0x15	Message length (21 bytes).
OST1 to OST14 encode the command state that the switch is closed and a value of 16, OpSw 24, OpSw 32, OpSw 40, OpOpSw 88, OpSw 96, OpSw104, and Opin the message format.	0 means that a Sw 48, OpSw 5	a switch is thrown. OpSw 8, OpSw 66, OpSw 64, OpSw 72, OpSw 80,
Byte 2:		
0 0 0 0 0 0 0 0	0x00	Configuration slot page number.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Configuration slot number.

<OST1>

Option switch table byte 1.

```
OpSw
\underline{\mathrm{Bit}}
            \overline{\mathrm{OpSw}} 7
d6
            {\rm OpSw}~6
d5
            {\rm OpSw}~5
d4
d3
            {\rm OpSw}~4
            \overline{\mathrm{OpSw}} 3
d2
            {\rm OpSw}~2
d1
            {\rm OpSw}~1
d0
```

Byte 5:

Bit OpSw d6 OpSw 15 d5 OpSw 14 d4 OpSw 13	
d3 OpSw 12 d2 OpSw 11 d1 OpSw 10 d0 OpSw 9	

Byte 6:

0	d6 d5 d4 d	$d3 \mid d2 \mid d1 \mid d0$	$\langle OST3 \rangle$	Option switch table byte 3.
Bit d6 d5 d4 d3 d2 d1	OpSw OpSw 23 OpSw 22 OpSw 21 OpSw 20 OpSw 19 OpSw 18			
d0	OpSw 17			

Byte 7:

0	d6	d5	d4	d3	d2	d1	d0	$\langle OST4 \rangle$	Option switch table byte 4.
---	----	----	----	----	----	----	----	------------------------	-----------------------------

$\underline{\mathrm{Bit}}$	OpSw
d6	$\overline{\mathrm{OpSw}}$ 31
d5	OpSw~30
d4	OpSw 29
d3	OpSw 28
d2	OpSw 27
d1	OpSw 26

 $\overline{\mathrm{OpSw}}$ 25

Byte 8:

d0

0	d6 d5 d4 d3 d2 d1 d0	$\langle OST5 \rangle$	Option switch table byte 5.
$\underline{\mathrm{Bit}}$	OpSw		
d6	$\overline{\mathrm{OpSw}}$ 39		
d5	OpSw 38		
d4	OpSw 37		
d3	OpSw~36		
d2	OpSw 35		
d1	OpSw 34		
d0	OpSw 33		

Byte 9:

0	d6 d5 d4 d3 d	d1 d0	$\langle OST6 \rangle$	Option switch table byte 6.
Bit d6 d5 d4 d3 d2 d1 d0	OpSw OpSw 47 OpSw 46 OpSw 45 OpSw 44 OpSw 43 OpSw 42 OpSw 41			

Byte 10:

0	d6 d	5 d4	d3	d2	d1	d0	$\langle OST7 \rangle$	Option switch table byte 7.
---	------	------	----	----	----	----	------------------------	-----------------------------

```
OpSw
\underline{\mathrm{Bit}}
          \overline{\mathrm{OpSw}} 55
d6
d5
          OpSw 54
          {\rm OpSw}~53
d4
d3
          {\rm OpSw}~52
          {\rm OpSw}~51
d2
          {\rm OpSw}~50
d1
          {
m OpSw}~49
d0
```

Byte 11:

0	d6 d5 d4	$d3 \mid d2 \mid d1 \mid d0$	$\langle OST8 \rangle$	Option switch table byte 8.
$\underline{\mathrm{Bit}}$	OpSw			
d6	$\overline{\mathrm{OpSw}}$ 63			
d5	OpSw~62			
d4	OpSw 61			
d3	OpSw~60			
d2	OpSw 59			
d1	OpSw 58			
d0	OpSw 57			

Byte 12:

0	d6 d5 d4 d3 d2 d1 d0	$\langle OST9 \rangle$	Option switch table byte 9.
Bit d6 d5 d4 d3 d2 d1	OpSw OpSw 71 OpSw 70 OpSw 69 OpSw 68 OpSw 67 OpSw 66		
d0	OpSw 65		

Byte 13:

0	d6 d5	d4	d3	d2	d1	d0	<ost10></ost10>	Option switch table byte 10.
---	-------	----	----	----	----	----	-----------------	------------------------------

$\underline{\mathrm{Bit}}$	OpSw
d6	$\overline{\mathrm{OpSw}}$ 79
d5	OpSw 78
d4	OpSw 77
d3	OpSw 76

d2 OpSw 75 d1 OpSw 74

d0 OpSw 73

Byte 14:

```
{\rm OpSw}
\underline{\mathrm{Bit}}
          \overline{\text{OpSw}} 87
d6
          {\rm OpSw~86}
d5
          OpSw~85
d4
          OpSw 84
d3
          {
m OpSw} 83
d2
d1
          {\rm OpSw~82}
          OpSw 81
d0
```

Byte 15:

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | CST12> Option switch table byte 12.

```
\underline{\text{Bit}}
          {\rm OpSw}
          \overline{\mathrm{OpSw}} 95
d6
          OpSw 94
d5
          OpSw 93
d4
          {\rm OpSw} 92
d3
          {\rm OpSw} 91
d2
          OpSw 90
d1
d0
          {
m OpSw} 89
```

Byte 16:

0	d6	d5	d4	d3	d2	d1	d0	<ost13></ost13>	Option switch table byte 13.
---	----	----	----	----	----	----	----	-----------------	------------------------------

```
{\rm OpSw}
\underline{\mathrm{Bit}}
          \overline{\mathrm{OpSw}} 103
d6
d5
          OpSw 102
          {
m OpSw}~101
d4
d3
          OpSw 100
          {\rm OpSw} 99
d2
          {
m OpSw} 98
d1
d0
          {\rm OpSw}~97
```

Byte 17:

0	d6 d5 d4 d3 d2 d1 d0	<OST14 $>$	Option switch table byte 14.
$\underline{\mathrm{Bit}}$	OpSw		
d6	$\overline{\mathrm{OpSw}}$ 111		
d5	OpSw 110		
d4	OpSw 109		
d3	OpSw 108		
d2	OpSw 107		

Byte 18:

d1

d0

 ${\rm OpSw}~106$

 ${\rm OpSw}~105$

Bit Function d6 d5 d4 d3 d2 d1	$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Unknown.
d0	Bit Function d6 d5 d4 d3 d2	

Byte 19:

	0	d6	d5	d4	d3	d2	d1	d0	Unknown.
--	---	----	----	----	----	----	----	----	----------

None.

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Bit Function		
d6		
d5		
d4		
d3 $d2$		
d2 d1		
d0		
Byte 20:		
-		
$oxed{0} \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	<chk></chk>	Checksum.
Response:		
Ack		
<lopc> <ack1> Meaning</ack1></lopc>		
$0x6E$ $0x7F$ $\overline{\text{Command OK}}$	ζ.	
Signature:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xEE	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x15	
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x00	
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	
Notes:		

9.64 SetLocoSlotDataP1

Description:		
This Command sets the locomotive sle	ot data for the s	specified slot.
Protocol:		
1		
Group:		
Variable-Byte Message		
Opcode:		
OPC_WR_SL_DATA		
Type:		
Command		
Encoding:		
Byte 0:		
1 1 1 0 1 1 1 1	0xEF	Opcode.
Byte 1:		
$egin{array}{ c c c c c c c c c c c c c c c c c c c$	0x0E	Message length (14 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<SLOT $#>$	Slot number in the range 0x00 to
		0x77. Slot $0x00$ is the dispatch special slot.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<stat1></stat1>	Slot Status 1.
$\underline{d3}$ $\underline{d6}$		
0 0 No consist indir		
0 1 Consist sub-men 1 0 Consist top-men		d
1 0 Consist top-inci	, downinke	· · · · · · · · · · · · · · · · · · ·

Consist mid-consist member, uplinked and downlinked.

The consist top-member is the locomotive slot which controls the speed, direction and status of all members of a Universal Consist. When a slot is uplinked the <SPD> byte contains the slot number of the top-member. A mid-consist is a slot that is the top-member of another consist, i.e. a consist of consists. A downlinked slot is one which has another slot uplinked to it.

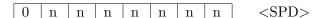
d5	$\underline{d4}$	
0	0	Free slot, no valid data. Not refreshed.
0	1	Common. Locomotive address in this slot. Refreshed.
1	0	Idle. Locomotive address in this slot. Not refreshed.
1	1	In Use. Locomotive address in this slot. Refreshed.
$\underline{d2}$		Advanced Consist Bit
0		Slot does not control an Advanced Consist.
1		Slot controls an Advanced Consist.
<u>d1</u>	<u>d0</u>	
0	0	28 step decoder. 3-byte packet regular mode
0	1	28 step decoder. Generate Motorola trinary packets for this
		mobile address
1	0	14 step decoder.
1	1	128 step decoder.

Byte 4:

0 n n n	n	n	n	n	$<$ ADR $>$
------------	---	---	---	---	-------------

If <ADR2> is 0 then this contains the NMRA Primary Address or if this slot controls an Advanced Consist it contains the NMRA Consist Address (CV19). If <ADR2> is greater than 0 then this contains the low 7 bits of the NMRA Extended Address.

Byte 5:



Speed in the range 0x00 to 0x7F. 0x00 means inertial stop and 0x01 means emergency stop. Other values mean increasing speed. If the slot is an uplinked slot in a consist then this byte will contain the slot number of the consist top-member.

Byte 6:

0	0	d5	d4	d3	d2	d1	d0	<dirf></dirf>	Locomotive	${\rm direction}$	and	state
									of functions	F0 to F4.		

- d5 Direction: 1 means forward and 0 means backwards.
- d4 F0 state: 1 means on and 0 means off.
- d3 F4 state: 1 means on and 0 means off.
- d2 F3 state: 1 means on and 0 means off.
- d1 F2 state: 1 means on and 0 means off.
- d0 F1 state: 1 means on and 0 means off.

Byte 7:

n	d6	n	n	43	d2	d1	d0	<TRK $>$	Global System Track Status
U	ao	U	U	l ao	az	l ar	au	<1nn>	Giodai System Track Status

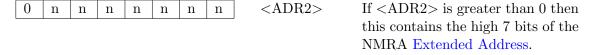
- d6 1 means this command station implements protocol 2 commands.
- d3 1 means the programming track is busy.
- d2 1 means this command station implements protocol 1 commands, 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on and global power is up.

Byte 8:

0	0	0	0	d3	d2	0	d0	$\langle SS2 \rangle$	Slot status 2

- d3 1 means expansion in ID1/2, 0 means encoded alias.
- d2 1 means expansion ID1/2 is not ID usage.
- d0 1 means this slot has suppressed advanced consist.

Byte 9:



Byte 10:

0	0	0	0	d3	d2	d1	d0	$\langle SND \rangle$	Function F5 to F8 states.

- d3 F8 state: 1 means on and 0 means off.
- d2 F7 state: 1 means on and 0 means off.
- d1 F6 state: 1 means on and 0 means off.
- d0 F5 state: 1 means on and 0 means off.

Byte 11:

None.

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<id1></id1>	7-bit ls ID code written by throt- tle when $STAT2.4 = 1$.
Byte 12:		
	<id2></id2>	7-bit ms ID code written by throt- tle when $STAT2.4 = 1$.
Byte 13:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk> Che</chk>	ecksum.
Response:		
Ack		
Signature:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xEF	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x0E	
Byte 2:		
$oxed{0 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	less than 0x78	
Byte 6:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 7:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 8:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 10:		
Notes:		

9.65 SetLocoSlotDataP2

Description:

This Command sets the locomotive slot data for the specified slot number.

Protocol:

2

Group:

Variable-Byte Message

Opcode:

OPC_WR_SL_DATA_P2 (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	0xEE	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x15	Message length (21 bytes).
Byte 2:		
0 0 0 0 0 d2 d1 d0	<slotp#></slotp#>	Slot page number in the range $0x0$ to $0x7$.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<slot#></slot#>	Slot number in the range $0x00$ to $0x77$.

Byte 4:

$0 \mid d6 \mid d5$	d4 d	3 d2 d1 d0 <stat1> Slot Status 1.</stat1>
d3	<u>d6</u>	
0	0	No consist indirection/linking.
0	1	Consist sub-member, uplinked.
1	0	Consist top-member, downlinked.
1	1	Consist mid-consist member, uplinked and downlinked.

The consist top-member is the locomotive slot which controls the speed, direction and status of all members of a Universal Consist. When a slot is uplinked the <SPD> byte contains the slot number of the top-member and the bits d0-d2 of Byte 9 contain the slot page number of the top-member. The top-member slot numbers are also stored in bytes 16 and 17. Why they are duplicated is unknown. A mid-consist is a slot that is the top-member of another consist, i.e. a consist of consists. A downlinked slot is one which has another slot uplinked to it.

$\underline{\mathrm{d}5}$	$\underline{d4}$	
0	0	Free slot, no valid data. Not refreshed.
0	1	Common. Locomotive address in this slot. Refreshed.
1	0	Idle. Locomotive address in this slot. Not refreshed.
1	1	In Use. Locomotive address in this slot. Refreshed.
$\frac{d2}{0}$		Advanced Consist Bit Slot does not control an Advanced Consist. Slot controls an Advanced Consist.
<u>d1</u>	<u>d0</u>	
0	0	28 step decoder. 3-byte packet regular mode
0	1	28 step decoder. Generate Motorola trinary packets for this mobile address
1	0	
1	U	14 step decoder.
1	1	128 step decoder.

Byte 5:

U n n n n n n n <al< th=""></al<>

If <ADR2> is 0 then this contains the NMRA Primary Address or if this slot controls an Advanced Consist it contains the NMRA Consist Address (CV19). If <ADR2> is greater than 0 then this contains the low 7 bits of the NMRA Extended Address.

Byte 6:

0	n	n	n	n	n	n	n	<ADR2 $>$	If <adr2> is greater than 0 then</adr2>
					•				this contains the high 7 bits of the

this contains the high 7 bits of the NMRA Extended Address.

Byte 7:

0	d6	d5	d4	d3	d2	d1	d0	<trk></trk>	Global system	track status
---	----	----	----	----	----	----	----	-------------	---------------	--------------

- d6 1 means this command station implements protocol 2 messages.
- d5 Reserved. Set to 0.
- d4 Reserved. Set to 0.
- d3 1 means the programming track is busy.
- d2 1 means this command station implements protocol 1 messages. 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on in the command station and track power is on.

Byte 8:

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | <SPD>

Speed in the range 0x00 to 0x7F. 0x00 means inertial stop and 0x01 means emergency stop. Other values mean increasing speed. If the slot is an uplinked slot in a consist then this byte will contain the slot number of the consist top-member.

Byte 9:

		0	d6	d5	d4	d3	d2	d1	d0
--	--	---	----	----	----	----	----	----	----

Functions.

- d6 F28 state: 1 means on and 0 means off
- d5 F20 state: 1 means on and 0 means off
- d4 F12 state: 1 means on and 0 means off

d3

- d2 Consist top-member slot page number bit 2 if slot uplinked.
- d1 Consist top-member slot page number bit 1 if slot uplinked.
- d0 Consist top-member slot page number bit 0 if slot uplinked.

Byte 10:

	0	d6	d5	d4	d3	d2	d1	d0
--	---	----	----	----	----	----	----	----

Direction and Functions.

d6

- d5 Direction: 1 means forwards and 0 means backwards
- d4 F0 state: 1 means on and 0 means off
- d3 F4 state: 1 means on and 0 means off
- d2 F3 state: 1 means on and 0 means off
- d1 F2 state: 1 means on and 0 means off
- d0 F1 state: 1 means on and 0 means off

Byte 11:

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0

Functions.

d6 F11 state: 1 means on and 0 means off d5 F10 state: 1 means on and 0 means off d4 F9 state: 1 means on and 0 means off d3 F8 state: 1 means on and 0 means off d2 F7 state: 1 means on and 0 means off d1 F6 state: 1 means on and 0 means off

F5 state: 1 means on and 0 means off

Byte 12:

d0

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0

Functions.

d6 F19 state: 1 means on and 0 means off d5 F18 state: 1 means on and 0 means off d4 F17 state: 1 means on and 0 means off d3 F16 state: 1 means on and 0 means off d2 F15 state: 1 means on and 0 means off d1 F14 state: 1 means on and 0 means off d0 F13 state: 1 means on and 0 means off

Byte 13:

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0

Functions.

d6 F27 state: 1 means on and 0 means off d5 F26 state: 1 means on and 0 means off d4 F25 state: 1 means on and 0 means off d3 F24 state: 1 means on and 0 means off d2 F23 state: 1 means on and 0 means off d1 F22 state: 1 means on and 0 means off d0 F21 state: 1 means on and 0 means off

Byte 14:

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0

Unknown.

d6

d5

d4

d3

d2

d1

d0

Byte 15:

 \mathbf{Ack}

$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Unknown.
d6	
d5	
d4	
d3	
d2	
d1	
d0	
Byte 16:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	Consist top-member slot number in the range 0x00 to 0x77 if slot uplinked.
Byte 17:	
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Consist top-member slot page number if slot uplinked.
d6	
d5	
d4	
d3	
d2 Consist top-member slot page number bit 2 if	slot up-
linked.	
d1 Consist top-member slot page number bit 1 if linked.	slot up-
d0 Consist top-member slot page number bit 0 if	slot up-
linked.	_
Byte 18:	
$oxed{0} egin{array}{ c c c c c c c c c c c c c c c c c c c$	Throttle id low bits.
Byte 19:	
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Throttle id high bits.
Byte 20:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	ecksum.
Response:	
A 1	

 $\begin{array}{ccc} \underline{<\mathrm{LOPC}>} & \underline{<\mathrm{ACK1}>} & \underline{\mathrm{Meaning}} \\ 0x6\mathrm{E} & 0x7\mathrm{F} & \overline{\mathrm{Command~OK}}. \end{array}$

Signature:

Byte 0:

1	1	1	0	1	1	1	0	0xEE

Byte 1:

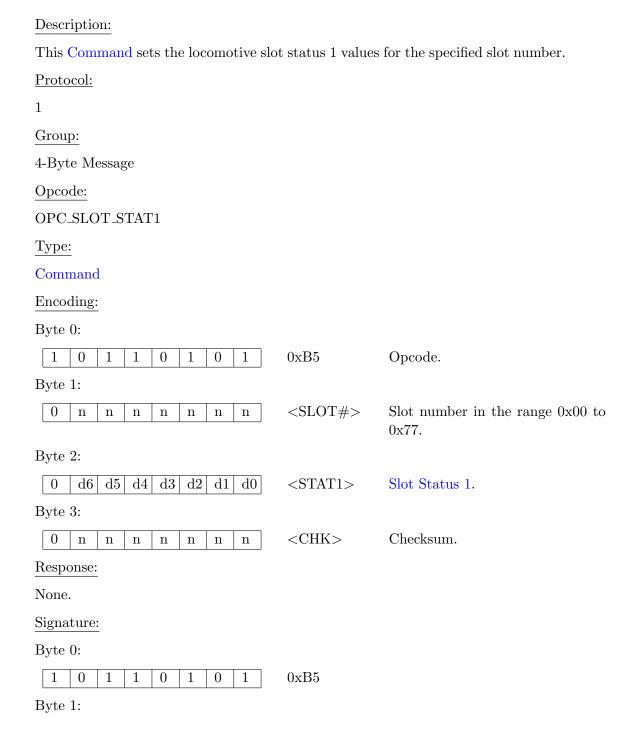
0	0	0	1	0	1	0	1	0x15
---	---	---	---	---	---	---	---	------

*** THERE SHOULD BE MORE ONCE ALL THE BYTES ARE DETERMINED ***

Notes:

None.

9.66 SetLocoSlotStat1



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	0	n	n	n	n	n	n	n	less than 0x78
N	lotes	<u>3:</u>							
Ν	Jone.								

d1

d0

A8.

A7.

9.67 SetSwState

Description: Command a switch to a specified state. *** CHECK THIS *** Group: 4-Byte Message Opcode: OPC_SW_REQ Type: Command Encoding: Byte 0: Opcode. 0 1 1 0 0 0 0 0xB0Byte 1: 0 d6 d5 d4d3 d2 d1 d0 <SW1> Switch address A6 to A0. d6A6. d5A5. d4A4. d3A3. d2A2. d1A1. d0A0. Byte 2: 0 d5 d4 d3 d2 d1 d0 <SW2> Switch address A10 to A7 and 0 switch control bits. d5Direction. 1 means closed/green, and 0 means thrown/red. d4Output. 1 means on, and 0 means off. A10. d3A9. d2

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Byte 3:

Response:

 \mathbf{Ack} if command failed, otherwise no response.

$$\begin{array}{ccc} \underline{<\mathrm{LOPC}>} & \underline{<\mathrm{ACK1}>} & \underline{\mathrm{Meaning}} \\ 0\mathrm{x}30 & 0\mathrm{x}00 & \overline{\mathrm{Command failed}}. \end{array}$$

Signature:

Byte 0:

1	0	1	1	0	0	0	0	0xB0

Byte 2:

0	0	×	×	×	×	×	×

Notes:

The on power on the command station sends a sequence of OPC_SW_REQ messages with the following values of SW1 and SW2:

$\underline{\mathrm{SW1}}$	$\underline{\mathrm{SW2}}$	Purpose
0x78	0x27	
0x79	0x27	
0x7A	0x27	
0x7B	0x27	
0x78	0x07	Interrogate all PM4 inputs?
0x79	0x07	Interrogate all BDL16 input reports?
0x7A	0x07	Interrogate all SE8 input reports?
0x7B	0x07	Interrogate all DS64 input reports.

9.68 SetSwWithAck

${\bf Description:}$

This Command sets a specified switch to a specified state. The switch responds with an **Ack**.

Group:

4-Byte Message

Opcode:

 $\mathrm{OPC_SW_ACK}$

Type:

Command

Encoding:

Byte 0:

1	0	1	1	1	1	0	1	0xBD	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	d6	d5	d4	d3	d2	d1	d0	<sw1></sw1>	Switch address A6 to A0.
---	----	----	----	----	----	----	----	-------------	--------------------------

- d6 A6.
- d5 A5.
- d4 A4.
- d3 A3.
- d2 A2.
- d1 A1.
- d0 A0.

Byte 2:

0	0	d5	d4	d3	d2	d1	d0	$\langle SW2 \rangle$	Switch	address	A10	to	A7	and
									switch o	control b	its.			

- d5 Direction. 1 means closed/green, and 0 means thrown/red.
- d4 Output. 1 means on, and 0 means off.
- d3 A10.
- d2 A9.
- d1 A8.
- d0 A7.

Byte 3:

0	n	n	n	n	n	n	n	<chk></chk>	Checksum.
---	---	---	---	---	---	---	---	-------------	-----------

Response:

\mathbf{Ack}

<LOPC> <ACK1> Meaning

0x3D 0x00 \overline{FIFO} is full, command rejected.

0x3D 0x7F Command accepted.

Signature:

Byte 0:

1 0 1 1 1 1 0 1	кBD
-----------------	-----

Byte 2:



$\underline{\text{Notes:}}$

None.

$9.69 \quad Slot Not Implemented$

Description:		
The SlotNotImplemented Response the command station.	e means that the	e slot requested is not supported by
Group:		
4-Byte Message		
Opcode:		
OPC_LONG_ACK		
Type:		
Response		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xB4	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x3B	
Byte 2:		
0 0 0 0 0 0 0 0	0x00	
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x70	Checksum.
Response:		
None		
Signature:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xB4	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x3B	

Byte 2:

|--|

Byte 3:

0	1	1	1	0	0	0	0	0x70
---	---	---	---	---	---	---	---	------

Notes:

None.

9.70 OPC_SV_PROG

This should be 0x00 for discover devices broadcast.

Operation: Program system variables. Group: Variable-Byte Message Direction: device \rightarrow device Encoding: Byte 0: 1 1 1 0 0 1 0 1 0xE5Opcode. Byte 1: 0 0 0 0 0 0 0x14Message length (20 bytes). 1 Byte 2: 0 Source id in the range 0x00 to $\langle SRC \rangle$ n n n \mathbf{n} \mathbf{n} n n 0x7F.Byte 3: Specifies the SV access type. 0 <SV $_$ CMD>n \mathbf{n} n \mathbf{n} n \mathbf{n} n Byte 4: 0 <DSTH>Destination id high in the range n \mathbf{n} \mathbf{n} n \mathbf{n} \mathbf{n} \mathbf{n} 0x00 to 0x7F. Byte 5: 0 <HOST>Device host identifier. \mathbf{n} \mathbf{n} n \mathbf{n} n \mathbf{n} \mathbf{n}

TT , T 1	ъ.
Host Id	<u>Device</u>
0x01	LNRP
0x04	UT4
0x0C	WTL12
0x14	DB210 Opto
0x15	DB210
0x16	DB220
0x1A	DCS210+
0x1B	DCS210
0x1C	DCS240
0x23	PR3
0x24	PR4
0x2A	DT402
0x32	DT500
0x33	DCS51
0x34	DCS52
0x3E	DT602
0x51	BXPA1
0x58	BXP88
0x5C	UR92
0x63	LNWI
Byte 6:	

Byte 6:

0 n	n	n	n	n	n	n
-----	---	---	---	---	---	---

Hardware version.

Host Id Device Slave all 0x000x18Slave RF24

Byte 7:

0	n	n	n	n	n	n	n
---	---	---	---	---	---	---	---

Reserved.

Byte 8:

0 d6 d5 d4 d3 d2 d1 d0

Software Version Number.

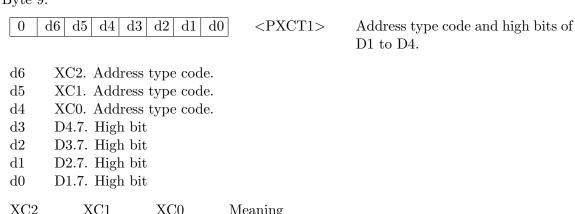
- d6version number bit 3 d5version number bit 2.
- d4version number bit 1
- d3version number bit 0
- d2subversion number bit 2
- subversion number bit 1 d1
- subversion number bit 0d0

for

e.g. 0x09 decodes as version 1.1.

This is set to 0x00 for discover devices broadcast message.

Byte 9:



<u>X</u> (C2	$\overline{\text{XC1}}$	$\underline{\text{XC0}}$	Meaning
0	(O	0	7 bit peer to peer addresses.
0	()	1	reserved.
0	-	1	0	reserved.
0	-	1	1	reserved.
1	()	0	reserved.
1	()	1	reserved.
1	-	1	0	reserved.
1	-	1	1	reserved.

Byte 10:

0	n	n	n	n	n	n	n	<d1></d1>	Data item 1. Low 7 bits.
---	---	---	---	---	---	---	---	-----------	--------------------------

Byte 11:

0	n	n	n	n	n	n	n	<d2></d2>	Data item 2. Low 7 bits.
---	---	---	---	---	---	---	---	-----------	--------------------------

This should be 0x01 for a discover devices broadcast message.

Byte 12:

0	n	n	n	n	n	n	n	<d3></d3>	Data item 3. Low 7 bits.
Byte	13:								
0	n	n	n	n	n	n	n	<d4></d4>	Data item 4. Low 7 bits.
Byte	14:								
0	n	n	n	n	n	n	n	<pxct2></pxct2>	Data type code and high bits

D5 to D8.

- d6 XC5. Data type code.
- d5 XC4. Data type code.
- d4 XC3. Data type code.
- d3 D8.7. High bit
- d2 D7.7. High bit
- d1 D6.7. High bit
- d0 D5.7. High bit

$\underline{\text{XC5}}$	$\underline{\text{XC4}}$	$\underline{\text{XC3}}$	Meaning
0	0	0	ANSI text string.
0	0	1	reserved.
0	1	0	reserved.
0	1	1	reserved.
1	0	0	reserved.
1	0	1	reserved.
1	1	0	reserved.
1	1	1	reserved.

Byte 15:

0	n	n	n	n	n	n	n	<d5></d5>	Data item 5. Low 7 bits.
---	---	---	---	---	---	---	---	-----------	--------------------------

Byte 16:

0 n n n n n n n cD6> Data i	0	ſ
-----------------------------	---	---

Byte 17:

_							
$\perp 0$	n	n	n	n	n	n	n

Byte 18:

0	$\mid n \mid 1$	n	n	n	n	n	n	<d8></d8>	Data item 8.	Low	7 bit
---	-----------------	---	---	---	---	---	---	-----------	--------------	-----	-------

Byte 19:

0	n	n	n	n	n	n	n	<chk></chk>	Checksum.
U	11	111	11	111	11	11	11	\O1111\	Checksum.

Description:

This command sends the data from one device to another peer to peer.

$\underline{\operatorname{SRC}}$	$\overline{\mathrm{DSTL}}$	$\overline{\text{DSTH}}$	Comments
0x0F	0x08	0x00	Discover devices broadcast message.
0x0F	0x10	0x00	Discover device response.

Response:

OPC_PEER_XFER_20 for discover devices.

Notes:

The discover response decoded peer transfer message encodes as follows:

```
D1 IPL Version NumberD2 Serial Number - low byte
```

D3 Serial Number - high byte

D4

D5 Serial Number 2 - low byte

D6 Serial Number 2 - high byte

D7

D8

The IPL version number is encoded as follows:

```
d6 version number bit 3
```

- d5 version number bit 2.
- d4 version number bit 1
- d3 version number bit 0
- d2 subversion number bit 2
- d1 subversion number bit 1
- d0 subversion number bit 0

e.g. 0x09 decodes as version 1.1.

These came from DigiPLII:

```
message Length = 20 e5 14 0f 10 00 24 00 00 00 02 00 08 07 00 00 00 00 00 00 38
```

 $message\ Length = 20\ e5\ 14\ 0f\ 10\ 00\ 24\ 00\ 00\ 00\ 00\ 00\ 57\ 13\ 00\ 00\ 00\ 00\ 00\ 71$

 $message \ Length = 20 \ e5 \ 14 \ 0f \ 10 \ 00 \ 1b \ 00 \ 00 \ 03 \ 02 \ 00 \ 54 \ 10 \ 00 \ 00 \ 00 \ 00 \ 00 \ 4f$

It reports PR4 with serial number 0x0788 ver 0 PR4 with serial 0x1357 ver 0 DCS240 with SN 0x0AAB ver 0.3 DCS210 with SN 0x10D4 ver 0.3

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9.71 SwState

T .	•	. •
LIDE	orin	tion:
$\mathcal{L}_{\mathcal{L}_{\mathcal{D}}}$	σ IID	· IIOII ·

This Response is returned in response to a GetSwState Command.

Group:

4-Byte Message

Opcode:

OPC_LONG_ACK

Type:

Response

Encoding:

Byte 0:

		1	0	1	1	0	1	0	0	0xB4	Opcode.
--	--	---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	0	1	1	1	1	0	0	0x3C
						l		

Byte 2:

0	0	d5	d4	0	0	0	0
---	---	----	----	---	---	---	---

<u>Bit</u> Meaning

d5 Switch state: 1 means closed/green and 0 means thrown/red

d4 Output state: 1 means on and 0 means off.

Byte 3:

	0	n	n	n	n	n	n	n	<CHK $>$	Checksum.
--	---	---	---	---	---	---	---	---	----------	-----------

Response:

None.

Signature:

Byte 0:

1	0	1	1	0	1	0	0	0xB4
---	---	---	---	---	---	---	---	------

Byte 1:

0	0	1	1	1	1	0	0
Notes	<u>:</u>						
Vone.							

9.72. TRANSREP 223

9.72 TransRep

Description:

Transponder input Report.

Group:

6-Byte Message

Opcode:

OPC_TRANS_REP

Type:

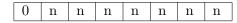
Report

Encoding:

Byte 0:

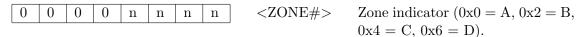
		1	1	0	1	0	0	0	0	0xD0	Opcode
--	--	---	---	---	---	---	---	---	---	------	--------

Byte 1:



A value of 0x20 means the positive detection of a transponder, 0x00 means no longer detected.

Byte 2:



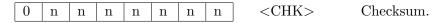
Byte 3:



Byte 4:



Byte 5:



Response:

None.

Signature:

1 1 0 1 0 0 0 0	$0 \times D0$
-----------------	---------------

*** THERE SHOULD BE MORE ***

Notes:

None.

9.73 UnlinkSlotsP1

Description:

This Command unlinks the sub-member or mid-consist slot SL1 from top-member slot SL2. If the command was successful then a **LocoSlotDataP1** response will be returned for the sub-member or mid-consist slot. An invalid link will return a **Ack** with a response code of 0x00.

code of 0x00.		
Protocol:		
1		
Group:		
Variable-Byte Message		
Opcode:		
OPC_UNLINK_SLOTS		
Type:		
Command		
Encoding:		
Byte 0:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0xB8	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<sl1></sl1>	Slot number of the sub-member or mid-consist in the range $0x00$ to $0x77$.
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<sl2></sl2>	Slot number of the top-member in the range $0x00$ to $0x77$.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.

Response:

Returns LocoSlotDataP1 or Ack

Signature:

Byte 0:	
1 0 1 1 1 0 0 0	0xB8
Byte 1:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	less than $0x78$
Byte 2:	
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	less than $0x78$
Notes:	
None.	

9.74 UnlinkSlotsP2

Description:

This Command unlinks sub-member or mid-consist slot SL1 from top-member slot SL2. If the command was successful then a **LocoSlotDataP2** response will be returned for the sub-member or mid-consist slot. An invalid link will return a **Ack** with a response code of 0x00.

Protocol: 2 Group: 6-Byte Message Opcode: OPC_D4_GROUP (unofficial mnemonic) Type: Command Encoding: Byte 0: Opcode. 1 1 0 1 0 1 0 0 0xD4Byte 1: $d2 \mid d1 \mid$ d0 $\langle SL1P \rangle$ Bits d2 to d0 contain the sub-1 1 1 member or mid-consist slot page number in the range 0x0 to 0x7. Byte 2: 0 n \mathbf{n} n \mathbf{n} n n $\langle SL1\# \rangle$ Sub-member or mid-consist slot number in the range 0x00 to 0x77. Byte 3: d0d2d1 $\langle SL2P \rangle$ Bits d2 to d0 contain the top-0 1 0 1 0 member slot page number in the

range 0x0 to 0x7.

Byte 4:

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<sl2#></sl2#>	Top-member slot number in the range $0x00$ to $0x77$.
Byte 5:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<chk></chk>	Checksum.
Response:		
LocoSlotDataP2 or Ack .		
Signature:		
Byte 0:		
1 1 0 1 0 1 0 0	0xD4	
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 2:		
$oxed{0 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	less than 0x78	}
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		
Byte 4:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	less than 0x78	}
Notes:		
None.		

Appendix A

Reference Tables

	MSD	0	1	2	3	4	5	6	7
LSD		000	001	010	011	100	101	110	111
0	0000	NUL	DLE	SPC	0	@	Р	(р
1	0000	SOH	DC1	!	1	A	Q	a	q
2	0010	STX	DC2	"	2	В	R	b	r
3	0011	ETX	DC3	#	3	С	S	С	\mathbf{s}
4	0100	EOT	DC4	\$	4	D	Τ	d	t
5	0101	ENG	NAK	%	5	Е	U	e	u
6	0110	ACK	SYN	&	6	F	V	f	v
7	0111	BEL	ETB	,	7	G	W	g	W
8	1000	BS	CAN	(8	Н	X	h	X
9	1001	HT	EM)	9	I	Y	i	У
A	1010	LF	SUB	*	:	J	Z	j	\mathbf{z}
В	1011	VT	ESC	+	;	K	[k	{
С	1100	FF	FS	,	<	L	\	1	
D	1101	CR	GS	-	=	M]	m	}
E	1110	SO	RS	•	>	N	^	n	~
F	1111	SI	US	/	?	О	-	О	DEL

TABLE A-1. ASCII Character Set (7-Bit Code)

Appendix B

Digitrax Loconet Products

Product	Description	Approx. Date	Product Code	IPL Bootloader Version
CT4	Quad Throttle	1993		
DB100	5 Amp DCC Booster with Auto Reversing	1993		
DB100+	5 Amp DCC Booster with Auto Reversing	1993		
DB100a	5 Amp DCC Booster with Auto Reversing	1994		
DB99	4.5 Amp DCC Booster	1994		
DT200	Command Station & Throttle	1994		
BT2	Buddy Throttle	1995		
UP1	Universal Panel, RJ12, 5 Pin Din & 1/4" Stereo Plug	1995		
UP2	Universal Panel	1995		
UP3	Universal Panel	1995		
UT1	Utility Throttle	1995		
DCS100	5 Amp DCC Command Station & Booster	1996		
DT100	Advanced Throttle	1996		
MS100	LocoNet PC Computer Interface - RS232	1996		
PR1	Computer Decoder Programmer - Serial	1996		
DB200+	8 Amp DCC Booster	1998		
DT100R	Advanced Radio Equipped Throttle	1998		
UT2	Utility Throttle	1998		
DB150	5 Amp DCC Command Station/Booster with intelligent Auto Reverse	1999		
BDL16	LocoNet Occupancy Detector, 16 Detection Sections	2000		

Product			Approx. Description Date		Product Code	IPL Bootloader Version	
DCS200	8 Amp DCC Command Station & Booster	2000					
DT300	Advanced LocoNet Throttle	2000					
DT300R	Radio Equipped Advanced Lo- coNet Throttle	2000					
PM4	Power Manager	2000					
BDL162	LocoNet Occupancy Detector, 16 Detection Sections	2002					
PM42	Quad Power Manager	2002					
SE8C	Signal Decoder	2003					
BDL168	LocoNet Occupancy Detector, 16 Detection Sections	2004					
DB200- OPTO	OPTO 8 Amp DCC Opto Booster	2006					
DCS50	All-in-one Command Station / Booster / Throttle	2006					
DT400	Super Walkaround / IR Throttle	2006					
DT400R	Super Radio Throttle	2006					
PR2	SoundFX Serial Port Decoder Programmer	2006					
UP5	LocoNet Universal Interconnect Panel	2006					
UR90	Infrared Receiver Front Panel	2006					
UR91	Simplex Radio Equipped / IR Receiver Panel	2006					
UR92	Infrared Receiver Panel	2006	0x5C	0			
UR93	Duplex Radio Transceiver / IR Receiver Panel	2006	0x5D	2			
UR93E	Duplex Radio Transceiver / IR Receiver Panel	2006					
UT4	Utility Throttle with 4 Digit Addressing and Infrared Capability	2006	0x04				
UT4R	Simplex Radio Equipped Utility Throttle with 4 Digit Addressing	2006					
DS54	Quad Stationary Decoder with Programmable LocoNet Inputs & Outputs	2006					
DS64	Quad Stationary Decoder	2006					
LNRP	Loconet Repeater Module	2007	0x01	1			
PR3	SoundFX USB Decoder Programmer	2008	0x23	1			
DT402	Super Throttle with Infrared Capability	2009	0x2A	1			

Product	Description	Approx.	Product Code	IPL Bootloader Version
DT402D	Duplex Radio Equipped Super Throttle	2009		
DT402R	Simplex Radio Equipped Super Throttle	2009		
UT4D	Duplex Radio Equipped Utility Throttle with 4 Digit Addressing	2009		
DCS51	All-in-one Command Station / Booster / Throttle	2010	0x33	1
DT402DCE	DCE Duplex Radio Equipped Super Throttle for Europe	2011		
UR92CE	Duplex Radio Transceiver / IR Receiver Panel for Europe	2011		
UT4DCE	Duplex Radio Equipped Utility Throttle with 4 Digit Address- ing for Europe	2011		
UP6Z	LocoNet Universal Interconnect Panel and 3 Amp Z Scale Voltage Reducer	2012		
LNRPXTRA	LocoNet Repeter Module	2013		
PR3XTRA	SoundFX USB Decoder Programmer	2013		
DCS210	5/8 Amp DCC Command Station & Booster	2016	0x1B	2
DCS240	5/8 Amp DCC Command Station & Booster	2016	0x1C	2
DT500	Advanced Super Throttle with Infrared Capability	2016	0x32	1
DT500D	Advanced Duplex Radio Equipped Super Throttle	2016		
DT500DCE	Advanced Duplex Radio Equipped Super Throttle CE (for Europe)	2016		
BXP88	LocoNet Occupancy Detector, 8 Detection Sections with Transponding & Power Management	2017	0x58	2
DB210	3/5/8 Amp Auto Reverseing DCC Booster	2017	0x15	2
DB210- OPTO	3/5/8 Amp Auto Reverseing DCC Booster that is Opto- Isolated for layouts with com- mon rail wiring	2017	0x14	2
DB220	Dual 3/5/8 Amp AutoReverseing DCC Booster	2017	0x16	2
LNWI	LocoNet WiFi Interface	2017	0x63	2

		Approx.	Product	IPL Bootloader
Product	Description	Date	Code	Version
PR4	SoundFX USB Decoder Programmer	2017	0x24	0
BXPA1	LocoNet DCC Auto-Reverser with Detection, Transponding and Power Management	2018	0x51	2
DCS52	All-in-one Command Station / Booster / Throttle	2019	0x34	2
DCS210+	DCC Command Station & Booster	2020	0x1A	
DT602	DT602 Advanced Super Throttle	2020	0x3E	2
DT602D	Advanced Duplex Super Throt- tle	2020		
DT602DE	Advanced Duplex Super Throt- tle CE (For Europe)	2020		
UT6	Utility Throttle	2020	0x06	2
UT6D	Duplex Radio Utility Throttle	2020		
UT6DE	Duplex Radio Utility Throttle CE (For Europe)	2020		
DS74	Quad Switch Stationary Decoder	2021		
DS78V	Eight Servo LocoNet Station- ary & Accessory decoder for turnout control	2021		

Appendix C

Command Station Option Switches

Switch #	Default	Effect on system operation
OpSw 01	t	do not change.
OpSw 02	t	t = command station mode
		c = booster only mode.
OpSw 03	t	t = command station's booster normal
		c = command station's booster is auto re-
		versing
OpSw 04	t	do not change
OpSw 05	t	do not change
OpSw 05	t	t = command station master mode off
		c = command station master mode off (rec-
		ommended)
OpSw 06	t	t = check for decoder before programming
		c = program without checking for device
OpSw 06	t	do not change
OpSw 07	t	do not change
OpSw 08	t	do not change
OpSw 09	c	do not change
	OpSw 01 OpSw 02 OpSw 03 OpSw 04 OpSw 05 OpSw 05 OpSw 06 OpSw 06 OpSw 07 OpSw 08	OpSw 01 t OpSw 02 t OpSw 03 t OpSw 04 t OpSw 05 t OpSw 05 t OpSw 06 t OpSw 06 t OpSw 07 t

Command]
Station	Switch #	Default	Effect on system operation
DCS210/DCS240/	Switch # OpSw 10		Effect on system operation do not change
, , ,	Opsw 10	c	do not change
DCS210+/			
DCS100/DCS200	O C 11	1	J
DCS210/DCS240/	OpSw 11	t	do not change
DCS210+/			
DCS100/DCS200	O C 10		
DCS210/DCS240/	OpSw 12	t	do not change
DCS210+/			
DCS100/DCS200	O C 19		1 1 11 200 1
DCS210/DCS240/	OpSw 13	t	t = loco address purge time 200 seconds
DCS210+/			c = loco address purge time 600 seconds
DCS100/DCS200	0.0.11		
DCS210/DCS240/	OpSw 14	t	t = loco address purging enabled
DCS210+/			c = loco address purging disabled
DCS100/DCS200			
DCS210/DCS240/	OpSw 15	t	t = purging will not change loco speed
DCS210+/			c = purging will force a loco to 0 speed
DCS100/DCS200			
DCS210/DCS240/	OpSw 16	t	do not change
DCS210+/			
DCS100/DCS200			
DCS210/DCS240/	OpSw 17	t	t = automatic advanced decode (FX) con-
			sists are enabled
DCS210+/			c = automatic advanced decode (FX) con-
			sists are disabled
DCS100/DCS200			
DCS210/DCS240/	OpSw 18	t	t = normal command station booster short
			circuit shutdown time
DCS210+/			c = extended command station booster
			short circuit shutdown time
DCS100/DCS200			
DCS210/DCS240/	OpSw 19	t	do not change
DCS100/DCS200			
DCS210+	OpSw 19		t = Ops mode feedback module not in-
			stalled
		c	c = Ops mode feedback module installed
DCS210/DCS240/	OpSw 20	t	t = enable address 0x00 or analog stretch-
			ing for conventional locos
DCS210+/			. $c = disable address 0x00 or analog$
			stretching for conventional locos
DCS100/DCS200			
DCS210/DCS240/	OpSw 21	c	SW21
DCS210+/			
DCS100/DCS200			
DCS210/DCS240/	OpSw 22	c	SW22
DCS210+/			
DCS100/DCS200			

Command			
Station	Switch #	Default	Effect on system operation
DCS210/DCS240/	OpSw 23	t	SW23
DCS210/DCS240/	Opsw 20		5 11 20
DCS100/DCS200			
DCS210/DCS240/	OpSw 24	t	do not change
DCS210/DCS240/	Opsw 24		do not change
DCS100/DCS200			
DCS100/DCS200	OpSw 25	t	t = enable route echo over Loconet
DCS210/DCS240/ DCS210+	Op5w 25	6	c = disable route echo over Loconet
DCS100/DCS200/	OpSw 25	t	t = enable aliasing
DC3100/DC3200/	Op5w 25	6	c = disable aliasing
DCS210/DCS240/	OpSw 26	c	t = disable routes
DCS210/DCS240/	Opsw 20		c = enable routes
DCS100/DCS200			c = enable routes
•	On S 27	+	t — anabla namnal awitah aammanda a lt a
DCS210/DCS240/	OpSw 27	t	t = enable normal switch commands, a.k.a. the "Bushby bit"
DCS210+/			c = disable normal switch commands, a.k.a.
DC5210+/			the "Bushby bit" (allows attached com-
DCC100/DCC200			puter to handle switch control logic)
DCS100/DCS200	On C 20	1	t anable interments commands at newer
DCS210/DCS240/	OpSw 28	t	t = enable interrogate commands at power
DCC910+/			on disable intermedate commands at nomen
DCS210+/			c = disable interrogate commands at power
DCC100/DCC200			on
DCS100/DCS200	On C 20	4	de not change
DCS210/DCS240/ DCS210+/	240/ OpSw 29 t do not change		do not change
DCS210+/ DCS100/DCS200			
DCS100/DCS200 DCS210/DCS240/	OpSw 30	t	do not change
DCS210/DCS240/ DCS210+/	Opsw su	l t	do not change
DCS100/DCS200			
*	On C 21	+	t - normal route/gwitch output rate when
DCS210/DCS240/	OpSw 31	t	t = normal route/switch output rate when
DCC210+/			not trinary c = fast route/switch output rate when not
DCS210+/			, -
DCS100/DCS200			trinary
DCS100/DCS200 DCS210/DCS240/	OpSw 32	t	do not change
DCS210/DCS240/ DCS210+/	Opow 32	l t	do not change
DCS210+/ DCS100/DCS200			
DCS100/DCS200 DCS210/DCS240/	OpSw 33	0	t = track power off at power on
' '	Opaw ss	С	
DCS210+/			c = allow track power to restore to prior state at power on
DCS100/DCS200			state at power on
DCS100/DCS200 DCS210/DCS240/	OpSw 34	t	t = disallow track to power up to run state,
DC3210/DC3240/	Opsw 34	l C	
DCC210 + /			if set to run prior to power up
DCS210+/			c = allow track to power up to run state, if
DCC100/DCC200			set to run prior to power up
DCS100/DCS200			

Command			
Station	Switch #	Default	Effect on system operation
DCS210/DCS240/	OpSw 35	t	t = enables loco reset button
DCS210+			c = disable loco reset button
DCS100/DCS200/	OpSw 35	t	do not change
DCS210/DCS240/	OpSw 36	t	c = clears all mobile decoder info and con-
			sists
DCS210+/			
DCS100/DCS200			
DCS210/DCS240/	OpSw 37	t	c = clears all routes
DCS210+/			
DCS100/DCS200			
DCS210/DCS210+	OpSw 38	t	t = loco reset button activates OpSw 39
			c = loco reset activates slot zero
DCS240	OpSw 38	t	do not change
DCS100/DCS200	OpSw 38	t	c = clear loco roster
DCS210/DCS240/	OpSw 39	t	c = clear all internal memory states, includ-
			ing OpSw 36 and 37
DCS100/DCS200			
DCS210+	OpSw 39	t	do not change
DCS210/DCS240/	OpSw 40	t	c = reset to factory defaults
DCS210+			
DCS100/DCS200	OpSw 40	t	do not change
DCS210/DCS240/	OpSw 41	t	t = diagnostic click disabled
DCS100/DCS200			c = diagnostic click when valid the Network
			commands incoming and routes being out-
			put
DCS210/DCS240/	OpSw 42	t	t = enable 2 short beeps when loco address
			purged
DCS100/DCS200			c = disable 2 short beeps when loco address
			purged
DCS210/DCS240/	OpSw 43	t	t = enable the Network update of command
			station's track status
DCS100/DCS200			c = disable the Network update of com-
			mand station's track status
DCS210	OpSw 44	t	do not change
DCS240	OpSw 44	t	t = maximum slots to 400
	OpSw 44		c = maximum slots to 120
DCS100/DCS200	OpSw 44	t	t = maximum slots to 22
	OpSw 44		c = maximum slots to 120
DCS210/DCS240/	OpSw 45	t	t = enable reply for switch state request
DCS100/DCS200			c = disable reply for switch state request
DCS210/DCS240/	OpSw 46	t	do not change
DCS100/DCS200			

Command				
Station	Switch #	Default	Effect on system operation	
DCS210/DCS240/	OpSw 47	t	t = normal program track setting	
DCS100/DCS200			c = program track is brake generator when	
			not programming. Braking is DCC set to	
			speed 0 (not emergency stop) for address 0,	
			light on, broadcast to all addresses.	
DCS210+	OpSw 49	t	t = disallow Idle state	
			c = allow Idle state	
DCS210+	OpSw 54	t	t = set speed to zero at power up	
			c = recall last speed at power up	
DCS210+/	OpSw 66	t	t = use advanced commands	
DCS240		c = do not use advanced comma		
DCS210+/	OpSw 70	t	t = enable command station probes	
DCS240			c = disable command station probes	
DCS210+	OpSw 71	t	t = enable command station disable	
			c = disable command station disable, just	
			defer	
DCS210+	OpSw 75	t	t = enable programming track precharge	
			c = disable programming track precharge	
DCS210+	OpSw 77	t	t = do not lockout legacy commands	
			c = after D5 commands lockout legacy	
			commands	
DCS210+	OpSw 78	t	t = do not send Ack on B0 switch command	
			c = send Ack on B0 switch command	

DCS240 Settings for SW21-SW23

<u>SW21</u>	$\underline{\mathrm{SW22}}$	$\underline{\mathrm{SW23}}$	Global system default type for new locos
t	\mathbf{t}	\mathbf{t}	28 step mode
t	\mathbf{t}	\mathbf{c}	28 step FX mode
t	\mathbf{c}	\mathbf{t}	14 step mode
t	\mathbf{c}	\mathbf{c}	reserved
c	\mathbf{t}	\mathbf{t}	Motorola Trinary
c	\mathbf{t}	\mathbf{c}	reserved
\mathbf{c}	\mathbf{c}	\mathbf{t}	128 step mode
c	\mathbf{c}	\mathbf{c}	128 step FX mode

Appendix D

List of Common Configuration Variables

The NMRA Standard "Configuration Variables For Digital Command Control" provides descriptions for Digital Decoder Configuration Variables (CVs). CVs allow the decoder to be customized for each locomotive, or other mobile or stationary devices. Unless otherwise specified, configuration Variables shall be stored in non-volatile memory and must not change when power is removed from the decoder over long extended periods of time. CVs defined by the NMRA are marked below as Mandatory, Recommended or Optional. CVs identified as Mandatory must be implemented in order to conform to the Standard, while those marked as Recommended are strongly encouraged but not mandatory, and those marked Optional are at the manufacturer's discretion. CVs marked as Read-Only indicates a CV whose value should be set by the manufacturer and which the user cannot modify. Many CVs are implementation specific and no uniform specification is required. Others must be implemented in a uniform fashion in order to achieve compatibility. A CV marked as Uniform Spec indicates a CV which requires implementation by manufacturers according to a common specification. CVs marked as Dynamic are used for Unsolicited Decoder Initiated Transmission.

CV	Name	Description	Range	<u>Default</u>
1	Primary Address	NMRA: Mandatory, Uniform Spec.	1 - 127	3
	v	Bits 0-6 contain an address with a value between 1 and 127. Bit seven must have a value of 0. If the value of CV1 is 0 then the decoder will go out of NMRA digital mode and convert to the alternate power source as defined by CV12. This setting will not affect the Digital Decoder's ability to respond to service mode packets. The default value for this CV is 3, if the decoder is not installed in a locomotive or other unit when shipped from the manufacturer.		
		ESU:		
		For Multiprotocol decoders: Range 1-255 for Motorola.		
2	Vstart	NMRA: Required	0 - 255	
		Vstart is used to define the voltage drive level used as the start voltage on the motor. The voltage drive levels shall correspond linearly to the voltage applied to the motor at speed step one, as a fraction of available rectified supply voltage. When the voltage drive level is equal to zero, there shall be zero voltage applied to the motor. When it is at maximum, 255, the full available rectified voltage shall be applied.		
3	Acceleration	NMRA: Required	0 - 255	
	Rate	Determines the decoder's acceleration rate. The formula for the acceleration rate shall be equal to (the contents of CV3 \times 0.896) / (number of speed steps in use). For example, if the contents of CV3 equals 2, then the acceleration is 0.064 sec/step for a decoder currently using 28 speed steps. If the content of this parameter equals 0 then there is no programmed momentum during acceleration.		
		ESU:		
		This value multiplied by 0.25 is the time from stop to maximum speed. For LokSound 5 DCC the unit is 0.896 seconds		
4	Deceleration	NMRA: Required	0 - 255	
	Rate	Determines a decoders braking rate, in the same fashion as ${\rm CV}3.$		
		ESU:		
		This value multiplied by 0.25 is the time from maximum speed to stop. For LokSound 5 DCC: The unit is 0.896 seconds.		
5	Vhigh	NMRA: Optional	0 - 255	
		Vhigh is used to specify the motor voltage drive levels at the maximum speed step. This value shall be specified as a fraction of available rectified supply voltage. When the contents of CV5 equals 255, the full available rectified voltage shall be applied. Values of 0 or 1 shall indicate that Vhigh is not used in the calculation of the speed table.		

CV	Name	Description	Range	<u>Default</u>
6	VMid	NMRA: Optional		
		Vmid specifies the voltage drive level at the middle speed step. Vmid is used to generate a performance curve in the decoder that translate speed step values into motor voltage drive levels and is specified as a fraction of available rectified supply voltage. Values of 0 or 1 shall indicate that Vmid is not used in the calculation of the speed table.		
		ESU:		
		Medium speed of the engine. Use only if 3-point speed table is enabled. For LokSound 5 DCC only.		
7	Manufacturer	NMRA: Mandatory, Read-Only		
	Version Number	This is reserved for the manufacturer to store information regarding the version of the decoder.		
		ESU: Internal software version of decoder	-	-
8	Manufacturer ID	NMRA: Mandatory, Read-Only, Uniform Spec.		
		${\rm CV8}$ shall contain the NMRA assigned id number of the manufacturer of this decoder.		
		ESU: Writing value 8 in this CV triggers a reset to factory default values	151	-
9	Total PWM Pe-	NMRA: Optional		
	riod	The value of CV9 sets the nominal PWM period at the decoder output and therefore the frequency is proportional to the reciprocal of the value. The recommend formula for PWM period should be: PWM period (uS) = $(131 + \text{MANTISSA} \times 4) \times 2 \text{ EXP}$, Where MANTISSA is in bits 0-4 bits of CV9 (low order) and EXP is bits 5-7 for CV9. If the value programmed into CV9 falls outside a decoder's capability, it is suggested (but not required) that the decoder "adjust" the value to the appropriate highest or lowest setting supported by the decoder.		
		ESU: Motor PWM frequency as a multiple of 1000 Hz.	10 - 50	40
10	EMF Feedback Cutout	NMRA: Optional Contains a value between 1 and 128 that indicates the speed step above which the back EMF motor control cuts off. When 14 or 28 speed steps are used the LSB's of the value are truncated appropriately.		
11	Packet time-out	NMRA: Required		
	Value	Contains the maximum time period that the decoder will maintain its speed without receiving a valid packet.		
12	Power Source	NMRA: Optional, Uniform Spec.		
	Conversion	Contains the identity of the alternate power source to which the decoder will be converted should CV1 contain zero. This is also the primary alternative power source selected should the decoder perform power source conversion. The currently assigned Power Source Conversion codes areas follows:		
		0b00000001 Analog Power Conversion 0b00000010 Radio 0b00000100 Zero-1 0b0001000 TRIX 0b00010000 CTC 16 / Railcommand 0b00100000 FMZ (Fleischmann)		

$\underline{\text{CV}}$	Name	Description	Range	Default
13	Alternate Mode Function Status	NMRA: Optional, Uniform Spec.		
		Indicates the status of each function (F1 through F8) when the unit is operating in alternate power mode, which cannot control the functions. If a function can be controlled, then the corresponding bit is ignored. A value of 0 indicates the function is off, while a value of 1 indicates the function is on. Bit 0 corresponds to F1, while Bit 7 corresponds to F8.		
		ESU: Status of functions F1 to F8 in analogue mode	0-255	1
14	Alternate Mode Function 2 Status	NMRA: Optional, Uniform Spec.		
		Indicates the status of each function (F9 through F12, & FL) when the unit is operating in alternate power mode, which cannot control the functions. If a function can be controlled, then the corresponding bit is ignored. A value of 0 indicates the function is off, while a value of 1 indicates the function is on. FL in the forward direction is controlled by bit 0, FL in the reverse direction is controlled by bit 1. Bit 2 corresponds to F9, while Bit 5 corresponds to F12.		
		ESU: Status of function F0, F9 to F12 in analogue mode	0-63	1
15 & 16	Decoder Lock	NMRA: Optional, Uniform Spec.		
		The Decoder Lock is used to change CVs in only one of several decoders with the same short address (CV1) or long address (CV17 and CV18) that are installed in the same locomotive. Assign a number to CV16 in each decoder (i.e. 1 to motor decoder, 2 to sound decoder, 3 or higher to other decoders) before the decoders are installed in the locomotive. To change a value in another CV of one of the installed decoders, first write the number 1 (motor), 2 (sound), or 3 or higher (other) into CV15, then send the new value to the CV to be changed. The decoders will compare CV15 to CV16 and, if the values are equal, the CV to be changed will be changed. If the values in CV15 and CV16 are different, the update will be ignored.		
17 & 18	Extended Address	NMRA: Optional, Uniform Spec.		
	uress	The Extended Address is the locomotives address when the decoder is set up for extended addressing (indicated by a value of 1 in bit 5 of CV29). CV17 contains the most significant bits of the two byte address and must have a value between 0b11000000 and 0b11100111, inclusive, in order for this two byte address to be valid. CV18 contains the least significant bits of the address and may contain any value.		
19	Consist Address	NMRA: Optional, Uniform Spec.		
		Contains a seven bit address in bit positions 0-6. Bit 7 indicates the relative direction of this unit within a consist, with a value of 0 indicating normal direction, and a value of 1 indicating a direction opposite the unit's normal direction. If the seven bit address in bits 0-6 is 0b00000000 the unit is not in a consist.		
		ESU: Additional address for consist operation. Value 0 or 128 means: consist address is disabled. $1-127$ consist address active, normal direction. $129-255$ consist address active reverse direction.	0-255	0

$\underline{\mathrm{CV}}$	Name	Description	Range	<u>Default</u>
21	Consist Address	NMRA: Optional, Uniform Spec.		
	Active for F1-F8	Defines for functions F1-F8 whether the function is controlled by the consist address. For each Bit a value of 1 indicates that the function will respond to instructions addressed to the consist address. A value of 0 indicates that the function will only respond to instructions addressed to the locomotive address. F1 is indicated by bit 0. F8 by bit 7.		
		ESU: Status of functions F1 to F8 in Consist mode. Meaning of the bits as in CV13	0-255	0
22	Consist Address Active for FL and F9-F12	NMRA: Optional, Uniform Spec.		
	13-112	Defines for function FL whether the function is controlled by the consist address. For each Bit a value of 1 indicates that the function will respond to instructions addressed to the consist address. A value of 0 indicates that the function will only respond to instructions addressed to the locomotive address. FL in the forward direction is indicated by bit 0, FL in the reverse direction is controlled by bit 1. Bit 2 corresponds to F9, while Bit 5 corresponds to F12.		
		ESU: Status of functions FL, F9 to F12 in Consist mode. Meaning of the bits as in CV14.	0-63	0
23	Acceleration Ad-	NMRA: Optional, Uniform Spec.		
	justment	This Configuration Variable contains additional acceleration rate information that is to be added to or subtracted from the base value contained in CV3 using the formula (the contents of CV23 \times .896) / (number of speed steps in use). This is a 7 bit value (bits 0-6) with bit 7 being reserved for a sign bit (0-add, 1-subtract). In case of overflow the maximum acceleration rate shall be used. In case of underflow no acceleration shall be used. The expected use is for changing momentum to simulate differing train lengths/loads, most often when operating in a consist.		
		ESU: Factor for adjusting Acceleration CV3. Values from 0 to 127 are added to CV3. If the values are to be subtracted, additionally set bit 7 (value 128). The unit is 0.896 seconds.	0 - 127	0
24	Deceleration Ad-	NMRA: Optional, Uniform Spec.		
	justment	This Configuration Variable contains additional braking rate information that is to be added to or subtracted from the base value contained in CV4 using the formula (the contents of CV24 \times .896) / (number of speed steps in use). This is a 7 bit value (bits 0-6) with bit 7 being reserved for a sign bit (0-add,1-subtract). In case of overflow the maximum deceleration rate shall be used. In case of underflow no deceleration shall be used. The expected use is for changing momentum to simulate differing train lengths/loads, most often when operating in a consist.		
		ESU: Factor for adjusting the deceleration CV4. Values from 0 to 127 are added to CV3. If the values are to be subtracted, additionally set bit 7 (value 128). The unit is 0.896 seconds.	0 - 127	0

$\underline{\text{CV}}$	$\underline{\text{Name}}$	Descrip	<u>otion</u>	Range	Default
25 Speed Table/Mid Range Cab Speed Step		NMRA	: Optional, Uniform Spec.		
	Биер	factory that th defines where In 14-s by two default speed r or 1 sh	e between 2 and 127 shall be used to indicate 1 of 126 preset speed tables. A value of 0b00000010 indicates to curve shall be linear. A value between 128 and 154 the 28-speed step position (1-26) which will define the mid range decoder speed value will be applied. peed mode the decoder will utilize this value divided If the value in this variable is outside the range, the mid cab speed of 14 (for 28 speed mode or 7 for 14 mode) shall be used as the mid speed value. Values of 0 all indicate that this CV is not used in the calculation speed table.		
27	Decoder Automatic Stopping Configuration	NMRA	: Optional, Uniform Spec.		
	Comiguration		o configure which actions will cause the decoder to atically stop.		
		$\underline{\mathrm{Bit}}$	Function		
		d7	Reserved		
		d6	Reserved		
		d5	Enable/Disable Auto Stop in the presence forward polarity DC. $0 = Disabled 1 = Enabled$		
		d4	Enable/Disable Auto Stop in the presence of reverse polarity DC. $0 = Disabled \ 1 = Enabled$		
		d3	Reserved		
		d2	Enable/Disable Auto Stop in the presence of an Signal Controlled Influence cutout signal. $0 = \text{Disabled}$ $1 = \text{Enabled}$		
		d1	Enable/Disable Auto Stop in the presence of an asymmetrical DCC signal which is more positive on the left rail. $0=$ Disabled $1=$ Enabled		

Enable/Disable Auto Stop in the presence of an asymmetrical DCC signal which is more positive on the right rail. 0 = Disabled 1 = Enabled

 $\underline{\mathrm{CV}}$ ${\bf Description}$ **Default** Name Range ESU: Allowed (enabled) Brake modes Bit Function Loco brakes with constant brake distance if Speed=0 $\,$ d7d6Selectrix brake diode, rakes if polarity is like driving direction d5Selectrix brake diode, brakes if polarity is against driving direction d4Brake on DC, if polarity like driving direction d3Brake on DC, if polarity against driving direction d2ZIMO® HLU brakes active d1ABC braking, voltage higher on the left hand side ABC braking, voltage higher on the right hand side Bi-Directional NMRA: Optional, Uniform Spec. 28 Communication ${\bf Configuration}$ Used to configure decoder's Bi-Directional communication characteristics when CV29-Bit 3 is set $\underline{\text{Bit}}$ <u>Function</u> d7Reserved d6Reserved d5Reserved d4Reserved d3Reserved d2Enable/Disable Initiated Broadcast Transmission using Signal Controlled Influence Signal. 0 = Disabled 1 = EnabledEnable/Disable Initiated Broadcast Transmission using Asymmetrical DCC Signal. 0 = Disabled 1 =Enabled d0Enable/Disable Unsolicited Decoder Initiated Transmission. 0 = Disabled 1 = EnabledESU: RailCom® Configuration 131 $\underline{\mathrm{Bit}}$ <u>Function</u> d7Enable/Disable RailCom® Plus automatic loco recognition. 0 = Disabled 1 = Enabledd1Enable/Disable Data transmission on Channel. 0 =Disabled 1 = Enabledd0Enable/Disable Channel 1 Address broadcast. 0 =

Disabled 1 = Enabled

$\underline{\text{CV}}$	Name		Descri	ption	Range	<u>Default</u>
29	Configurations Supported	-	NMRA	a: Mandatory, Uniform Spec.		
			$\underline{\mathrm{Bit}}$	Function		
				d7	Accessory Decoder: $0 = \text{Multifunction Decoder}, 1 = \text{Accessory Decoder}$ (see CV541 for a description of assignments for bits 0-6)	
			d6	Reserved		
			d5	0= one byte addressing, $1=$ two byte addressing (also known as extended addressing),		
			d4	Speed Table: $0 =$ speed table set by CV2, CV5, and CV6, $1 =$ Speed Table set by CV66 to CV95		
			d3	Bi-Directional Communications: $0 = \text{Bi-Directional}$ Communications disabled, $1 = \text{Bi-Directional}$ Communications enabled.		
			d2	Power Source Conversion: $0 = NMRA$ Digital Only, $1 = Power$ Source Conversion Enabled, See CV12 for more information.		
		d1	FL location: $0=$ bit 4 in Speed and Direction instructions control FL, $1=$ bit 4 in function group one instruction controls FL.			
			d0	Locomotive Direction: $0 = \text{normal}$, $1 = \text{reversed}$. This bit controls the locomotive's forward and backward direction in digital mode only. Directional sensitive functions, such as headlights (FL and FR), will also be reversed so that they line up with the locomotive's new forward direction.		
			This register contains important information, some of are only relevant for DCC operation.			
		$\underline{\mathrm{Bit}}$	Function			
			d5	0 = Short addresses (CV 1) in DCC mode 1 = Long addresses (CV 17 + 18) in DCC mode		
		d4	0 = Speed curve through CV 2, 5, 6 (LokSound 5 DCC ONLY). 1 = Speed curve through CV 67 - 94 (Multiprotocol)			
			d3	0 = Disable RailCom 1 = Enable RailCom	12	
			d2	0 = Disable analog operation $1 = $ Enable analog operation		
			d1	0 = 14 speed steps DCC 1 = 28 or 128 speed steps DCC		
			d0	0 = Normal direction of travel 1 = Reversed direction of travel		
30	Error tion	Informa-	NMRA	a: Optional, Uniform Spec.		
	01011		Config specifie	case where the decoder has an error condition this uration Variable shall contain the error condition as ed by the manufacturer. A value of 0 indicates that no las occurred		

error has occurred.

$\underline{\text{CV}}$	Name	Descrip	otion	Range	Default
31	Index High Byte	NMRA	: Optional, Uniform Spec.		
		when to contain may have clusive by the tains to contain	dexed Address is the address of the indexed CV page he decoder is set up for indexed CV operation. CV31 as the most significant bits of the two byte address and we any value between 0b00010000 and 0b11111111 in. Values of 0b00000000 thru 0b00001111 are reserved NMRA for future use. (4096 indexed pages) CV32 conhe least significant bits of the index address and may any value. This gives a total of 61,440 indexed pages, ith 256 bytes of CV data available to manufacturers.		
32	Index Low Byte	NMRA	: Optional, Uniform Spec.		
		See CV	731		
33-46	Output Locations 1-14 for Functions $FL(f)$, $FL(r)$, and $F1$ - $F12$	NMRA	.: Optional. Uniform Spec.		
		trol who custom mands cated i A valu control trol multip. 42 control default put 2,	ns a matrix indication of which function inputs contained Digital Decoder outputs. This allows the user to size which outputs are controlled by which input computed that Function FL(f) controls are indicated for CV33, FL (r) in CV34, F1 in CV35, to F12 in CV46. The of 1 in each bit location indicates that the function is that output. This allows a single function to conditiple outputs, or the same output to be controlled by the functions. CVs 33-37 control outputs 1-8. CVs 38-crol outputs 4-11 CVs 43-46 control outputs 7-14. The is is that FL (f) controls output 1, FL (r) controls output F1 controls output 3 to F12 controls output 14. The numbered output is in the LSB of the CV.		
47-64	Manufacturer Unique				
47	Protocol selection	ESU: V	Which protocols are active.	0 - 255	13
		$\underline{\mathrm{Bit}}$	Function		
		d3	Enable/Disable Selectrix® protocol (Not for LokSound 5 DCC). $0 = Disabled 1 = Enabled$		
		d2	Enable/Disable Motorola® protocol (Not for LokSound 5 DCC). $0 = Disabled 1 = Enabled$		
		d1	Enable/Disable M4 protocol (Not for LokSound 5 DCC). $0 = Disabled 1 = Enabled$		
		d0	Enable/Disable DCC protocol. $0 = Disabled 1 = Enabled$		

$\underline{\mathrm{CV}}$	Name	Description	$\underline{\text{Range}}$	Default
49	Extended Config-	ESU:	0-255	19
	uration #1	Bit Function		
		d7 Märklin® Consecutive addresses, "High"-Bit.		
		d6 Reserved		
		d5 Enable/Disable LGB® function button mode. $0 =$ Disabled $1 =$ Enabled		
		d4 Enable/Disable Automatic DCC speed step detection. $0 = Disabled 1 = Enabled$		
		d3 Märklin® Consecutive addresses, "low"-Bit		
		d2 Reserved		
		d1 Reserved		
		d 0 Enable/Disable Load control (Back-EMF). 0 = Disable d 1 = Enabled		
50	Analogue mode	Selection of allowed analogue modes	0 - 3	3
		Bit Function		
		d 2 Enable/Disable QSI Quantum Engineer DC Support. $0 = Disabled 1 = Enabled$		
		d 1 Enable/Disable DC Analogue mode. 0 = Disabled 1 = Enabled		
		d 0 Enable/Disable AC Analogue Mode. 0 = Disabled 1		
51	K Slow Cutoff	Inernal Speedstep, until K Slow is active	0 - 255	10
52	BEMF Param. K Slow "K" -	Portion of the PI-Controller valid for lower speed steps	0 - 255	10
53	Control Reference voltage	Defines the Back EMF voltage, which the motor should generate at maximum speed. The higher the efficiency of the motor, the higher this value may be set. If the engine does not reach maximum speed, reduce this parameter	0 - 255	130
54	Load control Parameter K	K–component of the internal PI-controller. Defines the effect of load control. The higher the value, the stronger the effect of Back EMF control.	0 - 255	50
55	Load control Parameter I	I–component of the internal PI-controller. Defines the momentum (inertia) of the motor. The higher the momentum of the motor (large flywheel or bigger motor), the lower this value has to be set.	0 - 255	100
56	BEMF Influence at VMin	0-100%. Defines the "Strengh" of the BEMF at minimum speed step	1 - 255	255
57	Steam chuff syn- chronisation #1	Defines the steam chuff synchronisation.	1 - 255	30
58	Steam chuff synchronisation #2	Defines the steam chuff synchronisation.	1 - 255	20
63	Sound volume "Master"	Master volume for all sounds.	0 - 192	128
64	Brake sound threshold "Brake On"	If the actual loco speed step is smaller than or equals the value indicated here, the brake sound is triggered.	0 - 255	60

$\underline{\text{CV}}$	Name	Description	Range	Default
65	Brake sound threshold "Brake Off"	If the actual loco speed step is smaller than the one indicated here (up to 255), the brake sound will be switched off again. Compare chapter 13.4.	0 - 255	7
66	Forward Trimm	Divided by 128 is the factor used to multiply the motor voltage when driving forward. The value 0 deactivates the trim.	0 - 255	128
67-94	Speed table	Defines motor voltage for speed steps. The values "in between" will be interpolated.	0 - 255	-
95	Reverse Trimm	Divided by 128 is the factor used to multiply the motor voltage when driving backwards. Value 0 deactivates the trim.	0 - 255	128
101	Shunting Mode Trimm	Divided by 128, this gives the factor by which the motor voltage is multiplied when the shunting gear is active. See section 10.1.2.	0 - 128	64
102	Brake Mode Exit Delay	Time as a multiple of 16 milliseconds that must pass before a detected braking distance is left again. See section 10.4.6.	0 - 255	12
103	Load adjustment "Optional Load"	Divided by 128, this gives the factor that changes CV3, CV4 and the sound when "Optional Load" is active. See section 10.7.	0 - 255	0
104	Load adjustment "Primary Load"	Divided by 128, this gives the factor that changes CV3, CV4 and the sound when "Primary Load" is active. See section 10.7.	0 - 255	255
105	User CV $\#1$	Free CV. Here you are able to save what ever you want.	0 - 255	0
106	User CV $\#2$	Free CV. Here you are able to save what ever you want.	0 - 255	0
111	Gearbox back- lash	Time as a multiple of $16~\mathrm{mS}$, for which the motor runs at minimum speed after reversing the direction to prevent gear box jerking.	0 - 255	0
112	Frequency for Flashing light effects	Flashing frequency for Strobe lighting effects. Multiple of 0.065536 seconds. See section 12.5.4.	0 - 255	20
113	Power Fail By- pass	The time that the decoder bridges via the PowerPack after an interruption of voltage. Unit: A multiple of 0.032768 sec. See section $6.12.2$.	0 - 255	32
116	Slow speed BEMF Sampling period	Frequency of BEMF measurement in 0.1 milliseconds at speed step 1 $$	50 - 200	50
117	Full speed BEMF Sampling period	Frequency of BEMF measurement in 0.1 milliseconds at speed step 255	50 - 200	150
118	Slow speed BEMF	Measurement gap length VMin Length of the BEMF measuring gap in 0.1 milliseconds at speed step 1 $$	10 - 20	150
119	Full speed BEMF	Measurement gap length Vmax Length of the BEMF measuring gap in 0.1 milliseconds at speed step 255	10 - 20	15
123	ABC Mode "Slow drive"	Speed which is valid in the slow driving section during ABC braking.	0	-

CV	Name	<u>Description</u>	Range	Default
124	Extended Config-	Additional important settings for decoders		21
	uration #2	Bit Function		
		d7 Reserved		
		d6 Enable/Disable Automatic parking Brake. $0 = Disabled$ abled $1 = Enabled$	S-	
		d5 Enable/Disable Motor is switched off for a few seconds when blocked to avoid burnout. 0 = Disabled = Enabled		
		d4 0 = Enable Output AUX9 (LokSound 5 H0 only). = Enable Wheel Sensor input (LokSound 5 H0 only)		
		d3 Enable/Disable SUSI protocol. $0 = Disabled 1 = Enabled$	1-	
		d2 Enable/Disable prime mover startup delay. $0 = Disabled$ abled $1 = Enabled$	s-	
		d0 Enable/Disable Decoder lock with CV 15 / 16. 0 : Disabled $1 = \text{Enabled}$	=	
125	Start voltage	Analog DC See section 10.8.	0 - 255	90
126	Maximum speed	Analog DC See section 10.8.	0 - 255	130
127	Start voltage	Analog AC See section 10.8.	0 - 255	90
128	Maximum speed	Analog AC See section 10.8.	0 - 255	130
129	Analog Functions	"Hysterese" Offset voltage for functions in analogue mode Chapter 10.8.	e. 0 - 255	15
130	Analog Motor	"Hysterese" Offset voltage for motor functions in analogu mode. Chapter 10.8.	ie 0 - 255	5
132	Grade Crossing Hold Time	Grade Crossing holding time. See chapter 12.5.3.	0 - 255	80
133	Sound Fader	Volume when sound fader is active. See chapter 13.5.	0 - 255	128
134	ABC-Mode "Sensibility"	Threshold, from which asymmentry on ABC shall be recognised.	g- 4 - 32	10
138	Smoke Unit Trim Fan	Divided by 128, this gives the factor by which the fan spee of synchronized smoke units can be adjusted.	ed 0 - 255	128
139	Smoke Unit Trim Temperature	Divided by 128, this gives the factor by which the temperature of synchronized smoke units can be adjusted.	re 0 - 255	128
140	Smoke TimeOut	Time until automatic shutdown of the smoke unit.	0 - 255	255
141	Smoke Chuff Min	Minimum duration of a steam chuff of an external smoke un in 0.041 seconds resolution.	it 0 - 255	10
142	Smoke Chuff max	Maximum duration of a steam chuff of an external smoke un in 0.041 seconds resolution.	it 0 - 255	125
143	Smoke Chuff Length	Divided by 128, this gives the factor by which the duration of the steam chuffs can be adjusted relative to the trigger pulse		100
144	Smoke Pre Heat Temperature	Preheating temperature in degrees Celsius for secondar smoke generators (cylinder smoke unit)	ry 0 - 255	150

$\underline{\text{CV}}$	Name	Description	Range	<u>Default</u>
149	ABC Shuttle Train Holdtimet	Time in seconds, which has to be passed for ABC shuttle train operation, before the direction of travel is changed. See section $10.4.4.3$.	0 - 255	255
150	HLU Speedlimit 1	HLU Speed limit 1. Internal speedstep.	0 - 255	42
151	HLU Speedlimit 2	(U) HLU Speed limit 2 (U). Internal speedstep.	0 - 255	85
152	HLU Speedlimit 3	HLU Speed limit 3. Internal speedstep.	0 - 255	127
153	HLU Speedlimit 4	(L) HLU Speed limit 4 (L). Internal speedstep.	0 - 255	170
154	HLU Speedlimit 5	HLU Speed limit 5. Internal speedstep.	0 - 255	212
155 -170	Sound CV 1 - Sound CV 16	16 CVs for selecting sounds that can be assigned within sound projects. Please note the documentation for the sound project.	0 - 255	0
179	Brake Function 1	Deceleration Value of which 33% of CV 4 will be deducted if the Brake Function 1 is active. See section 10.6.	0 - 255	80
180	Brake Function 2	Deceleration Value of which 33% of CV 4 will be deducted if the Brake Function 2 is active. See section 10.6.	0 - 255	40
181	Brake Function 3	Deceleration Value of which 33% of CV 4 will be deducted if the Brake Function 3 is active. See section 10.6.	0 - 255	40
182	Brake Function 1 max.	Speed Highest speed step that can be reached when Brake function 1 is active.	0 - 126	0
183	Brake Function 2 max.	Speed Highest speed step that can be reached when Brake function 1 is active.	0 - 126	126
184	Brake Function 3 max.	Speed Highest speed step that can be reached when Brake function 1 is active.	0 - 126	126
246	Automatic decoupling Driving speed	Speed of the loco while decoupling; the higher the value, the faster the loco. Value 0 switches the automatic coupler off. Automatic decoupling is only active if the function output is adjusted to "pulse" or "coupler".	0 - 255	0
247	Decoupling - Removing time	This value multiplied with 0.016 defines the time the loco needs for moving away from the train (automatic decoupling).	0 - 255	0
248	Decoupling - Pushing time -	This value multiplied with 0.016 defines the time the loco needs for pushing against the train (automatic decoupling).	0 - 255	0
249	Minimum steam chuff distance	Minimum distance of two steam chuffs, independant from sensor data. Compage chapter 13.3 .	0 - 255	0
250	Secondary steam chuff trigger	Defines the distance between two consecutive steam chuffs for the secondary steam chuff generator. The value indicates the promilles the steam chuff distances of the secondary steam chuff generator ought to be shorter then those of the primary steam chuff generator. It is needed.for steam locos with two independent boogies, such as "Big Boy" or "Mallet".	0 - 255	0

$\underline{\text{CV}}$	Name	Description	$\underline{\text{Range}}$	$\underline{\mathrm{Default}}$
253	Constant brake mode	Determines the constant brake mode. Only active, if CV254 > 0 Function CV 253 = 0: Decoder stops linearly CV 253 > 0 : Decoder stops constantly linear	0 – 255	0
254	Constant braking distance forward	A value >0 determines the way of brake distance it adheres to, independent from speed.	0 - 255	0
255	Constant braking distance	Constant braking distances during reverse driving. Only active, if value > 0, otherwise the value of CV 254 is used. Useful for reversible trains.	0 - 255	0

Appendix E

Revision History

Release Date	Changes
20 Feb 2022	LocoSlotDataP1 and LocoSlotDataP2 updated for Advanced Consists.
	Definitions for the three types of consist added to Glossary.
	Definitions for three types of addresses added to Glossary.
	Corrections to Slot Status 1 descriptions.
	SetCfgSlotDataP1 and SetCfgSlotDataP2 added.
	Consist descriptions added to Locomotive Control chapter.
	Placeholder extra chapters added and sequencing altered.
13 Feb 2022	Appendix B renamed Appendix D.
	Appendix B - Digitrax Loconet Products added.
	Appendix C - Command Station Option Switches added.
	GetCfgSlotDataP2 and CfgSlotDataP2 added.
	CfgSlotDataP1 updated for additional option switches and Product Code re-
	moved.
	ThrottleID removed from protocol 1 description.
	SwState renamed getSwState.
	SwState added.
	Additional definitions added to the Glossary.
30 Jan 2022	Appendix B - Revision History added.
	Index added.
	Glossary added.
	GetLocoSlotDataP1 and P2 updated for SlotNotImplemented response
	The following messages were updated for the Throttle ID field:
	LocoF0F6P2
	LocoF7F13P2
	LocoF14F20P2
	LocoF21F28P2
	LocoSpdDirP2
	LocoSlotDataP2 function mapping corrected
	NoFreeSlotsP1 and NoFreeSlotsP2 added and references to them added.
	IllegalMoveP1 and IllegalMoveP2 added and references to them added.
	SlotNotImplemented added.
23 Jan 2022	Baseline.

Glossary

address is the numeric identification code by which a decoder recognises commands directed specifically to it. 6, 63, 65, 71, 73

Advanced Consist Bit means bit d2 of the Slot Status 1 byte. 10, 138, 142, 199, 203

Advanced Consist is where the consist information is stored in CV19 in the mobile decoder. 8, 10, 138, 142, 199, 203

Basic Consist is where all locomotive mobile decoders in the consist have the same address. 8

Broadcast means a message sent by a device to all devices on the network. 37, 41

Command means a message sent to a device to request it to do something. 6, 7, 10, 37, 39, 42, 47, 54, 56, 58, 60, 62, 63, 65, 66, 67, 69, 71, 73, 74, 75, 81, 84, 88, 91, 95, 99, 102, 106, 109, 111, 113, 115, 117, 119, 121, 123, 125, 127, 129, 131, 133, 135, 147, 149, 150, 151, 157, 159, 161, 165, 173, 174, 183, 185, 186, 191, 198, 202, 208, 210, 212, 221, 225, 227

command station is the electronic device that generates DCC commands based upon inputs it receives and transmits them to decoders. 1

Common is a locomotive slot state that indicates that the slot is not currently in-use by a throttle but it is still being refreshed by the command station. A slot with a state of Common can be selected by any throttle on the network. 8

Consist Address is a consist address between 1 and 127. This is stored in CV19. 8, 9, 10, 138, 142, 199, 203

DCC stands for Digital Command Control. 1, 81

Direct Mode . 165, 169

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expanded slots means the command station slots that are accessed and manipulated by protocol 2 messages. 3

- Extended Address is a decoder address between 128 and 9983. This is stored in encoded form in CV17 and CV18. 8, 139, 142, 200, 203
- Free is a locomotive slot state that indicates that the slot does not have an address loaded in it. 5, 65, 71, 73, 153, 155
- Global System Track Status means the byte 7 of a LocoSlotDataP1 or LocoSlotDataP2 response. 3, 44, 139, 170, 188, 200
- Idle is a locomotive slot state that indicates that slot is not active and can be selected any throttle. The locomotive's decoder is not refreshed in this state. 185
- In-Use is a locomotive slot state that indicates that the slot has been made active by a throttle and can no longer be selected by another throttle. 77, 79, 157, 159
- **locomotive slot** is a memory location in the command station which holds information about a locomotive's decoder and current state. 3
- **Loconet** is the peer-to-peer local area network system architecture used by Digitrax to carry DCC and other commands across Digitrax command control systems. 1
- message means a sequence of two or more bytes sent over the network that conform to the network message format. The first byte of the message is an opcode and the last is a checksum. 2
- mobile decoder means an electronic device installed in a locomotive that receives a signal from the command station through the track, decodes it and tells the locomotive what to do. 1
- NMRA is the National Model Railroad Association, founded in 1935. One of its purposes is to define and manage model railroad standards related to interchange of equipment in North America. 63, 65, 71, 73
- **opcode** means the first byte of a network message. The opcode indicates the purpose and length of the message. 2

Operations Mode . 165, 166, 169, 170

Paged Mode . 165, 169

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peer-to-peer is a network communication scheme where messages between devices are not managed or controlled by a central controller or server. 1

- Physical Register Mode . 165, 169
- **physical throttle** means an electronic input device, often hand-held, that is used to tell the command station what commands to send to the decoders. 5
- **polled** is the process of interrogating a device to see if it has information or commands to send to the system. 1
- **polling** is the process by which devices are interrogated sequentially, one after the other, to see if they have information or commands to send to the system. 1
- **Primary Address** is a decoder address between 1 and 127. This is stored in CV1. 8, 10, 138, 142, 199, 203
- Product Code means the Digitrax assigned identifier code of a device's type. 85, 91
- **Programming Track** is an isolated track section used for programming decoder equipped locomotives or transponder equipped rolling stock. 165, 169
- **Report** means a message sent by a device in response to a change in its internal and/or external state. 37, 177, 179, 181, 223
- **Response** means a message sent in response to a Command message. 6, 7, 37, 39, 42, 47, 58, 60, 62, 63, 65, 67, 69, 71, 73, 75, 77, 79, 84, 91, 95, 109, 111, 153, 155, 169, 214, 221
- **signature** is the combination of bits and bytes within a message that uniquely identify the message type. 2
- Slot Status 1 means byte 3 of a LocoSlotDataP1 response or byte 4 of a LocoSlotDataP2 response. 5, 10, 137, 141, 198, 202, 208
- slot state means the current state of a locomotive slot. A locomotive slot can be in one the following states: Free, New, In-Use, Common or Idle. 5
- software throttle means a software application that is used to tell the command station what commands to send to the decoders. 5
- **standard slots** means the command station slots that are accessed and manipulated by protocol 1 messages. 3
- stationary decoder means an electronic device for a turnout or other accessory that receives a signal from the command station through the track, decodes it and tells the turnout or accessory what to do. 1

system slot is a memory location in the command station which holds system information. 3

throttle means a physical throttle or a software throttle. 5

Throttle ID means a pair of 7-bit numbers that identify (hopefully uniquely) the throttle to the command station. 5, 7, 119, 123, 131, 135, 151

Universal Consist is where the consist information is stored in the command station. 8, 137, 141, 198, 202

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