Network Notes

January 30, 2022

Contents

| 1 | Net | work F | Protocol 1 | |
|---|-----|---------|-----------------------|--|
| | 1.1 | Overvi | ew | |
| | 1.2 | Messas | ge Format | |
| | 1.3 | Slots . | | |
| | | 1.3.1 | Slot State | |
| | | 1.3.2 | Throttle ID | |
| | | 1.3.3 | Protocol 1 | |
| | | 1.3.4 | Protocol 2 | |
| | | 1.3.5 | Purging | |
| | 1.4 | Messag | $_{ m ges}$ | |
| | | 1.4.1 | Ack | |
| | | 1.4.2 | Busy | |
| | | 1.4.3 | CfgSlotDataP1 | |
| | | 1.4.4 | ConsistDirF0F4 | |
| | | 1.4.5 | GetBrdOpSw | |
| | | 1.4.6 | GetCfgSlotDataP1 | |
| | | 1.4.7 | GetInterfaceData | |
| | | 1.4.8 | GetLocoSlotDataLAdrP1 | |
| | | 1.4.9 | GetLocoSlotDataLAdrP2 | |
| | | 1.4.10 | GetLocoSlotDataP1 | |
| | | 1.4.11 | GetLocoSlotDataP2 | |
| | | 1.4.12 | GetLocoSlotDataSAdrP1 | |
| | | 1.4.13 | GetLocoSlotDataSAdrP2 | |
| | | 1.4.14 | IllegalMoveP1 | |
| | | 1.4.15 | IllegalMoveP2 | |
| | | 1.4.16 | IMMPacket | |
| | | 1.4.17 | InterfaceData | |
| | | 1.4.18 | IPLDataLoad | |
| | | 1.4.19 | IPLDevData | |

iv CONTENTS

| 1.4.20 | IPLDiscover | 55 |
|--------|-------------------|-----|
| 1.4.21 | IPLEndLoad | 59 |
| 1.4.22 | IPLSetAddr | 62 |
| 1.4.23 | IPLSetupBL2 | 65 |
| 1.4.24 | LinkSlotsP1 | 68 |
| 1.4.25 | LinkSlotsP2 | 70 |
| 1.4.26 | LocoBinStateP2 | 72 |
| 1.4.27 | LocoDirF0F4P1 | 74 |
| 1.4.28 | LocoDirF0F4P2 | 76 |
| 1.4.29 | LocoF0F6P2 | 78 |
| | LocoF5F8P1 | 80 |
| | LocoF7F13P2 | 82 |
| | LocoF5F11P2 | 84 |
| 1.4.33 | LocoF12F20F28P2 | 86 |
| 1.4.34 | LocoF13F19P2 | 88 |
| 1.4.35 | LocoF14F20P2 | 90 |
| 1.4.36 | LocoF21F27P2 | 92 |
| 1.4.37 | LocoF21F28P2 | 94 |
| 1.4.38 | LocoSlotDataP1 | 96 |
| | | 100 |
| 1.4.40 | LocoSpdP1 | 106 |
| 1.4.41 | | 108 |
| 1.4.42 | LocoSpdDirP2 | 110 |
| 1.4.43 | NoFreeSlotsP1 | 112 |
| 1.4.44 | NoFreeSlotsP2 | 114 |
| 1.4.45 | MoveSlotsP1 | 116 |
| 1.4.46 | MoveSlotsP2 | 118 |
| 1.4.47 | PeerXfer16 | 120 |
| 1.4.48 | ProgCV | 124 |
| 1.4.49 | ProgSlotDataP1 | 128 |
| 1.4.50 | PwrOff | 132 |
| 1.4.51 | PwrOn | 133 |
| 1.4.52 | Reset | 135 |
| 1.4.53 | SensRepGenIn | 136 |
| 1.4.54 | SensRepTurnIn | 138 |
| 1.4.55 | SensRepTurnOut | 140 |
| 1.4.56 | SetBrdOpSw | 142 |
| 1.4.57 | • | 144 |
| 1.4.58 | SetLocoSlotDataP1 | 145 |
| 1.4.59 | SetLocoSlotDataP2 | 149 |
| 1.4.60 | | 155 |

CONTENTS

| В | Rev | ision 1 | History | 215 |
|---|-------------|---------|---------------------------------|------------------|
| A | Ref | erence | Tables | 213 |
| | 4.3 | List of | f Common CVs | . 198 |
| | 4.2 | | amming Mobile Decoder Addresses | |
| | | 4.1.4 | Operations Mode Programming | |
| | | 4.1.3 | Direct Mode Programming | |
| | | 4.1.2 | Physical Register Programming | |
| | 7.1 | 4.1.1 | Paged Mode Programming | |
| * | 4.1 | _ | uction | |
| 4 | Cor | nfigura | tion Variables | 189 |
| | | 3.4.4 | End of File Record | . 186 |
| | | 3.4.3 | Data Records | . 185 |
| | | 3.4.2 | Parameter Records | |
| | J. 1 | 3.4.1 | Sync Records | |
| | 3.4 | | File Format | |
| | 3.3 | | vare Parameters | |
| | 3.2 | | pader Protocol 2 | |
| 3 | Up o | _ | Firmware oader Protocol 1 | 181 . 181 |
| | 2.1 2.2 | | ary | |
| 2 | | t Clock | c ary | 177 177 |
| | | | | |
| | | | UnlinkSlotsP1 | |
| | | | TransRep | |
| | | | SwState | |
| | | | SwReq | |
| | | 1.4.63 | | |
| | | 1.4.62 | SlotNotImplemented | . 159 |
| | | 1.4.61 | SetSwWithAck | . 157 |
| | | | | |

vi CONTENTS

Chapter 1

The Network Protocol

.

1.1 Overview

Loconet is a peer-to-peer distributed network system on which all devices can monitor the data flow. The network is event driven and is not polled by a centralised controller in normal operation. The normal network state is quiet, with no data traffic unless a device has information to send.

The network data is sent in asynchronous format using 1 start bit, 8 data bits and 1 stop bit. The 8 bit data is transmitted least significant bit first. The bit times are 60.0 μ S or 16,660 baud +/- 1.5%. A computer can connect to a USB interface at higher baud rates and the device will make the necessary conversion. Bytes may be transmitted back-to-back, with a start bit immediately following the stop bit of the previous character.

All the network communications are via multi-byte messages. The command station is the device that maintains the refresh stack for DCC packet generation and generates the DCC track data. Refresh of information is typically only performed for a mobile decoder. A stationary decoder is not refreshed and individual immediate commands are sent out to the track as requested.

The command station is only privileged in respect to performing the task of maintaining the locomotive refresh stack and generating DCC packets. In this way other network transactions may occur that the command station does not need to be involved with or understand, as long as they follow the message protocol and timing requirements. i.e. other devices may have a dialog on the network without disturbing or involving the command station. Devices on the network monitor the messages, check for format and data integrity and parse good messages to decode if action is required in the context. Devices such as throttles, input sensors, computer interfaces and control panels may generate the network messages without needing prompting or polling by a central controller.

Devices frequently will be added and removed from an operating the network. The devices and protocol are tolerant of electrical and data transients. The format chosen gives a good degree of data integrity, guaranteed quick network-state synchronisation, high data throughput, good distribution of access to many competing devices and low event latency.

1.2 Message Format

The data bytes on the network are defined as 8 bit data with the most significant bit as an opcode flag bit. If the most significant bit is 1, then the 7 least significant bits are interpreted as an opcode. The opcode may only occur once in a valid message and it is the first byte of a message. The opcode does not necessarily uniquely identify a message type. Sometimes the opcode must be used in combination with other bits or bytes in the message to determine the message lsignature. All the remaining bytes in the message must have a most significant bit of 0, including the last checksum byte. The checksum is the 1's complement of the byte wise exclusive or of all the bytes in the message, except the checksum itself. To validate data accuracy, all the bytes in a correctly formatted message are exclusive or'ed. If this resulting byte value is 0xFF, then the message data is accepted as good. Any message that has format or framing errors, data errors or is a fragment caused by noise glitches and does not completely follow the message format will be ignored by all receivers, and a new opcode will be scanned for re-synchronisation.

The opcodes may be examined to determine message length and if subsequent response message is required. Data bits d6 and d5 encode the message length. The message length includes the opcode and the checksum bytes. When bit d3 equals 1 a follow-on message or reply is expected. For variable byte messages the byte following the opcode in the message is a 7 bit byte count.

| d7 | $\underline{d6}$ | $\underline{\mathrm{d}5}$ | $\underline{d4}$ | $\underline{\mathrm{d}3}$ | $\underline{d2}$ | $\underline{d1}$ | $\underline{d0}$ | |
|----|------------------|---------------------------|------------------|---------------------------|------------------|------------------|------------------|--------------------------|
| 1 | 0 | 0 | \mathbf{E} | D | \mathbf{C} | В | A | 2 byte message |
| 1 | 0 | 1 | \mathbf{E} | D | \mathbf{C} | В | A | 4 byte message |
| 1 | 1 | 0 | \mathbf{E} | D | \mathbf{C} | В | A | 6 byte message |
| 1 | 1 | 1 | \mathbf{E} | D | \mathbf{C} | В | \mathbf{A} | Variable length message. |

The A,B,C,D,E are bits available to encode 32 opcodes per message length.

1.3. SLOTS 3

1.3 Slots

The command station contains an array of read/write slots. There are two classes of slots (locomotive slot and system slot) and two protocols for manipulating the slots. Protocol 1 allows up to 120 locomotive slots and each slot contains 10 bytes of data relating to the locomotive. Protocol 2 allows up to 960 locomotive slots and each slot contains 15 bytes of data relating to the locomotive. Not all command stations implement both protocols. A command station may also not implement the maximum number of locomotive slots for the protocols it supports. Where a command station implements both protocols do not mix protocols - choose one or the other. The user should check the Global System Track Status bits in a LocoSlotDataP1 or LocoSlotDataP2 response to determine which protocols are supported. In this document message mnemonics that are suffixed "P1" belong to protocol 1 and those suffixed "P2" belong to protocol 2. Protocol 1 uses a single 7 bit number to identify a slot. Protocol 2 uses a 3 bit number to identify the page or bank of slots and a 7 bit number to identify the slot within the page or bank. In both protocols slots numbered 0 to 119 (0x00 to 0x77) are locomotive slots and those numbered 120 to 127 (0x78 to 0x7F) are system slots. The slot number is similar to a file handle. System slots are encoded differently from the locomotive slots.

| System Slot# | Description |
|---------------------------------|------------------------|
| $\overline{123 \text{ (0x7B)}}$ | Fast Clock |
| 124 (0x7C) | Programming Interface |
| 127 (0x7F) | Option Switch Settings |

Initially all locomotive slots are empty and are said to be Free. A Free slot does not have a locomotive address loaded and no DCC commands are generated by the command station for it. To control a locomotive a throttle must request a slot from the command station and take ownership of it.

1.3.1 Slot State

A locomotive slot's slot state is determined by bits d5 and d4 of the Slot Status 1 byte of the applicable **LocoSlotData1** or **LocoSlotDataP2** response and whether the locomotive's address has been loaded. The slot state determines whether DCC commands are generated for it and if throttles can take control of it.

| Slot State | $\underline{\mathrm{d5}}$ | $\underline{d4}$ | Address Loaded | Decoder Refreshed | Any Throttle |
|------------|---------------------------|------------------|----------------|-------------------|--------------|
| Free | 0 | 0 | No | No | Yes |
| New | 0 | 0 | Yes | No | Yes |
| Common | 0 | 1 | Yes | Yes | Yes |
| Idle | 1 | 0 | Yes | No | Yes |
| In-Use | 1 | 1 | Yes | Yes | No |

1.3.2 Throttle ID

The Throttle ID for a physical throttle is derived from the throttle's serial number. Digitrax serial numbers are 16-bit numbers. The Throttle ID is split into two parts consisting of the least significant bits of the low and high bytes of the serial number respectively. For example a physical throttle with the serial number of 0xFFFE would have a Throttle ID of 0x7E 0x7F with 0x7E being the low byte. The low byte of the Throttle ID is required by some of the protocol 2 commands to ensure that only the throttle that has ownership of the locomotive slot is the one that updates the slot. A software throttle should choose a Throttle ID that does not clash with that of a physical throttle.

1.3.3 Protocol 1

- 1. The throttle requests a slot for the locomotive address by sending either a **Get-LocoSlotDataSAdrP1** or **GetLocoSlotDataLAdrP1** request to the command station. Which one depends on what type of address the locomotive's decoder is programmed to use.
- 2. If a slot has been previously loaded with the locomotive's address, then the command station will return a **LocoSlotDataP1** response.
- 3. If the locomotive's address is not currently in a slot, then the command station will load the new locomotive address into a Free slot, with speed equal to zero, direction forwards, functions off and 128 step mode, and return a **LocoSlotDataP1** response.
- 4. If there are no Free slots to load the new locomotive address into, the command station with return a **NoFreeSlotsP1** response and this procedure is terminated.
- 5. The throttle must then examine the slot data bytes to work out how to process the command station response.
- 6. If the slot state is New, Common or Idle then the throttle requests a "null move" operation by sending the command station a **MoveSlotsP1** request. The command station returns a **LocoSlotDataP1** response.

1.3. SLOTS 5

7. If the slot state is In-Use and the slot's Throttle ID does not match that of the throttle then the throttle should ask the user if they wish to "steal" the slot. If the answer is no then this procedure is terminated.

- 8. The throttle now takes ownership of the slot by updating the slot's Throttle ID to that of the throttle and writing the updated slot data to the command station by sending a **SetLocoSlotDataP1** request. If the request is successful then the command station will return a **setSlotDataOKP1** response.
- 9. The throttle will then be able to update speed, direction and function information. Whenever slot information is changed in an active slot, the slot is flagged to be updated as the next DCC packet sent to the track.

1.3.4 Protocol 2

- 1. The throttle requests a slot for the locomotive address by sending either a **Get-LocoSlotDataSAdrP2** or **GetLocoSlotDataLAdrP2** request to the command station. Which one depends on what type of address the locomotive's decoder is programmed to use.
- 2. If a slot has been previously loaded with the locomotive's address, then the command station will return a **LocoSlotDataP2** response.
- 3. If the locomotive's address is not currently in a slot, then the command station will load the new locomotive address into a Free slot, with speed equal to zero, direction forwards, functions off and 128 step mode, and return a **LocoSlotDataP2** response.
- 4. If there are no Free slots to load the new locomotive address into, the command station with return a **NoFreeSlotsP2** response and this procedure is terminated.
- 5. The throttle must then examine the slot data bytes to work out how to process the command station response.
- 6. If the slot state is New, Common or Idle then the throttle requests a "null move" operation by sending the command station a **MoveSlotsP2** request. The command station returns a **LocoSlotDataP2** response.
- 7. If the slot state is In-Use and the slot's Throttle ID does not match that of the throttle then the throttle should ask the user if they wish to "steal" the slot. If the answer is no then this procedure is terminated.
- 8. The throttle now takes ownership of the slot by updating the slot's Throttle ID to that of the throttle and writing the updated slot data to the command station

by sending a **SetLocoSlotDataP2** request. If the request is successful then the command station will return a **setSlotDataOKP2** response.

9. The throttle will then be able to update speed, direction and function information. Whenever slot information is changed in an active slot, the slot is flagged to be updated as the next DCC packet sent to the track.

Example:

1.3.5 Purging

0xb4 0x6e 0x7f 0x5a

If a device disconnects from the network and so does not access or reference a slot within the system purge time, the command station will force the un-accessed slot to Common status so other system devices can use the slot. The typical purge time of a command station is about 200 seconds. A good "ping" or slot update activity is about every 100 seconds, i.e. if a user makes no change to a throttle/slot within 100 seconds, the throttle/device should automatically send another speed update at the current speed to reset the purge timeout for that slot.

1.4 Messages

The following information is provided for each of the messages:

Description:

Description of the message's function.

Protocol:

Which protocol the message belongs to. Only messages that relate to refresh slots belong to a protocol.

Group:

Which message size group the message belongs to.

Opcode:

The opcode mnemonic. This is the Digitrax assigned mnemonic when known.

Type:

The message type - broadcast, command, response, or message.

Encoding:

How the message is encoded byte by byte.

Response:

The response expected from a command message, if applicable.

Signature:

The bits and bytes that must be tested to determine the message's unique type.

Notes:

Any notes.

1.4.1 Ack

| Description: | | |
|--|----------------|--|
| This message provides a response code message type. | e from a comma | and. This is the generic form of this |
| Group: | | |
| 4-Byte Message | | |
| Opcode: | | |
| OPC_LONG_ACK | | |
| Type: | | |
| Response | | |
| Encoding: | | |
| Byte 0: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xB4 | Opcode. |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <lopc></lopc> | Opcode of the command that this message is a response to with the most significant bit set to 0. |
| Byte 2: | | |
| | <ack1></ack1> | Response code. |
| Byte 3: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <chk></chk> | Checksum. |
| Response: | | |
| None. | | |
| Signature: | | |
| Byte 0: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xB4 | |

Notes:

None.

1.4.2 Busy

Description:

The **Busy** broadcast message allows the command station to keep the network active whilst it is performing a task that requires a response, and entails a significant processing delay, i.e. it can ensure no new requests are started until it has responded to the last message. The **Busy** message should be simply stripped and ignored.

Group: 2-Byte Message Opcode: OPC_BUSY Type: Broadcast Encoding: Byte 0: 1 0 0 0 0 0 0 1 0x81Opcode. Byte 1: 0 0 Checksum. 1 0x7EResponse: None Signature: Byte 0: 1 0 0 0 0 0 0 1 0x81Notes: None.

1.4.3 CfgSlotDataP1

Description:

This response provides the current command station configuration slot data. It is sent by the command station in response to the **GetCfgSlotDataP1** command.

Protocol:

1

Group:

Variable-Byte Message

Opcode:

OPC_SL_RD_DATA

Type:

Response

Encoding:

Byte 0:

| 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0xE7 | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

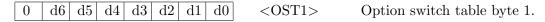
| 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0x0E | Message length (14 bytes) |
|---|---|---|---|---|---|---|---|------|---------------------------|

OST1 to OST6 encode the command station's option switch table. The narrative is based upon information in the the DCS210 and DCS240 user manuals. A bit value of 1 means that the switch is closed and a value of 0 means that a switch is thrown. OpSw 8, OpSw 16, OpSw 24, OpSw 32 and OpSw 40 cannot be read due to bit 7 being cleared in the message format. The manual shows these switches as defaulting to thrown, i.e. 0, and are flagged in all cases except OpSw 40 as "do not change".

Byte 2:

| 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0x7F | Configuration slot number. |
|---|---|---|---|---|---|---|---|------|----------------------------|
|---|---|---|---|---|---|---|---|------|----------------------------|

Byte 3:



| $\underline{\mathrm{Bit}}$ | Switch # | <u>Default</u> | Effect on system operation |
|----------------------------|--------------------------------|----------------|---|
| d6 | $\overline{\mathrm{OpSw}\ 07}$ | t | do not change |
| d5 | OpSw~06 | \mathbf{t} | t = check for decoder before programming |
| | | | c = program without checking for device |
| d4 | OpSw~05 | \mathbf{t} | do not change |
| d3 | OpSw 04 | \mathbf{t} | do not change |
| d2 | OpSw~03 | \mathbf{t} | t = command station's booster normal |
| | | | c = command station's booster is auto reversing |
| d1 | OpSw~02 | \mathbf{t} | t = command station mode |
| | | | c = booster only mode. |
| d0 | OpSw~01 | \mathbf{t} | do not change. |
| | | | |

Byte 4:

| 0 | d6 d5 d | 4 d3 d2 d1 | d0 <ost2> Option switch table byte 2.</ost2> | | | | |
|-----|----------|---------------|--|--|--|--|--|
| Bit | Switch 7 | # Default | Effect on system operation | | | | |
| d6 | OpSw 1 | <u>5</u> t | t = purging will not change loco speed | | | | |
| | | | c = purging will force a loco to 0 speed | | | | |
| d5 | OpSw 1 | 4 t | t = loco address purging enabled | | | | |
| | | | c = loco address purging disabled | | | | |
| d4 | OpSw 13 | 3 t | t = loco address purge time 200 seconds | | | | |
| | | | c = loco address purge time 600 seconds | | | | |
| d3 | OpSw 1 | 2 t | do not change | | | | |
| d2 | OpSw 1 | 1 t | do not change | | | | |
| d1 | OpSw 10 | 0 с | do not change | | | | |
| d0 | OpSw 0 | 9 c | do not change | | | | |

Byte 5:

| 0 | $d6 \mid d5 \mid d4 \mid$ | d3 d2 d1 | d0 <ost3> Option switch table byte 3.</ost3> |
|----------------------------|--------------------------------|-----------------|--|
| $\underline{\mathrm{Bit}}$ | Switch # | <u>Default</u> | Effect on system operation |
| d6 | $\overline{\mathrm{OpSw}\ 23}$ | t | SW23 |
| d5 | OpSw~22 | \mathbf{c} | SW22 |
| d4 | OpSw 21 | $^{\mathrm{c}}$ | SW21 |
| d3 | OpSw 20 | \mathbf{t} | t = enable address 0x00 or analog stretching for conven- |
| | | | tional locos |
| | | | . $c = disable address 0x00 or analog stretching for con-$ |
| | | | ventional locos |
| d2 | OpSw 19 | \mathbf{t} | do not change |
| d1 | OpSw 18 | \mathbf{t} | t = normal command station booster short circuit shut- |
| | | | down time |
| | | | c = extended command station booster short circuit |
| | | | shutdown time |
| d0 | OpSw 17 | t | t = automatic advanced decode (FX) consists are enabled |
| | | | c = automatic advanced decode (FX) consists are dis- |
| | | | abled |
| CMIO | 1 CWOO CW | 09 Cl-1-1 | |
| <u>5W2</u> | 1 SW22 SW | 23 Global sys | stem default type for new locos |

| \mathbf{t} | \mathbf{t} | \mathbf{t} | 28 step mode | |
|--------------|--------------|--------------|------------------|--|
| \mathbf{t} | \mathbf{t} | \mathbf{c} | reserved | |
| \mathbf{t} | \mathbf{c} | \mathbf{t} | 14 step mode | |
| \mathbf{t} | \mathbf{c} | \mathbf{c} | reserved | |
| \mathbf{c} | \mathbf{t} | \mathbf{t} | reserved | |
| \mathbf{c} | \mathbf{t} | \mathbf{c} | reserved | |
| \mathbf{c} | \mathbf{c} | \mathbf{t} | 128 step mode | |
| \mathbf{c} | \mathbf{c} | \mathbf{c} | 128 step FX mode | |
| | | | | |

Byte 6:

| 0 | $d6 \mid d5 \mid d4 \mid$ | $d3 \mid d2 \mid d1 \mid$ | d0 | $\langle OST4 \rangle$ | Option switch table byte 4. | | | | | |
|----------------------------|--------------------------------|---------------------------|-----|--|---------------------------------------|--|--|--|--|--|
| $\underline{\mathrm{Bit}}$ | Switch # | <u>Default</u> | Eff | fect on system of | pperation | | | | | |
| d6 | $\overline{\mathrm{OpSw}\ 31}$ | \mathbf{t} | t = | $\overline{t = normal route/switch o}$ utput rate when not trinary | | | | | | |
| | | | c = | = fast route/swi | tch output rate when not trinary | | | | | |
| d5 | OpSw~30 | t | do | not change | | | | | | |
| d4 | OpSw 29 | t | do | not change | | | | | | |
| d3 | OpSw 28 | \mathbf{t} | t = | = enable interro | gate commands at power on | | | | | |
| | | | c = | = disable interre | ogate commands at power on | | | | | |
| d2 | OpSw 27 | t | t = | enable normal | switch commands, a.k.a. the "Bushby | | | | | |
| | | | bit | ,,,, | | | | | | |
| | | | c = | = disable normal | switch commands, a.k.a. the "Bushby | | | | | |
| | | | bit | " (allows attach | ned computer to handle switch control | | | | | |
| | | | log | gic) | | | | | | |
| d1 | OpSw 26 | \mathbf{c} | | = disable routes | | | | | | |
| | | | c = | = enable routes | | | | | | |
| d0 | OpSw 25 | \mathbf{t} | t = | = enable route e | echo over the Network | | | | | |
| | | | c = | = disbale route | echo over the Network | | | | | |

Byte 7:

| 1 | 0 | d6 | 0 | 0 | d3 | d2 | d1 | d0 | <TRK $>$ | Global system track status. |
|---|---|----|---|---|----|----|-----|----|----------|-----------------------------|
| | 0 | uo | 0 | U | uo | uz | u i | uo | < 11t11/ | Ciobai system track status. |

- d6 1 means this command station implements version 2 slot commands. This can be turned off on the DCS240 by setting the OpSw 44 to be closed.
- d3 1 means the programming track is busy.
- d2 1 means this master implements the Network version 1.1 capability, 0 means the master is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on in the master, global power up.

Byte 8:

| 0 | $d6 \mid d5 \mid d4 \mid$ | $d3 \mid d2 \mid d1 \mid$ | d0 <ost5></ost5> | Option switch table byte 5. |
|----------------------------|---------------------------|---------------------------|-----------------------|--|
| $\underline{\mathrm{Bit}}$ | Switch # | <u>Default</u> | Effect on system op | peration |
| d6 | OpSw 39 | \mathbf{t} | | al memory states, including OpSw 36 |
| • | 0 0 0 | | and 37 | |
| d5 | OpSw 38 | t | | on activates OpSw 39 |
| | | | c = loco reset activ | ates slot zero |
| d4 | OpSw 37 | \mathbf{t} | c = clears all routes | S |
| d3 | OpSw~36 | \mathbf{t} | c = clears all mobil | e decoder info and consists |
| d2 | OpSw~35 | t | t = enables loco res | set buttone |
| | | | c = disable loco res | set button |
| d1 | OpSw 34 | \mathbf{t} | t = disallow track t | o power up to run state, if set to run |
| | | | prior to power up | |
| | | | c = allow track to | power up to run state, if set to run |
| | | | prior to power up | |
| d0 | OpSw 33 | $^{\mathrm{c}}$ | t = track power off | at power on |
| | | | c = allow track power | ver to restore to prior state at power |
| | | | on | |
| | | | | |

Byte 9:

| 0 | $d6 \mid d5 \mid d4 \mid$ | $d3 \mid d2 \mid d1 \mid$ | d0 <ost6> Option switch table byte 6.</ost6> |
|------------------------------------|---|-----------------------------------|--|
| $\frac{\mathrm{Bit}}{\mathrm{d}6}$ | $\frac{\text{Switch } \#}{\text{OpSw } 47}$ | $\frac{\text{Default}}{\text{t}}$ | $\frac{\text{Effect on system operation}}{\text{t = normal program track setting}}$ |
| | | | c = program track is brake generator when not programming. Braking is DCC set to speed 0 (not emergency stop) for address 0, light on, broadcast to all addresses. |
| d5 | OpSw 46 | \mathbf{t} | do not change |
| d4 | OpSw 45 | t | t = enable reply for switch state request |
| | | | c = disable reply for switch state request |
| d3 | OpSw 44 | \mathbf{t} | do not change (DCS210) |
| | OpSw 44 | \mathbf{t} | maximum slots to 400 (DCS240) and enable protocol 2 |
| | | | support |
| | OpSw 44 | c | maximum slots to 120 (DCS240) and disable protocol 2 support |
| d2 | OpSw 43 | \mathbf{t} | t = enable the Network update of command station's |
| | | | track status |
| | | | c = disable the Network update of command station's track status |
| d1 | OpSw 42 | \mathbf{t} | t = enable 2 short beeps when loco address purged |
| | o p.o | | c = disable 2 short beeps when loco address purged |
| d0 | OpSw 41 | t | t = diagnostic click disabled |
| | • | | c = diagnostic click when valid the Network commands |
| | | | incoming and routes being output |
| | | | - · · · · · · · · · · · · · · · · · · · |

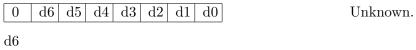
Byte 10:

| 0 | d6 d5 d4 d3 d2 d1 d0 | Unknown. |
|----|----------------------------------|----------|
| d6 | | |
| d6 | | |
| d4 | | |
| d3 | | |
| d2 | | |
| d1 | | |
| d0 | | |

Byte 11:

| | 0 | n | n | n | n | n | n | n | <csm></csm> | Product code. | |
|---------------------|------|---|---|---|--------|------|-----------|---|-------------|---------------|--|
| <u>Product Code</u> | | | | | | Mode | <u>el</u> | | | | |
| | 0x1B | | | | Ι | OCS2 | 210 | | | | |
| | 0x1C | | | Ι | DCS240 | | | | | | |
| | | | | | | | | | | | |

Byte 12:



d5

аэ d4

d3

d2

 $\begin{array}{c} d1 \\ d0 \end{array}$

Byte 13:

| 0 | n n n n | n |
|---|---------|---|
|---|---------|---|

Response:

None.

Signature:

Byte 0:

|--|

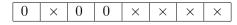
Byte 1:

| 1 | _ | _ | _ | _ | | | | _ | |
|---|---|---|-----|---|---|---|---|---|-----------------------|
| | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | $0 \times 0 \times 0$ |
| | | | l . | | | | l | | |

Byte 2:

| 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0x7F |
|---|---|---|---|---|---|---|---|------|

Byte 7:



Notes:

None.

1.4.4 ConsistDirF0F4

Description:

This command sets the consist element's direction and function F0 to F4 states.

Protocol:

1

Group:

4-Byte Message

Opcode:

OPC_CONSIST_FUNC

Type:

Command

Encoding:

Byte 0:

| 1 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 0xB6 | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

| 0 | n | n | n | n | n | n | n | <slot#></slot#> | Slot number in the range 0x00 to |
|---|---|---|---|---|---|---|---|-----------------|----------------------------------|
| | | | | | | | • | , | 0x77. |

Byte 2:

| 0 | 0 | d5 | d4 | d3 | d2 | d1 | d0 | <DIRF $>$ | Consist | element's | direction | and |
|---|---|----|----|----|----|----|----|-----------|----------|------------|-----------|-----|
| | | | | | | | | | function | F0 to F4 s | states. | |

- d5 Direction: 1 means forward and 0 means backwards.
- d4 F0 state: 1 means on and 0 means off.
- d3 F4 state: 1 means on and 0 means off.
- d2 F3 state: 1 means on and 0 means off.
- d1 F2 state: 1 means on and 0 means off.
- d0 F1 state: 1 means on and 0 means off.

Byte 3:

| 0 | n | n | n | n | n | n | n | <CHK $>$ | Checksum. |
|---|---|---|---|---|---|---|---|----------|-----------|
|---|---|---|---|---|---|---|---|----------|-----------|

Response:

1.4. MESSAGES 19 None. Signature: Byte 0: $1 \quad 0$ 0 0xB6Byte 1: 0 n n n n less than 0x78 n Byte 2: $0 \quad 0$ X X

Notes: None.

1.4.5 GetBrdOpSw

Description:

Get board option switch setting.

Group:

6-Byte Message

Opcode:

OPC_BRD_OPSW (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

| 1 | 1 | 0 | 1 | Ω | Ω | n | Ω | 02D0 | Oncode |
|---|---|---|---|---|---|---|---|------|---------|
| 1 | 1 | U | I | U | U | U | U | UXDU | Opcode. |

Byte 1:

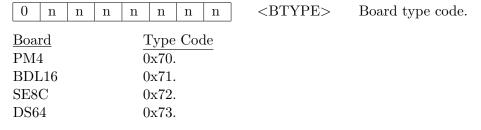
| 0 | 1 | 1 | 0 | 0 | 0 | 1 | d0 |
|---|---|---|---|---|---|---|----|
|---|---|---|---|---|---|---|----|

The bit d0 is the most significant bit of the board id.

Byte 2:



Byte 3:



Byte 4:

The high nibble encodes the byte number, and the low nibble the bit number. The byte number is calculated as (OpSw# - 1) >> 3 and the bit number is (OpSw# - 1) - byte number \times 8.

Byte 5:

Response:

 \mathbf{Ack} ;- *** SHOULD NOT BE A RESPONSE ***

Signature:

Byte 0:

1 1 0 1 0 0 0 0 0 0xD0

Byte 1:

 $oxed{0\ |\ 1\ |\ 1\ |\ 0\ |\ 0\ |\ 0\ |\ 1\ |\ \times}$

Notes:

*** THIS HAS NOT BEEN TESTED ***

 $1 \quad 0$

Byte 1:

1

0

1

1

0xBB

$1.4.6 \quad GetCfgSlotDataP1$

| Description: | | |
|--|------------------|----------------------------------|
| This command requests the configurat a $\mathbf{CfgSlotDataP1}$ message. | ion slot data. T | he command station responds with |
| Protocol: | | |
| 1 | | |
| Group: | | |
| 4-Byte Message | | |
| Opcode: | | |
| OPC_RQ_SL_DATA | | |
| Type: | | |
| Command | | |
| Encoding: | | |
| Byte 0: | | |
| 1 0 1 1 1 0 1 1 | 0xBB | Opcode. |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F | |
| Byte 2: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 | |
| Byte 3: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <chk></chk> | Checksum. |
| Response: | | |
| CfgSlotDataP1 | | |
| Signature: | | |
| Byte 0: | | |

| | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0x7F |
|---|-----|----|---|---|---|---|---|---|------|
| Е | yte | 2: | | | | | | | |
| | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |

Notes:

None.

1.4.7 GetInterfaceData

| Description: | | | | | |
|---|---------------|---------------------|-----------|------|------|
| This command is sent by a computer attached network interface device. | to request | an InterfaceData | response | from | the |
| Group: | | | | | |
| 2-Byte Message | | | | | |
| Opcode: | | | | | |
| OPC_BUSY | | | | | |
| Type: | | | | | |
| Command | | | | | |
| Applicable Hardware: | | | | | |
| Digitrax PR4 and DCS240. | | | | | |
| Encoding: | | | | | |
| Byte 0: | | | | | |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | 0x81 | Opcode. | | | |
| Byte 1: | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7E | Checksum. | | | |
| Response: | | | | | |
| Interface device returns an $\mathbf{Interface}\mathbf{D}$ | Oata respons | se. | | | |
| Signature: | | | | | |
| None - the command is intercepted by work. | by the interf | ace and is not pass | sed on to | the | net- |
| Notes: | | | | | |
| None. | | | | | |
| | | | | | |

1.4.8 GetLocoSlotDataLAdrP1

Description:

This command requests a slot for the selected locomotive address. If the locomotive is found in the slot table then the command station returns an **LocoSlotDataP1** response with the slot information. If it is not found then the command station will put the locomotive into a free slot and then return an **LocoSlotDataP1** response with the slot information. If there are no free slots then the command station returns an **NoFreeSlotsP1** response.

The command station will generate NMRA 14 bit or long address packets for the locomotive. The address must be in the range 128 to 9983.

Protocol: 1 Group: 4-Byte Message Opcode: OPC_LOCO_ADR Type: Command Encoding: Byte 0: 1 0 1 1 1 0xBFOpcode. Byte 1: 0 <ADR2>n Address high 7 bits. n n n n n n Byte 2: 0 n <ADR>Address low 7 bits. n n n \mathbf{n} Byte 3: 0 <CHK> Checksum. n \mathbf{n} \mathbf{n} n \mathbf{n} \mathbf{n} \mathbf{n} Response:

LocoSlotDataP1 if success, otherwise NoFreeSlotsP1

Signature:

Byte 0:

1 0 1 1 1 1 1 0xBF

Byte 1:

Notes:

This command is not supported by the Digitrax $\mathrm{DT}200$ command station.

1.4.9 GetLocoSlotDataLAdrP2

Description:

This command requests a slot for the selected locomotive address. If the locomotive is found in the slot table then the command station returns an **LocoSlotDataP2** response with the slot information. If it is not found then the command station will put the locomotive into a free slot and then return an **LocoSlotDataV2** response with the slot information. If there are no free slots then the command station returns an **NoFreeSlotsP2** response.

The command station will generate NMRA 14 bit or long address packets for the locomotive. The address must be in the range 128 to 9983.

Protocol: 2 Group: 4-Byte Message Opcode: OPC_LOCO_ADR_P2 (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 0 0 0xBEOpcode. Byte 1: 0 <ADR2>Address high 7 bits. n n n \mathbf{n} n n n Byte 2: 0 n <ADR>Address low 7 bits. n \mathbf{n} n \mathbf{n} Byte 3: 0 n <CHK> Checksum. \mathbf{n} \mathbf{n} n n \mathbf{n} \mathbf{n} Response:

LocoSlotDataP2 if success, otherwise NoFreeSlotsP2.

| Signature: | |
|--|----------------|
| Byte 0: | |
| 1 0 1 1 1 1 0 | 0xBE |
| Byte 1: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | not equal to 0 |
| Notes: | |
| None. | |

1.4.10 GetLocoSlotDataP1

Description:

Signature:

Byte 0:

Byte 1:

1 0

0

1

1

1

This command requests the locomotive slot data for the specified slot number. The command station responds with a **LocoSlotDataP1** response.

Protocol: 1 Group: 4-Byte Message Opcode: OPC_RQ_SL_DATA Type: Command Encoding: Byte 0: Opcode. 1 0 1 1 1 0 1 1 0xBBByte 1: <SLOT#>0 Slot number in the range 0x00 to n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} 0x77.Byte 2: $0 \quad 0$ 0 0x000 0 0 0 d0Byte 3: 0 n <CHK> Checksum. n n n \mathbf{n} n n Response: LocoSlotDataP1 or SlotNotImplemented

0xBB

| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | less than $0x78$ |
|--|------------------|
| Byte 2: | |
| 0 0 0 0 0 0 0 d0 | 0x 0 0 |
| Notes: | |
| None. | |

1.4.11 GetLocoSlotDataP2

Description:

This command requests the locomotive slot data for the specified slot number. The command station responds with a **LocoSlotDataP2** response.

Protocol:

2

Group:

4-Byte Message

 $\underline{\text{Direction:}} \rightarrow \text{Switch}$

Opcode:

OPC_RQ_SL_DATA

Type:

Command

Encoding:

Byte 0:

| 1 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 0xBB | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
| | | | | | | | | | |

Byte 1:

| 0 | n | n | n | n | n | n | n | <SLOT $#$ $>$ | Slot number in the range 0x00 t | to |
|---|---|---|---|---|---|---|---|---------------|---------------------------------|----|
| | | | | | • | • | | | 0x77. | |

Byte 2:



Byte 3:

| 0 | n | n | n | n | n | n | n | <CHK $>$ | Checksum. |
|---|---|---|---|---|---|---|---|----------|-----------|
|---|---|---|---|---|---|---|---|----------|-----------|

Response:

LocoSlotDataP2 or SlotNotImplemented

Signature:

| Byte | 0: |
|------|----|
| · | |

| 1 0 1 1 1 0 1 1 | 0xBB |
|-----------------|------|
|-----------------|------|

Byte 1:

| 0 | n | n | n | n | n | n | n | less than 0x78 |
|---|---|---|---|---|---|---|---|----------------|
| | | | | | | | | |

Byte 2:

| | | 0 | 1 | 0 | 0 | × | × | × | × |
|--|--|---|---|---|---|---|---|---|---|
|--|--|---|---|---|---|---|---|---|---|

Notes:

1.4.12 GetLocoSlotDataSAdrP1

Description:

This command requests a slot for the selected locomotive address. If the locomotive is found in the slot table then the command station returns an **LocoSlotDataP1** response with the slot information. If it is not found then the command station will put the locomotive into a free slot and then return an **LocoSlotDataP1** response with the slot information. If there are no free slots then the command station returns an **NoFreeSlotsP1** response.

The command station will generate NMRA 7 bit or short address packets for the locomotive. The address has the range 0 to 127. The analog locomotive is selected with address 0.

Protocol: 1 Group: 4-Byte Message Opcode: OPC_LOCO_ADR Type: Command Encoding: Byte 0: 1 0 1 0xBFOpcode. 1 1 1 1 Byte 1: 0 0 0 0 00 0 0x000 Byte 2: 0 Short address in the range 0 to n n n \mathbf{n} n \mathbf{n} \mathbf{n} <ADR>127. Byte 3: <CHK> Checksum. 0

Response:

${\bf LocoSlotDataP1} \ {\bf if} \ {\bf success}, \ {\bf otherwise} \ {\bf NoFreeSlotsP1}$

Signature:

Byte 0:

| 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0xBF |
|---|---|---|---|---|---|---|---|------|
| 1 | U | + | 1 | | | | | UADI |

Byte 1:

| 0 0 0 0 0 0 0 0 0 0 0 0x | 0 0 0 0 0 |
|--------------------------|---------------|
|--------------------------|---------------|

Notes:

1.4.13 GetLocoSlotDataSAdrP2

Description:

This command requests a slot for the selected locomotive address. If the locomotive is found in the slot table then the command station returns an **LocoSlotDataP2** response with the slot information. If it is not found then the command station will put the locomotive into a free slot and then return an **LocoSlotDataP2** response with the slot information. If there are no free slots then the command station returns an **NoFreeSlotsP2** response.

The command station will generate NMRA 7 bit or short address packets for the locomotive. The address has the range 0 to 127. The analog locomotive is selected with address 0.

0. Protocol: 2 Group: 4-Byte Message Opcode: OPC_LOCO_ADR_P2 (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 0 0 0xBEOpcode. 1 1 1 1 Byte 1: 0 0 0 0 0 0 0 0x000 Byte 2: 0 Short address in the range 0 to n \mathbf{n} n \mathbf{n} n \mathbf{n} \mathbf{n} <ADR>127. Byte 3: <CHK> Checksum. 0

Response:

${\bf LocoSlotDataP2} \ {\bf if} \ {\bf success}, \ {\bf otherwise} \ {\bf NoFreeSlotsP2}$

Signature:

Byte 0:

| 1 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0xBE |
|---|---|---|---|---|---|---|---|------|
| | _ | | _ | _ | _ | | _ | J |

Byte 1:

| $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | 0 0 0 0 0x00 |
|---|--------------|
|---|--------------|

Notes:

1.4.14 IllegalMoveP1

Description:

1 0

1 0

1 0

0

0xB4

The **IllegalMoveP1** response means that the slot move request was not successful. This could be because the slot combination is invalid or that the slot in question was In-Use.

Use. Protocol: 1 Group: 4-Byte Message Opcode: OPC_LONG_ACK Type: Response Encoding: Byte 0: 1 0 0xB4Opcode. 1 1 0 1 0 0 Byte 1: 0 0 0 0x3A1 1 1 1 0 Byte 2: $0 \quad 0$ 0 0 0 0 0 0 0x00Byte 3: 0 1 1 0 1 0x71Checksum. Response: None Signature: Byte 0:

| Byte 1: | | | | | | | | | | |
|---------------------------------------|---|---|---|---|---|---|------|--|--|--|
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 1 | 1 | 1 | 0 | 1 | 0 | 0x3A | | | |
| Byte 2: | | | | | | | | | | |
| 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 | | | |
| Byte 3: | | | | | | | | | | |
| $\begin{bmatrix} 0 & 1 \end{bmatrix}$ | 1 | 1 | 0 | 0 | 0 | 1 | 0x71 | | | |
| Notes: | | | | | | | | | | |

1.4.15 IllegalMoveP2

Description:

1 0

1 0

0

1

0

0xB4

The IllegalMoveP2 response means that the slot move request was not successful. This could be because the slot combination is invalid or that the slot in question was In-Use

Use. Protocol: 2 Group: 4-Byte Message Opcode: OPC_LONG_ACK Type: Response Encoding: Byte 0: 1 0 0xB4Opcode. 1 1 0 1 0 0 Byte 1: 0 1 0 1 0 1 0 0 0x54Byte 2: $0 \quad 0$ 0 0 0 0 0 0 0x00Byte 3: $0 \quad 0$ 1 1 0x1FChecksum. Response: None Signature: Byte 0:

| Byte 1: | | | | | | | | |
|---------------------------------------|---|---|---|---|---|---|------|--|
| 0 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0x54 | |
| Byte 2: | | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 | |
| Byte 3: | | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 1 | 1 | 1 | 1 | 1 | 0x1F | |
| Notes: | | | | | | | | |

1.4.16 IMMPacket

| Dog | min. | tion. |
|------|------|-------|
| Desc | шр | tion: |

Send n-byte DCC immediate packet.

Group:

Variable-Byte Message

Opcode:

OPC_IMM_PACKET

Type:

Command

Encoding:

Byte 0:

| 1 | 1 | 1 | 0 | 1 | 1 | 0 | 1 | 0xED | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

| | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0x0B | Message length | (11 b | ytes) |
|--|---|---|---|---|---|---|---|---|------|----------------|-------|-------|
|--|---|---|---|---|---|---|---|---|------|----------------|-------|-------|

Byte 2:

| | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0x7F |
|--|---|---|---|---|---|---|---|---|------|
|--|---|---|---|---|---|---|---|---|------|

Byte 3:

| 0 | d6 | d5 | d4 | 0 | d2 | d1 | d0 | <REPS $>$ | Number | of immediate | bytes | and |
|---|----|----|----|---|----|----|----|-----------|-----------|--------------|-------|-----|
| | | | | | | | | | repeat co | ount. | | |

- d6 N2. Number of immediate bytes.
- d5 N1. Number of immediate bytes.
- d4 No. Number of immediate bytes.
- d2 R2. Repeat count.
- d1 R1. Repeat count.
- d0 R0. Repeat count.

Byte 4:

| 0 | 0 | 1 | d4 | d3 | d2 | d1 | d0 | <dhii></dhii> | High bits of IM1 to IM5. |
|---|---|---|----|----|----|----|----|---------------|--------------------------|
|---|---|---|----|----|----|----|----|---------------|--------------------------|

0

1

1 1 1

0x7F

| d4 IM5.7. High bit. d3 IM4.7. High bit. d2 IM3.7. High bit. d1 IM2.7. High bit. d0 IM1.7. High bit. |
|---|
| Byte 5: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 6: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 7: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 8: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 9: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 10: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Response: |
| Ack. |
| $\frac{\langle \text{LOPC} \rangle}{0\text{x7D}} \frac{\langle \text{ACK1} \rangle}{0\text{x7F}}$ Meaning Command OK, if command station. |
| 0x7E |
| 0x7D $0x00$ Internal buffer busy or full. |
| Signature: |
| Byte 0: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 1: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 2: |

Byte 3:

Byte 4:

| 0 | 0 | 1 | × | × | × | × | × |
|---|---|---|---|---|---|---|---|

Notes:

1.4.17 InterfaceData

| Description: | | | | | | | | |
|---|-----------|------------------------------------|--|--|--|--|--|--|
| This is sent by an interface device in response to a getInterfaceData command. | | | | | | | | |
| Group: | | | | | | | | |
| Variable-Byte Message | | | | | | | | |
| Opcode: | | | | | | | | |
| OPC_PEER_XFER | | | | | | | | |
| Type: | | | | | | | | |
| Response | | | | | | | | |
| Applicable Hardware: | | | | | | | | |
| Digitrax PR4 and DCS240. | | | | | | | | |
| Encoding: | | | | | | | | |
| Byte 0: | | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xE5 | Opcode. | | | | | | |
| Byte 1: | | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x10 | Message length (16 bytes). | | | | | | |
| Byte 2: | | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x22 | | | | | | | |
| Byte 3: | | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x22 | | | | | | | |
| Byte 4: | | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x01 | | | | | | | |
| Byte 5: | | | | | | | | |
| 0 0 0 0 0 0 0 0 | 0x00 | | | | | | | |
| Byte 6: | | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d1></d1> | Serial Number low byte low 7 bits. | | | | | | |
| | | | | | | | | |

| Byte 7: | | |
|--|----------------------|---|
| | <d2></d2> | Serial Number high byte low 7 bits. |
| Byte 8: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d3></d3> | It contains a value but the mean- |
| | | ing is unknown. |
| Byte 9: | | |
| 0 n n n n n n n | <d4></d4> | Unknown - set to zero for PR4 and DCS240. |
| Byte 10: | | 2 02 10 |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | <pxct2></pxct2> | Unknown - set to zero for PR4 and |
| | | DCS240. |
| Byte 11: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | $\langle D5 \rangle$ | Maybe hardware version. |
| Byte 12: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d6></d6> | Software version. |
| Byte 13: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d7></d7> | Maybe hardware version. |
| Byte 14: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d8></d8> | Product code. |
| Byte 15: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <chk></chk> | Checksum. |
| Response: | | |
| None | | |
| Signature: | | |
| Byte 0: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xE5 | |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x10 | |
| | | |

```
Byte 2:
 \begin{vmatrix} 0 & 0 \end{vmatrix}
             0
                             0
                                    0x22
Byte 3:
 0
    0
              0
                             0
                                    0x22
Byte 4:
 0
    0
              0
                                    0x01
Byte 5:
 0 \quad 0
                                    0x00
         0
             0
                 0
                             0
Byte 10:
 0 \quad 0
         0
                                    0x00
             0
                 0
                         0
                             0
Notes:
PR4 #1
<DO> 0xe5 OPCODE
<D1> 0x10 LENGTH
<D2> 0x22 SRC
<D3> 0x22 DSTL
<D4> 0x01 DSTH
<D5> 0x00 PXCT1 <- I would have expected b4 = 1
<D6> 0x08 Serial Number Low Byte
<D7> 0x07 Serial Number High Byte - Actual serial number 0x0788
<D8> 0x16
<D9> 0x00
<D10> 0x00 PXCT2
<D11> 0x00
<D12> 0x00
<D13> 0x00
<D14> 0x24 Product Code for PR4
<D15> 0x36 CHSUM
PR4 #2
<DO> Oxe5 OPCODE OPC_PEER_XFER
<D1> Ox10 LENGTH
<D2> 0x22 SRC
```

```
<D3> 0x22 DSTL
<D4> 0x01 DSTH
<D5> 0x00 PXCT1
<D6> 0x57 Serial Number Low Byte
<D7> 0x13 Serial Number High Byte - Actual serial number 0x1357
<D8> 0x16
<D9> 0x00
<D10> 0x00 PXCT2
<D11> 0x00
<D12> 0x00
<D13> 0x00
<D14> 0x24 Product Code for PR4
<D15> 0x7d CHKSUM
DCS240
<DO> Oxe5 OPCODE
<D1> 0x10 Length
<D2> 0x22 SRC
<D3> 0x22 DSTL
<D4> 0x01 DSTH
<D5> 0x00 PXCT1 <- I would have expected b4 to be 1
<D6> 0x2b Serial Number Low Byte
<D7> 0x0a Serial Number High Byte - Actual serial number 0x0aab
<D8> 0x14
<D9> 0x00
<D10> 0x00 PXCT2
<D11> 0x01 Hardware Version?
<D12> 0x03 Software Version
<D13> 0x01 Hardware Version?
<D14> 0x1c Product Code for DCS240
<D15> 0x21
```

1.4.18 IPLDataLoad

Description:

This command loads firmware data into a device that supports IPL. D1 is the lowest addressed byte and D8 is the highest addressed byte.

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Command

Encoding:

| Byte 0: | | |
|--|-----------------|---|
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xE5 | Opcode. |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x10 | Message length (16 bytes). |
| Byte 2: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | Broadcast id. |
| Byte 3: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | Broadcast id. |
| Byte 4: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | Broadcast id. |
| Byte 5: | | |
| 0 1 0 0 d3 d2 d1 d0 | <pxct1></pxct1> | Download code 0x40 and high bits of D1 to D4. |

- d3 D4.7. High bit
- d2 D3.7. High bit
- d1 D2.7. High bit
- d0 D1.7. High bit

| Byte 6: | | |
|--|----------------------|--|
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d1></d1> | Data Byte 1. Low 7 bits. |
| Byte 7: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d2></d2> | Data Byte 2. Low 7 bits. |
| Byte 8: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d3></d3> | Data Byte 3. Low 7 bits. |
| Byte 9: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d4></d4> | Data Byte 4. Low 7 bits. |
| Byte 10: | | |
| 0 0 1 0 n n n n | <pxct2></pxct2> | Data type code 0x20 and high bits for D5 to D8. |
| d3 D8.7. High bit d2 D7.7. High bit d1 D6.7. High bit d0 D5.7. High bit | | |
| Byte 11: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | $\langle D5 \rangle$ | Data Byte 5. Low 7 bits. |
| Byte 12: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | | |
| | <d6></d6> | Data Byte 6. Low 7 bits. |
| Byte 13: | <d6></d6> | Data Byte 6. Low 7 bits. |
| | <d6></d6> | Data Byte 6. Low 7 bits. Data Byte 7. Low 7 bits. |
| Byte 13: | | |
| Byte 13: 0 n n n n n n n | | |
| Byte 13: 0 n n n n n n n Byte 14: | <d7></d7> | Data Byte 7. Low 7 bits. |
| Byte 13: 0 n n n n n n n n Byte 14: 0 n n n n n n n n | <d7></d7> | Data Byte 7. Low 7 bits. |
| Byte 13: 0 n n n n n n n n Byte 14: 0 n n n n n n n Byte 15: | <d7> <d8></d8></d7> | Data Byte 7. Low 7 bits. Data Byte 8. Low 7 bits. |
| Byte 13: 0 n n n n n n n n Byte 14: 0 n n n n n n n n Byte 15: 0 n n n n n n n n | <d7> <d8></d8></d7> | Data Byte 7. Low 7 bits. Data Byte 8. Low 7 bits. |

Byte 0:

| 1 1 1 0 0 1 0 1 | 0xE5 |
|--|------|
| Byte 1: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x10 |
| Byte 2: | |
| 0 1 1 1 1 1 1 1 | 0x7F |
| Byte 3: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F |
| Byte 4: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F |
| Byte 5: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 10: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Notes: | |
| None. | |

1.4.19 IPLDevData

Description:

An IPL capable device sends this response in response to an **IPLDiscover** broadcast message.

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Response

Encoding:

Byte 0:

| 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0xE5 | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

| 0 | 0 0 | 1 | 0 | 1 | 0 | 0 | 0x14 | Message length (20 bytes). |
|---|-----|---|---|---|---|---|------|----------------------------|
|---|-----|---|---|---|---|---|------|----------------------------|

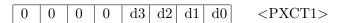
Byte 2:

| (|) | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0x0F |
|---|---|---|---|---|---|---|---|---|------|
|---|---|---|---|---|---|---|---|---|------|

Byte 3:

| 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0x10 |
|---|---|---|---|---|---|---|---|------|
| _ | - | | | - | - | - | _ | |

Byte 4:



- d3 D4.7. High bit
- d2 D3.7. High bit
- d1 D2.7. High bit
- d0 D1.7. High bit

Byte 5:

| 0 | n | n | n | n | n | n | n | <d1></d1> | Product code low 7 bits. |
|---|---|---|---|---|---|---|---|-----------|--------------------------|
|---|---|---|---|---|---|---|---|-----------|--------------------------|

| Product Code | Device |
|--------------|------------|
| 0x01 | LNRP |
| 0x04 | UT4 |
| 0x0C | WTL12 |
| 0x14 | DB210 Opto |
| 0x15 | DB210 |
| 0x16 | DB220 |
| 0x1A | DCS210+ |
| 0x1B | DCS210 |
| 0x1C | DCS240 |
| 0x23 | PR3 |
| 0x24 | PR4 |
| 0x2A | DT402 |
| 0x32 | DT500 |
| 0x33 | DCS51 |
| 0x34 | DCS52 |
| 0x3E | DT602 |
| 0x51 | BXPA1 |
| 0x58 | BXP88 |
| 0x5C | UR92 |
| 0x63 | LNWI |

Byte 6:

| 0 | n | n | n | n | n | n | n | <D $2>$ | Hardware version 2 low 7 bits |
|---|---|---|---|---|---|---|---|---------|-------------------------------|
|---|---|---|---|---|---|---|---|---------|-------------------------------|

 $\begin{array}{cc} \underline{D2} & \underline{Meaning} \\ 0x00 & \overline{Slave \ all} \\ 0x18 & Slave \ RF24 \end{array}$

Byte 7:

| 0 | n | n | n | n | n | n | n | <d3></d3> | Data item 3. Low 7 bits. |
|---|---|---|---|---|---|---|---|-----------|--------------------------|
|---|---|---|---|---|---|---|---|-----------|--------------------------|

Byte 8:

| 0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | <d4></d4> | Software | ${\bf Version}$ | Number | low | 7 |
|---|----|----|----|----|----|----|----|-----------|----------|-----------------|--------|-----|---|
| | | | | • | | • | | | bits. | | | | |

d6version number bit 3 d5version number bit 2. d4version number bit 1 d3version number bit 0 d2subversion number bit 2 d1subversion number bit 1 d0subversion number bit 0 e.g. 0x09 decodes as version 1.1. Byte 9: 0 0 0 0 d3 | d2 | d1 | d0 |<PXCT2> High bits of D5 to D8. d3D8.7. High bit d2D7.7. High bit D6.7. High bit d1d0D5.7. High bit Byte 10: < D5 >Data item 5. Low 7 bits. 0 n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} n n Byte 11: 0 n < D6 >Serial number low byte low 7 bits. n n n \mathbf{n} n Byte 12: 0 \mathbf{n} <D7>Serial number high byte low 7 bits. \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} n Byte 13: 0 n <D8> Data item 8. Low 7 bits. \mathbf{n} \mathbf{n} n \mathbf{n} n \mathbf{n} Byte 14: 0 0 0 0 <PXCT3> High bits for D9 to D12. \mathbf{n} n \mathbf{n} n D12.7. High bit d3d2D11.7. High bit d1D10.7. High bit d0D9.7. High bit Byte 15:

<D9>

Data item 9. Low 7 bits.

0

Byte 16:

n

 \mathbf{n}

n | n

 \mathbf{n}

n

n

| | 0 | n | n | n | n | n | n | n | <d10></d10> | Data item 10. Low 7 bits. |
|---|------|-----|---|---|---|---|---|---|-------------|---------------------------|
| Ι | Byte | 17: | | | | | | | | |
| | 0 | n | n | n | n | n | n | n | <d11></d11> | Data item 11. Low 7 bits. |
| I | Byte | 18: | | | | | | | | |
| | 0 | n | n | n | n | n | n | n | <d12></d12> | Data item 8. Low 12 bits. |
| Ι | Byte | 19: | | | | | | | | |
| | 0 | n | n | n | n | n | n | n | <chk></chk> | Checksum. |
| | | | | | | | | | | |

Response:

None.

Notes:

These came from DigiPLII:

 $e5\ 14\ 0f\ 10\ 00\ 24\ 00\ 00\ 00\ 02\ 00\ 08\ 07\ 00\ 00\ 00\ 00\ 00\ 00\ 38$

 $e5\ 14\ 0f\ 10\ 00\ 24\ 00\ 00\ 00\ 00\ 00\ 57\ 13\ 00\ 00\ 00\ 00\ 00\ 00\ 71$

 $e5\ 14\ 0f\ 10\ 00\ 1b\ 00\ 00\ 03\ 02\ 00\ 54\ 10\ 00\ 00\ 00\ 00\ 00\ 00\ 4f$

PR4 with serial number 0x0788 ver 0

PR4 with serial 0x1357 ver 0

DCS210 with SN 0x10D4 ver 0.3

DCS240 with SN 0x0AAB ver 0.3

1.4.20 IPLDiscover

${\bf Description:}$

Byte 7:

 $0 \quad 0$

0

0 0

0 0

0

0x00

This broadcast message requests IPL capable devices to report their IPL information. The devices each respond with a **IPLDevData** response.

| devices each respond with a IPLDevData response. | | | | | | |
|--|------|----------------------------|--|--|--|--|
| Group: | | | | | | |
| Variable-Byte Message | | | | | | |
| Opcode: | | | | | | |
| OPC_PEER_XFER | | | | | | |
| Type: | | | | | | |
| Broadcast | | | | | | |
| Encoding: | | | | | | |
| Byte 0: | | | | | | |
| 1 1 1 0 0 1 0 1 | 0xE5 | Opcode. | | | | |
| Byte 1: | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x14 | Message length (20 bytes). | | | | |
| Byte 2: | | | | | | |
| 0 0 0 0 1 1 1 1 | 0x0F | | | | | |
| Byte 3: | | | | | | |
| 0 0 0 0 1 0 0 | 0x08 | | | | | |
| Byte 4: | | | | | | |
| 0 0 0 0 0 0 0 0 | 0x00 | | | | | |
| Byte 5: | | | | | | |
| | 0x00 | | | | | |
| Byte 6: | | | | | | |
| | 0x00 | | | | | |

| Ε | Byte 8: | | | | | | | |
|---|---------------------------------------|----|---|---|---|-----|-------------|-----------|
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| E | Byte 9: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| Ε | Byte 10: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| E | Byte 11: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 1 | 0x01 | |
| Ε | Byte 12: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| Ε | Byte 13: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| E | Byte 14: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| Ε | Byte 15: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| E | Byte 16: | | | | | | | |
| | $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| Ε | Byte 17: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| E | Byte 18: | | | | | | | |
| | 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 | |
| Ε | Byte 19: | | | | | | | |
| | 0 n | n | n | n | n | n n | <chk></chk> | Checksum. |
| Т | oenonec | ٠. | | | | | | |

Response:

${\bf IPLDevData}$

Signature:

| Byte 0: | | | | | | |
|---|---|---|---|---|-----|------|
| $\begin{array}{ c c c c }\hline 1 & 1 & \hline \end{array}$ | 1 | 0 | 0 | 1 | 0 1 | 0xE5 |
| Byte 1: | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 1 | 0 | 1 | 0 0 | 0x14 |
| Byte 2: | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 1 | 1 | 1 1 | 0x0F |
| Byte 3: | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 1 | 0 | 0 0 | 0x08 |
| Byte 4: | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 0 | 0x00 |
| Byte 5: | | | | | | |
| 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 |
| Byte 6: | | | | | | |
| 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 |
| Byte 7: | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 0 | 0x00 |
| Byte 8: | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 0 | 0x00 |
| Byte 9: | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 0 | 0x00 |
| Byte 10: | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 0 | 0x00 |
| Byte 11: | | | | | | |
| 0 0 | 0 | 0 | 0 | 0 | 0 1 | 0x01 |
| Byte 12: | | | | | | |
| 0 0 | 0 | 0 | 0 | 0 | 0 0 | 0x00 |
| Byte 13: | | | | | | |
| | | | | | | |

| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x 0 0 |
|--|----------|
| Byte 14: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Byte 15: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Byte 16: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Byte 17: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Byte 18: | |
| 0 0 0 0 0 0 0 0 | 0x00 |
| Notes: | |
| None. | |

1.4.21 IPLEndLoad

| Description: | | |
|--|---------|----------------------------|
| This command ends a device firmware | update. | |
| Group: | | |
| Variable-Byte Message | | |
| Opcode: | | |
| OPC_PEER_XFER | | |
| Type: | | |
| Command | | |
| Encoding: | | |
| Byte 0: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xE5 | Opcode. |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x10 | Message length (16 bytes). |
| Byte 2: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | Broadcast id. |
| Byte 3: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | Broadcast id. |
| Byte 4: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | Broadcast id. |
| Byte 5: | | |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | 0x40 | Download code. |
| Byte 6: | | |
| | 0x00 | |
| Byte 7: | | |
| | 0x00 | |
| | | |

| Byte 8: | | |
|-----------------|-------------|---------------------|
| | 0x00 | |
| Byte 9: | | |
| | 0x00 | |
| Byte 10: | | |
| 0 1 0 0 n n n n | 0x40 | End load type code. |
| Byte 11: | | |
| | 0x00 | |
| Byte 12: | | |
| | 0x00 | |
| Byte 13: | | |
| 0 0 0 0 0 0 0 0 | 0x00 | |
| Byte 14: | | |
| 0 0 0 0 0 0 0 0 | 0x00 | |
| Byte 15: | | |
| | <chk></chk> | Checksum. |
| Response: | | |
| None | | |
| Signature: | | |
| Byte 0: | | |
| | 0xE5 | |
| Byte 1: | | |
| | 0x10 | |
| Byte 2: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | |
| Byte 3: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | |

| Byte 4: | | | | | | | |
|---------------------------------------|---|---|---|---|---|---|------|
| $0 \mid 1$ | 1 | 1 | 1 | 1 | 1 | 1 | 0x7F |
| Byte 5: | | | | | | | |
| $0 \mid 1$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x40 |
| Byte 6: | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Byte 7: | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Byte 8: | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Byte 9: | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Byte 10: | | | | | | | |
| 0 1 | 0 | 0 | n | n | n | n | 0x40 |
| Byte 11: | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Byte 12: | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Byte 13: | | | | | | | |
| 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Byte 14: | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Notes: | | | | | | | |

$1.4.22 \quad IPLSetAddr$

D1.7. High bit

d0

| Description: | | |
|---|-------------------|---|
| This command sets the address of wh | ere to load the r | next block of firmware data. |
| Group: | | |
| Variable-Byte Message | | |
| Opcode: | | |
| OPC_PEER_XFER | | |
| Type: | | |
| Command | | |
| Encoding: | | |
| Byte 0: | | |
| $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | 0xE5 | OPC_PEER_XFER opcode. |
| Byte 1: | | |
| $egin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x10 | Message length (16 bytes). |
| Byte 2: | | |
| $egin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F | Broadcast id. |
| Byte 3: | | |
| $egin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F | Broadcast id. |
| Byte 4: | | |
| $egin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F | Broadcast id. |
| Byte 5: | | |
| 0 1 0 0 d3 d2 d1 d0 | <pxct1></pxct1> | Download code 0x40 and high bits of D1 to D4. |
| d3 D4.7. High bit | | |
| d2 D3.7. High bit | | |
| d1 D2.7. High bit | | |

| Byte 6: | | |
|--|-----------------|--|
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d1></d1> | Address High Byte. Low 7 bits. |
| Byte 7: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d2></d2> | Address Mid Byte. Low 7 bits. |
| Byte 8: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d3></d3> | Address Low Byte. Low 7 bits. |
| Byte 9: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d4></d4> | Reserved always 0x00. Low 7 bits. |
| Byte 10: | | |
| 0 0 0 1 n n n n | <pxct2></pxct2> | Address type code 0x10 and high bits for D5 to D8. |
| d3 D8.7. High bit d2 D7.7. High bit | | |
| d1 D6.7. High bit $d0$ D5.7. High bit | | |
| Byte 11: | | |
| | <d5></d5> | Reserved always 0x00. Low 7 bits. |
| Byte 12: | | |
| | <d6></d6> | Reserved always 0x00. Low 7 bits. |
| Byte 13: | | v |
| | <d7></d7> | Reserved always 0x00. Low 7 bits. |
| Byte 14: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d8></d8> | Reserved always 0x00. Low 7 bits. |
| Byte 15: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <chk></chk> | Checksum. |
| Response: | | |
| None | | |
| Signature: | | |

Byte 0:

| 1 1 1 0 0 1 0 1 | 0xE5 |
|--|----------|
| Byte 1: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x10 |
| Byte 2: | |
| 0 1 1 1 1 1 1 1 | 0x7F |
| Byte 3: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F |
| Byte 4: | |
| $egin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F |
| Byte 5: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 9: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Byte 10: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 11: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Byte 12: | |
| 0 0 0 0 0 0 0 0 | 0x 0 0 |
| Byte 13: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Byte 14: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Notes: | |
| None. | |

1.4.23 IPLSetupBL2

| Desc | rip | tion: |
|------|-----|-------|
| | | |

This command initiates a firmware update for a device that supports IPL Bootloader 2 protocol.

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Command

Encoding:

Byte 0:

0 1 0

| 1 1 1 0 0 1 0 1 | 0xE5 | Opcode. |
|--|------|----------------------------|
| Byte 1: | | |
| 0 0 0 1 0 0 0 0 | 0x10 | Message length (16 bytes). |
| Byte 2: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F | Broadcast id. |
| Byte 3: | | |
| 0 1 1 1 1 1 1 1 | 0x7F | Broadcast id. |
| Byte 4: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F | Broadcast id. |
| Byte 5: | | |

<PXCT1>

Download code 0x40 and high bits

of D1 to D4.

d3 D4.7. High bit

0

d3 d2 d1 d0

- d2 D3.7. High bit
- d1 D2.7. High bit
- d0 D1.7. High bit

| Byte 6: | | | |
|---|-----------------|---|--|
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | <d1></d1> | Manufacturer code. Low 7 bits. | |
| Code Manufacturer 0x00 Digitrax | | | |
| Byte 7: | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d2></d2> | Product code. Low 7 bits. | |
| Byte 8: | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d3></d3> | Hardware version. Low 7 bits. | |
| Byte 9: | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d4></d4> | Software version. Low 7 bits. | |
| Byte 10: | | | |
| 0 0 0 0 n n n n | <pxct2></pxct2> | Setup download type code 0x00 and high bits for D5 to D8. | |
| d3 D8.7. High bit d2 D7.7. High bit d1 D6.7. High bit d0 D5.7. High bit Byte 11: | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d5></d5> | Options. Low 7 bits. | |
| Byte 12: | | | |
| | <d6></d6> | Reserved always 0x00. Low 7 bits. | |
| Byte 13: | | | |
| $egin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d7></d7> | Number of blocks to erase 7. Low 7 bits. | |
| This is calculated as INT (0.5 \pm (Last Address - First Address) / Erase Blk Size). | | | |
| Byte 14: | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <d8></d8> | Reserved always 0x00. Low 7 bits. | |
| Byte 15: | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <chk></chk> | Checksum. | |

| None | |
|--|------|
| Signature: | |
| Byte 0: | |
| | 0xE5 |
| Byte 1: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x10 |
| Byte 2: | |
| 0 1 1 1 1 1 1 1 | 0x7F |
| Byte 3: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F |
| Byte 4: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7F |
| Byte 5: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 10: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 12: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Byte 14: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 |
| Notes: | |
| None. | |

1.4.24 LinkSlotsP1

Description:

This command requests the command station to link slot SL1 to slot SL2. If the command was successful then a **LocoSlotDataP1** response will be returned. An invalid link will return a **Ack** with a response code of 0x00.

Protocol: 1 Group: 4-Byte Message Opcode: OPC_LINK_SLOTS Type: Command Encoding: Byte 0: 1 0 Opcode. 0 1 1 1 0 1 0xB9Byte 1: 0 <SL1> Slot number in the range 0x01 to n \mathbf{n} n \mathbf{n} \mathbf{n} \mathbf{n} n 0x77.Byte 2: 0 $\langle SL2 \rangle$ Slot number in the range 0x01 to \mathbf{n} n n n \mathbf{n} \mathbf{n} n 0x77.Byte 3: 0 <CHK> Checksum. n \mathbf{n} n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n}

Response:

LocoSlotDataP1

or

Ack

| $\frac{\langle \text{LOPC} \rangle}{0x39} \stackrel{\langle \text{LOPC} \rangle}{0x}$ | ACK1> | | | nk failed. |
|---|-------|-----|---|---------------------------------|
| Signature: | | | | |
| Byte 0: | | | | |
| $\begin{bmatrix} 1 & 0 & 1 \end{bmatrix}$ | 1 1 | 0 0 | 1 | 0xB9 |
| Byte 1: | | | | |
| 0 n n | n n | n n | n | in the range $0x01$ to $0x77$. |

Byte 2:

 $\boxed{0 \mid n \mid n \mid n \mid n \mid n \mid n \mid n}$ in the range 0x01 to 0x77.

 $\underline{\text{Notes:}}$

None.

1.4.25 LinkSlotsP2

${\bf Description:}$

This command requests the command station to link slot SL1 to slot SL2. If the command was successful then a **LocoSlotDataP2** response will be returned. An invalid link will return a \mathbf{Ack} with a response code of 0x00.

| Protocol: | | |
|--|---------------|---|
| 2 | | |
| Group: | | |
| 6-Byte Message | | |
| Opcode: | | |
| OPC_D4_GROUP (unofficial mnemoni | (c) | |
| Type: | | |
| Command | | |
| Encoding: | | |
| Byte 0: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xD4 | Opcode. |
| Byte 1: | | |
| 0 0 1 1 1 d2 d1 d0 | <sl1p></sl1p> | Bits d2 to d0 contain the SL1 slot page number in the range 0x0 to 0x7. |
| Byte 2: | | |
| | <sl1#></sl1#> | Slot number SL1 in the range $0x00$ to $0x77$. |
| Byte 3: | | |
| 0 1 0 0 0 d2 d1 d0 | <sl2p></sl2p> | Bits d2 to d0 contain the SL2 slot page number in the range 0x0 to 0x7. |
| Byte 4: | | |
| | <sl2#></sl2#> | Slot number SL2 in the range $0x00$ to $0x77$. |

1.4. MESSAGES 71 Byte 5: 0 n <CHK> Checksum. \mathbf{n} n \mathbf{n} n n n Response: ${\bf LocoSlotDataP2} \ {\rm or} \ {\bf Ack}.$ Signature: Byte 0: 1 1 0 1 0 1 0 0xD4Byte 1: $0 \quad 0$ 1 1 1 X X × Byte 2: 0 n less than 0x78n \mathbf{n} \mathbf{n} n n \mathbf{n} Byte 3: 0 1 0 0 0 \times \times X Byte 4:

less than 0x78

0 n

Notes:

None.

n

n

n

n

n

 \mathbf{n}

LocoBinStateP2 1.4.26

Description:

This command sets the locomotive's binary states with addresses in the range 1 to 32767.

The address of 0 is a broadcast command and will set or reset all binary states. Protocol: 2? Group:

Opcode:

6-Byte Message

OPC_D4_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

| T T O T O O ONDI | | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0xD4 | Opco |
|------------------|--|---|---|---|---|---|---|---|---|------|------|
|------------------|--|---|---|---|---|---|---|---|---|------|------|

Byte 1:

0 0 0 d4d3 | d2 | d1 | d0 | $\langle SLOTP \rangle$ Bits d2 to d0 contain the slot page number in the range 0x0 to 0x7. The bit d4 contains the function state where 1 means on and 0 means off. The bit d3 contains the high bit of the binary state address (bit 14).

Byte 2:

| | 0 | n | n | n | n | n | n | n | $\langle SLOT\# \rangle$ | Slot number. |
|---|------|----|---|---|---|---|---|---|--------------------------|-----------------------------------|
| Ε | Byte | 3: | | | | | | | | |
| | 0 | n | n | n | n | n | n | n | <bsa0></bsa0> | Binary state address bits 0 to 6. |

Byte 4:

| 0 | n | n | n | n | n | n | n | <BSA1 $>$ | Binary state address bits 7 to |
|---|---|---|---|---|---|---|---|-----------|--------------------------------|
|---|---|---|---|---|---|---|---|-----------|--------------------------------|

Byte 5:

Response:

None.

Signature:

Byte 0:

1 1 0 1 0 1 0 0 0xD4

Byte 1:

Notes:

*** THIS HAS NOT BEEN TESTED ***

1.4.27 LocoDirF0F4P1

| T . | | | |
|------|------|-------|----|
| Desc | rin | t10 | n· |
| Desc | JIID | יטנטי | ш. |
| | | | |

This function sets the locomotive's direction and function F0 to F4 states.

Protocol:

1

Group:

4-Byte Message

Opcode:

OPC_LOCO_DIRF

Type:

Command

Encoding:

Byte 0:

| 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0xA1 | Opcode |
|---|---|----------|---|---|---|---|---|-------|--------|
| | 0 | <u>+</u> | U | 0 | U | 0 | | UAILI | Opcode |

Byte 1:

| 0 | n | n | n | n | n | n | n | <slot#></slot#> | Slot number in the range 0x00 to |
|---|---|---|---|---|---|---|---|-----------------|----------------------------------|
| | | | | | | | • | , | 0x77. |

Byte 2:

| 0 | 0 | d5 | d4 | d3 | d2 | d1 | d0 | <DIRF $>$ | Locomotive's | ${\rm direction}$ | and | state |
|---|---|----|----|----|----|----|----|-----------|----------------|-------------------|-----|-------|
| | | | | | | | | | of functions F | 0 to F4. | | |

- d5 Direction: 1 means forward and 0 means backwards.
- d4 F0 state: 1 means on and 0 means off.
- d3 F4 state: 1 means on and 0 means off.
- d2 F3 state: 1 means on and 0 means off.
- d1 F2 state: 1 means on and 0 means off.
- d0 F1 state: 1 means on and 0 means off.

Byte 3:

| 0 | n | n | n | n | n | n | n | <CHK $>$ | Checksum. |
|---|---|---|---|---|---|---|---|----------|-----------|
|---|---|---|---|---|---|---|---|----------|-----------|

Response:

75 None. Signature: Byte 0: $1 \quad 0$ 0 1 0xA1Byte 1: 0 n n n n less than 0x78n n Byte 2: $0 \quad 0$ X X

1.4. MESSAGES

Notes: None.

Direction and function states.

1.4.28 LocoDirF0F4P2

Description:

This command sets the locomotive's direction and function F0 to F4 states.

Protocol:

2

Group:

6-Byte Message

Opcode:

OPC_D4_GROUP (unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

| 1 | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0xD4 | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

| 0 | 0 | 1 | 0 | 0 | d2 | d1 | d0 | $\langle SLOTP \rangle$ | Bits d2 to d0 contain the slot page |
|---|---|---|---|---|----|----|----|-------------------------|--------------------------------------|
| | | | | | | | | | number in the range $0x0$ to $0x7$. |

Byte 2:

| $\begin{bmatrix} 0 & n & n & n & n & n & n & n \end{bmatrix}$ $\langle SLOT\# \rangle$ Slot number |
|--|
|--|

Byte 3:

| 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0x06 | Subcode |
|---|---|---|---|---|---|---|---|------|---------|
|---|---|---|---|---|---|---|---|------|---------|

Byte 4:

|--|

- d5 Direction: 1 means forward and 0 means backwards.
- d4 F0 state: 1 means on and 0 means off.

0 0 d5 d4 d3 d2 d1 d0

- d3 F4 state: 1 means on and 0 means off.
- d2 F3 state: 1 means on and 0 means off.
- d1 F2 state: 1 means on and 0 means off.
- d0 F1 state: 1 means on and 0 means off.

Byte 5:

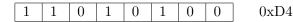
| $\mid 0 \mid n \mid$ Checks |
|--|
|--|

Response:

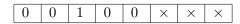
None.

Signature:

Byte 0:



Byte 1:



Byte 3:



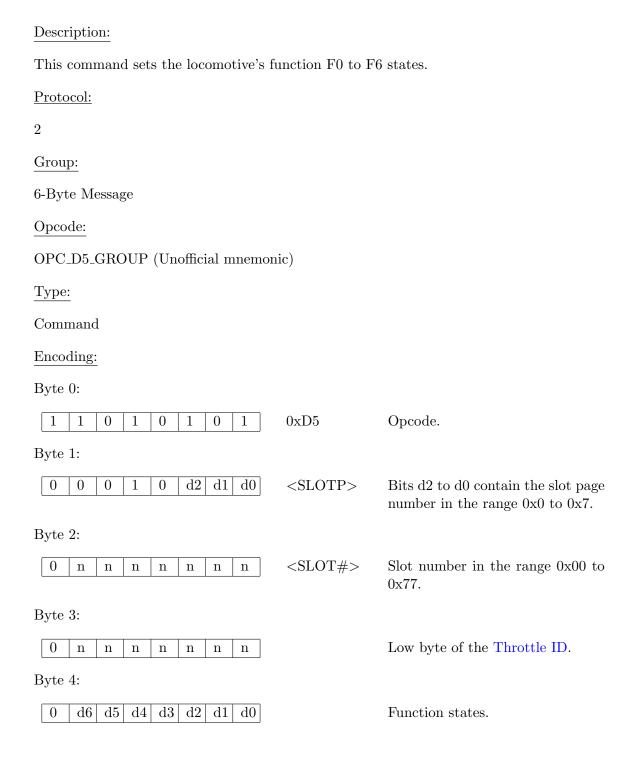
Byte 4:



Notes:

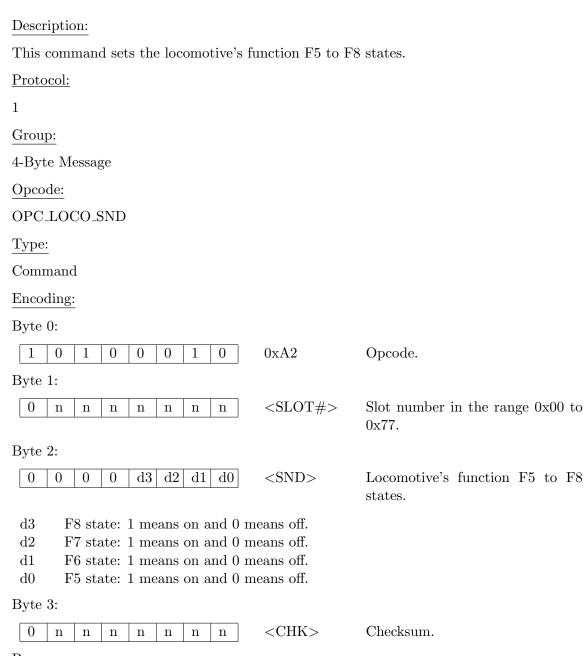
This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

1.4.29 LocoF0F6P2



| d6 F6 state: 1 means on and 0 means off. |
|--|
| d5 F5 state: 1 means on and 0 means off. |
| d4 F0 state: 1 means on and 0 means off. |
| d3 F4 state: 1 means on and 0 means off. |
| d2 F3 state: 1 means on and 0 means off. |
| d1 F2 state: 1 means on and 0 means off. |
| d0 F1 state: 1 means on and 0 means off. |
| Byte 5: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| |
| Response: |
| None. |
| Signature: |
| Byte 0: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 1: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 2: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Notes: |
| None. |

1.4.30 LocoF5F8P1



Response:

None.

Signature:

| D | \sim | |
|----------|--------|--|
| Right | | |
| Byte | 0: | |
| | | |

| 1 0 1 0 0 0 1 0 0 | xA2 |
|-------------------|-----|
|-------------------|-----|

Byte 1:

| 0 n n n n n n n less tha | n 0x78 |
|--------------------------|--------|
|--------------------------|--------|

Byte 2:

| | 0 | 0 | 0 | 0 | × | X | × | X |
|--|---|---|---|---|---|---|---|---|
|--|---|---|---|---|---|---|---|---|

Notes:

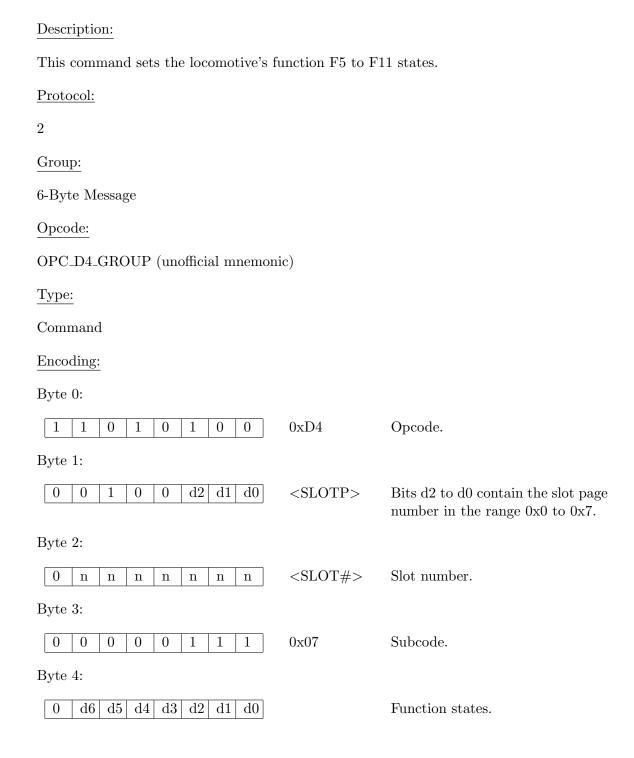
None.

1.4.31 LocoF7F13P2



| d5 F12 state d4 F11 state d3 F10 state d2 F9 state: d1 F8 state: | e: 1 means on and 0 mes: 1 means on and 0 mes: 1 means on and 0 mes: 1 means on and 0 means on a | means off. means off. means off. neans off. neans off. | |
|--|--|--|-----------|
| Byte 5: | | | |
| $\begin{bmatrix} 0 & n & n & n \end{bmatrix}$ | n n n | <chk></chk> | Checksum. |
| Response: | | | |
| None. | | | |
| Signature: | | | |
| Byte 0: | | | |
| $\begin{array}{ c c c c c }\hline 1 & 1 & 0 & 1 \\ \hline \end{array}$ | 0 1 0 1 | 0xD5 | |
| Byte 1: | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | $1 \times \times \times$ | | |
| Byte 2: | | | |
| $\begin{bmatrix} 0 & n & n & n \end{bmatrix}$ | n n n n | less than 0x78 | |
| Notes: | | | |
| None. | | | |

1.4.32 LocoF5F11P2

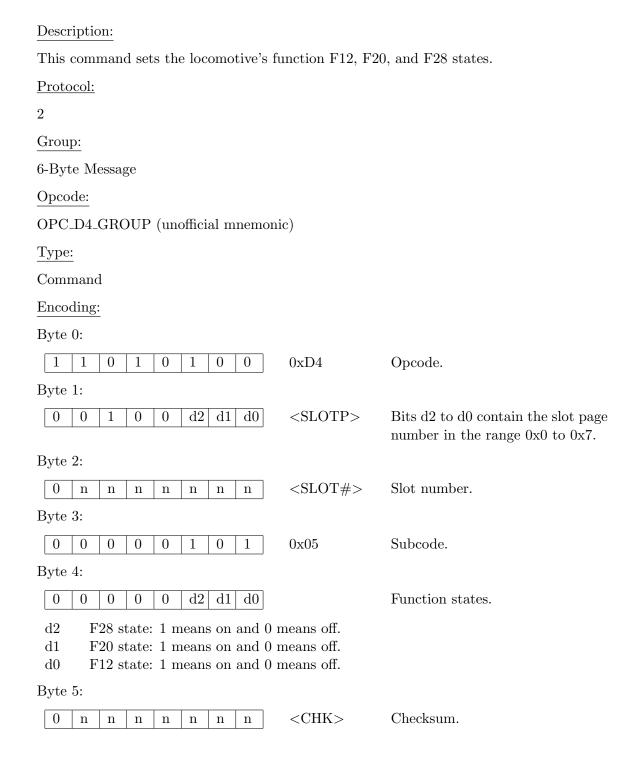


| | d6 | \mathbf{F} | 11 st | tate: | 1 m | eans | s on | and | 0 means off. | | | | |
|--------------------------|--|--------------|--------------|-------|-----------------------|------|------|------|--------------|-----------|--|--|--|
| | d5 | \mathbf{F} | 10 st | tate: | 1 m | eans | s on | and | 0 means off. | | | | |
| | d4 F9 state: 1 means on and 0 means off. | | | | | | | | | | | | |
| | d3 | | | | | | | | | | | | |
| | d2 F7 state: 1 means on and 0 means off. | | | | | | | | | | | | |
| | d1 F6 state: 1 means on and 0 means off. | | | | | | | | | | | | |
| | d0 | \mathbf{F} | $5~{ m sta}$ | ate: | $1 \text{ m}\epsilon$ | eans | on a | nd 0 | means off. | | | | |
| В | yte | 5: | | | | | | | | | | | |
| | 0 | n | n | n | n | n | n | n | <chk></chk> | Checksum. | | | |
| R | Response: | | | | | | | | | | | | |
| N | one. | | | | | | | | | | | | |
| $\underline{\mathbf{S}}$ | igna | ture | <u>:</u> | | | | | | | | | | |
| В | yte | 0: | | | | | | | | | | | |
| | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0xD4 | | | | |
| В | yte | 1: | | | | | | | | | | | |
| | 0 | 0 | 1 | 0 | 0 | × | × | × | | | | | |
| В | Syte | 3: | | | | | | | | | | | |
| | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0x07 | | | | |

$\underline{\text{Notes:}}$

This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

1.4.33 LocoF12F20F28P2



| Response: | | | | | | | | | | |
|---------------------------------------|---|---|---|---|---|---|------|--|--|--|
| None. | | | | | | | | | | |
| Signature: | | | | | | | | | | |
| Byte 0: | | | | | | | | | | |
| 1 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0xD4 | | | |
| Byte 1: | | | | | | | | | | |
| 0 0 | 1 | 0 | 0 | × | × | × | | | | |
| Byte 3: | | | | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 0 | 0 | 0 | 1 | 0 | 1 | 0x05 | | | |
| Byte 4: | | | | | | | | | | |
| | | | | | | | | | | |

 \times | \times | \times

Notes:

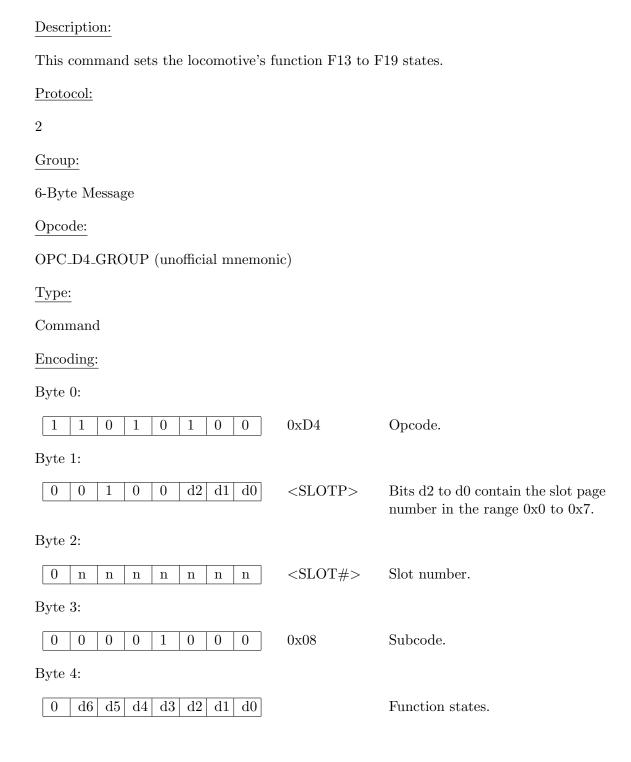
0 0

0

 $0 \mid 0$

This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

1.4.34 LocoF13F19P2



```
d6
        F19 state: 1 means on and 0 means off.
 d5
        F18 state: 1 means on and 0 means off.
 d4
        F17 state: 1 means on and 0 means off.
 d3
        F16 state: 1 means on and 0 means off.
 d2
        F15 state: 1 means on and 0 means off.
 d1
        F14 state: 1 means on and 0 means off.
 d0
        F13 state: 1 means on and 0 means off.
Byte 5:
 0
                                         <CHK>
                                                         Checksum.
      \mathbf{n}
           n
               n
                    \mathbf{n}
                        \mathbf{n}
                             n
                                 n
Response:
None.
Signature:
Byte 0:
  1
       1
           0
                1
                    0
                        1
                             0
                                 0
                                         0xD4
Byte 1:
           1
                0
                    0
                             \times
                                 X
Byte 3:
  0
       0
           0
                0
                        0
                                         0x08
```

Notes:

1

0

0

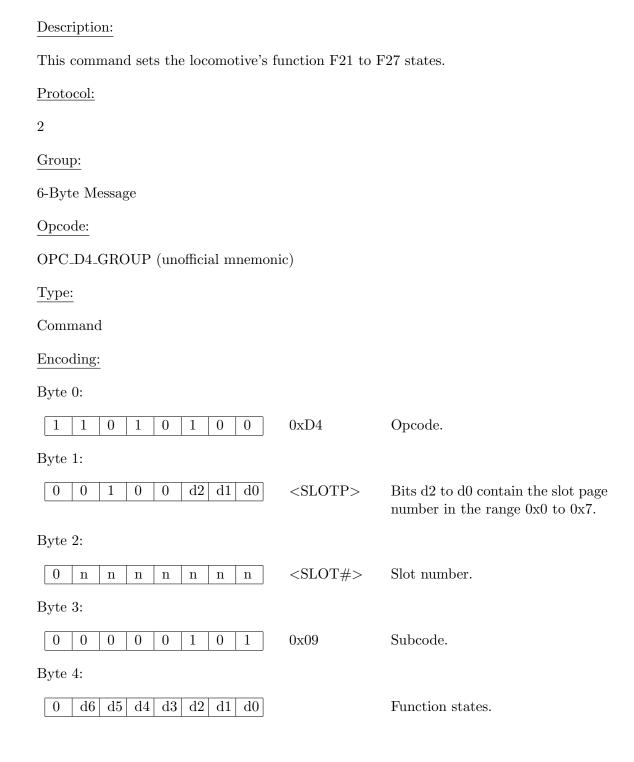
This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

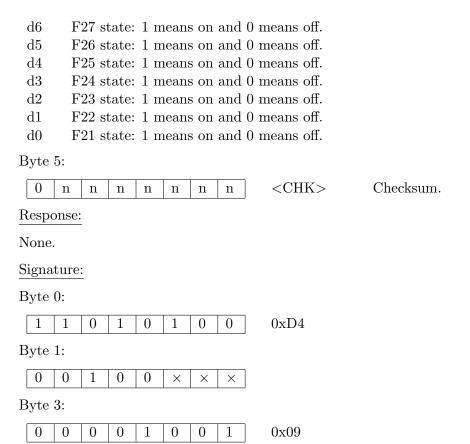
1.4.35 LocoF14F20P2



| d6 F20 state: 1 means on and 0 means off. |
|--|
| d5 F19 state: 1 means on and 0 means off. |
| d4 F18 state: 1 means on and 0 means off. |
| d3 F17 state: 1 means on and 0 means off. |
| d2 F16 state: 1 means on and 0 means off. |
| d1 F15 state: 1 means on and 0 means off. |
| d0 F14 state: 1 means on and 0 means off. |
| Byte 5: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Response: |
| None. |
| Signature: |
| Byte 0: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 1: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 2: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Notes: |
| None. |

1.4.36 LocoF21F27P2

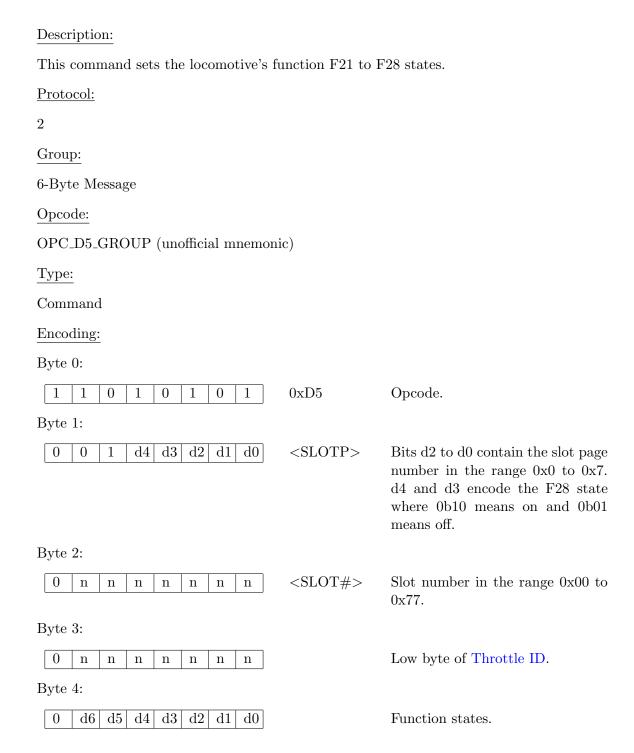




Notes:

This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

1.4.37 LocoF21F28P2



| d6 F27 state: 1 means on and 0 means off. |
|--|
| d5 F26 state: 1 means on and 0 means off. |
| d4 F25 state: 1 means on and 0 means off. |
| d3 F24 state: 1 means on and 0 means off. |
| d2 F23 state: 1 means on and 0 means off. |
| d1 F22 state: 1 means on and 0 means off. |
| d0 F21 state: 1 means on and 0 means off. |
| Byte 5: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Response: |
| None. |
| Signature: |
| Byte 0: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Byte 1: |
| |
| Byte 2: |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
| Notes: |
| None. |

1.4.38 LocoSlotDataP1

| Description: | | |
|--|------------------|--|
| This response provides the data for a | specific locomot | tive slot. |
| Protocol: | | |
| 1 | | |
| Group: | | |
| Variable-Byte Message | | |
| Opcode: | | |
| OPC_SL_RD_DATA | | |
| Type: | | |
| Response | | |
| Encoding: | | |
| Byte 0: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xE7 | Opcode. |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x0E | Message length (14 bytes). |
| Byte 2: | | |
| | <slot#></slot#> | Slot number in the range 0x00 to 0x77. Slot 0x00 is the dispatch special slot. |
| Byte 3: | | special slot. |
| d7 d6 d5 d4 d3 d2 d1 d0 | <stat1></stat1> | Slot status 1. |
| <u>d7</u> <u>d6</u> | | |
| 0 Free, no consist | _ | |
| 0 1 Consist sub-mer | | |
| 1 0 Consist top-mer | nber. | |

Consist Mid-Consist member.

1

1

Note: d7 is set to 0 in the message by the command station and so may not correctly reflect the actual setting in the slot table.

| | d5 | $\underline{d4}$ | |
|-----------------------|-----------------------|-----------------------|---|
| | 0 | 0 | Free slot, no valid data. Not refreshed. |
| | 0 | 1 | Common. Locomotive address in this slot. Refreshed. |
| | 1 | 0 | Idle. Locomotive address in this slot. Not refreshed. |
| | 1 | 1 | In Use. Locomotive address in this slot. Refreshed. |
| | | <u>d3</u> | |
| | | 0 | No slot consist linked into this slot. |
| | | 1 | Slot consist linked into this slot. |
| | | | |
| $\underline{d2}$ | <u>d1</u> | $\underline{d0}$ | |
| $\frac{d2}{0}$ | $\frac{d1}{0}$ | $\frac{d0}{0}$ | 28 step decoder. 3-byte packet regular mode |
| | | | 28 step decoder. 3-byte packet regular mode 28 step decoder. Generate trinary packets for this mobile address |
| 0 | 0 | 0 | |
| 0 | 0 | 0 1 | 28 step decoder. Generate trinary packets for this mobile address |
| 0 0 0 | 0 0 1 | 0 1 0 | 28 step decoder. Generate trinary packets for this mobile address 14 step decoder. |
| 0 0 0 0 | 0 0 1 1 | 0 1 0 1 | 28 step decoder. Generate trinary packets for this mobile address 14 step decoder. 128 step decoder. |
| 0 0 0 0 1 | 0 0 1 1 0 | 0 1 0 1 0 | 28 step decoder. Generate trinary packets for this mobile address14 step decoder.128 step decoder.28 step decoder. Allow advanced consisting |

Byte 4:

| 0 | n | n | n | n | n | n | n | <adr></adr> |
|---|----|----|----|----|----|----|----|-------------|
| U | 11 | 11 | 11 | 11 | 11 | 11 | 11 | \11D10/ |

If <ADR2> is 0 then this contains the NMRA short address. If <ADR2> is greater than 0 then this contains the low 7 bits of the NMRA long address.

Byte 5:

| 0 | n | n | n | n | n | n | n | <spd></spd> |
|---|---|---|---|---|---|---|---|-------------|
|---|---|---|---|---|---|---|---|-------------|

Speed in the range 0x00 to 0x7F. 0x00 means inertial stop and 0x01 means emergency stop. Other values mean increasing speed.

Byte 6:

| 0 | 0 | d5 | d4 | d3 | d2 | d1 | d0 | <dirf></dirf> | Locomotive | ${\rm direction}$ | and | state |
|---|---|----|----|----|----|----|----|---------------|--------------|-------------------|-----|-------|
| | | | | | | | | | of functions | F0 to F4. | | |

- d5 Direction: 1 means forward and 0 means backwards.
- d4 F0 state: 1 means on and 0 means off.
- d3 F4 state: 1 means on and 0 means off.
- d2 F3 state: 1 means on and 0 means off.
- d1 F2 state: 1 means on and 0 means off.
- d0 F1 state: 1 means on and 0 means off.

Byte 7:

| 0 | d6 0 | 0 | d3 | d2 | d1 | d0 | <TRK $>$ | Global system track status. |
|---|------|---|----|----|----|----|----------|-----------------------------|
|---|------|---|----|----|----|----|----------|-----------------------------|

- d6 1 means this command station implements protocol 2 messages. This can be turned off on the DCS240 by setting the OpSw 44 to be closed.
- d3 1 means the programming track is busy.
- d2 1 means this command station implements protocol 1 messages and 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on in the command station and the track power is on.

Byte 8:

| 0 | 0 | 0 | 0 | d3 | d2 | 0 | d0 | $\langle SS2 \rangle$ | Slot status 2. |
|---|---|---|---|----|----|---|----|-----------------------|----------------|
|---|---|---|---|----|----|---|----|-----------------------|----------------|

- d3 1 means expansion in ID1/2, 0 means encoded alias.
- d2 1 means expansion ID1/2 is not ID usage.
- d0 1 means this slot has suppressed advanced consist.

Byte 9:

| 0 | n | n | n | n | n | n | n | <adr2></adr2> | If $\langle ADR2 \rangle$ is greater than 0 then |
|---|---|---|---|---|---|---|---|---------------|--|
| | | | | | | | | | this contains the high 7 bits of the |
| | | | | | | | | | NMRA long address. |

Byte 10:

| 0 | 0 | 0 | 0 | d3 | d2 | d1 | d0 | $\langle SND \rangle$ | Function F5 to F8 states. |
|---|---|---|---|----|----|----|----|-----------------------|---------------------------|
| | | | | | | | | | |

- d3 F8 state: 1 means on and 0 means off.
- d2 F7 state: 1 means on and 0 means off.
- d1 F6 state: 1 means on and 0 means off.
- d0 F5 state: 1 means on and 0 means off.

Byte 11:

| 0 | n | n | n | n | n | n | n | <id1></id1> | 7-bit ls ID code written by throt- |
|---|---|---|---|---|---|---|---|-------------|------------------------------------|
| | | | | | | | | | tle when $STAT2.4 = 1$. |

Byte 12:

Byte 13:

Response:

None.

Signature:

Byte 0:

1 1 1 0 0 1 1 1 0 0xE7

Byte 1:

0 0 0 0 1 1 1 0 0x0E

Byte 2:

Byte 6:

Byte 7:

Byte 8:

Byte 10:

Notes:

None.

1.4.39 LocoSlotDataP2

| T . | • | . • | |
|--------|-------|-------|--|
| 1 1000 | rin | tion: | |
| Desc | עונגע | ulom. | |
| | | | |

This response provides data for a specific locomotive slot.

Protocol:

2

Group:

Variable-Byte Message

Opcode:

OPC_SL_RD_DATA_P2 (unofficial mnemonic)

Type:

Response

Encoding:

Byte 0:

| 1 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0xE6 | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

| 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0x15 | Message length (21 bytes). |
|---|---|---|---|---|---|---|---|------|----------------------------|
|---|---|---|---|---|---|---|---|------|----------------------------|

Byte 2:



Byte 3:

| 0 | n | n | n | n | n | n | n | <SLOT $#>$ | Slot number in the range 0x00 to |
|---|---|---|---|---|---|---|---|------------|----------------------------------|
| | | | | | | | | | 0x77. |

Byte 4:

1

1

| 0 | d6 d5 | 6 d4 | d3 d2 | d1 | d0 | $\langle STAT1 \rangle$ | Slot status 1. | | | | |
|---|---------|-----------|-------|---------------------------|----|-------------------------|----------------|--|--|--|--|
| | d7 | <u>d6</u> | | | | | | | | | |
| | 0 | 0 | Free | Free, no consist linking. | | | | | | | |
| | 0 | | | | | | | | | | |
| | 1 | 0 | Con | Consist top-member. | | | | | | | |

Consist Mid-Consist member.

Note: d7 is set to 0 in the message by the command station and so may not correctly reflect the actual setting in the slot table.

| $\underline{\mathrm{d}5}$ | $\underline{d4}$ | |
|---------------------------|------------------|---|
| 0 | 0 | Free slot, no valid data. Not refreshed. |
| 0 | 1 | Common. Locomotive address in this slot. Refreshed. |
| 1 | 0 | Idle. Locomotive address in this slot. Not refreshed. |
| 1 | 1 | In Use. Locomotive address in this slot. Refreshed. |

d3
0 No slot consist linked into this slot.
1 Slot consist linked into this slot.

| $\underline{d2}$ | $\underline{d1}$ | $\underline{d0}$ | |
|------------------|------------------|------------------|---|
| 0 | 0 | 0 | 28 step decoder. 3-byte packet regular mode |
| 0 | 0 | 1 | 28 step decoder. Generate trinary packets for this mobile address |
| 0 | 1 | 0 | 14 step decoder. |
| 0 | 1 | 1 | 128 step decoder. |
| 1 | 0 | 0 | 28 step decoder. Allow advanced consisting |
| 1 | 0 | 1 | reserved |
| 1 | 1 | 0 | reserved |
| 1 | 1 | 1 | 128 step decoder. Allow advanced consisting |

Byte 5:

| 0 |) n | n | n | n | n | n | n | <ADR $>$ | Low ad |
|---|-------|---|---|---|---|---|---|----------|--------|

Byte 6:

| 0 | n | n | n | n | n | n | n | <adr2></adr2> | High address. |
|---|---|---|---|---|---|---|---|---------------|---------------|
| | | l | | l | | | | | 0 |

Byte 7:

| 0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | <trk></trk> | Global system track status. |
|---|----|----|----|----|----|----|----|-------------|-----------------------------|
|---|----|----|----|----|----|----|----|-------------|-----------------------------|

- d6 1 means this command station implements protocol 2 messages. This can be turned off on the DCS240 by setting the OpSw 44 to be closed.
- d5 Reserved. Set to 0.
- d4 Reserved. Set to 0.
- d3 1 means the programming track is busy.
- d2 1 means this command station implements protocol 1 messages. 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on in the command station and track power is on.

Byte 8:

0 d6 d5 d4 d3 d2 d1 d0 <SPD>

Speed in the range 0x00 to 0x7F. 0x00 means inertial stop and 0x01 means emergency stop. Other values mean increasing speed.

Byte 9:

d6 F28 state: 1 means on and 0 means off d5 F20 state: 1 means on and 0 means off d4 F12 state: 1 means on and 0 means off d3

Functions.

Byte 10:

d2 d1 d0

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0

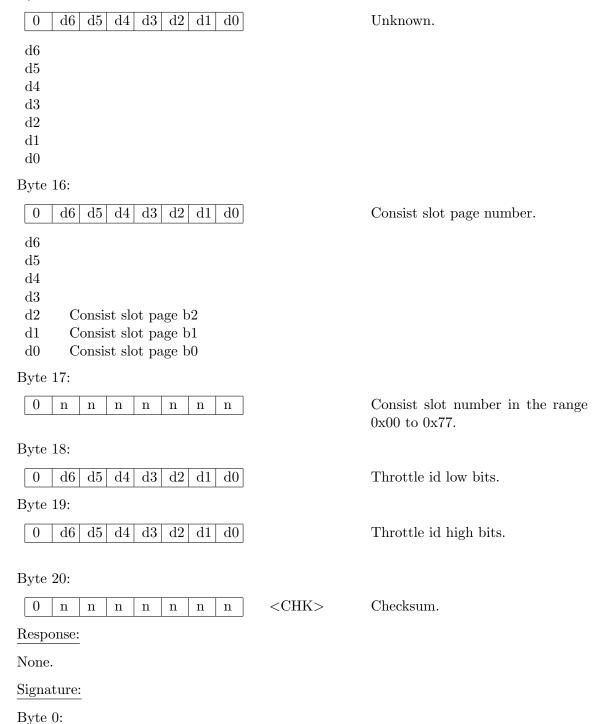
Direction and Functions.

d6

- d5 Direction: 1 means forwards and 0 means backwards
- d4 F0 state: 1 means on and 0 means off
- d3 F4 state: 1 means on and 0 means off
- d2 F3 state: 1 means on and 0 means off
- d1 F2 state: 1 means on and 0 means off
- d0 F1 state: 1 means on and 0 means off

Byte 11:

| 0 | d6 d5 d4 d3 d2 d1 d0 | Functions. |
|---|---|------------------|
| d6 | F11 state: 1 means on and 0 means off | |
| d5 | F10 state: 1 means on and 0 means off | |
| d4 | F9 state: 1 means on and 0 means off | |
| d3 | F8 state: 1 means on and 0 means off | |
| d2 | F7 state: 1 means on and 0 means off | |
| d1 | F6 state: 1 means on and 0 means off | |
| d0 | F5 state: 1 means on and 0 means off | |
| Byte | 12: | |
| 0 | d6 d5 d4 d3 d2 d1 d0 | Functions. |
| d6 | F19 state: 1 means on and 0 means off | |
| d5 | F18 state: 1 means on and 0 means off | |
| d4 | F17 state: 1 means on and 0 means off | |
| d3 | F16 state: 1 means on and 0 means off | |
| d2 | F15 state: 1 means on and 0 means off | |
| d1 | F14 state: 1 means on and 0 means off | |
| d0 | F13 state: 1 means on and 0 means off | |
| Byte | 13: | |
| 0 | d6 d5 d4 d3 d2 d1 d0 | Functions. |
| | | |
| d6 | F27 state: 1 means on and 0 means off | |
| d6 $d5$ | F27 state: 1 means on and 0 means off F26 state: 1 means on and 0 means off | |
| | | |
| d5 | F26 state: 1 means on and 0 means off | |
| d5 d4 d3 d2 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off | |
| d5 d4 d3 d2 d1 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off | |
| d5 d4 d3 d2 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off | |
| d5 d4 d3 d2 d1 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | |
| d5 d4 d3 d2 d1 d0 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | Unknown. |
| d5 d4 d3 d2 d1 d0 Byte | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | Unknown. |
| d5 d4 d3 d2 d1 d0 Byte | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | Unknown. |
| d5 d4 d3 d2 d1 d0 Byte 0 d6 d5 d4 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | ${ m Unknown}.$ |
| d5 d4 d3 d2 d1 d0 Byte 0 d6 d5 d4 d3 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | Unknown. |
| d5 d4 d3 d2 d1 d0 Byte 0 d6 d5 d4 d3 d2 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | Unknown. |
| d5 d4 d3 d2 d1 d0 Byte 0 d6 d5 d4 d3 d2 d1 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | ${\rm Unknown.}$ |
| d5 d4 d3 d2 d1 d0 Byte 0 d6 d5 d4 d3 d2 | F26 state: 1 means on and 0 means off F25 state: 1 means on and 0 means off F24 state: 1 means on and 0 means off F23 state: 1 means on and 0 means off F22 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off F21 state: 1 means on and 0 means off | Unknown. |



| 1 1 1 0 0 1 1 0 | 0xE6 |
|--|------|
| Byte 1: | |
| 0 0 0 1 0 1 0 1 | 0x15 |
| Byte 2: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 7: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |

Notes:

1.4.40 LocoSpdP1

Description:

 $1 \quad 0$

0

1

0

0 0

0

0xA0

This command sets the locomotive's speed in the range 0 to 127. 0 means inertial stop and 1 means emergency stop. Other values mean increasing speed.

Protocol: 1 Group: 4-Byte Message Opcode: OPC_LOCO_SPD Type: Command Encoding: Byte 0: 1 Opcode. 0 1 0 0 0 0 0 0xA0Byte 1: 0 n n <SLOT#>Slot number in the range 0x00 to \mathbf{n} n \mathbf{n} \mathbf{n} n 0x77.Byte 2: 0 $\langle SPD \rangle$ Locomotive speed in the range 0 n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} n to 127. Byte 3: 0 n \mathbf{n} <CHK>Checksum. n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} Response: None. Signature: Byte 0:

Byte 1:

| 0 | n n | n | n | n | n | n | less than 0x78 |
|---|-----|---|---|---|---|---|----------------|
|---|-----|---|---|---|---|---|----------------|

Notes:

1.4.41 LocoSpdP2

Description:

Byte 5:

0

n

n n

 \mathbf{n}

 $n \mid n$

n

This function sets the locomotive's speed in the range 0 to 127. 0 means inertial stop and 1 means emergency stop. Other values mean increasing speed.

Protocol: 2 Group: 6-Byte Message Opcode: OPC_D4_GROUP (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 Opcode. 1 0 1 0 1 0 0 0xD4Byte 1: 0 0 0 d2d1 d0<SLOTP> Bits d2 to d0 contain the slot page 0 1 number in the range 0x0 to 0x7. Byte 2: 0 <SLOT#>Slot number in the range 0x00 to n \mathbf{n} \mathbf{n} n \mathbf{n} \mathbf{n} \mathbf{n} 0x77.Byte 3: 0 0 0 0 0 1 0 0 0x04Subcode. Byte 4: 0 $\langle SPD \rangle$ Locomotive speed in the range n n n n \mathbf{n} n \mathbf{n} 0x00 to 0x7F.

<CHK>

Checksum.

| Response | <u>e:</u> | | | | | | |
|---------------------------------------|-----------|---|---|---|---|---|------|
| None. | | | | | | | |
| Signatur | <u>e:</u> | | | | | | |
| Byte 0: | | | | | | | |
| 1 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0xD4 |
| Byte 1: | | | | | | | |
| $\begin{bmatrix} 0 & 0 \end{bmatrix}$ | 1 | 0 | 0 | × | × | × | |
| Byte 3: | | | | | | | |
| | | | | | | | |

1

0

0

Notes:

0

0

0

0 0

This command was identified in the output from the iTrain commercial model railway control application. It has not been made to work outside of the original context. It is included in this manual as the "missing link" information that enables it to function may be found in the future. Until that time use the D5 Group commands for protocol 2 control.

0x04

Locomotive speed in the range

0x00 to 0x7F.

LocoSpdDirP2 1.4.42

Description:

n

n

n

n

 \mathbf{n}

n

 \mathbf{n}

This function sets the locomotive's speed in the range 0 to 127 and direction. 0 means

inertial stop and 1 means emergency stop. Other values mean increasing speed. Protocol: 2 Group: 6-Byte Message Opcode: OPC_D5_GROUP (unofficial mnemonic) Type: Command Encoding: Byte 0: 1 0 0 0xD5Opcode. 1 1 0 1 1 Byte 1: 0 0 0 0 d3d2d1d0<SLOTP> Bits d2 to d0 contain the slot page number in the range 0x0 to 0x7. Bit d3 contains the direction where 1 means forwards and 0 means backwards. Byte 2: 0 <SLOT#>Slot number in the range 0x00 to \mathbf{n} n \mathbf{n} n n \mathbf{n} n 0x77.Byte 3: Low byte of the Throttle ID. 0 n n n \mathbf{n} \mathbf{n} \mathbf{n} n Byte 4:

 $\langle SPD \rangle$

| T . | _ |
|------|------|
| Byte | h. • |
| DVUC | ٠,, |
| | |

| $\mid 0 \mid n \mid$ Checks |
|--|
|--|

Response:

None.

Signature:

Byte 0:

| 1 | 1 | n | 1 | n | 1 | n | 1 | 0.5 |
|---|---|---|---|---|---|---|---|------|
| 1 | 1 | U | 1 | U | 1 | U | 1 | OXDO |

Byte 1:

Byte 2:

| 0 | n | n | n | n | n | n | n | less than 0x78 |
|---|----|----|----|----|----|----|----|----------------|
| 0 | 11 | ** | 11 | 11 | 11 | 11 | 11 | 1000 than 0210 |

Notes:

1.4.43 NoFreeSlotsP1

| Description: | | |
|--|------------------|--------------------------|
| The NoFreeSlotsP1 response means | s that there are | no Free slots available. |
| Protocol: | | |
| 1 | | |
| Group: | | |
| 4-Byte Message | | |
| Opcode: | | |
| OPC_LONG_ACK | | |
| Type: | | |
| Response | | |
| Encoding: | | |
| Byte 0: | | |
| 1 0 1 1 0 1 0 0 | 0xB4 | Opcode. |
| Byte 1: | | |
| 0 0 1 1 1 1 1 1 | 0x3F | |
| Byte 2: | | |
| 0 0 0 0 0 0 0 0 | 0x00 | |
| Byte 3: | | |
| 0 1 1 1 0 1 0 0 | 0x74 | Check sum. |
| Response: | | |
| None | | |
| Byte 0: | | |
| 1 0 1 1 0 1 0 0 | 0xB4 | |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x3F | |

Byte 2:

| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 | |
|--|--|---|---|---|---|---|---|---|---|------|--|
|--|--|---|---|---|---|---|---|---|---|------|--|

Byte 3:

| (| 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0x74 |
|---|---|---|---|---|---|---|---|------|
|---|---|---|---|---|---|---|---|------|

Notes:

1.4.44 NoFreeSlotsP2

| Description: | | | | | | | |
|--|------|------------|--|--|--|--|--|
| The NoFreeSlotsP2 response means that there are no Free slots available. | | | | | | | |
| Protocol: | | | | | | | |
| 2 | | | | | | | |
| Group: | | | | | | | |
| 4-Byte Message | | | | | | | |
| Opcode: | | | | | | | |
| OPC_LONG_ACK | | | | | | | |
| Type: | | | | | | | |
| Response | | | | | | | |
| Encoding: | | | | | | | |
| Byte 0: | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xB4 | Opcode. | | | | | |
| Byte 1: | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x3E | | | | | | |
| Byte 2: | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 | | | | | | |
| Byte 3: | | | | | | | |
| 0 1 1 1 0 1 0 1 | 0x75 | Check sum. | | | | | |
| Response: | | | | | | | |
| None | | | | | | | |
| Byte 0: | | | | | | | |
| 1 0 1 1 0 1 0 0 | 0xB4 | | | | | | |
| Byte 1: | | | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x3E | | | | | | |

Byte 2:

0 0 0 0 0 0 0 0 0 0 000

Byte 3:

0 1 1 1 0 1 0 1 0x75

Notes:

1.4.45 MoveSlotsP1

| T | | . • |
|----------|------|--------|
| I)es | crin | tion: |
| | CIIP | ororr. |

Move slots.

| $\underline{\operatorname{SRC}}$ | $\overline{\mathrm{DEST}}$ | Action |
|----------------------------------|----------------------------|---|
| 0 | × | Dispatch get. Return LocoSlotDataP1 of dispatch slot. |
| SRC | 0 | Dispatch put. Mark slot as dispatch. |
| SRC | SRC | Null move. SRC is set to in use. |
| SRC | DEST | Move slot data from SRC to DEST if not in use. Clear SRC. |

Protocol:

1

Group:

4-Byte Message

Opcode:

OPC_MOVE_SLOTS

Type:

Command

Encoding:

0

1

1

Byte 0:

| Byte 1: | | |
|--|---------------|---|
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <src></src> | Source slot number in the range $0x00$ to $0x77$. |
| Byte 2: | | |
| | <dest></dest> | Destination slot number in the range $0x00$ to $0x77$. |
| Byte 3: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <CHK $>$ | Checksum. |

0xBA

Opcode.

Response:

 ${\bf LocoSlotDataP1} \ {\bf if} \ {\bf successful}, \ {\bf otherwise} \ {\bf IllegalMoveP1}$

0

1

0

| Signature: |
|------------|
| |

Byte 0:

|--|

Byte 1:

| | | | | | | | | • |
|---|---|---|---|---|---|---|---|---------------|
| 0 | n | n | n | n | n | n | n | less than 0x7 |

Byte 2:

| 0 | n | n | n | n | n | n | n | less than 0x78 |
|---|---|---|---|---|---|---|---|----------------|

Notes:

1.4.46 MoveSlotsP2

| T . | | . • |
|-----|------|--------|
| Des | crin | tion |
| | OLID | OIOII. |

Move slots.

| $\underline{\operatorname{SRC}}$ | $\overline{\mathrm{DEST}}$ | Action |
|----------------------------------|----------------------------|---|
| 0 | × | Dispatch get. Return LocoSlotDataP2 of dispatch slot. |
| SRC | 0 | Dispatch put. Mark slot as dispatch. |
| SRC | SRC | Null move. SRC is set to in use. |
| SRC | DEST | Move slot data from SRC to DEST if not in use. Clear SRC. |

$\underline{\text{Protocol:}}$

2

Group:

6-Byte Message

Opcode:

OPC_D4_GROUP (Unofficial Mnemonic)

0

 $1 \mid 0 \mid 0$

Type:

Command

Encoding:

0 1

Byte 0:

| Byte 1: | | |
|--------------------|---------------|---|
| 0 0 1 1 1 d2 d1 d0 | <srcp></srcp> | Bits d2 to d0 contain the source slot page number in the range $0x0$ to $0x7$. |

0xD4

Byte 2:

| 0 | n | n | n | n | n | n | n | <src></src> | Source slot number. |
|---|---|---|---|---|---|---|---|-------------|---------------------|

Byte 3:

| 0 | 0 | 0 | 0 | 0 | d2 | d1 | d0 | <DESTP $>$ | Bits d2 to d0 contain the destina- |
|---|---|---|---|---|----|----|----|------------|------------------------------------|
| | | | | | | | | | tion slot page number in the range |

0x0 to 0x7.

Opcode.

Byte 4:

1.4. MESSAGES 119 <DEST> 0 Destination slot number. n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} n Byte 5: 0 n Checksum. <CHK>n n \mathbf{n} \mathbf{n} n n Response: ${\bf LocoSlotDataP2} \ {\bf if} \ {\bf successful}, \ {\bf otherwise} \ {\bf IllegalMoveP2}.$ Signature:

Byte 0: 1 1 0 1 0 1 0 0 0xD4Byte 1: $0 \quad 0$ 1 1 1 \times X × Byte 3: 0 0 0 0 0 \times × \times

Notes:

1.4.47 PeerXfer16

Description:

This command sends the 8 bytes of data from one device to another peer to peer. This message takes many forms and so what is presented here is a generic description. The specific forms are included elsewhere as detailed messages in their own right.

| $\underline{\operatorname{SRC}}$ | $\overline{\mathrm{DSTL}}$ | $\overline{\mathrm{DSTH}}$ | Comments |
|----------------------------------|----------------------------|----------------------------|--|
| 0x00 | | | Source is command station. |
| Don't Care | 0x00 | 0x00 | Broadcast Message. |
| 0x70 to $0x7E$ | | | Reserved. |
| 0x7F | 0x00 | 0x00 | Broadcast throttle message transfer. |
| 0x7F | ID1 | ID2 | Throttle message transfer. ID1 and ID2 en- |
| | | | code ID. |

Protocol:

1

Group:

Variable-Byte Message

Opcode:

OPC_PEER_XFER

Type:

Message

Encoding:

Byte 0:

| _ 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0xE5 | Opcode. |
|------|------|---|---|---|---|---|---|-------------|---|
| Byte | e 1: | | | | | | | | |
| 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0x10 | Message length (16 bytes). |
| Byte | e 2: | | | | | | | | |
| 0 | n | n | n | n | n | n | n | <src></src> | Source id in the range $0x00$ to $0x7F$. |

Byte 3:

| 0 n n n n n n n |] <dstl></dstl> | Destination id low in the range $0x00$ to $0x7F$. |
|--|-------------------------|---|
| Byte 4: | | |
| |] <dsth></dsth> | Destination id high in the range $0x00$ to $0x7F$. |
| Byte 5: | | |
| 0 d6 d5 d4 d3 d2 d1 d0 |] <pxct1></pxct1> | Address type code and high bits of D1 to D4. |
| d6 XC2. Address type code. | | |
| d5 XC1. Address type code. | | |
| d4 XC0. Address type code. | | |
| d3 D4.7. High bit | | |
| d2 D3.7. High bit | | |
| d1 D2.7. High bit d0 D1.7. High bit | | |
| do Di.r. ingn bit | | |
| $\underline{\text{XC2}}$ $\underline{\text{XC1}}$ $\underline{\text{XC0}}$ | Meaning | |
| 0 0 | 7 bit peer to peer | addresses. |
| | reserved. | |
| | reserved. | |
| | reserved. IPL download. | |
| | reserved. | |
| | reserved. | |
| | reserved. | |
| Byte 6: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |] <d1></d1> | Data item 1. Low 7 bits. |
| Byte 7: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |] <d2></d2> | Data item 2. Low 7 bits. |
| Byte 8: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |] <d3></d3> | Data item 3. Low 7 bits. |
| Byte 9: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |] <d4></d4> | Data item 4. Low 7 bits. |
| Byte 10: | | |

| 0 | n | n | n | n | n | n | n |] <pxct2></pxct2> | Data type code and high bits for D5 to D8. |
|--|------------------|------------------------------------|--------------------------------------|--|-----------------------|-------------------|------------------|--|---|
| d6 d5 d4 d3 d2 d1 d0 | X X D D | C4. C3. 8.7. 7.7. 6.7. | Data Data High High High | a typa typa typa typa typa typa typa typ | pe co | de. | | | |
| $\frac{\text{XC5}}{2}$ | | | <u>KC4</u> | | <u>XC</u> | <u>23</u> | | Meaning | IDI I I I |
| 0 0 0 0 1 | | 0 1 1 0 |) | | 0 1 0 1 0 | | | ANSI text string. setup subcode. IPL download addr IPL download send IPL download verif IPL download end code. | ress subcode. I data subcode. Ty data subcode. |
| 1 | | C | | | 1 | | | reserved. | |
| 1 1 | | 1 1 | | | 0 1 | | | reserved. | |
| Option | ns fl | lags | | | | | | | |
| priva p | te riv riv | sta ate ate ate | star star | tic tic tic | fina fina fina | ali ali ali | nt (nt l nt l | CHECK_SOFTWARE_VE DO_NOT_CHECK_HARI REQUIRE_HARDWARE_ | E_VERSION = 0x00; ERSION_LESS = 0x04; OWARE_VERSION = 0x00; _VERSION_EXACT_MATCH = 0x01; OWARE_VERSIONS = 0x03; |
| Byte | 11: | | | | | | | | |
| 0 | n | n | n | n | n | n | n |] <d5></d5> | Data item 5. Low 7 bits. |
| Byte 3 | n | n | n | n | n | n | n |] <d6></d6> | Data item 6. Low 7 bits. |
| 0 | n | n | n | n | n | n | n |] <d7></d7> | Data item 7. Low 7 bits. |
| Byte | 14: | | | | | | | | |
| 0 | n | n | n | n | n | n | n |] <d8></d8> | Data item 8. Low 7 bits. |

| Byte | 15: |
|--|-----|
| \mathbf{D} , \mathbf{y} \mathbf{u} | 10. |

| 0 | n | n | n | n | n | n | n | <chk></chk> | Checksum. |
|---|---|---|---|---|---|---|---|-------------|-----------|
|---|---|---|---|---|---|---|---|-------------|-----------|

Response:

None

Signature:

Byte 0:

|--|

Byte 1:

| 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0x10 |
|---|---|---|---|---|---|---|---|------|

Notes:

1.4.48 **ProgCV**

Description:

The **ProgCV** command is used to read and write a locomotive's mobile decoder configuration variables.

Group:

Variable-Byte Message

Opcode:

OPC_WR_SL_DATA

Type:

Command

Encoding:

Byte 0:

| 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 0xEF | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

| 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0x0E | Message length. |
|---|---|---|---|---|---|---|---|------|-----------------|
|---|---|---|---|---|---|---|---|------|-----------------|

Byte 2:

| 0 1 1 1 1 0 0 0 0x7C Programming slot number. |
|---|
|---|

Byte 3:

| 0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | <PCMD $>$ | Programming command |
|---|----|----|----|----|----|----|----|-----------|---------------------|
|---|----|----|----|----|----|----|----|-----------|---------------------|

- d6 0 means read and 1 means write
- d5 1 means byte mode and 0 means bit mode
- d4 TY1
- d3 TY0
- d2 0 means service mode on programming track, 1 means operations mode on mainline.
- d1 1 unknown
- d0 1 unknown

| $\underline{\mathrm{d}5}$ | $\underline{d4}$ | $\underline{d3}$ | $\underline{d2}$ | $\underline{d1}$ | $\underline{d0}$ | Programming Mode |
|---------------------------|------------------|------------------|------------------|------------------|------------------|--------------------------------------|
| 1 | 0 | 0 | 0 | × | × | Paged mode byte read/write on ser- |
| | | | | | | vice track |
| 1 | 0 | 1 | 0 | × | × | Direct mode byte read/write on ser- |
| | | | | | | vice track |
| 0 | 0 | 1 | 0 | × | × | Direct mode bit read/write on ser- |
| | | | | | | vice track |
| \times | 1 | 0 | 0 | × | × | Physical register byte read/write on |
| | | | | | | service track |
| \times | 1 | 1 | 0 | × | × | Service track reserved function |
| 1 | 0 | 0 | 1 | × | × | Ops mode byte program on mainline |
| | | | | | | no feedback |
| 1 | 0 | 1 | 1 | × | × | Ops mode byte program on mainline |
| | | | | | | with feedback |
| 0 | 0 | 0 | 1 | × | × | Ops mode bit program on mainline |
| | | | | | | no feedback |
| 0 | 0 | 1 | 1 | × | × | Ops mode bit program on mainline |
| | | | | | | with feedback |
| | | | | | | |

Byte 4:

| | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
|--|---|---|---|---|---|---|---|------|
|--|---|---|---|---|---|---|---|------|

Byte 5:

| 0 | n | n | n | n | n | n | n | <HOPSA $>$ | In operations mode programming |
|---|---|---|---|---|---|---|---|------------|------------------------------------|
| | | | | | | | | | this contains the 7 high address |
| | | | | | | | | | bits of the locomotive to program. |

0x00 if service mode.

Byte 6:

| $\begin{bmatrix} 0 & n & n & n & n & n & n & n \end{bmatrix}$ < LOF |
|---|
|---|

In operations mode programming this contains the 7 low address bits of the locomotive to program. 0x00 if service mode.

Byte 7:

| | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
|--|---|---|---|---|---|---|---|---|------|
|--|---|---|---|---|---|---|---|---|------|

Byte 8:

| 0 | 0 | d5 | d4 | 0 | 0 | d1 | d0 | <cvh></cvh> |
|---|---|----|----|---|---|----|----|-------------|

Configuration Variable number high 3 bits and most significant bit of data byte.

| d5 CV9 d4 CV8 | | |
|--|---------------|--|
| d1 DATA7 d0 CV7 | | |
| Byte 9: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <cvh></cvh> | Configuration Variable number low 7 bits. CV1 is $0x0000$, CV2 is $0x0001$ etc. |
| Byte 10: | | |
| 0 n n n n n n n | <data></data> | Data value low 7 bits. |
| Byte 11: | | |
| | <snh></snh> | Throttle ID low 7 bits of low byte. |
| Byte 12: | CONIT | |
| 0 n n n n n n n n Byte 13: | <snl></snl> | Throttle ID low 7 bits of high byte. |
| 0 1 1 1 1 1 0 | 0x 7 E | Checksum. |
| Response: | ONTE | |
| Ack and if command is accepted a Pr | rogSlotData m | essage |
| Signature: | | |
| Byte 0: | | |
| | 0xEF | |
| Byte 1: | | |
| | 0x0E | |
| Byte 2: | | |
| | 0x7C | |
| Byte 4: 0 0 0 0 0 0 0 0 0 | 0x00 | |
| 0 0 0 0 0 0 0 0 0 0 0 0 Byte 7: | UXUU | |
| | 0x00 | |

Byte 8:

| _ | | | | | | | |
|---|---|-------|---|---|---|---|---|
| 0 | (|) × | × | 0 | 0 | × | × |

Notes:

1.4.49 ProgSlotDataP1

Description:

This response provides data for the programming slot.

Group:

Variable-Byte Message

Opcode:

OPC_SL_RD_DATA

Type:

Response

Encoding:

Byte 0:

| 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0xE7 | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

| 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0x0E | Message length |
|---|---|---|---|---|---|---|---|------|----------------|
|---|---|---|---|---|---|---|---|------|----------------|

Byte 2:

| Ω | 1 | 1 | 1 | 1 | 1 | 0 | Ω | 070 | Drogramming glot number |
|---|---|---|---|---|---|---|---|------|--------------------------|
| U | 1 | I | 1 | 1 | 1 | U | U | UXIC | Programming slot number. |

Byte 3:

| 0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | <pcmd></pcmd> | Last programming command. |
|---|----|----|----|----|----|----|----|---------------|---------------------------|
|---|----|----|----|----|----|----|----|---------------|---------------------------|

- d6 0 means read and 1 means write
- d5 1 means byte mode and 0 means bit mode
- d4 TY1
- d3 TY0
- d2 0 means service mode on programming track, 1 means operations mode on mainline.
- d1 1 unknown
- d0 1 unknown

| $\underline{d5}$ | $\underline{d4}$ | d3 | $\underline{d2}$ | $\underline{d1}$ | $\underline{d0}$ | Programming Mode |
|------------------|------------------|----|------------------|------------------|------------------|--------------------------------------|
| 1 | 0 | 0 | 0 | × | × | Paged mode byte read/write on ser- |
| | | | | | | vice track |
| 1 | 0 | 0 | 1 | × | × | Direct mode byte read/write on ser- |
| | | | | | | vice track |
| 0 | 0 | 0 | 1 | × | × | Direct mode bit read/write on ser- |
| | | | | | | vice track |
| × | 0 | 1 | 0 | × | × | Physical register byte read/write on |
| | | | | | | service track |
| × | 0 | 1 | 1 | × | × | Service track reserved function |
| 1 | 0 | 0 | 1 | × | × | Ops mode byte program no feedback |
| 1 | 0 | 1 | 1 | × | × | Ops mode byte program with feed- |
| | | | | | | back |
| 0 | 0 | 0 | 1 | × | × | Ops mode bit program no feedback |
| 0 | 0 | 1 | 1 | × | × | Ops mode bit program with feed- |
| | | | | | | back |

Byte 4:

| 0 0 0 0 d3 d2 d1 d0 < | |
|---------------------------------------|--|
|---------------------------------------|--|

- d3 1 means user aborted the previous command
- d2 1 means failed to detect read compare acknowledge from decoder
- d1 1 means no write acknowledge response from decoder
- $\,\mathrm{d}0-1$ means service mode programming track is empty no decoder detected

Byte 5:

| 0 | n | n | n | n | n | n | n | <HOPSA $>$ | In operations mode programming |
|---|---|---|---|---|------------------------------------|---|-----------------------|------------|----------------------------------|
| | | | • | | • | • | | | this contains the 7 high address |
| | | | | | bits of the locomotive to program. | | | | |
| | | | | | | | 0x00 if service mode. | | |

Byte 6:

| 0 | n | n | n | n | n | n | n | <lopsa> In operations mode programming</lopsa> |
|---|---|---|---|---|---|---|---|--|
| | • | | | | | | | this contains the 7 low address |
| | | | | | | | | bits of the locomotive to program. |
| | | | | | | | | 0x00 if service mode. |

Byte 7:

| 0 | d6 0 | 0 | d3 | d2 | d1 | d0 | <TRK $>$ | Global system track status. |
|---|------|---|----|----|----|----|----------|-----------------------------|
|---|------|---|----|----|----|----|----------|-----------------------------|

- d6 1 means this command station implements version 2 slot commands. This can be turned off on the DCS240 by setting the OpSw 44 to be closed.
- d3 1 means the programming track is busy.
- d2 1 means this master implements the Network version 1.1 capability, 0 means the master is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on in the master, global power up.

Byte 8:

| Byte 8: | | |
|--|------------------------------|--|
| 0 0 d5 d4 0 0 d1 d0 | <cvh></cvh> | Configuration Variable number high 3 bits and most significant bit of data byte. |
| d5 CV9 | | |
| d4 CV8 | | |
| d1 DATA7 | | |
| d0 CV7 | | |
| Byte 9: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <cvh></cvh> | Configuration Variable number low 7 bits. CV1 is 0x0000, CV2 is 0x0001 etc. |
| Byte 10: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <data></data> | Data value low 7 bits. |
| Byte 11: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | $\langle \text{SNH} \rangle$ | Throttle ID low 7 bits of low byte. |
| Byte 12: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | $\langle SNL \rangle$ | Throttle ID low 7 bits of high byte. |
| Byte 13: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7E | Checksum. |

Response:

None.

Signature:

| Byte | 0: | |
|------|----|---|
| 1 | 1 | 1 |

| 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0xE7 |
|---|---|---|---|---|---|---|---|------|
|---|---|---|---|---|---|---|---|------|

Byte 1:

| 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0x0E |
|---|---|---|---|---|---|---|---|------|

Byte 2:

| | | | | | | | | 1 |
|---|---|---|---|---|---|---|---|------|
| 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0x7C |

Byte 4:



Byte 7:



Byte 8:

Notes:

1.4.50 PwrOff

| Description: | |
|--|-----------|
| This command turns the track power off. | |
| Group: | |
| 2-Byte Message | |
| Opcode: | |
| OPC_GPOFF | |
| Type: | |
| Command | |
| Encoding: | |
| Byte 0: | |
| 1 0 0 0 0 0 1 0 0x82 | Opcode. |
| Byte 1: | |
| $\begin{bmatrix} 0 & 1 & 1 & 1 & 1 & 1 & 0 & 1 \end{bmatrix}$ 0x7D | Checksum. |
| Response: | |
| None. | |
| Signature: | |
| Byte 0: | |
| 1 0 0 0 0 0 1 0 0x82 | |
| Notes: | |
| None. | |
| | |

1.4.51 PwrOn

Description:

This command turns the track power on.

Group:

2-Byte Message

Opcode:

OPC_GPON

Type:

Command

Encoding:

Byte 0:

| 1 0 0 0 0 0 1 1 | 0x83 Opcode. |
|-----------------|--------------|
|-----------------|--------------|

Byte 1:

| _ | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 |

Response:

After power on the command station sends an **getCfgSlotDataP1** message. It also sends a sequence of OPC_SW_REQ messages with the following values of SW1 and SW2:

| $\underline{\mathrm{SW1}}$ | SW2 | Purpose |
|----------------------------|------|--------------------------------------|
| 0x78 | 0x27 | |
| 0x79 | 0x27 | |
| 0x7A | 0x27 | |
| 0x7B | 0x27 | |
| 0x78 | 0x07 | Interrogate all PM4 inputs? |
| 0x79 | 0x07 | Interrogate all BDL16 input reports? |
| 0x7A | 0x07 | Interrogate all SE8 input reports? |
| 0x7B | 0x07 | Interrogate all DS64 input reports. |

Signature:

Byte 0:

| 1 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0x83 |
|---|---|---|---|---|---|---|---|------|
|---|---|---|---|---|---|---|---|------|

Notes: None.

1.4.52 Reset

Description:

This broadcast message is sent by a command station when its "Loco Reset" button has been pressed. Software should reload any locally cached slot data from the command station.

Group:

2-Byte Message

Opcode:

OPC_LOCO_RESET

Type:

Broadcast

Encoding:

Byte 0:

| | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0x8A | Opcode. |
|--|---|---|---|---|---|---|---|---|------|---------|
|--|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

| | | | | | | | | 1 | |
|---|---|---|---|---|---|---|---|------|-----------|
| 0 | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 0x75 | Checksum. |

Response:

None.

Signature:

Byte 0:

| 1 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0x8A |
|---|---|---|---|---|---|---|---|------|

Notes:

$1.4.53 \quad Sens Rep Gen In$

Description:

General sensor input report.

Group:

4-Byte Message

Opcode:

OPC_INPUT_REP

Type:

Message

Encoding:

Byte 0:

| 1 | 0 | 1 | 1 | 0 | 0 | 1 | 0 | 0xB2 | Opcode |
|---|---|---|---|---|---|---|---|------|---------|
| _ | | | _ | | | | | | Opcouc. |

Byte 1:

```
0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | <IN1> Sensor address A7 to A1.
```

- d6 A7.
- d5 A6.
- d4 A5.
- d3 A4.
- d2 A3.
- d1 A2.
- d0 A1.

Byte 2:

| 0 | 1 | d5 | d4 | d3 | d2 | d1 | d0 | <IN $2>$ | Sensor address A11 to A8, A0 and |
|---|---|----|----|----|----|----|----|----------|----------------------------------|
| | | | | | | | | | sensor input state. |

- d5 A0.
- d4 Input state: 1 means sensor input >= 6V, and 0 means sensor input = 0V.
- d3 A11.
- d2 A10.
- d1 A9.
- d0 A8.

Byte 3:

Response:

None.

Signature:

Byte 0:

1 0 1 1 0 0 1 0 0xB2

Byte 2:

 $0 \mid 1 \mid \times \mid \times \mid \times \mid \times \mid \times \mid \times$

Notes:

1.4.54 SensRepTurnIn

Description:

Turnout sensor input report.

Group:

4-Byte Message

Opcode:

OPC_SW_REP

Type:

Message

Encoding:

Byte 0:

| 1 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 0xB1 | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
| | | | | | | | | | |

Byte 1:

```
0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | SN1> Sensor address A7 to A1.
```

- d6 A7.
- d5 A6.
- d4 A5.
- d3 A4.
- d2 A3.
- d1 A2.
- d0 A1.

Byte 2:

- d5 A0.
- d4 Input sensor state, 1 means sensor >= 6V, 0 means sensor = 0V.
- d3 A11.
- d2 A10.
- d1 A9.
- d0 A8.

Byte 3:

Response:

None.

Signature:

Byte 0:

1 0 1 1 0 0 0 1 0xB1

Byte 2:

Notes:

d0

A7.

${\bf 1.4.55 \quad Sens Rep Turn Out}$

| Description: | | |
|---|-------------------|--|
| Turnout sensor output report. | | |
| Group: | | |
| 4-Byte Message | | |
| Opcode: | | |
| OPC_SW_REP | | |
| Type: | | |
| Message | | |
| Encoding: | | |
| Byte 0: | | |
| | 0. D1 | 0 1 |
| 1 0 1 1 0 0 0 1 | 0xB1 | Opcode. |
| Byte 1: | | |
| $ \begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <sn1></sn1> | Sensor address A6 to A0. |
| d6 A6. | | |
| d5 A5. | | |
| d4 A4. | | |
| d3 A3. | | |
| d2 A2. | | |
| d1 A1. | | |
| d0 A0. | | |
| Byte 2: | | |
| 0 0 d5 d4 d3 d2 d1 d0 | <sn2></sn2> | Sensor address A10 to A7 and sensor state. |
| d5 0 means closed output line is o | ff and 1 means th | ue closed |
| output line is on. | ii and i means or | ic closed |
| d4 0 means thrown output line | is off and 1 me | eans the |
| thrown output line is on. | is on and 1 me | ans one |
| d3 A10. | | |
| d2 A9. | | |
| d1 A8. | | |

| \mathbf{D} | 0 |
|------------------|----|
| Byte | |
| \mathbf{D}_{V} | v. |
| | |

| 0 |
|---|
|---|

Response:

None.

Signature:

Byte 0:

| 1 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 0xB1 |
|---|---|---|---|---|---|---|---|------|

Byte 2:

Notes:

0

d6

 $d5 \mid d4 \mid$

d3

d2

 $d1 \mid d0 \mid$

1.4.56 SetBrdOpSw

Description: Set board OpSw. Group: 6-Byte Message Opcode: OPC_BRD_OPSW (Unofficial mnemonic) Type: Broadcast Encoding: Byte 0: 0xD0Opcode. 1 1 0 1 0 0 0 0 Byte 1: 0 d0The bit d0 is the most significant 0 0 bit of the board id. Byte 2: <BIDL>Least significant 7 bits of the 0 \mathbf{n} \mathbf{n} \mathbf{n} n \mathbf{n} \mathbf{n} \mathbf{n} board id. Byte 3: <BTYPE> Board type code. 0 n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} Type Code <u>Board</u> PM40x70.BDL16 0x71.SE8C 0x72.DS640x73.Byte 4:

The high nibble encodes the byte number, and the low nibble the bit number. The byte number is calculated as (OpSw# - 1) >> 3 and the bit number is (OpSw# - 1) - byte number \times 8.

Byte and bit number.

| Darto | Ξ. |
|-------|----|
| pyte | o: |
| | |

| ١ | 0 | n | n | n | n | n | n | n | <chk></chk> | Checksu |
|---|---|----|----|----|----|----|----|----|-------------|---------|
| | U | 11 | 11 | 11 | 11 | 11 | 11 | 11 | <0111X/ | Checksu |

Response:

 \mathbf{Ack}

Signature:

Byte 0:

| 1 | 1 | 0 | 1 | n | 0 | 0 | 0 | $0 \times D0$ |
|---|---|---|---|---|---|---|---|---------------|
| | | 0 | _ | 0 | U | 0 | 0 | OAD |

Byte 1:

| 0 | 1 | 1 | 1 | 0 | 0 | 1 | × |
|---|---|---|---|---|---|---|---|

Notes:

1.4.57 SetIdleState

| Description: | | | | | |
|--|---------------|-------------|---------|------------|----|
| This command sets the network to "emergency stop. | 'idle" state. | The command | station | broadcasts | an |
| Group: | | | | | |
| 2-Byte Message | | | | | |
| Opcode: | | | | | |
| OPC_IDLE | | | | | |
| Type: | | | | | |
| Command | | | | | |
| Encoding: | | | | | |
| Byte 0: | | | | | |
| 1 0 0 0 0 1 0 1 | 0x85 | Opcode. | | | |
| Byte 1: | | | | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x7A | Checksum. | | | |
| Response: | | | | | |
| None | | | | | |
| Signature: | | | | | |
| Byte 0: | | | | | |
| 1 0 0 0 0 1 0 1 | 0x85 | | | | |
| Notes: | | | | | |
| None. | | | | | |
| | | | | | |

1.4.58 SetLocoSlotDataP1

| T . | • | . • |
|------|-----|--------|
| Desc | rin | fion. |
| DCOC | TID | UIOII. |
| | | |

This command sets the locomotive slot data for the specified slot.

Protocol:

1

Group:

Variable-Byte Message

Opcode:

 $OPC_WR_SL_DATA$

Type:

Command

Encoding:

Byte 0:

| 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 0xEF | Opcode. |
|---|---|---|---|---|---|---|---|------|---------|
|---|---|---|---|---|---|---|---|------|---------|

Byte 1:

Byte 2:

| 0 | \mathbf{n} | n | n | n | n | n | n | $\langle SLOT\# \rangle$ Slot number in the range 0x00 to |
|---|--------------|---|---|---|---|---|---|---|
| | | | | | | | | 0x77. Slot $0x00$ is the dispatch |
| | | | | | | | | special slot. |

Byte 3:

1

| d7 | d6 d | d5 d4 | d3 d2 | d1 d | 10 | <stat1></stat1> | Slot status 1. |
|----|------|----------------|-------|----------|----|-----------------|----------------|
| | d7 | d6 | | | | | |
| | 0 | $\overline{0}$ | Free | | | | |
| | 0 | 1 | Con | sist sul | | | |
| | 1 | 0 | Con | sist top | -m | ember. | |

Consist Mid-Consist member.

Note: d7 is set to 0 in the message by the command station and so may not correctly reflect the actual setting in the slot table.

| | d5 0 0 1 1 | $\frac{d4}{0}$ 1 0 1 | Free slot, no valid data. Not refreshed. Common. Locomotive address in this slot. Refreshed. Idle. Locomotive address in this slot. Not refreshed. In Use. Locomotive address in this slot. Refreshed. |
|------------------|--|----------------------|--|
| | | <u>d3</u> | |
| | | 0 | No slot consist linked into this slot. |
| | | 1 | Slot consist linked into this slot. |
| $\underline{d2}$ | <u>d1</u> | <u>d0</u> | |
| 0 | 0 | 0 | 28 step decoder. 3-byte packet regular mode |
| 0 | 0 | 1 | 28 step decoder. Generate trinary packets for this mobile address |
| 0 | 1 | 0 | 14 step decoder. |
| 0 | 1 | 1 | 128 step decoder. |
| 1 | 0 | 0 | 28 step decoder. Allow advanced consisting |
| 1 | 0 | 1 | reserved |
| 1 | 1 | 0 | reserved |
| 1 | 1 | 1 | 128 step decoder. Allow advanced consisting |

Byte 4:

| | 0 | n | n | n | n | n | n | n | <ADR $>$ | If | < | <A $]$ | D] | R2> | is | 0 | then | this |
|--|---|---|---|---|---|---|---|---|----------|----|---|--------|----|-----|----|---|------|------|
|--|---|---|---|---|---|---|---|---|----------|----|---|--------|----|-----|----|---|------|------|

tains the NMRA short address. If <ADR2> is greater than 0 then this contains the low 7 bits of the NMRA long address.

con-

NMRA long addr

Byte 5:

| 0 | n | n | n | n | n | n | n | $\langle SPD \rangle$ | Speed in the range 0x00 to |
|---|---|---|---|---|---|---|---|-----------------------|----------------------------------|
| | | | • | | | | • | | 0x7F. 0x00 means inertial stop |
| | | | | | | | | | and $0x01$ means emergency stop. |
| | | | | | | | | | Other values mean increasing |

speed.

Byte 6:

| 0 | 0 | d5 | d4 | d3 | d2 | d1 | d0 | $\langle \text{DIRF} \rangle$ | Locomotive | direction | and | state |
|---|---|----|----|----|----|----|----|-------------------------------|--------------|-----------|-----|-------|
| | | | | | | | | | of functions | F0 to F4. | | |

- d5Direction: 1 means forward and 0 means backwards.
- d4F0 state: 1 means on and 0 means off.
- d3F4 state: 1 means on and 0 means off.
- d2F3 state: 1 means on and 0 means off.
- d1F2 state: 1 means on and 0 means off.
- d0F1 state: 1 means on and 0 means off.

Byte 7:

| - | | | | | | | | | | |
|---|---|----|---|---|----|----|----|----|----------|-----------------------------|
| | 0 | d6 | 0 | 0 | d3 | d2 | d1 | d0 | <TRK $>$ | Global system track status. |

- d61 means this command station implements protocol 2 messages. This can be turned off on the DCS240 by setting the OpSw 44 to be closed.
- d31 means the programming track is busy.
- d21 means this command station implements protocol 1 messages and 0 means the command station is a DT200.
- d10 means the track is paused, broadcast an emergency stop.
- d01 means the DCC packets are on in the command station and the track power is

Byte 8:

| 0 | 0 | 0 | 0 | d3 | d2 | 0 | d0 | $\langle SS2 \rangle$ | Slot status 2. |
|---|---|---|---|----|----|---|----|-----------------------|----------------|
|---|---|---|---|----|----|---|----|-----------------------|----------------|

- 1 means expansion in ID1/2, 0 means encoded alias. d3
- d21 means expansion ID1/2 is not ID usage.
- d01 means this slot has suppressed advanced consist.

Byte 9:

| 0 | n | n | n | n | n | n | n | <ADR2 $>$ | If $\langle ADR2 \rangle$ is greater than 0 then |
|---|---|---|---|---|---|---|---|-----------|--|
| | | | | | | | | | this contains the high 7 bits of the |
| | | | | | | | | | NMRA long address. |

Byte 10:

| 0 | 0 | 0 | 0 | d3 | d2 | d1 | d0 | $\langle SND \rangle$ | Function F5 to F8 states. |
|----|---|-------|------|------|-----|------|------|-----------------------|---------------------------|
| d3 | F | 8 sta | ate: | 1 me | ans | on a | nd 0 | means off | |

- F8 state: 1 means on and 0 means off.
- d2F7 state: 1 means on and 0 means off.
- d1F6 state: 1 means on and 0 means off.
- d0F5 state: 1 means on and 0 means off.

Byte 11:

| 0 | n | n | n | n | n | n | n | <id1></id1> | 7-bit ls ID code written by throt- |
|---|---|---|---|---|---|---|---|-------------|------------------------------------|
| | | | | | | | | | tle when $STAT2.4 = 1$. |

| Byte 12: | |
|--|--|
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <ID2> 7-bit ms ID code written by throttle when STAT2.4 = 1. |
| Byte 13: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <chk> Checksum.</chk> |
| Response: | |
| \mathbf{Ack} | |
| Signature: | |
| Byte 0: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xEF |
| Byte 1: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x0E |
| Byte 2: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | less than 0x78 |
| Byte 6: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 7: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 8: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Byte 10: | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | |
| Notes: | |
| None. | |

1.4.59 SetLocoSlotDataP2

${\bf Description:}$

This command sets the locomotive slot data for the specified slot number.

Protocol:

2

Group:

Variable-Byte Message

Opcode:

OPC_WR_SL_DATA_P2 (Unofficial mnemonic)

Type:

Command

Encoding:

Byte 0:

| | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 0 | 0xEE | Opcode. |
|---|---|---|---|---|---|---|---|---|------|---------|
| _ | | - | | | | | | | | |

Byte 1:

| 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0x15 | Message length | (21 | bytes). |
|---|---|---|---|---|---|---|---|------|----------------|-----|---------|
|---|---|---|---|---|---|---|---|------|----------------|-----|---------|

Byte 2:

| 0 | 0 | 0 | 0 | 0 | d2 | d1 | d0 | <SLOTP# $>$ | Slot page number in the range $0x0$ |
|---|---|---|---|---|----|----|----|-------------|-------------------------------------|
| | | | | | | | | | to $0x7$. |

Byte 3:

| 0 | n | n | n | n | n | n | n | <slot#></slot#> | Slot number | in t | $_{ m the}$ | range | 0x00 | to |
|---|---|---|---|---|---|---|---|-----------------|-------------|------|-------------|-------|------|----|
| | | | | | | | | , | 0x77. | | | | | |

Byte 4:

0

1

1

| 0 |) | d6 | d5 | d4 | d3 | d2 | d1 | d0 | <stat1></stat1> | Slot status 1. |
|---|---|---------------------------|----------|----------------|----|-------|----|------|-----------------|----------------|
| | | ď | 7 | d6 | | | | | | |
| | | $\frac{\underline{a}}{0}$ | <u>.</u> | $\frac{ao}{0}$ |] | Free, | no | cons | ist linking. | |
| | | 0 | | 1 | | | | | nember. | |

Consist Mid-Consist member.

Consist top-member.

Note: d7 is set to 0 in the message by the command station and so may not correctly reflect the actual setting in the slot table.

| d5 | $\underline{d4}$ | |
|-----------|------------------|---|
| 0 | 0 | Free slot, no valid data. Not refreshed. |
| 0 | 1 | Common. Locomotive address in this slot. Refreshed. |
| 1 | 0 | Idle. Locomotive address in this slot. Not refreshed. |
| 1 | 1 | In Use. Locomotive address in this slot. Refreshed. |
| | | |
| | $\underline{d3}$ | |
| | 0 | No slot consist linked into this slot. |
| | 1 | Slot consist linked into this slot. |
| | | |
| <u>d1</u> | $\underline{d0}$ | |
| 0 | 0 | 28 step decoder. 3-byte packet regular mode |
| 0 | 1 | 28 step decoder. Generate trinary packets for this mobile address |
| 1 | 0 | 14 step decoder. |
| 1 | 1 | 128 step decoder. |
| 0 | 0 | 28 step decoder. Allow advanced consisting |
| 0 | 1 | reserved |

128 step decoder. Allow advanced consisting

Byte 5:

1

1

0

1

reserved

 $\underline{d2}$

0

1 1 1

1

Byte 6:

| | | | | | | | | 1 | |
|---|---|---|---|---|---|---|---|---------------|---------------|
| 0 | n | n | n | n | n | n | n | <adr2></adr2> | High address. |

Byte 7:

| 0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | <trk></trk> | Global system track status. |
|---|----|----|----|----|----|----|----|-------------|-----------------------------|
|---|----|----|----|----|----|----|----|-------------|-----------------------------|

- d6 1 means this command station implements protocol 2 messages. This can be turned off on the DCS240 by setting the OpSw 44 to be closed.
- d5 Reserved. Set to 0.
- d4 Reserved. Set to 0.
- d3 1 means the programming track is busy.
- d2 1 means this command station implements protocol 1 messages. 0 means the command station is a DT200.
- d1 0 means the track is paused, broadcast an emergency stop.
- d0 1 means the DCC packets are on in the command station and track power is on.

Byte 8:

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0 | $\langle SPD \rangle$

Speed in the range 0x00 to 0x7F. 0x00 means inertial stop and 0x01 means emergency stop. Other values mean increasing speed.

Byte 9:

 0
 d6
 d5
 d4
 d3
 d2
 d1
 d0

 d6
 F28 state: 1 means on and 0 means off

 d5
 F20 state: 1 means on and 0 means off

 d4
 F12 state: 1 means on and 0 means off

 $\frac{d3}{d2}$

d1

d0

Byte 10:

0 | d6 | d5 | d4 | d3 | d2 | d1 | d0

Direction and Functions.

Functions.

d6d5 Direction: 1 means forwards and 0 means backwards

d4 F0 state: 1 means on and 0 means off

d3 F4 state: 1 means on and 0 means off

d2 F3 state: 1 means on and 0 means off

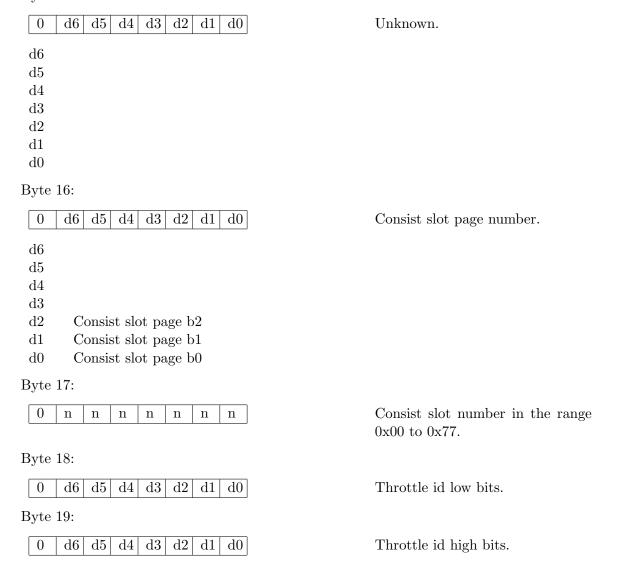
d1 F2 state: 1 means on and 0 means off

d0 F1 state: 1 means on and 0 means off

Byte 11:

| 0 d6 d5 | $oxed{d4} oxed{d3} oxed{d2} oxed{d1} oxed{d0}$ | Functions. |
|------------------|---|------------|
| d6 F11 s | tate: 1 means on and 0 means off | |
| | tate: 1 means on and 0 means off | |
| d4 F9 sta | ate: 1 means on and 0 means off | |
| d3 F8 sta | ate: 1 means on and 0 means off | |
| d2 F7 sta | ate: 1 means on and 0 means off | |
| d1 F6 sta | ate: 1 means on and 0 means off | |
| d0 F5 sta | ate: 1 means on and 0 means off | |
| Byte 12: | | |
| 0 d6 d5 | $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | Functions. |
| d6 F19 s | tate: 1 means on and 0 means off | |
| d5 F18 s | tate: 1 means on and 0 means off | |
| | tate: 1 means on and 0 means off | |
| | tate: 1 means on and 0 means off | |
| | tate: 1 means on and 0 means off | |
| | tate: 1 means on and 0 means off | |
| d0 F13 s | tate: 1 means on and 0 means off | |
| Byte 13: | | |
| 0 d6 d5 | $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | Functions. |
| d6 F27 s | tate: 1 means on and 0 means off | |
| d5 F26 s | tate: 1 means on and 0 means off | |
| $d4 	ext{F25 s}$ | tate: 1 means on and 0 means off | |
| d3 F24 s | tate: 1 means on and 0 means off | |
| $d2 	ext{F23 s}$ | tate: 1 means on and 0 means off | |
| | tate: 1 means on and 0 means off | |
| d0 F21 s | tate: 1 means on and 0 means off | |
| Byte 14: | | |
| 0 d6 d5 | $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | Unknown. |
| d6 | | |
| d5 | | |
| d4 | | |
| d3 | | |
| d2 | | |
| d1 | | |
| d0 | | |

Byte 15:



Byte 20:

| 0 | n | n | n | n | n | n | n | <chk></chk> | Checksum. |
|---|---|---|---|---|---|---|---|-------------|-----------|
|---|---|---|---|---|---|---|---|-------------|-----------|

Response:

\mathbf{Ack}

$$\begin{array}{ccc} \underline{<\mathrm{LOPC}>} & \underline{<\mathrm{ACK1}>} & \underline{\mathrm{Meaning}} \\ 0x6\mathrm{E} & 0x7\mathrm{F} & \overline{\mathrm{Command OK}}. \end{array}$$

| Signature |
|-----------|
| Byte 0: |

| 1 | 1 | 1 | 0 | 1 | 1 | 1 | 0 | 0xEE |
|---|---|---|---|---|---|---|---|------|

Byte 1:

| 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0x15 |
|---|---|---|---|---|---|---|---|------|

*** THERE SHOULD BE MORE ONCE ALL THE BYTES ARE DETERMINED ***

 $\underline{\text{Notes:}}$

1.4.60 SetLocoSlotStat1

$\label{eq:Description:} Description:$

This command sets the locomotive slot status 1 values for the specified slot number.

Protocol:

1

Group:

4-Byte Message

Opcode:

OPC_SLOT_STAT1

Type:

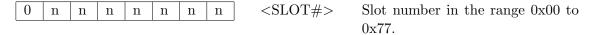
Command

Encoding:

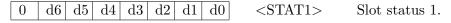
Byte 0:

| | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 0xB5 | Opcode. |
|--|---|---|---|---|---|---|---|---|------|---------|
|--|---|---|---|---|---|---|---|---|------|---------|

Byte 1:



Byte 2:



Byte 3:

| 0 | n | n | n | n | n | n | n | <chk></chk> | Checksum. |
|---|---|---|---|---|---|---|---|-------------|-----------|
| | | | | | | | | | |

Response:

None.

Signature:

Byte 0:

| | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 0xB5 |
|--|---|---|---|---|---|---|---|---|------|
|--|---|---|---|---|---|---|---|---|------|

Byte 1:

| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | less than 0x78 |
|--|----------------|
| Notes: | |
| None. | |

1.4.61 SetSwWithAck

Description:

This command sets a specified switch to a specified state. The switch responds with an \mathbf{Ack} .

Group:

4-Byte Message

Opcode:

 OPC_SW_ACK

Type:

Command

Encoding:

Byte 0:

| 1 | n | 1 | 1 | 1 | 1 | n | 1 | 0vBD | Oncode |
|---|---|---|---|---|---|---|---|------|--------|
| I | U | I | 1 | I | 1 | 0 | I | UXDD | Opcode |

Byte 1:

| 0 | $d6 \mid d$ | d4 | d3 | d2 | d1 | d0 | <sw1></sw1> | Switch | address | ; | A6 | : A6 t | A6 to | : A6 to . | 6 A6 to A | A6 to A | A6 to A | A6 to A0 |
|---|-------------|----|----|----|----|----|-------------|--------|---------|---|----|--------|-------|-------|-------|-------|-------|-----------|-----------|---------|---------|----------|

- d6 A6.
- d5 A5.
- d4 A4.
- d3 A3.
- d2 A2.
- d1 A1.
- d0 A0.

Byte 2:

- d5 Direction. 1 means closed/green, and 0 means thrown/red.
- d4 Output. 1 means on, and 0 means off.
- d3 A10.
- d2 A9.
- d1 A8.
- d0 A7.

Byte 3:

| > |
|---|
|---|

Response:

\mathbf{Ack}

<LOPC> <ACK1> Meaning

0x3D 0x00 \overline{FIFO} is full, command rejected.

0x3D 0x7F Command accepted.

Signature:

Byte 0:

| 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0xBD |
|---|---|---|---|---|---|---|---|------|
|---|---|---|---|---|---|---|---|------|

Byte 2:

| 0 | 0 | × | × | × | × | × | × |
|---|---|---|---|---|---|---|---|

Notes:

${\bf 1.4.62 \quad Slot Not Implemented}$

| Description: | | |
|---|-----------------|---------------------------------------|
| The SlotNotImplemented response the command station. | e means that th | ne slot requested is not supported by |
| Group: | | |
| 4-Byte Message | | |
| Opcode: | | |
| OPC_LONG_ACK | | |
| Type: | | |
| Response | | |
| Encoding: | | |
| Byte 0: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xB4 | Opcode. |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x3B | |
| Byte 2: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x00 | |
| Byte 3: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x70 | Checksum. |
| Response: | | |
| None | | |
| Signature: | | |
| Byte 0: | | |
| | 0xB4 | |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0x3B | |

| E | $_{ m Syte}$ | 2: | | | | | | | |
|---|--------------|----|---|---|---|---|---|---|------|
| | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0x00 |
| Е | Byte | 3: | | | | | | | |
| | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0x70 |
| | | | | | | | | | |

Notes:

1.4.63 OPC_SV_PROG

Operation: Program system variables.

Group: Variable-Byte Message

Direction: device \rightarrow device

Encoding:

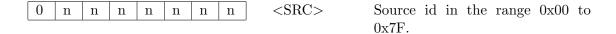
Byte 0:

| 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0xE5 | Opcode. |
|---|---|---|---|---|---|---|---|---------|---------|
| _ | _ | _ | | " | _ | ~ | _ | 0.1.1.0 | Opeout |

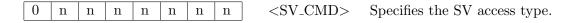
Byte 1:

| 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0x14 | Message length (20 bytes) |
|---|---|---|---|---|---|---|---|------|---------------------------|
|---|---|---|---|---|---|---|---|------|---------------------------|

Byte 2:



Byte 3:



Byte 4:



Byte 5:

| 0 | n | n | n | n | n | n | n | <HOST $>$ | Device host identifier. |
|---|---|---|---|---|---|---|---|-----------|-------------------------|
|---|---|---|---|---|---|---|---|-----------|-------------------------|

This should be 0x00 for discover devices broadcast.

d0

subversion number bit 0

| Host Id | Device | |
|---------|---|--------------------------|
| 0x01 | LNRP | |
| 0x04 | UT4 | |
| 0x0C | WTL12 | |
| 0x14 | DB210 Opto | |
| 0x15 | DB210 | |
| 0x16 | DB220 | |
| 0x1A | DCS210+ | |
| 0x1B | DCS210 | |
| 0x1C | DCS240 | |
| 0x23 | PR3 | |
| 0x24 | PR4 | |
| 0x2A | DT402 | |
| 0x32 | DT500 | |
| 0x33 | DCS51 | |
| 0x34 | DCS52 | |
| 0x3E | DT602 | |
| 0x51 | BXPA1 | |
| 0x58 | BXP88 | |
| 0x5C | UR92 | |
| 0x63 | LNWI | |
| Byte 6: | | |
| 0 n | n n n n n n | Hardware version. |
| TT . T1 | D . | |
| Host Id | <u>Device</u> | |
| 0x00 | Slave all | |
| 0x18 | Slave RF24 | |
| Byte 7: | | |
| 0 n | n n n n n | Reserved. |
| Byte 8: | | |
| 0 d6 | d5 d4 d3 d2 d1 d0 | Software Version Number. |
| d6 ve | rsion number bit 3 | |
| d5 ve | ersion number bit 2. | |
| d4 ve | rsion number bit 1 | |
| d3 ve | ersion number bit 0 | |
| | bversion number bit 2 | |
| | bversion number bit 1 | |
| 10 | 1 | |

e.g. 0x09 decodes as version 1.1.

This is set to 0x00 for discover devices broadcast message.

Byte 9:

| 0 | d6 d5 d4 d3 d2 d1 d0 | <pxct1></pxct1> | Address type code and high bits of D1 to D4. |
|----|--|-----------------|--|
| d6 | XC2. Address type code. | | |
| d5 | XC1. Address type code. | | |
| d4 | XC0. Address type code. | | |

d3 D4.7. High bit

d2 D3.7. High bit

d1 D2.7. High bit

d0 D1.7. High bit

| $\underline{\text{XC2}}$ | <u>XC1</u> | $\underline{\text{XC0}}$ | Meaning |
|--------------------------|------------|--------------------------|-------------------------------|
| 0 | 0 | 0 | 7 bit peer to peer addresses. |
| 0 | 0 | 1 | reserved. |
| 0 | 1 | 0 | reserved. |
| 0 | 1 | 1 | reserved. |
| 1 | 0 | 0 | reserved. |
| 1 | 0 | 1 | reserved. |
| 1 | 1 | 0 | reserved. |
| 1 | 1 | 1 | reserved. |

Byte 10:

| 0 | n | n | n | n | n | n | n | <d1></d1> | Data item 1. Low 7 bits. |
|---|---|---|---|---|---|---|---|-----------|--------------------------|

Byte 11:

| 0 | n | n | n | n | n | n | n | <D2 $>$ | Data item 2. Low 7 bits |
|---|---|---|---|---|---|---|---|---------|-------------------------|

This should be 0x01 for a discover devices broadcast message.

Byte 12:

| 0 | n | n | n | n | n | n | n | <d3></d3> | Data item 3. | Low 7 bits. |
|---|---|---|---|---|---|---|---|-----------|--------------|-------------|
|---|---|---|---|---|---|---|---|-----------|--------------|-------------|

Byte 13:

Byte 14:

| 0 | n | n | n | n | n | n | n | <pxct2></pxct2> | Data type code and high bits for |
|---|---|---|---|---|---|---|---|-----------------|----------------------------------|
| | | | | | | | | | D5 to D8. |

- d6 XC5. Data type code.
- d5 XC4. Data type code.
- d4 XC3. Data type code.
- d3 D8.7. High bit
- d2 D7.7. High bit
- d1 D6.7. High bit
- d0 D5.7. High bit

| $\underline{\text{XC5}}$ | $\underline{\text{XC4}}$ | $\underline{\text{XC3}}$ | Meaning |
|--------------------------|--------------------------|--------------------------|-------------------|
| 0 | 0 | 0 | ANSI text string. |
| 0 | 0 | 1 | reserved. |
| 0 | 1 | 0 | reserved. |
| 0 | 1 | 1 | reserved. |
| 1 | 0 | 0 | reserved. |
| 1 | 0 | 1 | reserved. |
| 1 | 1 | 0 | reserved. |
| 1 | 1 | 1 | reserved. |

Byte 15:

| • | 7 ł | 7 b | 7 bi |
|--------------------|----------------------|----------------------|-----------------------|
| ĺ | | 1 | 1 |
| ĺ | | 1 | 1 |
| j | i | i | Ĺ |
| j | j | j | j |
|) |) | i | j |
| Э | C |) | ij |
| ł | |) | oj |
|] | ł | b | bi |
| | ł | b | bi |
| | ł | b | bi |
| 7 | ł | b | bi |
| 7 | ' ł | b' | ' bi |
| | 7 ł | 7 b | 7 bi |
| ۲ | 7 ł | 7 b | 7 bi |
| - | 7 ł | 7 b | 7 bi |
| | 71 | 7 b | 7 bi |
| r ' | 7 l | 7 b | ⁷ 7 bi |
| 7 7 | 7 1 | 77 b | $7 \mathrm{bi}$ |
| v 7 | v 7 l | v 7 b | v 7 bi |
| v ' | v7 | v 7 b | v 7 bi |
| w ' | w 7 ł | w 7 b | w 7 bi |
| w ' | w 7 ł | w 7 b | w 7 bi |
| w | w 7 ł | w 7 b | w 7 bi |
| w | w 7 l | w 7 b | w 7 bi |
| w | w 7 l | w 7 b | w 7 bi |
| ow ' | ow 7 l | ow 7 b | ow 7 bi |
| ow ' | ow $7 \mathrm{l}$ | ow 7 b | ow 7 bi |
| ow ' | ow 7 l | ow 7 b | ow 7 bi |
| ow ' | ow 7 l | ow 7 b | ow 7 bi |
| ow ' | Low 7 h | Low 7 b | Low 7 bi |
| Low ' | Low 7 l | Low 7 b | Low 7 bi |
| Low 7 | Low 7 h | Low 7 b | Low 7 bi |
| Low | Low 7 h | Low 7 b | Low 7 bi |
| Low | Low 7 h | Low 7 b | Low 7 bi |
| Low | Low 7 h | Low 7 b | Low 7 bi |
| Low 7 | Low 7 h | Low 7 b | Low 7 bi |
| . Low | . Low 7 h | . Low 7 b | . Low 7 bi |
| Low ' | . Low 7 h | . Low 7 b | Low 7 bi |
| 5. Low 7 | 5. Low 7 h | 5. Low 7 b | 5. Low 7 bi |
| 5. Low 7 | 5. Low 7 h | 5. Low 7 b | 5. Low 7 bi |
| 5. Low 7 | 5. Low 7 h | 5. Low 7 b | 5. Low 7 bi |
| 5. Low 7 | 5. Low 7 h | 5. Low 7 b | 5. Low 7 bi |
| 1 5. Low 7 | 15. Low 7 h | 1 5. Low 7 b | 1 5. Low 7 bi |
| a 5. Low 7 | a 5. Low 7 h | a 5. Low 7 b | a 5. Low 7 bi |
| n 5. Low 7 | n 5. Low 7 h | n 5. Low 7 b | n 5. Low 7 bi |
| m 5. Low 7 | m 5. Low 7 h | m 5. Low 7 b | m 5. Low 7 bi |
| m 5. Low 7 | m 5. Low 7 h | m 5. Low 7 b | m 5. Low 7 bi |
| em 5. Low 7 | em 5. Low 7 h | em 5. Low 7 b | em 5. Low 7 bi |
| em 5. Low 7 | em 5. Low 7 h | em 5. Low 7 b | em 5. Low 7 bi |
| em 5. Low | em 5. Low 7 h | em 5. Low 7 b | em 5. Low 7 bi |
| tem 5. Low 7 | tem 5. Low 7 h | tem 5. Low 7 b | tem 5. Low 7 bi |
| tem 5. Low | item 5. Low 7 h | item 5. Low 7 b | item 5. Low 7 bi |
| item 5. Low | item 5. Low 7 h | item 5. Low 7 b | item 5. Low 7 bi |
| item 5. Low 7 | item 5. Low 7 h | item 5. Low 7 b | item 5. Low 7 bi |
| item 5. Low 7 | item 5. Low 7 h | item 5. Low 7 b | item 5. Low 7 bi |
| item 5. Low | ı item 5. Low 7 k | ı item 5. Low 7 b | ı item 5. Low 7 bi |
| a item 5. Low 7 | a item 5. Low 7 h | a item 5. Low 7 b | a item 5. Low 7 bi |
| a item 5. Low 7 | a item 5. Low 7 h | a item 5. Low 7 b | a item 5. Low 7 bi |
| ta item 5. Low 7 | ta item 5. Low 7 h | a item 5. Low 7 b | a item 5. Low 7 bi |
| ta item 5. Low 7 | ta item 5. Low 7 h | ta item 5. Low 7 b | ta item 5. Low 7 bi |
| ta item 5. Low 7 | ta item 5. Low 7 h | ta item 5. Low 7 b | ta item 5. Low 7 bi |
| ata item 5. Low 7 | ata item 5. Low 7 h | ata item 5. Low 7 b | ata item 5. Low 7 bi |
| ata item 5. Low 7 | ata item 5. Low 7 h | ata item 5. Low 7 b | ata item 5. Low 7 bi |
| ata item 5. Low 7 | ata item 5. Low 7 h | ata item 5. Low 7 b | ata item 5. Low 7 bi |
| ata item 5. Low 7 | ata item 5. Low 7 h | ata item 5. Low 7 b | ata item 5. Low 7 bi |
| Oata item 5. Low 7 | Oata item 5. Low 7 h | Oata item 5. Low 7 b | Oata item 5. Low 7 bi |
| Oata item 5. Low 7 | Oata item 5. Low 7 h | Oata item 5. Low 7 b | Oata item 5. Low 7 bi |
| Oata item 5. Low 7 | Oata item 5. Low 7 h | Oata item 5. Low 7 b | Oata item 5. Low 7 bi |
| Oata item 5. Low 7 | Oata item 5. Low 7 h | Oata item 5. Low 7 b | Oata item 5. Low 7 bi |
| Oata item 5. Low 7 | Oata item 5. Low 7 h | Oata item 5. Low 7 b | Oata item 5. Low 7 bi |
| Data item 5. Low ' | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low 7 | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |
| Data item 5. Low | Data item 5. Low 7 h | Data item 5. Low 7 b | Data item 5. Low 7 bi |

Byte 16:

| 0 | n | n | n | n | n | n | n | <d6></d6> | Data item 6. Low 7 bits |
|---|---|---|---|---|---|---|---|-----------|-------------------------|
|---|---|---|---|---|---|---|---|-----------|-------------------------|

Byte 17:

| 0 | n | n | n | n | n | n | n | <d7></d7> | Data item 7. Low 7 bits. |
|---|---|---|---|---|---|---|---|-----------|--------------------------|
|---|---|---|---|---|---|---|---|-----------|--------------------------|

Byte 18:

| 0 | n n n n | 1 <d8></d8> | Data item 8. Low 7 bi |
|---|---------|-------------|-----------------------|
|---|---------|-------------|-----------------------|

Byte 19:

| 0 | n | n | n | n | n | n | n | <chk></chk> | Checksum. |
|---|----|----|----|----|----|---|----|-------------|-----------|
| U | 11 | 11 | 11 | 11 | 11 | n | 11 | <unk></unk> | Checksum. |

Description:

This command sends the data from one device to another peer to peer.

| $\underline{\operatorname{SRC}}$ | $\overline{\mathrm{DSTL}}$ | $\overline{\mathrm{DSTH}}$ | Comments |
|----------------------------------|----------------------------|----------------------------|-------------------------------------|
| 0x0F | 0x08 | 0x00 | Discover devices broadcast message. |
| 0x0F | 0x10 | 0x00 | Discover device response. |

Response:

OPC_PEER_XFER_20 for discover devices.

Notes:

The discover response decoded peer transfer message encodes as follows:

```
D1 IPL Version Number
```

- D2 Serial Number low byte
- D3 Serial Number high byte

D4

- D5 Serial Number 2 low byte
- D6 Serial Number 2 high byte

D7

D8

The IPL version number is encoded as follows:

- d6 version number bit 3
- d5 version number bit 2.
- d4 version number bit 1
- d3 version number bit 0
- d2 subversion number bit 2
- d1 subversion number bit 1
- d0 subversion number bit 0

e.g. 0x09 decodes as version 1.1.

These came from DigiPLII:

message Length = 20 e5 14 0f 10 00 24 00 00 00 02 00 08 07 00 00 00 00 00 38

 $message\ Length = 20\ e5\ 14\ 0f\ 10\ 00\ 24\ 00\ 00\ 00\ 00\ 00\ 57\ 13\ 00\ 00\ 00\ 00\ 00\ 71$

 $message \ Length = 20 \ e5 \ 14 \ 0f \ 10 \ 00 \ 1b \ 00 \ 00 \ 03 \ 02 \ 00 \ 54 \ 10 \ 00 \ 00 \ 00 \ 00 \ 00 \ 4f$

It reports PR4 with serial number 0x0788 ver 0 PR4 with serial 0x1357 ver 0 DCS240 with SN 0x0AAB ver 0.3 DCS210 with SN 0x10D4 ver 0.3

1.4.64 SwReq

Description:

Command a turnout controller to a specified state. *** CHECK THIS ***

Group:

4-Byte Message

Opcode:

OPC_SW_REQ

Type:

Command

Encoding:

Byte 0:

| 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0xB0 | Opcode. |
|---|---|---|---|---|---|---|---|--------|---------|
| | | | _ | | | | | UALD 0 | Opcouc. |

Byte 1:

| 0 | d6 d5 d4 d3 | d2 d1 d0 | $\langle SW1 \rangle$ S | Switch address A6 to A |
|---|-------------|----------|-------------------------|------------------------|
|---|-------------|----------|-------------------------|------------------------|

- d6 A6.
- d5 A5.
- d4 A4.
- d3 A3.
- d2 A2.
- d1 A1.
- d0 A0.

Byte 2:

- d5 Direction. 1 means closed/green, and 0 means thrown/red.
- d4 Output. 1 means on, and 0 means off.
- d3 A10.
- d2 A9.
- d1 A8.
- d0 A7.

Byte 3:

| 0 | $n \mid n$ | n | 11 | n | n | n |
|---|------------|---|----|---|---|---|

Response:

 \mathbf{Ack} if command failed, otherwise no response.

$$\begin{array}{ccc} \underline{<\mathrm{LOPC}>} & \underline{<\mathrm{ACK1}>} & \underline{\mathrm{Meaning}} \\ 0\mathrm{x}30 & 0\mathrm{x}00 & \overline{\mathrm{Command failed}}. \end{array}$$

Signature:

Byte 0:

| 1 | n | 1 | 1 | n | n | n | n | 0vB0 |
|---|---|---|---|---|---|---|---|------|
| T | U | I | 1 | U | U | U | U | UXBU |

Byte 2:

| 0 | 0 | × | × | × | × | × | × |
|---|---|---|---|---|---|---|---|

Notes:

The on power on the command station sends a sequence of OPC_SW_REQ messages with the following values of SW1 and SW2:

| $\frac{SW1}{0x78}$ | $\frac{SW2}{0x27}$ | Purpose |
|--------------------|--------------------|--------------------------------------|
| 0x79 | 0x27 | |
| 0x7A | 0x27 | |
| 0x7B | 0x27 | |
| 0x78 | 0x07 | Interrogate all PM4 inputs? |
| 0x79 | 0x07 | Interrogate all BDL16 input reports? |
| 0x7A | 0x07 | Interrogate all SE8 input reports? |
| 0x7B | 0x07 | Interrogate all DS64 input reports. |

Byte 0:

1.4.65 SwState

| Description: | | |
|---|-------------|---|
| Request state of switch. *** NEED TO | O CHECK *** | |
| Group: | | |
| 4-Byte Message | | |
| Opcode: | | |
| OPC_SW_STATE | | |
| Type: | | |
| Message? | | |
| Encoding: | | |
| Byte 0: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 0xBC | Opcode. |
| Byte 1: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <sw1></sw1> | Switch address A6 to A0. |
| Byte 2: | | |
| $ \begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <sw2></sw2> | Switch address A10 to A7 and switch control bits. |
| d5 Direction. 1 means closed/gree d4 Output. 1 means on, and 0 me d3 A10. d2 A9. d1 A8. d0 A7. | | thrown/red. |
| Byte 3: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <chk></chk> | Checksum. |
| Response: | | |
| Ack | | |
| Signature: | | |

Byte 2:

| 0 | 0 | × | × | × | × | × | × |
|---|---|---|---|---|---|---|---|

Notes:

This needs to be tested to see what the real purpose is.

Signature:

1.4.66 TransRep

| Description: | | |
|--|-----------------|---|
| Transponder input report. | | |
| Group: | | |
| 6-Byte Message | | |
| Opcode: | | |
| OPC_TRANS_REP | | |
| Type: | | |
| Broadcast | | |
| Encoding: | | |
| Byte 0: | | |
| | 0xD0 | Opcode. |
| Byte 1: | | |
| | | A value of 0x20 means the positive detection of a transponder, 0x00 means no longer detected. |
| Byte 2: | | |
| | <zone#></zone#> | Zone indicator $(0x0 = A, 0x2 = B, 0x4 = C, 0x6 = D).$ |
| Byte 3: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <adr></adr> | Locomotive address low bits. |
| Byte 4: | | |
| $oxed{0 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$ | <adr2></adr2> | Locomotive address high bits. |
| Byte 5: | | |
| $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | <chk></chk> | Checksum. |
| Response: | | |
| None. | | |

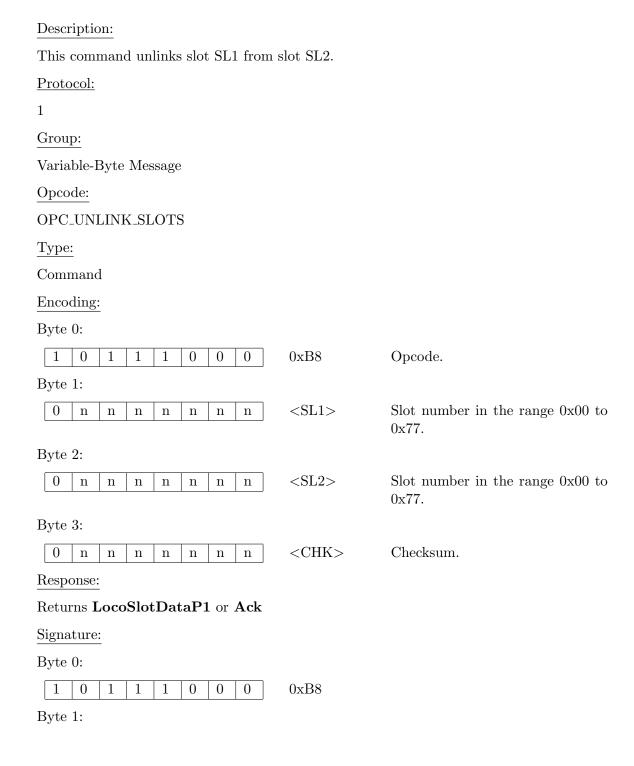
Byte 0:

| | - | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0xD0. |
|--|---|---|---|---|---|---|---|---|-------|
|--|---|---|---|---|---|---|---|---|-------|

*** THERE SHOULD BE MORE ***

Notes:

1.4.67 UnlinkSlotsP1



| | 0 | n | n | n | n | n | n | n | less than 0x78 |
|---|-----|----|---|---|---|---|---|---|----------------|
| Ε | yte | 2: | | | | | | | |
| | 0 | n | n | n | n | n | n | n | less than 0x78 |
| | | | | | | | | | • |

Notes:

1.4.68 UnlinkSlotsP2

${\bf Description:}$

This command unlinks slot SL1 from a consist. The command station sets SL_CONUP/DN flags appropriately. If the command was successful then a $\bf LocoSlotDataP2$ response will be returned. An invalid link will return a $\bf Ack$ with a response code of 0x00.

| Protocol: | | |
|----------------------------------|---------------|---|
| 2 | | |
| Group: | | |
| 6-Byte Message | | |
| Opcode: | | |
| OPC_D4_GROUP (unofficial mnemoni | ic) | |
| Type: | | |
| Command | | |
| Encoding: | | |
| Byte 0: | | |
| 1 1 0 1 0 1 0 0 | 0xD4 | Opcode. |
| Byte 1: | | |
| 0 0 1 1 1 d2 d1 d0 | <sl1p></sl1p> | Bits d2 to d0 contain the SL1 slot page number in the range 0x0 to 0x7. |
| Byte 2: | | |
| | <sl1#></sl1#> | Slot number SL1 in the range $0x00$ to $0x77$. |
| Byte 3: | | |
| 0 1 0 1 0 d2 d1 d0 | <sl1p></sl1p> | Bits d2 to d0 contain the SL1 slot page number in the range 0x0 to 0x7. This is the same value as byte 1. |
| Byte 4: | | |

1.4. MESSAGES 175 Slot number SL1 in the range 0x000 \mathbf{n} \mathbf{n} n \mathbf{n} n n n <SL1#>to 0x77. This is the same value as byte 2. Byte 5: 0 n <CHK> Checksum. n \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} n Response: LocoSlotDataP2 or Ack. Signature: Byte 0: 1 1 0 0xD41 0 0 Byte 1: 0 0 X \times Byte 2: 0 n less than 0x78 \mathbf{n} \mathbf{n} \mathbf{n} \mathbf{n} n n Byte 3: 0 1 0 1 0 \times × Byte 4: 0 n less than 0x78 \mathbf{n} n n n n n

Notes:

None.

Chapter 2

Fast Clock

2.1 Summary

The command station provides a system fast clock and parameters are stored in slot #123 (0x7B). Use OPC_WR_SL_DATA to write new clock information, The current slot information can be read using OPC_RQ_SL_DATA. This will return an OPC_SL_RD_DATA message containing the fast clock information. This message is called the "sync". Other throttles will update to this sync. Note that all attached display devices keep a current clock calculation based on this sync read value, i.e. devices must not continuously poll the clock slot to generate time, but use this merely to restore sync and follow current rate etc. The clock slot is typically "pinged" or read every 70 to 100 seconds, by a single user, so all attached devices can synchronise any phase drifts. Upon seeing a sync read, all devices should reset their local sub-minute phase counter and invalidate the sync update ping generator.

2.2 Slot #123 Encoding

Byte 0:

| 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 |
|---|---|---|---|---|---|---|---|

Byte 1:



Byte 2:

| 0 | n | n | n | n | n | n | n | <FRACL $>$ | Sub-minute counter low bits. |
|---|---|---|---|---|---|---|---|------------|------------------------------|
|---|---|---|---|---|---|---|---|------------|------------------------------|

Byte 3:

| | 0 | n | n | n | n | n | n | n | <frach></frach> | Sub-minute counter high b |
|--|---|---|---|---|---|---|---|---|-----------------|---------------------------|
|--|---|---|---|---|---|---|---|---|-----------------|---------------------------|

The implementation and meaning of FRACL and FRACH depend upon the specific clock generator. These values should not be used externally. These values are reset when a valid sync message is seen.

That said, the following timing was derived for the DCS240.

Get:

$$maxTick = 0xBFF$$

$$ticks = maxTick - (0x3FFF - ((\& 0x7F) - ((\& 0x7F) <<7)))$$

$$seconds = 60.0 * ticks / (maxTick + 1)$$

Set:

$$temp = ticks - maxTick + 0x3FFF$$

$$\langle FRACL \rangle = temp \& 0x7F$$

$$\langle FRACH \rangle = (temp >> 7) \& 0x7F$$

Byte 4:

| 0 | n | n | n | n | n | n | n | <MINS $>$ | Fast clock minutes. | This is en- |
|---|---|---|---|---|---|---|---|-----------|---------------------|-------------|
| | | | | | | | | | coded. | |

Get:

temp =
$$((255 - \langle MINS \rangle) \& 0x7F) \mod 60$$

$$minutes = (60 - temp) \mod 60$$

Set:

$$<$$
MINS $> = (255 - (60 - minutes)) & 0x7F$

Byte 5:

179

| | s. | | | | | | | |
|---|--|--|--|--|--|--|--|--|
| d6 Reserved. Set to 0. | | | | | | | | |
| d5 Reserved. Set to 0. | | | | | | | | |
| d4 Reserved. Set to 0. | | | | | | | | |
| d3 1 means the programming track is busy. | | | | | | | | |
| d2 1 means this master implements the Network version | | | | | | | | |
| 1.1 capability, 0 means the master is a DT200. | | | | | | | | |
| d1 0 means the track is paused, broadcast an emergency stop. | | | | | | | | |
| d0 1 means the DCC packets are on in the master, global | | | | | | | | |
| power up. | | | | | | | | |
| Byte 6: | | | | | | | | |
| $oxed{0 \ \ n \ \ n \ \ n \ \ n \ \ n}$ HRS> Fast clock hours. This is expression. | encoded. | | | | | | | |
| Get: | | | | | | | | |
| $temp = ((256 - \langle HRS \rangle) \& 0x7F) \mod 24$ | temp = $((256 - \langle HRS \rangle) \& 0x7F) \mod 24$ | | | | | | | |
| $hours = (24 - temp) \mod 24$ | | | | | | | | |
| Set: | | | | | | | | |
| <HRS $> = (256 - (24 - hours)) & 0x7F$ | | | | | | | | |
| Byte 7: | | | | | | | | |
| | er of 24 | | | | | | | |
| Byte 8: | | | | | | | | |
| $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$ | | | | | | | | |
| Byte 9: | | | | | | | | |
| $\begin{bmatrix} 0 & n & n & n & n & n & n \end{bmatrix}$ <id1> Device ID low bits.</id1> | | | | | | | | |
| Byte 10: | | | | | | | | |
| | | | | | | | | |

ID1 and ID2 indicate the device that last set the clock. 0x00, 0x00 means that not set has happened. 0x7F, 0x7X are reserved for computer use.

Chapter 3

Updating Firmware

3.1 Bootloader Protocol 1

3.2 Bootloader Protocol 2

- 1. The IPL Setup message is sent twice to initiate the download.
- 2. Each block is sent,
- 3. The IPL End Operation message is sent.

A block consists of an IPL Address message following by 1 or more IPL Data messages.

3.3 Firmware Parameters

| <u>PC</u> | <u>Device</u> | $\overline{\mathrm{DT}}$ | $\underline{\mathrm{BV}}$ | $\underline{\mathrm{HV}}$ | $\underline{\mathrm{SV}}$ | $\underline{\mathrm{CK}}$ | $\underline{\mathrm{DL}}$ | <u>OP</u> | <u>PB</u> | $\underline{\mathrm{EB}}$ | $\underline{\mathrm{ED}}$ | $\overline{\mathrm{DC}}$ |
|-----------|---------------|--------------------------|---------------------------|---------------------------|---------------------------|---------------------------|---------------------------|-----------|-----------|---------------------------|---------------------------|--------------------------|
| 0x01 | LNRP | 09OCT15 | 1 | 0 | 3 | 64 | 8 | 1 | 64 | 64 | 8 | 0x50 |
| 0x04 | UT4 | | | | | | | | | | | |
| 0x06 | UT6 | 05APR21 | 2 | 0 | 1 | 64 | 11 | 2 | 512 | 4096 | 40 | 0x2E |
| 0x0C | WTL12 | | | | | | | | | | | |
| 0x14 | DB210O | 06MAR20 | 2 | 1 | 2 | 64 | 7 | 2 | 512 | 4096 | 25 | 0x08 |
| 0x15 | DB210 | 06MAR20 | 2 | 1 | 2 | 64 | 7 | 2 | 512 | 4096 | 25 | 0x08 |
| 0x16 | DB220 | 06MAR20 | 2 | 1 | 2 | 64 | 7 | 2 | 512 | 4096 | 25 | 0x08 |
| 0x1A | DCS210+ | | | | | | | | | | | |
| 0x1B | DCS210 | 06MAR20 | 2 | 1 | 3 | 64 | 5 | 2 | 256 | 2048 | 40 | 0x2C |
| 0x1C | DCS240 | 06MAR20 | 2 | 1 | 3 | 64 | 5 | 2 | 256 | 2048 | 40 | 0x2C |
| 0x23 | PR3 | 12FEB 14 | 1 | 0 | 8 | 64 | 5 | 1 | - | 1024 | - | 0x14 |
| 0x24 | PR4 | 05JAN 18 | 0 | 0 | 0 | 64 | 5 | 2 | 256 | 2048 | 60 | 0x1C |
| 0x2A | DT402 | 10OCT16 | 1 | 0 | 17 | 64 | 15 | 1 | 64 | 64 | 15 | 0x73 |
| 0x2A | DT402 | 05 AUG16 | 1 | 0 | 17 | 64 | 15 | 1 | 64 | 64 | 15 | 0x4B |
| 0x32 | DT500 | 10OCT16 | 1 | 0 | 1 | 64 | 15 | 1 | 64 | 64 | 15 | 0x0E |
| 0x33 | DCS51 | 06OCT14 | 1 | 0 | 5 | 64 | 12 | 1 | - | - | - | 0x0E |
| 0x34 | DCS52 | 17JUN21 | 2 | 0 | 1 | 64 | 11 | 2 | 512 | 4096 | 40 | 0x2C |
| 0x3E | DT602 | 15JUL 21 | 2 | 0 | 1 | 64 | 11 | 2 | 512 | 4096 | 40 | 0x30 |
| 0x51 | BXPA1 | 18JUN21 | 2 | 0 | 1 | 64 | 6 | 2 | 512 | 4096 | 41 | 0x0A |
| 0x58 | BXP88 | 21OCT17 | 2 | 0 | 2 | 64 | 50 | 2 | 256 | 2048 | 100 | 0x18 |
| 0x5C | UR92 | 07DEC15 | 0 | 1 | 8 | 64 | 16 | 1 | 64 | 64 | 16 | 0x24 |
| 0x5D | UR93 | 30 AUG 21 | 2 | 0 | 0 | 64 | 5 | 2 | 512 | 4096 | 25 | 0x0A |
| 0x63 | LNWI | 11MAR21 | 2 | 1 | 2 | 64 | 5 | 2 | 512 | 4096 | 25 | 0x12 |
| | | | | | | | | | | | | |

| $\overline{\mathrm{EBS}}$ | $\underline{\text{LNRP}}$ | $\overline{\mathrm{DT402}}$ | $\overline{\mathrm{DT500}}$ | $\underline{\text{DCS51}}$ | $\overline{\text{DCS}52}$ |
|---------------------------|---------------------------|-----------------------------|-----------------------------|----------------------------|---------------------------|
| 32 | 0x20 | 0x65 | 0x1B | - | 0x00 |
| 64 | 0x50 | 0x73 | 0x0E | 0x0E | 0x00 |
| 128 | 0x28 | 0x3A | 0x47 | - | 0x00 |
| 192 | 0x1B | 0x7C | 0x05 | - | 0x2B |
| 256 | 0x14 | 0x5D | 0x64 | - | 0x40 |
| 1024 | 0x05 | 0x18 | 0x19 | - | 0x30 |
| 2048 | 0x03 | 0x0C | 0x0D | - | 0x58 |
| 4096 | 0x02 | 0x06 | 0x07 | - | 0x2C |
| 8192 | 0x01 | 0x03 | 0x04 | - | 0x16 |
| 16384 | - | 0x02 | 0x02 | - | 0x0B |
| 32768 | - | 0x01 | 0x01 | - | 0x06 |
| 65536 | - | - | - | - | 0x03 |

3.4 DMF File Format

The manufacturer's DMF file format is a modified form of the Intel Hexadecimal Object File Format. Character encoding is ASCII. The file consists of multiple "records" each terminated by an ASCII linefeed character (0x0A). There are four types of record and they appear in the file in the following order:

- 1. Sync records
- 2. Parameter records
- 3. Data records
- 4. End of File record

3.4.1 Sync Records

Sync records are used to identify the file as a DMF file. There are six sync records each consisting of a single ASCII # character (0x23).

Example:

#

#

#

#

#

3.4.2 Parameter Records

| PARAM MARK | PARAMETER NAME | NAME TERMINATOR | VALUE |
|-------------------------|----------------|-------------------------|----------------|
| ! (0x21) < SPC > (0x20) | ASCII text | : (0x3A) < SPC > (0x20) | decimal value |
| 2 bytes | variable bytes | 2 byte | variable bytes |

PARAMETER RECORD FORMAT

Parameter records start with a two byte PARAM MARK. This consists of the ASCII code for the exclamation mark (0x21) followed by the ASCII code for the space character (0x20). The PARAMETER NAME is next. The PARAMETER NAME is ASCII encoded text identifying the parameter. The PARAMETER NAME may include spaces. It is terminated by the NAME TERMINATOR which consists of the ASCII code for a colon

(0x3A) followed by the ASCII code for a space character (0x20). The last field is the VALUE field. This is a decimal numeric value encoded as ASCII text.

The following parameters have been identified and they are presented in the following order (it is not known if the order is significant):

- 1. Bootloader Version
- 2. Manufacturer Code
- 3. Product Code
- 4. Hardware Version
- 5. Software Version
- 6. Chunk Size
- 7. Delay
- 8. Options
- 9. First Address
- 10. Last Address
- 11. Prog Blk Size
- 12. Erase Blk Size
- 13. Erase Dly

Example:

- ! Bootloader Version: 2
- ! Manufacturer Code: 0
- ! Product Code: 88
- ! Hardware Version: 0
- ! Software Version: 2
- ! Chunk Size: 64
- ! Delay: 50
- ! Options: 2
- ! First Address: 24576 ! Last Address: 73728
- ! Prog Blk Size: 256
- ! Erase Blk Size: 2048
- ! Erase Dly: 100

3.4.3 Data Records

| RECORD MARK | RECLEN | LOAD OFFSET | RECTYP | DATA | CHKSUM |
|-------------|---------|-------------|---------|---------|---------|
| (:) | | | | | |
| 1 byte | 2 bytes | 6 bytes | 2 bytes | n bytes | 2 bytes |

DATA RECORD FORMAT

The data record provides a set of hexadecimal digits that represent the ASCII code for data bytes that make up a portion of a memory image.

Each data record begins with a RECORD MARK field containing the ASCII code for the colon (:) character (0x3A).

Each record has a RECLEN field which specifies the number of bytes of data which follows the RECTYP field of the record. Note that one data byte is represented by two ASCII characters. The maximum value of the RECLEN field is hexadecimal "FF" or 255. Although the maximum is 255, the manufacturer seems to prefer 64.

Each record has a LOAD OFFSET field which specifies the 24-bit starting load offset of the data bytes.

The RECTYP field for data records is "00".

Each record has a variable length DATA field, it consists of zero or more bytes encoded as pairs of hexadecimal digits.

Each record ends with a CHKSUM field that contains the ASCII hexadecimal representation of the two's complement of the 8-bit bytes that result from converting each pair of ASCII hexadecimal digits to one byte of binary, from and including the RECLEN field to and including the last byte of the DATA field. Therefore, the sum of all the ASCII pairs in a record after converting to binary, form the RECLEN field up to and including the CHKSUM field, is zero.

The contents of the individual fields within the record are:

RECORD MARK This field contains 0x3A, the encoding of the ASCII colon

(:) character.

RECLEN The field contains two ASCII hexadecimal digits that specify

the number of data bytes in the record. The maximum value

is "FF" or 0x4646 (255 decimal).

LOAD OFFSET This field contains six ASCII hexadecimal digits representing

the address at which the first byte of the data is to be placed.

Most significant digit is presented first.

RECTYP This field contains 0x3030, the hexadecimal encoding of the

ASCII characters "00", which specifies the record type to be

a data record.

DATA This field contains pairs of ASCII hexadecimal digits, one

pair for each data byte.

CHKSUM This field contains the check sum on the RECLEN, LOAD

OFFSET, RECTYP, and DATA fields.

Example:

:400060000057AAC3880FAAC388559AC38855AAC388553AC38855AAC38855AAC3884AO 0C38855AAC38855AAC3882DFCC38861B8C3882DFCC38861B8C3886D

3.4.4 End of File Record

| RECORD MARK | RECLEN | LOAD OFFSET | RECTYP | CHKSUM |
|-------------|---------|-------------|---------|---------|
| (:) | "00" | "000000" | "01" | "FF" |
| 1 byte | 2 bytes | 6 bytes | 2 bytes | 2 bytes |

END OF FILE RECORD FORMAT

The End of File Record specifies the end of the file.

The contents of the individual fields within the record are:

RECORD MARK This field contains 0x3A, the encoding of the ASCII colon

(:) character.

RECLEN The field contains 0x3030, the hexadecimal encoding of the

ASCII characters "00". Since this record does not contain

any DATA bytes, the length is zero.

LOAD OFFSET This field contains 303030303030H, the hexadecimal encod-

ing of the ASCII characters "000000", since this field is not

used for this record.

RECTYP This field contains 0x3031, the hexadecimal encoding of the

ASCII characters "01", which specifies the record type to be

an End of File Record.

CHKSUM This field contains the check sum on the RECLEN, LOAD

OFFSET, and RECTYP fields. Since all the fields are static, the check sum can also be calculated statically, and the value is 4646H, the hexadecimal encoding of the ASCII characters

"FF".

Example:

:000000001FF

Chapter 4

Programming Configuration Variables (CVs)

4.1 Introduction

The decoders installed in your locomotives provide you with the ability to create a more realistic operating experience through the configuration variables (CVs for short). The network protocol supports configuration of up to 1024 CVs.

It is a good idea to run your decoders with the default CV values that come pre-programmed in your decoders until you get used to the performance characteristic and how they work on your layout. Once you are comfortable with running the trains, then you can begin customizing locomotive characteristics.

Each CV (configuration variable) controls a specific characteristic of the decoder, which in turn controls how the locomotive performs. See your decoder manual for a list of the most commonly used CVs and their meanings. Each decoder comes pre-programmed from the factory with the default settings outlined in your decoder manual. You can change your decoder's performance characteristics by changing the CV values entered in the CVs you want to change. Each of these CVs can be set up when your command station is in the programming mode. The CVs are remembered in the decoder until it is reprogrammed to with a different CV value. Please refer to your mobile decoder manual for a complete listing of the CVs supported by each decoder.

Programming decoder CVs is usually done on an isolated programming track.

There are four programming modes:

- Paged mode
- Physical register mode
- Direct mode
- Operations mode

4.1.1 Paged Mode Programming

4.1.2 Physical Register Programming

Physical Register Mode can only read CV01-CV08. You should not rely on values in the display for CVs above 08 when reading back in physical register mode.

4.1.3 Direct Mode Programming

This is the preferred programming mode.

4.1.4 Operations Mode Programming

Operations mode programming lets you program CVs in locomotives equipped with Extended Packet Format decoders while they are on the mainline. A typical use for Ops mode programming would be to change the acceleration rate (CV03) or the deceleration rate (CV04) of your locomotives to simulate the weight and braking capability of the train to compensate for changing the number of cars or power units on a train.

Operations Mode read back can only be used with decoders that are capable of operations mode read back when there is a device attached to the network that supports operations mode read back. Digitrax transponding decoders and the DCS210 or DCS240 command stations would allow operations mode read back.

4.2 Programming Mobile Decoder Addresses

Be sure that only the loco you want to program is on the programming track. If you are using operations mode programming, the loco you want to program can be anywhere on the layout but it must have a decoder that is capable of operations mode programming installed.

There are two addressing methods - short and long. The short addresses can take a value between 0 and 127, and long addresses a value between 128 and 9983. The bit 5 of mobile decoder's configuration register (CV29) determines what addressing method is used. If bit 5 is set to 1 then long addresses are used, and when bit 5 is 0 then short addresses are used. Short addresses are stored in CV1, and long addresses in CV17 and CV18. The address values stored in CV17 and CV18 are not the high and low bytes of the address value. The CV17 and CV18 values must be calculated from the address value as follows:

```
TEMP = address + 49152
CV18 = TEMP \& 0xFF
CV17 = TEMP >> 8
Example:
address = 4007
TEMP = 49152 + 4007 = 53159 = 0xCFA7
CV18 = 0xA7 = 167
CV17 = 0xCF = 207
read cv
Read CV
unknown
   65830.9ms
<DO> 0xef 0b11101111 <- OPC_PROG</pre>
<D1> 0x0e 0b00001110 <- Message Length</pre>
<D2> 0x7c 0b01111100 <- Special programming slot number
<D3> 0x2b 0b00101011 <- PCMD
d7 0
d6 0 - read
d5 1 - byte mode
d4 0 - TV1
d2 0 - service mode on programming track
d1 1 - unknown
d0 1 - unknown
```

Direct mode byte read on service track

d0 0 - reserved

```
<D4> 0x00 0b00000000 - 0x00
<D5> 0x00 0b00000000 - HOPSA - Ops mode programming - 7 high address bits of Loco to pro
<D6> 0x0e 0b00001110 - LOPSA - Ops Mode programming - 7 low address bits of loco to prog
<D7> 0x00 0b00000000 - TRK - normal track status for command station - this doesn't look
<D8> 0x00 0b00000000 - CVH
<D9> 0x00 0b00000000 - CVL
<D10> 0x0f 0b00001111 - DATA
<D11> 0x6d 0b01101101 - Throttle serial number
<D12> 0x52 0b01010010 - Throttle serial number
<D13> 0x77 0b01110111
response
    1722.5ms
<DO> 0xe7 0b11100111 <- Opcode
<D1> 0x0e 0b00001110 <- length
<D2> 0x7c 0b011111100 <- Programming slot</pre>
<D3> 0x2b 0b00101011 <- PCMD
<D4> 0x00 0b00000000 <- PSTAT - success
<D5> 0x00 0b00000000 <- HOPSA
<D6> 0x02 0b00000010 <- LOPSA should be 0
<D7> 0x47 0b01000111 <- TRK
<D8> 0x02 0b00000010 <- CVH : 0, 0, CV9, CV8, 0, 0, D7, CV7</pre>
<D9> 0x04 0b00000100 <- CVL - CV5</pre>
<D10> 0x16 0b00010110 <- low 7 bits of value</pre>
<D11> 0x6d 0b01101101 <- SN
<D12> 0x52 0b01010010 <- SN
<D13> 0x2b 0b00101011 <- CHK
value displayed is 150 10010110
PCMD
 d7 = 0
 d6 	 1 = write, 0 = read
 d5 1 = \text{byte operation}, 0 = \text{bit operation (if possible)}
 d4 	ext{TV1}
 d3 TV0
 d2 1 = Ops mode on mainlines, 0 = service mode on programming track
 d1 0 - reserved
```

| Byte Mode | Ops Mode | $\overline{\text{TV1}}$ | $\underline{\text{TV0}}$ | Meaning |
|-----------|----------|-------------------------|--------------------------|--|
| 1 | 0 | 0 | 0 | Paged mode byte read/write on service track |
| 1 | 0 | 0 | 1 | Direct mode byte read/write on service track |
| 0 | 0 | 0 | 1 | Direct mode bit read/write on service track |
| × | 0 | 1 | 0 | Physical register byte read/write on service track |
| × | 0 | 1 | 1 | Service track reserved function |
| 1 | 1 | 0 | 0 | Ops mode byte program no feedback |
| 1 | 1 | 0 | 1 | Ops mode byte program with feedback |
| 0 | 1 | 0 | 0 | Ops mode bit program no feedback |
| 0 | 1 | 0 | 1 | Ops mode bit program with feedback |

ack

<D0> 0xb4 0b10110100

<D1> 0x6f 0b01101111

<D2> 0x01 0b00000001

<D3> 0x25 0b00100101

unknown

1731.6ms <DO> 0xe7 0b11100111

<D1> 0x0e 0b00001110

<D2> 0x7c 0b01111100

<D3> 0x2b 0b00101011

<D4> 0x00 0b00000000

<D5> 0x00 0b00000000

<D6> 0x02 0b00000010

<D7> 0x47 0b01000111

<D8> 0x00 0b00000000

<D9> 0x00 0b00000000

<D10> 0x0f 0b00001111

<D11> 0x6d 0b01101101

<D12> 0x52 0b01010010

<D13> 0x34 0b00110100

ack

10.6ms <DO> 0xb4 0b10110100

<D1> 0x3b 0b00111011

<D2> 0x00 0b00000000

<D3> 0x70 0b01110000

Read CV 2

unknown

```
6772.5ms <DO> 0xef 0b11101111
<D1> 0x0e 0b00001110
<D2> 0x7c 0b01111100
<D3> 0x2b 0b00101011
<D4> 0x00 0b00000000
<D5> 0x00 0b00000000
<D6> 0x0e 0b00001110
<D7> 0x00 0b00000000
<D8> 0x00 0b00000000
<D9> 0x01 0b00000001
<D10> 0x0f 0b00001111
<D11> 0x6d 0b01101101
<D12> 0x52 0b01010010
<D13> 0x76 0b01110110
ack
      15.5ms <DO> 0xb4 0b10110100
<D1> 0x6f 0b01101111
<D2> 0x01 0b00000001
<D3> 0x25 0b00100101
unknown
    1720.8ms <DO> 0xe7 0b11100111
<D1> 0x0e 0b00001110
<D2> 0x7c 0b01111100
<D3> 0x2b 0b00101011
<D4> 0x00 0b00000000
<D5> 0x00 0b00000000
<D6> 0x02 0b00000010
<D7> 0x47 0b01000111
<D8> 0x00 0b00000000
<D9> 0x01 0b0000001
<D10> 0x07 0b00000111
<D11> 0x6d 0b01101101
<D12> 0x52 0b01010010
<D13> 0x3d 0b00111101
----- CV2
unknown
   11836.0ms <DO> 0xef 0b11101111
<D1> 0x0e 0b00001110
```

```
<D2> 0x7c 0b01111100
<D3> 0x2b 0b00101011
<D4> 0x00 0b00000000
<D5> 0x00 0b00000000
<D6> 0x0e 0b00001110
<D7> 0x00 0b00000000
<D8> 0x00 0b00000000
<D9> 0x01 0b00000001
<D10> 0x07 0b00000111
<D11> 0x6d 0b01101101
<D12> 0x52 0b01010010
<D13> 0x7e 0b01111110
ack
       6.1ms <DO> 0xb4 0b10110100
<D1> 0x6f 0b01101111
<D2> 0x01 0b00000001
<D3> 0x25 0b00100101
unknown
    1730.2ms <DO> 0xe7 0b11100111
<D1> 0x0e 0b00001110
<D2> 0x7c 0b01111100
<D3> 0x2b 0b00101011
<D4> 0x00 0b00000000
<D5> 0x00 0b00000000
<D6> 0x02 0b00000010
<D7> 0x47 0b01000111
<D8> 0x00 0b00000000
<D9> 0x01 0b00000001
<D10> 0x07 0b00000111
<D11> 0x6d 0b01101101
<D12> 0x52 0b01010010
<D13> 0x3d 0b00111101
<- failure nothing on prog track
<D0> 0xe7 0b11100111 <- opcode
<D1> 0x0e 0b00001110 <- length
<D2> 0x7c 0b01111100 <- prog slot
<D3> 0x2b 0b00101011 <- PCMD
```

```
<D4> 0x01 0b00000001 <- PSTAT
<D5> 0x00 0b00000000
<D6> 0x01 0b00000001
<D7> 0x47 0b01000111
<D8> 0x02 0b00000010
<D9> 0x04 0b00000100
<D10> 0x16 0b00010110
<D11> 0x6d 0b01101101
<D12> 0x52 0b01010010
<D13> 0x29 0b00101001
PSTAT
d7 0 - reserved
d6 0 - reserved
d5 0 - reserved
d4 0 - reserved
d3 1 = user aborted command
d2 1 = failed to detect read compare ack from decoder
d1 1 = no write ack from decoder
d0 - 1 = service mode programming track empty - no decoder detected
----> write 150 to CV5
unknown
    7846.9ms
<DO> 0xef 0b11101111
<D1> 0x0e 0b00001110
<D2> 0x7c 0b01111100
<D3> 0x6b 0b01101011
<D4> 0x00 0b00000000
<D5> 0x00 0b00000000
<D6> 0x0e 0b00001110
<D7> 0x00 0b00000000
<D8> 0x02 0b00000010
<D9> 0x04 0b00000100
<D10> 0x16 0b00010110
<D11> 0x6d 0b01101101
<D12> 0x52 0b01010010
```

<D13> 0x28 0b00101000

ack

4.6ms

<D0> 0xb4 0b10110100

<D1> 0x6f 0b01101111

<D2> 0x01 0b00000001

<D3> 0x25 0b00100101

unknown

894.9ms

<D0> 0xe7 0b11100111

<D1> 0x0e 0b00001110

<D2> 0x7c 0b01111100

<D3> 0x6b 0b01101011

<D4> 0x00 0b00000000

<D5> 0x00 0b00000000

<D6> 0x02 0b00000010

<D7> 0x47 0b01000111

<D8> 0x02 0b00000010

<D9> 0x04 0b00000100

<D10> 0x16 0b00010110

<D11> 0x6d 0b01101101

<D12> 0x52 0b01010010

<D13> 0x6b 0b01101011

---> write 150 to CV5 nothing on prog track

unknown

11349.0ms <DO> 0xef 0b11101111

<D1> 0x0e 0b00001110

<D2> 0x7c 0b01111100

<D3> 0x6b 0b01101011

<D4> 0x00 0b00000000

<D5> 0x00 0b00000000

<D6> 0x0e 0b00001110

<D7> 0x00 0b00000000

<D8> 0x02 0b0000010

<D9> 0x04 0b00000100

<D10> 0x16 0b00010110

<D11> 0x6d 0b01101101

```
<D12> 0x52 0b01010010
<D13> 0x28 0b00101000
ack
       6.0ms <DO> 0xb4 0b10110100
<D1> 0x6f 0b01101111
<D2> 0x01 0b0000001
<D3> 0x25 0b00100101
unknown
     723.9ms <DO> 0xe7 0b11100111
<D1> 0x0e 0b00001110
<D2> 0x7c 0b01111100
<D3> 0x6b 0b01101011
<D4> 0x01 0b0000001
<D5> 0x00 0b00000000
<D6> 0x02 0b00000010
<D7> 0x47 0b01000111
<D8> 0x02 0b00000010
<D9> 0x04 0b00000100
<D10> 0x16 0b00010110
<D11> 0x6d 0b01101101
<D12> 0x52 0b01010010
<D13> 0x6a 0b01101010
```

4.3 List of Common CVs

The NMRA Standard "Configuration Variables For Digital Command Control" provides descriptions for Digital Decoder Configuration Variables (CVs). CVs allow the decoder to be customized for each locomotive, or other mobile or stationary devices. Unless otherwise specified, configuration Variables shall be stored in non-volatile memory and must not change when power is removed from the decoder over long extended periods of time. CVs defined by the NMRA are marked below as Mandatory, Recommended or Optional. CVs identified as Mandatory must be implemented in order to conform to the Standard, while those marked as Recommended are strongly encouraged but not mandatory, and those marked Optional are at the manufacturer's discretion. CVs marked as Read-Only indicates a CV whose value should be set by the manufacturer and which the user cannot modify.

199

Many CVs are implementation specific and no uniform specification is required. Others must be implemented in a uniform fashion in order to achieve compatibility. A CV marked as Uniform Spec indicates a CV which requires implementation by manufacturers according to a common specification. CVs marked as Dynamic are used for Unsolicited Decoder Initiated Transmission.

| $\underline{\text{CV}}$ | Name | Description | Range | <u>Default</u> |
|-------------------------|----------------------|---|---------|----------------|
| 1 | Primary Address | NMRA: Mandatory, Uniform Spec. | 1 - 127 | 3 |
| | | Bits 0-6 contain an address with a value between 1 and 127. Bit seven must have a value of 0. If the value of CV1 is 0 then the decoder will go out of NMRA digital mode and convert to the alternate power source as defined by CV12. This setting will not affect the Digital Decoder's ability to respond to service mode packets. The default value for this CV is 3, if the decoder is not installed in a locomotive or other unit when shipped from the manufacturer. | | |
| | | ESU: | | |
| | | For Multiprotocol decoders: Range 1-255 for Motorola. | | |
| 2 | Vstart | NMRA: Required | 0 - 255 | |
| | | Vstart is used to define the voltage drive level used as the start voltage on the motor. The voltage drive levels shall correspond linearly to the voltage applied to the motor at speed step one, as a fraction of available rectified supply voltage. When the voltage drive level is equal to zero, there shall be zero voltage applied to the motor. When it is at maximum, 255, the full available rectified voltage shall be applied. | | |
| 3 | Acceleration Rate | NMRA: Required | 0 - 255 | |
| | | Determines the decoder's acceleration rate. The formula for the acceleration rate shall be equal to (the contents of CV3 \times 0.896) / (number of speed steps in use). For example, if the contents of CV3 equals 2, then the acceleration is 0.064 sec/step for a decoder currently using 28 speed steps. If the content of this parameter equals 0 then there is no programmed momentum during acceleration. | | |
| | | ESU: | | |
| | | This value multiplied by 0.25 is the time from stop to maximum speed. For LokSound 5 DCC the unit is 0.896 seconds | | |
| 4 | Deceleration | NMRA: Required | 0 - 255 | |
| | Rate | Determines a decoders braking rate, in the same fashion as ${\rm CV3}.$ | | |
| | | ESU: | | |
| | | This value multiplied by 0.25 is the time from maximum speed to stop. For LokSound 5 DCC: The unit is 0.896 seconds. | | |
| 5 | Vhigh | NMRA: Optional | 0 - 255 | |
| | | Vhigh is used to specify the motor voltage drive levels at the maximum speed step. This value shall be specified as a fraction of available rectified supply voltage. When the contents of CV5 equals 255, the full available rectified voltage shall be applied. Values of 0 or 1 shall indicate that Vhigh is not used in the calculation of the speed table. | | |

| $\underline{\text{CV}}$ | Name | <u>Description</u> | Range | Default |
|-------------------------|-----------------|--|---------|---------|
| 6 | VMid | NMRA: Optional | | |
| | | Vmid specifies the voltage drive level at the middle speed step. Vmid is used to generate a performance curve in the decoder that translate speed step values into motor voltage drive levels and is specified as a fraction of available rectified supply voltage. Values of 0 or 1 shall indicate that Vmid is not used in the calculation of the speed table. | | |
| | | ESU: | | |
| | | Medium speed of the engine. Use only if 3-point speed table is enabled. For LokSound 5 DCC only. | | |
| 7 | Manufacturer | NMRA: Mandatory, Read-Only | | |
| | Version Number | This is reserved for the manufacturer to store information regarding the version of the decoder. | | |
| | | ESU: Internal software version of decoder | - | - |
| 8 | Manufacturer ID | NMRA: Mandatory, Read-Only, Uniform Spec. | | |
| | | ${\rm CV8}$ shall contain the NMRA assigned id number of the manufacturer of this decoder. | | |
| | | ESU: Writing value 8 in this CV triggers a reset to factory default values | 151 | - |
| 9 | Total PWM Pe- | NMRA: Optional | | |
| | riod | The value of CV9 sets the nominal PWM period at the decoder output and therefore the frequency is proportional to the reciprocal of the value. The recommend formula for PWM period should be: PWM period (uS) = $(131 + \text{MANTISSA} \times 4) \times 2 \text{ EXP}$, Where MANTISSA is in bits 0-4 bits of CV9 (low order) and EXP is bits 5-7 for CV9. If the value programmed into CV9 falls outside a decoder's capability, it is suggested (but not required) that the decoder "adjust" the value to the appropriate highest or lowest setting supported by the decoder. | | |
| | | ESU: Motor PWM frequency as a multiple of 1000 Hz. | 10 - 50 | 40 |
| 10 | EMF Feedback | NMRA: Optional | | |
| | Cutout | Contains a value between 1 and 128 that indicates the speed step above which the back EMF motor control cuts off. When 14 or 28 speed steps are used the LSB's of the value are truncated appropriately. | | |
| 11 | Packet time-out | NMRA: Required | | |
| | Value | Contains the maximum time period that the decoder will maintain its speed without receiving a valid packet. | | |
| 12 | Power Source | NMRA: Optional, Uniform Spec. | | |
| | Conversion | Contains the identity of the alternate power source to which the decoder will be converted should CV1 contain zero. This is also the primary alternative power source selected should the decoder perform power source conversion. The currently assigned Power Source Conversion codes areas follows: | | |
| | | 0b00000001 Analog Power Conversion 0b00000010 Radio 0b00000100 Zero-1 0b00001000 TRIX 0b00010000 CTC 16 / Railcommand 0b00100000 FMZ (Fleischmann) | | |

| $\underline{\text{CV}}$ | Name | <u>Description</u> | Range | Default |
|-------------------------|-------------------|--|-------|---------|
| 13 | Alternate Mode | NMRA: Optional, Uniform Spec. | | |
| | Function Status | Indicates the status of each function (F1 through F8) when the unit is operating in alternate power mode, which cannot control the functions. If a function can be controlled, then the corresponding bit is ignored. A value of 0 indicates the function is off, while a value of 1 indicates the function is on. Bit 0 corresponds to F1, while Bit 7 corresponds to F8. | | |
| | | ESU: Status of functions F1 to F8 in analogue mode | 0-255 | 1 |
| 14 | Alternate Mode | NMRA: Optional, Uniform Spec. | | |
| | Function 2 Status | Indicates the status of each function (F9 through F12, & FL) when the unit is operating in alternate power mode, which cannot control the functions. If a function can be controlled, then the corresponding bit is ignored. A value of 0 indicates the function is off, while a value of 1 indicates the function is on. FL in the forward direction is controlled by bit 0, FL in the reverse direction is controlled by bit 1. Bit 2 corresponds to F9, while Bit 5 corresponds to F12. | | |
| | | ESU: Status of function F0, F9 to F12 in analogue mode | 0-63 | 1 |
| 15 & 16 | Decoder Lock | NMRA: Optional, Uniform Spec. | | |
| | | The Decoder Lock is used to change CVs in only one of several decoders with the same short address (CV1) or long address (CV17 and CV18) that are installed in the same locomotive. Assign a number to CV16 in each decoder (i.e. 1 to motor decoder, 2 to sound decoder, 3 or higher to other decoders) before the decoders are installed in the locomotive. To change a value in another CV of one of the installed decoders, first write the number 1 (motor), 2 (sound), or 3 or higher (other) into CV15, then send the new value to the CV to be changed. The decoders will compare CV15 to CV16 and, if the values are equal, the CV to be changed will be changed. If the values in CV15 and CV16 are different, the update will be ignored. | | |
| 17 & 18 | Extended Address | NMRA: Optional, Uniform Spec. | | |
| | uress | The Extended Address is the locomotives address when the decoder is set up for extended addressing (indicated by a value of 1 in bit 5 of CV29). CV17 contains the most significant bits of the two byte address and must have a value between 0b11000000 and 0b11100111, inclusive, in order for this two byte address to be valid. CV18 contains the least significant bits of the address and may contain any value. | | |
| 19 | Consist Address | NMRA: Optional, Uniform Spec. | | |
| | | Contains a seven bit address in bit positions 0-6. Bit 7 indicates the relative direction of this unit within a consist, with a value of 0 indicating normal direction, and a value of 1 indicating a direction opposite the unit's normal direction. If the seven bit address in bits 0-6 is 0b00000000 the unit is not in a consist. | | |
| | | ESU: Additional address for consist operation. Value 0 or 128 means: consist address is disabled. $1-127$ consist address active, normal direction. $129-255$ consist address active reverse direction. | 0-255 | 0 |

| $\underline{\mathrm{CV}}$ | Name | Description | Range | Default |
|---------------------------|--|---|---------|---------|
| 21 | Consist Address | NMRA: Optional, Uniform Spec. | | |
| | Active for F1-F8 | Defines for functions F1-F8 whether the function is controlled by the consist address. For each Bit a value of 1 indicates that the function will respond to instructions addressed to the consist address. A value of 0 indicates that the function will only respond to instructions addressed to the locomotive address. F1 is indicated by bit 0. F8 by bit 7. | | |
| | | ESU: Status of functions F1 to F8 in Consist mode. Meaning of the bits as in CV13 | 0-255 | 0 |
| 22 | Consist Address Active for FL and F9-F12 | NMRA: Optional, Uniform Spec. | | |
| | F9-F12 | Defines for function FL whether the function is controlled by the consist address. For each Bit a value of 1 indicates that the function will respond to instructions addressed to the consist address. A value of 0 indicates that the function will only respond to instructions addressed to the locomotive address. FL in the forward direction is indicated by bit 0, FL in the reverse direction is controlled by bit 1. Bit 2 corresponds to F9, while Bit 5 corresponds to F12. | | |
| | | ESU: Status of functions FL, F9 to F12 in Consist mode. Meaning of the bits as in CV14. | 0-63 | 0 |
| 23 | Acceleration Adjustment | NMRA: Optional, Uniform Spec. | | |
| | justinent | This Configuration Variable contains additional acceleration rate information that is to be added to or subtracted from the base value contained in CV3 using the formula (the contents of CV23 \times .896) / (number of speed steps in use). This is a 7 bit value (bits 0-6) with bit 7 being reserved for a sign bit (0-add, 1-subtract). In case of overflow the maximum acceleration rate shall be used. In case of underflow no acceleration shall be used. The expected use is for changing momentum to simulate differing train lengths/loads, most often when operating in a consist. | | |
| | | ESU: Factor for adjusting Acceleration CV3. Values from 0 to 127 are added to CV3. If the values are to be subtracted, additionally set bit 7 (value 128). The unit is 0.896 seconds. | 0 - 127 | 0 |
| 24 | Deceleration Ad- | NMRA: Optional, Uniform Spec. | | |
| | justment | This Configuration Variable contains additional braking rate information that is to be added to or subtracted from the base value contained in CV4 using the formula (the contents of CV24 \times .896) / (number of speed steps in use). This is a 7 bit value (bits 0-6) with bit 7 being reserved for a sign bit (0-add,1-subtract). In case of overflow the maximum deceleration rate shall be used. In case of underflow no deceleration shall be used. The expected use is for changing momentum to simulate differing train lengths/loads, most often when operating in a consist. | | |
| | | ESU: Factor for adjusting the deceleration CV4. Values from 0 to 127 are added to CV3. If the values are to be subtracted, additionally set bit 7 (value 128). The unit is 0.896 seconds. | 0 - 127 | 0 |

| $\underline{\text{CV}}$ | Name | Descri | ption | Range | <u>Default</u> |
|-------------------------|--|--|---|-------|----------------|
| 25 | Speed Table/Mid Range Cab Speed Step | NMRA | A: Optional, Uniform Spec. | | |
| | Step | factory that the defines where In 14-s by two default speed: or 1 sh | the between 2 and 127 shall be used to indicate 1 of 126 by preset speed tables. A value of 0b00000010 indicates the curve shall be linear. A value between 128 and 154 is the 28-speed step position (1-26) which will define the mid range decoder speed value will be applied. Speed mode the decoder will utilize this value divided to If the value in this variable is outside the range, the timid cab speed of 14 (for 28 speed mode or 7 for 14 mode) shall be used as the mid speed value. Values of 0 hall indicate that this CV is not used in the calculation speed table. | | |
| 27 | Decoder Automatic Stopping Configuration | NMRA | A: Optional, Uniform Spec. | | |
| | | | to configure which actions will cause the decoder to atically stop. | | |
| | | $\underline{\mathrm{Bit}}$ | <u>Function</u> | | |
| | | d7 | Reserved | | |
| | | d6 | Reserved | | |
| | | d5 | Enable/Disable Auto Stop in the presence forward polarity DC. $0 = Disabled 1 = Enabled$ | | |
| | | d4 | Enable/Disable Auto Stop in the presence of reverse polarity DC. $0 = Disabled 1 = Enabled$ | | |
| | | d3 | Reserved | | |
| | | d2 | Enable/Disable Auto Stop in the presence of an Signal Controlled Influence cutout signal. $0 = Disabled$ $1 = Enabled$ | | |
| | | d1 | Enable/Disable Auto Stop in the presence of an asymmetrical DCC signal which is more positive on the left rail. $0 = \text{Disabled } 1 = \text{Enabled}$ | | |
| | | d0 | Enable/Disable Auto Stop in the presence of an asymmetrical DCC signal which is more positive on the right rail. $0 = \text{Disabled } 1 = \text{Enabled}$ | | |
| | | | | | |

 $\underline{\mathrm{CV}}$ Description **Default** Name Range ESU: Allowed (enabled) Brake modes Bit Function Loco brakes with constant brake distance if Speed=0 $\,$ d7d6Selectrix brake diode, rakes if polarity is like driving direction d5Selectrix brake diode, brakes if polarity is against driving direction d4Brake on DC, if polarity like driving direction d3Brake on DC, if polarity against driving direction d2ZIMO® HLU brakes active d1ABC braking, voltage higher on the left hand side ABC braking, voltage higher on the right hand side Bi-Directional NMRA: Optional, Uniform Spec. 28 Communication ${\bf Configuration}$ Used to configure decoder's Bi-Directional communication characteristics when CV29-Bit 3 is set $\underline{\text{Bit}}$ <u>Function</u> d7Reserved d6Reserved d5Reserved d4Reserved d3Reserved d2Enable/Disable Initiated Broadcast Transmission using Signal Controlled Influence Signal. 0 = Disabled 1 = EnabledEnable/Disable Initiated Broadcast Transmission using Asymmetrical DCC Signal. 0 = Disabled 1 =Enabled Enable/Disable Unsolicited Decoder Initiated Transmission. 0 = Disabled 1 = EnabledESU: RailCom® Configuration 131 $\underline{\mathrm{Bit}}$ <u>Function</u> d7Enable/Disable RailCom® Plus automatic loco recognition. 0 = Disabled 1 = Enabledd1Enable/Disable Data transmission on Channel. 0 =Disabled 1 = EnabledEnable/Disable Channel 1 Address broadcast. 0 =Disabled 1 = Enabled

| CV | Name | | Descrip | otion_ | Range | <u>Default</u> |
|----|------------|---------|----------------------------|--|-------|----------------|
| 29 | Configurat | | NMRA | : Mandatory, Uniform Spec. | | |
| | Supported | l | $\underline{\mathrm{Bit}}$ | <u>Function</u> | | |
| | | | d7 | Accessory Decoder: $0 = \text{Multifunction Decoder}, 1 = \text{Accessory Decoder}$ (see CV541 for a description of assignments for bits 0-6) | | |
| | | | d6 | Reserved | | |
| | | | d5 | 0= one byte addressing, $1=$ two byte addressing (also known as extended addressing), | | |
| | | | d4 | Speed Table: $0 =$ speed table set by CV2, CV5, and CV6, $1 =$ Speed Table set by CV66 to CV95 | | |
| | | | d3 | Bi-Directional Communications: $0 = \text{Bi-Directional}$ Communications disabled, $1 = \text{Bi-Directional}$ Communications enabled. | | |
| | | | d2 | Power Source Conversion: $0 = \text{NMRA}$ Digital Only, $1 = \text{Power}$ Source Conversion Enabled, See CV12 for more information. | | |
| | | | d1 | FL location: $0=$ bit 4 in Speed and Direction instructions control FL, $1=$ bit 4 in function group one instruction controls FL. | | |
| | | | d0 | Locomotive Direction: $0 = \text{normal}$, $1 = \text{reversed}$. This bit controls the locomotive's forward and backward direction in digital mode only. Directional sensitive functions, such as headlights (FL and FR), will also be reversed so that they line up with the locomotive's new forward direction. | | |
| | | | | This register contains important information, some of are only relevant for DCC operation. | | |
| | | | $\underline{\mathrm{Bit}}$ | Function | | |
| | | | d5 | 0 = Short addresses (CV 1) in DCC mode 1 = Long addresses (CV 17 + 18) in DCC mode | | |
| | | | d4 | 0 = Speed curve through CV 2, 5, 6 (LokSound 5 DCC ONLY). 1 = Speed curve through CV 67 - 94 (Multiprotocol) | | |
| | | | d3 | $0 = \text{Disable RailCom}$ \mathbb{R} $1 = \text{Enable RailCom}$ | 12 | |
| | | | d2 | 0 = Disable analog operation $1 = $ Enable analog operation | | |
| | | | d1 | 0 = 14 speed steps DCC 1 = 28 or 128 speed steps DCC | | |
| | | | d0 | 0 = Normal direction of travel 1 = Reversed direction of travel | | |
| 30 | Error In | nforma- | NMRA | a: Optional, Uniform Spec. | | |
| | | | Config specifie | case where the decoder has an error condition this uration Variable shall contain the error condition as ed by the manufacturer. A value of 0 indicates that no as occurred. | | |

error has occurred.

| $\underline{\text{CV}}$ | Name | Descri | otion | $\underline{\text{Range}}$ | $\underline{\mathrm{Default}}$ |
|-------------------------|--|--|---|----------------------------|--------------------------------|
| 31 | Index High Byte | NMRA | a: Optional, Uniform Spec. | | |
| | | when to contain may had clusive by the tains to contain | dexed Address is the address of the indexed CV page the decoder is set up for indexed CV operation. CV31 as the most significant bits of the two byte address and ave any value between 0b00010000 and 0b11111111 in. Values of 0b00000000 thru 0b00001111 are reserved NMRA for future use. (4096 indexed pages) CV32 conhe least significant bits of the index address and may any value. This gives a total of 61,440 indexed pages, ith 256 bytes of CV data available to manufacturers. | | |
| 32 | Index Low Byte | NMRA | a: Optional, Uniform Spec. | | |
| | | See CV | 731 | | |
| 33-46 | Output Locations 1-14 for Functions $FL(f)$, $FL(r)$, and $F1-F12$ | Contai trol wl custon mands cated i A valu contro trol m multip 42 con default put 2, | ns a matrix indication of which function inputs connich Digital Decoder outputs. This allows the user to nize which outputs are controlled by which input comnic. The outputs that Function FL(f) controls are indin CV33, FL (r) in CV34, F1 in CV35, to F12 in CV46. e of 1 in each bit location indicates that the function is that output. This allows a single function to connictiple outputs, or the same output to be controlled by the functions. CVs 33-37 control outputs 1-8. CVs 38-trol outputs 4-11 CVs 43-46 control outputs 7-14. The is is that FL (f) controls output 1, FL (r) controls output F1 controls output 3 to F12 controls output 14. The numbered output is in the LSB of the CV. | | |
| 47-64 | Manufacturer Unique | | | | |
| 47 | Protocol selection | ESU: Y | Which protocols are active. | 0 - 255 | 13 |
| | | $\underline{\mathrm{Bit}}$ | <u>Function</u> | | |
| | | d3 | Enable/Disable Selectrix® protocol (Not for LokSound 5 DCC). $0 = Disabled 1 = Enabled$ | | |
| | | d2 | Enable/Disable Motorola® protocol (Not for LokSound 5 DCC). $0 = Disabled 1 = Enabled$ | | |
| | | d1 | Enable/Disable M4 protocol (Not for LokSound 5 DCC). $0 = Disabled 1 = Enabled$ | | |
| | | d0 | Enable/Disable DCC protocol. $0 = Disabled 1 = Enabled$ | | |

| $\underline{\mathrm{CV}}$ | Name | Description | | Range | Default |
|---------------------------|--|---|--|---------|---------|
| 49 | Extended Config- | ESU: | | 0-255 | 19 |
| | uration #1 | Bit Function | | | |
| | | d7 Märklin® Consecutive address | es, "High"-Bit. | | |
| | | d6 Reserved | | | |
| | | d5 Enable/Disable LGB® function Disabled $1 = \text{Enabled}$ | on button mode. $0 =$ | | |
| | | d4 Enable/Disable Automatic DO tion. $0 = Disabled 1 = Enabled$ | | | |
| | | d3 Märklin® Consecutive address | es, "low"-Bit | | |
| | | d2 Reserved | | | |
| | | d1 Reserved | | | |
| | | d0 Enable/Disable Load control (label 1 = Enabled | Back-EMF). $0 = Dis-$ | | |
| 50 | Analogue mode | Selection of allowed analogue modes | | 0 - 3 | 3 |
| | | Bit Function | | | |
| | | d2 Enable/Disable QSI Quantum l $0 = Disabled 1 = Enabled$ | Engineer DC Support. | | |
| | | d 1 Enable/Disable DC Analogue r $=$ Enabled | node. $0 = Disabled 1$ | | |
| | | d0 Enable/Disable AC Analogue M = Enabled | Mode. $0 = Disabled 1$ | | |
| 51 | K Slow Cutoff | nernal Speedstep, until K Slow is activ | <i>т</i> е | 0 - 255 | 10 |
| 52 | BEMF Param. K Slow "K" - | Portion of the PI-Controller valid for lo | ower speed steps | 0 - 255 | 10 |
| 53 | Control Reference voltage | Defines the Back EMF voltage, which the rate at maximum speed. The higher motor, the higher this value may be so not reach maximum speed, reduce this | the efficiency of the et. If the engine does | 0 - 255 | 130 |
| 54 | Load control Parameter K | K—component of the internal PI-control of load control. The higher the value, to Back EMF control. | | 0 - 255 | 50 |
| 55 | Load control Parameter I | r—component of the internal PI-contronentum (inertia) of the motor. The hof the motor (large flywheel or bigger to value has to be set. | nigher the momentum | 0 - 255 | 100 |
| 56 | BEMF Influence at VMin | 0-100%. Defines the "Strengh" of the speed step | BEMF at minimum | 1 - 255 | 255 |
| 57 | Steam chuff syn- chronisation #1 | Defines the steam chuff synchronisation | 1. | 1 - 255 | 30 |
| 58 | Steam chuff synchronisation #2 | Defines the steam chuff synchronisation | 1. | 1 - 255 | 20 |
| 63 | Sound volume "Master" | Master volume for all sounds. | | 0 - 192 | 128 |
| 64 | Brake sound threshold "Brake On" | If the actual loco speed step is smalled value indicated here, the brake sound is | - | 0 - 255 | 60 |

| $\underline{\text{CV}}$ | Name | Description | $\underline{\text{Range}}$ | Default |
|-------------------------|--|---|----------------------------|---------|
| 65 | Brake sound threshold "Brake Off" | If the actual loco speed step is smaller than the one indicated here (up to 255), the brake sound will be switched off again. Compare chapter 13.4. | 0 - 255 | 7 |
| 66 | Forward Trimm | Divided by 128 is the factor used to multiply the motor voltage when driving forward. The value 0 deactivates the trim. | 0 - 255 | 128 |
| 67-94 | Speed table | Defines motor voltage for speed steps. The values "in between" will be interpolated. | 0 - 255 | - |
| 95 | Reverse Trimm | Divided by 128 is the factor used to multiply the motor voltage when driving backwards. Value 0 deactivates the trim. | 0 - 255 | 128 |
| 101 | Shunting Mode Trimm | Divided by 128, this gives the factor by which the motor voltage is multiplied when the shunting gear is active. See section 10.1.2. | 0 - 128 | 64 |
| 102 | Brake Mode Exit Delay | Time as a multiple of 16 milliseconds that must pass before a detected braking distance is left again. See section 10.4.6. | 0 - 255 | 12 |
| 103 | Load adjustment "Optional Load" | Divided by 128, this gives the factor that changes CV3, CV4 and the sound when "Optional Load" is active. See section 10.7. | 0 - 255 | 0 |
| 104 | Load adjustment "Primary Load" | Divided by 128, this gives the factor that changes CV3, CV4 and the sound when "Primary Load" is active. See section 10.7. | 0 - 255 | 255 |
| 105 | User CV #1 | Free CV. Here you are able to save what ever you want. | 0 - 255 | 0 |
| 106 | User CV $\#2$ | Free CV. Here you are able to save what ever you want. | 0 - 255 | 0 |
| 111 | Gearbox back- lash | Time as a multiple of $16~\mathrm{mS}$, for which the motor runs at minimum speed after reversing the direction to prevent gear box jerking. | 0 - 255 | 0 |
| 112 | Frequency for Flashing light effects | Flashing frequency for Strobe lighting effects. Multiple of 0.065536 seconds. See section 12.5.4. | 0 - 255 | 20 |
| 113 | Power Fail By- pass | The time that the decoder bridges via the PowerPack after an interruption of voltage. Unit: A multiple of 0.032768 sec. See section 6.12.2. | 0 - 255 | 32 |
| 116 | Slow speed BEMF Sampling period | Frequency of BEMF measurement in 0.1 milliseconds at speed step 1 $$ | 50 - 200 | 50 |
| 117 | Full speed BEMF Sampling period | Frequency of BEMF measurement in 0.1 milliseconds at speed step 255 | 50 - 200 | 150 |
| 118 | Slow speed BEMF | Measurement gap length VMin Length of the BEMF measuring gap in 0.1 milliseconds at speed step 1 $$ | 10 - 20 | 150 |
| 119 | Full speed BEMF | Measurement gap length Vmax Length of the BEMF measuring gap in 0.1 milliseconds at speed step 255 | 10 - 20 | 15 |
| 123 | ABC Mode "Slow drive" | Speed which is valid in the slow driving section during ABC braking. | 0 | - |

| $\underline{\mathrm{CV}}$ | $\underline{\text{Name}}$ | Description | Range | Default |
|---------------------------|--------------------------------|--|------------|---------|
| 124 | Extended Configuration #2 | Additional important settings for decoders | | 21 |
| | drawon _{#F} 2 | Bit Function | | |
| | | d7 Reserved | | |
| | | d6 Enable/Disable Automatic parking Brake. $0 = Disabled$ abled $1 = Enabled$ | 3- | |
| | | d5 Enable/Disable Motor is switched off for a few seconds when blocked to avoid burnout. 0 = Disabled = Enabled | | |
| | | d4 0 = Enable Output AUX9 (LokSound 5 H0 only). = Enable Wheel Sensor input (LokSound 5 H0 only) | | |
| | | d3 Enable/Disable SUSI protocol. $0 = Disabled 1 = Enabled$ | 1- | |
| | | d2 Enable/Disable prime mover startup delay. $0 = Disabled$ abled $1 = Enabled$ | 3- | |
| | | d0 Enable/Disable Decoder lock with CV 15 / 16. 0 : Disabled 1 = Enabled | = | |
| 125 | Start voltage | Analog DC See section 10.8. | 0 - 255 | 90 |
| 126 | Maximum speed | Analog DC See section 10.8. | 0 - 255 | 130 |
| 127 | Start voltage | Analog AC See section 10.8. | 0 - 255 | 90 |
| 128 | Maximum speed | Analog AC See section 10.8. | 0 - 255 | 130 |
| 129 | Analog Functions | "Hysterese" Offset voltage for functions in analogue mode Chapter 10.8 . | e. 0 - 255 | 15 |
| 130 | Analog Motor | "Hysterese" Offset voltage for motor functions in analogu mode. Chapter 10.8 . | ie 0 - 255 | 5 |
| 132 | Grade Crossing Hold Time | Grade Crossing holding time. See chapter 12.5.3. | 0 - 255 | 80 |
| 133 | Sound Fader | Volume when sound fader is active. See chapter 13.5. | 0 - 255 | 128 |
| 134 | ABC-Mode "Sensibility" | Threshold, from which asymmetry on ABC shall be recognised. | g- 4 - 32 | 10 |
| 138 | Smoke Unit Trim Fan | Divided by 128, this gives the factor by which the fan spee of synchronized smoke units can be adjusted. | d 0 - 255 | 128 |
| 139 | Smoke Unit Trim Temperature | Divided by 128, this gives the factor by which the temperatur of synchronized smoke units can be adjusted. | re 0 - 255 | 128 |
| 140 | Smoke TimeOut | Time until automatic shutdown of the smoke unit. | 0 - 255 | 255 |
| 141 | Smoke Chuff Min | Minimum duration of a steam chuff of an external smoke un in 0.041 seconds resolution. | it 0 - 255 | 10 |
| 142 | Smoke Chuff max | Maximum duration of a steam chuff of an external smoke un in 0.041 seconds resolution. | it 0 - 255 | 125 |
| 143 | Smoke Chuff Length | Divided by 128, this gives the factor by which the duration of the steam chuffs can be adjusted relative to the trigger pulse. | | 100 |
| 144 | Smoke Pre Heat Temperature | Preheating temperature in degrees Celsius for secondar smoke generators (cylinder smoke unit) | у 0 - 255 | 150 |

| $\underline{\text{CV}}$ | Name | <u>Description</u> | Range | <u>Default</u> |
|-------------------------|--|---|---------|----------------|
| 149 | ABC Shuttle Train Holdtimet | Time in seconds, which has to be passed for ABC shuttle train operation, before the direction of travel is changed. See section $10.4.4.3$. | 0 - 255 | 255 |
| 150 | HLU Speedlimit 1 | HLU Speed limit 1. Internal speedstep. | 0 - 255 | 42 |
| 151 | HLU Speedlimit 2 | (U) HLU Speed limit 2 (U). Internal speedstep. | 0 - 255 | 85 |
| 152 | HLU Speedlimit 3 | HLU Speed limit 3. Internal speedstep. | 0 - 255 | 127 |
| 153 | HLU Speedlimit 4 | (L) HLU Speed limit 4 (L). Internal speedstep. | 0 - 255 | 170 |
| 154 | HLU Speedlimit 5 | HLU Speed limit 5. Internal speedstep. | 0 - 255 | 212 |
| 155 -170 | Sound CV 1 - Sound CV 16 | 16 CVs for selecting sounds that can be assigned within sound projects. Please note the documentation for the sound project. | 0 - 255 | 0 |
| 179 | Brake Function 1 | Deceleration Value of which 33% of CV 4 will be deducted if the Brake Function 1 is active. See section 10.6. | 0 - 255 | 80 |
| 180 | Brake Function 2 | Deceleration Value of which 33% of CV 4 will be deducted if the Brake Function 2 is active. See section 10.6. | 0 - 255 | 40 |
| 181 | Brake Function 3 | Deceleration Value of which 33% of CV 4 will be deducted if the Brake Function 3 is active. See section 10.6. | 0 - 255 | 40 |
| 182 | Brake Function 1 max. | Speed Highest speed step that can be reached when Brake function 1 is active. | 0 - 126 | 0 |
| 183 | Brake Function 2 max. | Speed Highest speed step that can be reached when Brake function 1 is active. | 0 - 126 | 126 |
| 184 | Brake Function 3 max. | Speed Highest speed step that can be reached when Brake function 1 is active. | 0 - 126 | 126 |
| 246 | Automatic de- coupling Driving speed | Speed of the loco while decoupling; the higher the value, the faster the loco. Value 0 switches the automatic coupler off. Automatic decoupling is only active if the function output is adjusted to "pulse" or "coupler". | 0 - 255 | 0 |
| 247 | Decoupling - Removing time | This value multiplied with 0.016 defines the time the loco needs for moving away from the train (automatic decoupling). | 0 - 255 | 0 |
| 248 | Decoupling - Pushing time | This value multiplied with 0.016 defines the time the loco needs for pushing against the train (automatic decoupling). | 0 - 255 | 0 |
| 249 | Minimum steam chuff distance | Minimum distance of two steam chuffs, independant from sensor data. Compage chapter 13.3 . | 0 - 255 | 0 |
| 250 | Secondary steam chuff trigger | Defines the distance between two consecutive steam chuffs for the secondary steam chuff generator. The value indicates the promilles the steam chuff distances of the secondary steam chuff generator ought to be shorter then those of the primary steam chuff generator. It is needed for steam locos with two independent boogies, such as "Big Boy" or "Mallet". | 0 – 255 | 0 |

| $\underline{\text{CV}}$ | Name | <u>Description</u> | $\underline{\text{Range}}$ | $\underline{\mathrm{Default}}$ |
|-------------------------|--|---|----------------------------|--------------------------------|
| 253 | Constant brake mode | Determines the constant brake mode. Only active, if CV254 > 0 Function CV $253 = 0$: Decoder stops linearly CV $253 > 0$: Decoder stops constantly linear | 0 – 255 | 0 |
| 254 | Constant braking distance forward | A value > 0 determines the way of brake distance it adheres to, independent from speed. | 0 - 255 | 0 |
| 255 | Constant brak- ing distance backward | Constant braking distances during reverse driving. Only active, if value > 0, otherwise the value of CV 254 is used. Useful for reversible trains. | 0 - 255 | 0 |

Appendix A

Reference Tables

| | MSD | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
|-----|------|-----|-----|-----|-----|-----|-----|-----|--------------|
| LSD | | 000 | 001 | 010 | 011 | 100 | 101 | 110 | 111 |
| 0 | 0000 | NUL | DLE | SPC | 0 | @ | Р | 6 | р |
| 1 | 0000 | SOH | DC1 | ! | 1 | A | Q | a | q |
| 2 | 0010 | STX | DC2 | " | 2 | В | R | b | r |
| 3 | 0011 | ETX | DC3 | # | 3 | С | S | С | \mathbf{s} |
| 4 | 0100 | EOT | DC4 | \$ | 4 | D | Τ | d | t |
| 5 | 0101 | ENG | NAK | % | 5 | E | U | е | u |
| 6 | 0110 | ACK | SYN | & | 6 | F | V | f | v |
| 7 | 0111 | BEL | ETB | , | 7 | G | W | g | W |
| 8 | 1000 | BS | CAN | (| 8 | Н | X | h | X |
| 9 | 1001 | HT | EM |) | 9 | I | Y | i | У |
| A | 1010 | LF | SUB | * | : | J | Z | j | \mathbf{z} |
| В | 1011 | VT | ESC | + | ; | K | [| k | { |
| С | 1100 | FF | FS | , | < | L | \ | 1 | |
| D | 1101 | CR | GS | - | = | M |] | m | } |
| E | 1110 | SO | RS | • | > | N | < | n | ~ |
| F | 1111 | SI | US | / | ? | О | _ | О | DEL |

TABLE A-1. ASCII Character Set (7-Bit Code)

Appendix B

Revision History

| Release Date | Changes |
|--------------|---|
| 30 Jan 2022 | Appendix B - Revision History added. |
| | Index added. |
| | Glossary added. |
| | GetLocoSlotDataP1 and P2 updated for SlotNotImplemented response |
| | The following messages were updated for the Throttle ID field: |
| | LocoF0F6P2 |
| | LocoF7F13P2 |
| | LocoF14F20P2 |
| | LocoF21F28P2 |
| | LocoSpdDirP2 |
| | LocoSlotDataP2 function mapping corrected |
| | NoFreeSlotsP1 and NoFreeSlotsP2 added and references to them added. |
| | IllegalMoveP1 and IllegalMoveP2 added and references to them added. |
| | SlotNotImplemented added. |
| 23 Jan 2022 | Baseline. |

Glossary

- address is the numeric identification code by which a decoder recognises commands directed specifically to it. 4, 5, 25, 27, 33, 35
- **command station** is the electronic device that generates DCC commands based upon inputs it receives and transmits them to decoders. 1
- **Common** is a locomotive slot state that indicates that the slot is not currently in-use by a throttle but it is still being refreshed by the command station. A slot with a state of Common can be selected by any throttle on the network. 6
- DCC stands for Digital Command Control. 1, 41
- Global System Track Status means the byte 7 of a LocoSlotDataP1 or LocoSlotDataP2 response. 2
- **In-Use** is a locomotive slot state that indicates that the slot has been made active by a throttle and can no longer be selected by another throttle. 37, 39
- **locomotive slot** is a memory location in the command station which holds information about a locomotive's decoder and current state. 2
- **Loconet** is the peer-to-peer local area network system architecture used by Digitrax to carry DCC and other commands across Digitrax command control systems. 1
- **Isignature** is the combination of bits and bytes within a message that uniquely identify the message type. 2
- message means a sequence of two or more bytes sent over the network that conform to the network message format. The first byte of the message is an opcode and the last is a checksum. 2

mobile decoder means an electronic device installed in a locomotive that receives a signal from the command station through the track, decodes it and tells the locomotive what to do. 1

- NMRA is the National Model Railroad Association, founded in 1935. One of its purposes is to define and manage model railroad standards related to interchange of equipment in North America. 25, 27, 33, 35
- **opcode** means the first byte of a network message. The opcode indicates the purpose and length of the message. 2
- **peer-to-peer** is a network communication scheme where messages between devices are not managed or controlled by a central controller or server. 1
- **physical throttle** means an electronic input device, often hand-held, that is used to tell the command station what commands to send to the decoders. 4
- **polled** is the process of interrogating a device to see if it has information or commands to send to the system. 1
- **polling** is the process by which devices are interrogated sequentially, one after the other, to see if they have information or commands to send to the system. 1
- Slot Status 1 means byte 3 of a LocoSlotDataP1 response or byte 4 of a LocoSlotDataP2 response. 3
- slot state means the current state of a locomotive slot. A locomotive slot can be in one the following states: Free, New, In-Use, Common or Idle. 3
- software throttle means a software application that is used to tell the command station what commands to send to the decoders. 4
- stationary decoder means an electronic device for a turnout or other accessory that receives a signal from the command station through the track, decodes it and tells the turnout or accessory what to do. 1
- **system slot** is a memory location in the command station which holds system information. 2
- throttle means a physical throttle or a software throttle. 3
- **Throttle ID** means a pair of 7-bit numbers that identify (hopefully uniquely) the throttle to the command station. 4, 5, 78, 82, 90, 94, 110

Index

| Ack, 8 | IllegalMoveP2, 39 IMMPacket, 41 |
|---|--|
| Bootloader 2, 65 | InterfaceData, 44 |
| Busy, 10 | Interrogate, 55 |
| CfgSlotDataP1, 11 Checksum, 2 Command Station, 1 Configuration Variables, 124, 128, 189 Consist, 18 ConsistDirF0F4, 18 | IPLDataLoad, 48 IPLDevData, 51 IPLDiscover, 55 IPLEndLoad, 59 IPLSetAddr, 62 IPLSetupBL2, 65 |
| Consisting, 68, 70, 172, 174 | |
| CVs, see Configuration Variables | LinkSlotsP1, 68 LinkSlotsP2, 70 |
| DCC, 41 | Loco Reset, 135 |
| Direction, 74, 76, 110 | LocoBinStateP2, 72 |
| Fast Clock, 3 Firmware Update, 65 Functions, 74, 76, 78, 80, 82, 84, 86, 88, 90, 92, 94 | LocoDirF0F4P1, 74 LocoDirF0F4P2, 76 LocoF0F6P2, 78 LocoF12F20F28P2, 86 LocoF13F19P2, 88 |
| GetBrdOpSw, 20 GetCfgSlotDataP1, 22 GetInterfaceData, 24 GetLocoSlotDataLAdrP1, 25 GetLocoSlotDataLAdrP2, 27 GetLocoSlotDataP1, 29 GetLocoSlotDataP2, 31 GetLocoSlotDataSAdrP1, 33 GetLocoSlotDataSAdrP2, 35 Hardware Version, 52 | LocoF14F20P2, 90 LocoF21F27P2, 92 LocoF21F28P2, 94 LocoF5F11P2, 84 LocoF5F8P1, 80 LocoF7F13P2, 82 LocoSlotDataP1, 96 LocoSlotDataP2, 100 LocoSpdDirP2, 110 LocoSpdP1, 106 LocoSpdP2, 108 |
| IllegalMoveP1, 37 | Message Format, 2 |

220 INDEX

| Message Length, 2 | Software Version, 52 | | |
|---|--|--|--|
| MoveSlotsP1, 116 | Speed, 106, 108, 110 | | |
| MoveSlotsP2, 118 | SwReq, 166 | | |
| N. D. Cl. (D1 110 | SwState, 168 | | |
| NoFreeSlotsP1, 112 | Throttle ID 4 | | |
| NoFreeSlotsP2, 114 | Throttle ID, 4 | | |
| Null Move, 116, 118 | Track Power, 132, 133 TransRep, 170 | | |
| Opcode, 2 | Transfrep, 170 | | |
| opeoue, 2 | UnlinkSlotsP1, 172 | | |
| PeerXfer16, 120 | UnlinkSlotsP2, 174 | | |
| Power, 132, 133 | , | | |
| Product Code, 51 | | | |
| ProgCV, 124 | | | |
| Programming, 124, 128 | | | |
| Direct Mode, 190 | | | |
| Operations Mode, 190 | | | |
| Paged Mode, 190 | | | |
| Physical Register Mode, 190 | | | |
| ProgSlotDataP1, 128 | | | |
| Protocol 1, 3 | | | |
| Protocol 2, 3 | | | |
| PwrOff, 132 | | | |
| PwrOn, 133 | | | |
| Reset, 135 | | | |
| SensRepGenIn, 136 | | | |
| SensRepTurnIn, 138 | | | |
| SensRepTurnOut, 140 | | | |
| SetBrdOpSw, 142 | | | |
| SetIdleState, 144 | | | |
| SetLocoSlotDataP1, 145 | | | |
| SetLocoSlotDataP2, 149 | | | |
| SetLocoSlotStat1, 155 | | | |
| SetSwWithAck, 157 | | | |
| Signature, 2 | | | |
| Slot, 27, 29, 31, 35, 96, 100, 116, 118, 145, | | | |
| 149 | | | |
| Slot State, 3 | | | |
| SlotNotImplemented, 159 | | | |
| slots, 3 | | | |