Digitrax Loconet Notes

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Chapter 1

Loconet Protocol

1.1 Overview

LocoNet is a peer to peer distributed network system on which all devices can monitor the network data flow. The network is event driven by different devices in time, and is not polled by a centralised controller in normal operation. The normal LocoNet state is idle, with no data traffic unless a device has information to send. With no traffic flow, the network is quiet.

LocoNet data is sent in asynchronous format using 1 start bit, 8 data bits and 1 stop bit. The 8 bit data is transmitted least significant bit first. The bit times are 60.0 μ S or 16,660 baud +/- 1.5%. A computer can connect to a Digitrax USB interface at higher baud rates and the device will make the necessary conversion. Bytes may be transmitted back-to-back, with a start bit immediately following the stop bit of the previous character.

Any message that has format or framing errors, data errors or is a fragment caused by noise glitches and does not completely follow the message format will be ignored by all receivers, and a new opcode will be scanned for re-synchronisation.

The OPC_BUSY opcode is included to allow the master to keep the network active whilst it is performing a task that requires a response, and entails a significant processing delay, i.e. it can ensure no new requests are started until it has responded to the last message. This OPC_BUSY opcode should be simply stripped and ignored.

If a device disconnects from LocoNet and so does not access or reference a slot within the system purge time, the master will force the un-accessed slot to common status so other system devices can use the slot. The typical purge time of a master is about 200 seconds. A good "ping" or slot update activity is about every 100 seconds, i.e. if a user makes no

change to a throttle/slot within 100 seconds, the throttle/device should automatically send another speed update at the current speed to reset the purge timeout for that slot.

1.2 Message Format

All LocoNet communications are via multi-byte messages. The master is defined as the device that is maintaining the refresh stack for DCC packet generation and is actively generating the DCC track data. Refresh of information is typically only performed for mobile decoders. Stationary type decoders are not refreshed and individual immediate commands are sent out to the track as requested.

The master is only privileged in respect to performing the task of maintaining the locomotive refresh stack and generating DCC packets. In this way other network transactions may occur that the master does not need to be involved with or understand, as long as they follow the message protocol and timing requirements. i.e. Other devices may have a dialog on the network without disturbing or involving the master. Devices on LocoNet monitor the messages, check for format and data integrity and parse good messages to decode if action is required in the context. Devices such as throttles, input sensors, computer interfaces and control panels may generate LocoNet messages without needing prompting or polling by a central controller.

Devices frequently will be added and removed from an operating LocoNet. The devices and protocol are tolerant of electrical and data transients. The format chosen gives a good degree of data integrity, guaranteed quick network-state synchronisation, high data throughput, good distribution of access to many competing devices and low event latency. Also, the devices may be operated without need for unique ID or other requirements that can make network administration awkward.

The data bytes on LocoNet are defined as 8 bit data with the most significant bit as an opcode flag bit. If the most significant bit, d7, is 1 then the 7 least significant bits are interpreted as a network opcode. The opcode byte may only occur once in a valid message and is the first byte of a message. All the remaining bytes in the message must have a most significant bit of 0, including the last checksum byte. The checksum is the 1's complement of the byte wise exclusive or of all the bytes in the message, except the checksum itself. To validate data accuracy, all the bytes in a correctly formatted message are exclusive or'ed. If this resulting byte value is 0xFF, then the message data is accepted as good.

The opcodes may be examined to determine message length and if subsequent response message is required. Data bits d6 and d5 encode the message length. The message length includes the opcode and the checksum bytes. The bit d3 = 1 implies that a follow-on message or reply is expected.

d7	<u>d6</u>	$\underline{\mathrm{d}5}$	$\underline{d4}$	<u>d3</u>	$\underline{\mathrm{d}2}$	$\underline{d1}$	$\underline{d0}$	
1	0	0	${f E}$	D	\mathbf{C}	В	A	2 byte message
1	0	1	${ m E}$	D	\mathbf{C}	В	A	4 byte message
1	1	0	\mathbf{E}	D	\mathbf{C}	В	A	6 byte message
1	1	1	\mathbf{E}	D	\mathbf{C}	В	A	Variable length message. The next byte in
								the message is a 7 bit byte count.

The A,B,C,D,E are bits available to encode 32 opcodes per message length.

1.3 Refresh Slots

The model of the master refresh stack is an array of up to 120 read/write refresh slots. The slot address is a principal component and is generally the second byte or 1st argument of a message to the master. The refresh slot contains up to 10 data bytes relating to a locomotive and also controls a task in the track DCC refresh stack. Most mobile decoder or locomotive operations process the slot associated with the locomotive to be controlled. The slot number is a similar shorthand ID# to a file handle. Slot addresses 120-127 are reserved for system and master control. Slot #124 (0x7C) is allocated for read/write access to the programming track, and the format of the 10 data bytes is not the same as a normal slot.

1.4 Standard Address Selection

To request a mobile or locomotive decoder task in the refresh stack, a throttle device requests a locomotive address for use, (opcode OPC_LOCO_ADR). The master (or computer in a limited master environment) responds with a slot data read for the slot, (OPC_SL_RD_DATA), that contains this locomotive address and all of its state information. If the address is currently not in any slot, the master will load this new locomotive address into a new slot (speed=0, direction forwards, functions off and 128 step mode) and return this as a OPC_SL_RD_DATA. If no inactive slots are free to load the new locomotive address, the response will be the OPC_LONG_ACK with a fail code 0x00.

The throttle/computer must then examine the slot data bytes to work out how to process the master response. If the slot status 1 byte shows the slot to be common idle or new the throttle may change the slot to in use by performing a null move instruction (see OPC_MOVE_SLOTS) on this slot. This activation mechanism is used to guarantee proper slot usage interlocking in a multi-user asynchronous environment.

If the slot return information shows the locomotive requested is in use or up-consisted (i.e. the SL_CONUP , bit 6 of slot status 1 = 1) the user should not use the slot. Any

up-consisted locomotives must be unlinked before usage. Always process the result from the OPC_LINK_SLOTS and OPC_UNLINK_SLOTS commands, since the master reserves the right to change the reply slot number and can reject the linking tasks under several circumstances. Verify the reply slot number and the link UP/DN bits in slot status 1 are as you expected.

The throttle will then be able to update speed, direction and function information. Whenever slot information is changed in an active slot, the slot is flagged to be updated as the next DCC packet sent to the track. If the slot is part of linked consist slots the whole consist chain is updated consecutively.

If a throttle is disconnected from the LocoNet, upon reconnection (if the throttle retains the slot state from before disconnection) it will request the full status of the slot it was previously using. If the reported status and speed, function data etc., from the master exactly matches the remembered slot state the throttle will continue using the slot. If the SLOT data does not match, the throttle will assume the slot was purged free by the system and will go through the setup log on procedure again.

With this procedure the throttle does not need to have a unique ID number. slot addresses do not imply they contain any particular locomotive address. The system can be mapped such that the slot address matches the locomotive address within, if the user directly reads and writes to slots without using the master to allocate locomotive addresses.

1.5 Opcodes

OPC_BUSY

Operation: Indicates that the master is busy. 2-Byte Message Group: Direction: \leftrightarrow Command Station Encoding: Byte 0: 1 Opcode. 0 0 0 0 0 0 0x81Byte 1: Checksum. 0 1 1 1 1 0 0x7E

Description:

This message indicates that the master is busy. When sent to a command station it responds with an OPC_PEER_XFER message.

Response:

None.

Notes:

None.

OPC_CONSIST_FUNC

Operation: Set function bits in a consist uplink element.

Group: 4-Byte Message

 $\underline{\text{Direction:}} \rightarrow \text{Command Station}$

Encoding:

Byte 0:

1	0	1	1	0	1	1	0	0xB6	Opcode.

Byte 1:

0	n	n	n	n	n	n	n	<SLOT $#$ $>$	Slot number in the range $0x00$ to $0x7F$.
---	---	---	---	---	---	---	---	---------------	---------------------------------------------

Byte 2:

	0	d6	d5	d4	d3	d2	d1	d0	<DIRF $>$	Consist element's direction and state of func-
_						'	'			tions F0 to F4.

- d6 Reserved. Set to 0.
- d5 Locomotive direction. 1 means forward, 0 means backwards.
- d4 F0 state. 1 means on, and 0 means off.
- d3 F4 state. 1 means on, and 0 means off.
- d2 F3 state. 1 means on, and 0 means off.
- d1 F2 state. 1 means on, and 0 means off.
- d0 F1 state. 1 means on, and 0 means off.

Byte 3:

$\begin{array}{ c c c c c c c c c c c c c c c c c c c$									
Description:									
This function sets the consist element's direction and function F0 to F4 states.									
Response:									
None.									
Notes:									
None.									
$\mathrm{OPC}_{\operatorname{-}}\mathrm{GPOFF}$									
Operation: Global power off request.									
Group: 2-Byte Message									
$\underline{\text{Direction:}} \rightarrow \text{Command Station}$									
Encoding:									
Byte 0:									
1 0 0 0 1 0 0x82 Opcode.									
Byte 1:									
0 1 1 1 1 0 1 0x7D Checksum.									
Description:									
This command turns off the track power.									
Response:									
None.									
Notes:									
None.									

OPC_GPON

Operation: Global power on request.

Group: 2-Byte Message

Direction: \rightarrow Command Station

Encoding:

Byte 0:

		1	0	0	0	0	0	1	1	0x83	Opcode
--	--	---	---	---	---	---	---	---	---	------	--------

Byte 1:

0	1	1	1	1	1	0	0	0x7C	Checksur

Description:

This command sends a global power on request.

Response:

The command station sends an OPC_RQ_SL_DATA message for slot 0x7F. It also sends a sequence of OPC_SW_REQ messages with the following values of SW1 and SW2:

SW1SW20x780x270x790x270x7A0x270x7B0x270x78ox070x790x070x7A0x07

OATH OAT

 $0x7B \quad 0x07$

Notes:

None.

OPC_IDLE

Operation: Force idle state and broadcast emergency stop.

Group: 2-Byte Message

 $\underline{\text{Direction:}} \rightarrow \text{Command Station}$ Encoding: Byte 0: 1 0 0 0 0 0 0x85Opcode. 1 1 Byte 1: 0 1 1 1 1 0 1 0 0x7AChecksum. Description: This command forces Loconet into the idle state and broadcasts an emergency stop. None Notes: None.

OPC_IMM_PACKET

Operation: Send n-byte packet immediate.

Variable-Byte Message Group:

Direction: \rightarrow command station

E

Encoding:		
Byte 0:		
1 1 1 0 1 1 0 1	0xED	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x0B	Message length (11 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	0x7F	Source id in the range 0x00 to 0x7F.

Byte 3:

0 d6 d5 d4 d3 d2 d1 d0 <REPS>Number of immediate bytes and repeat count.

- d6 N2. Number of immediate bytes.
- d5 N1. Number of immediate bytes.
- d4 No. Number of immediate bytes.
- d3 A4. Reserved. Set to 0.
- d2 R2. Repeat count.
- d1 R1. Repeat count.
- d0 R0. Repeat count.

Byte 4:

0	$0 \boxed{1 d4 d3 d2 d1 d0}$	<dhii></dhii>	High bits of IM1 to IM5.
d4	IM5.7. High bit.		
d3	IM4.7. High bit.		
d2	IM3.7. High bit.		
d1	IM2.7. High bit.		
d0	IM1.7. High bit.		
D 4 F			

Byte 5:

$\begin{bmatrix} 0 & d6 & d5 & d4 & d3 & d2 & d1 & d0 \end{bmatrix}$ < IM1> Data item	1 low 7 bit	S.
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Byte 6:

	0	d6	d5	d4	d3	d2	d1	d0	<IM $2>$	Data item 2 low 7 bits.
--	---	----	----	----	----	----	----	----	----------	-------------------------

Byte 7:

$d6 \mid d5 \mid d4 \mid d3 \mid d2 \mid d1 \mid d0$ <im3> Data item 3 low 7 b</im3>	0) d6	d5	d4	d3	d2	d1	d0	<im3></im3>	Data item 3 low 7 b
--------------------------------------------------------------------------------------	---	------	----	----	----	----	----	----	-------------	---------------------

Byte 8:

0	d6	d5	d4	d3	d2	d1	d0	<im4></im4>	Data item 4 low 7 bits.
---	----	----	----	----	----	----	----	-------------	-------------------------

Byte 9:

0	d6	d5	d4	d3	d2	d1	d0	<IM5 $>$	Data item 5 low 7 bits
U	uo	լա	u4	լաթ	uz	uı	uu	<11V10>	Data Item 5 low 7 bits

Byte 10:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum.
---	---	---	---	---	---	---	---	----------	-----------

Description:

Send n-byte packet immediate.

Response:

OPC_LONG_ACK.

Notes:

None.

OPC_INPUT_REP

Operation: General sensor input report.

Group: 4-Byte Message

 $\underline{\text{Direction:}} \ \ \underline{\text{General sensor}} \to$

Encoding:

Byte 0:

1	0	1	1	0	0	1	0	0xB2	Opcode.

Byte 1:

0	d6 d5	d4	d3	d2	d1	d0	<in1></in1>	Sensor address A7 to A
---	---------	----	----	----	----	----	-------------	------------------------

- d6 A7.
- d5 A6.
- d4 A5.
- d3 A4.
- d2 A3.
- d1 A2.
- d0 A1.

Byte 2:

0	1	d5	d4	d3	d2	d1	d0	<in2></in2>	Switch	${\rm address}$	A11	to	A8	and	sensor	input
	•								state.							

- d5 A0
- d4 Input state. 1 means sensor input >= 6V, and 0 means sensor input = 0V.
- d3 A11.
- d2 A10.
- d1 A9.
- d0 A8.

Byte 3:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum.
Descr	iptic	n:							

Response:

None.

 $\underline{\text{Notes:}}$

None.

OPC_LINK_SLOTS

General sensor report.

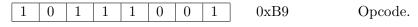
Operation: Link slots.

Group: 4-Byte Message

Direction: \rightarrow Command Station

Encoding:

Byte 0:



Byte 1:



Byte 2:

0 n n n n n	n n	$\langle SL2 \rangle$ Slot	number in the range $0x00$ to $0x7F$.
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Byte 3:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum.
---	---	---	---	---	---	---	---	----------	-----------

Description:

This function links slot SL1 to slot SL2. The command station sets SL_CONUP/DN flags appropriately. Invalid link will return a fail acknowledgement.

Response:

OPC_SL_RD_DATA or OPC_LONG_ACK.

Notes:

None.

OPC_LOCO_ADR

Operation: Request a slot number for a locomotive.

Group: 4-Byte Message

<u>Direction</u>: \rightarrow Command Station

Encoding:

Byte 0:

	1	0	1	1	1	1	1	1	0xBF	Opcode.
E	Byte	1:								
	0	n	n	n	n	n	n	n	<adr2></adr2>	High address.
E	Byte	2:								
	0	n	n	n	n	n	n	n	<adr></adr>	Low address.
Ε	$_{ m Syte}$	3:								
	0	n	n	n	n	n	n	n	<chk></chk>	Checksum.

Description:

This message requests the slot number for the selected locomotive address. If the locomotive is found in the slot table then the command station returns an OPC_SL_RD_DATA message with the slot information. If it is not found then the command station will put the locomotive into a free slot and then return an OPC_SL_RD_DATA message with the slot information. If there are no free slots then the command station returns an OPC_LONG_ACK error code.

Note that regular short 7 bit NMRA addresses are denoted by $\langle ADR2 \rangle = 0$. The Analog, zero stretched, locomotive is selected when both $\langle ADR2 \rangle = 0$ and $\langle ADR \rangle = 0$. $\langle ADR \rangle$ is always a 7 bit value. If $\langle ADR2 \rangle$ is non-zero then the master will generate NMRA type 14 bit or long address packets using all 14 bits from $\langle ADR2 \rangle$ and $\langle ADR \rangle$ with $\langle ADR2 \rangle$ being the most significant address bits. Note that a DT200 Master does not process 14 bit

address requests and will consider the <ADR2> to be zero. You can check the <TRK> return bits to see if the master is a DT200.

Response:

OPC_SL_RD_DATA if success, otherwise OPC_LONG_ACK.

Notes:

The Loconet 1.1 specification specifies that $\langle ADR2 \rangle$ value is 0x00.

OPC_LOCO_DIRF

Operation: Set locomotive direction and function F0 to F4 states.

Group: 4-Byte Message

 $\underline{\text{Direction:}} \rightarrow \text{Command Station}$

Encoding:

Byte 0:

1	0	1	0	0	0	0	1	0xA1	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	n	n	n	n	n	n	n	<SLOT $#>$	Slot number in the range 0x00 to 0x7F.
---	---	---	---	---	---	---	---	------------	----------------------------------------

Byte 2:

0	d6	d5	d4	d3	d2	d1	d0	<dirf></dirf>	Locomotive's direction and state of functions
									F0 to F4.

- d6 Reserved. Set to 0.
- d5 Locomotive direction. 1 means forward, 0 means backwards.
- d4 F0 state. 1 means on, and 0 means off.
- d3 F4 state. 1 means on, and 0 means off.
- d2 F3 state. 1 means on, and 0 means off.
- d1 F2 state. 1 means on, and 0 means off.
- d0 F1 state. 1 means on, and 0 means off.

Byte 3:

0	n	n	n	n	n	n	n	<chk></chk>	Checksum.
---	---	---	---	---	---	---	---	-------------	-----------

Description:
This function sets the locomotive's direction and function F0 to F4 states.
Response:
None.
Notes:
None.
OPC_LOCO_RESET
Operation: Loco reset button has been pressed on the command station.

Encoding:

Byte 0:

Group:

	1	0	0	0	1	0	1	0	0x8A	Opcode.
--	---	---	---	---	---	---	---	---	------	---------

Byte 1:

0 1 1 1 1 0 1 0 1 0 1 0 0	Checksum.
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Description:

The Loco reset button has been pressed.

2-Byte Message

 $\underline{\text{Direction:}} \ \ \text{Command Station} \to$

Response:

None, this is a response.

Notes:

None.

OPC_LOCO_SND

Operation: Set locomotive sound functions.

Group: 4-Byte Message

 $\underline{\text{Direction:}} \rightarrow \text{Command Station}$

Encoding:

Byte 0:

		1	0	1	0	0	0	1	0	0xA2	Opcode
--	--	---	---	---	---	---	---	---	---	------	--------

Byte 1:

0	n	n	n	n	n	n	n	<SLOT $#>$	Slot number in the range $0x00$ to $0x7F$.
---	---	---	---	---	---	---	---	------------	---------------------------------------------

Byte 2:

0	d6	d5	d4	d3	d2	d1	d0	$\langle SND \rangle$	Locomotive's function F5 to F8 states.

- d6 Reserved. Set to 0.
- d5 Reserved. Set to 0.
- d4 Reserved. Set to 0.
- d3 Reserved. Set to 0.
- d3 Sound 4 / F8.
- d2 Sound 3 / F7.
- d1 Sound 2 / F6.
- d0 Sound 1 / F5.

Byte 3:

0	n n n	n n n	<chk> Checks</chk>
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Description:

This function sets the locomotive's function F5 to F8 states.

Response:

None.

Notes:

None.

OPC_LOCO_SPD

Operation: Set locomotive speed.

4-Byte Message Group: Direction: \rightarrow Command Station Encoding: Byte 0: 1 0 1 0 0 0 0 0 0xA0Opcode. Byte 1: 0 <SLOT#>Slot number in the range 0x00 to 0x7F. n \mathbf{n} n n \mathbf{n} \mathbf{n} n Byte 2: 0 <SPD> Locomotive speed in the range 0x00 to 0x7F. \mathbf{n} \mathbf{n} n \mathbf{n} n \mathbf{n} n 0x00 means inertial stop and 0x01 means emergency stop. Other values mean increasing speed. Byte 3: 0 <CHK> Checksum. n n n n n n n Description: This function sets the locomotive's speed. Response: None. Notes: None.

OPC_LONG_ACK

Operation: Long acknowledge.

Group: 4-Byte Message

Direction: \rightarrow Command Station

Encoding:

Byte 0:

	1	0	1	1	0	1	0	0	0xB4	Opcode.
Ι	Byte	1:								
	0	n	n	n	n	n	n	n	<lopc></lopc>	Opcode that this message is a response to with the most significant bit set to 0.
Ι	Byte	2:								
	0	n	n	n	n	n	n	n	<ack1></ack1>	Response code.
Ι	Byte	3:								
	0	n	n	n	n	n	n	n	<chk></chk>	Checksum.

Description:

This message provides a response code from a command.

Response:

None, it is the response.

Notes:

Responding Opcode	\leq LOPC $>$	\leq ACK1 $>$	Meaning
OPC_SW_ACK	0x3D	0x00	$\overline{\text{DCS}100}$ FIFO is full, command rejected.
OPC_SW_ACK	0x3D	0x7F	DCS100 command accepted.
OPC_MOVE_SLOTS	0x3A	0x00	Illegal move.
OPC_LINK_SLOTS	0x39	0x00	Invalid link, link failed.
OPC_SW_REQ	0x30	0x00	Command failed.
OPC_LOCO_ADR	0x3F	0x00	No free slot, command failed.
OPC_IMM_PACKET	0x7D	0x7F	Command OK, if not limited master.
OPC_IMM_PACKET	0x7E	<lim address $>$	Command OK, if limited master.
OPC_IMM_PACKET	0x7D	0x00	Internal buffer busy or full.

$\mathbf{OPC_MOVE_SLOTS}$

Operation: Move slot.

4-Byte Message Group:

 $\underline{\text{Direction:}} \ \to \text{Switch}$

Encoding:

Byte 0:

$egin{array}{ c c c c c c c c c c c c c c c c c c c$	0xBA	Opcode.
Byte 1:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<src></src>	Source slot number in the range $0x00$ to $0x77$.
Byte 2:		
	<dest></dest>	Destination slot number in the range $0x00$ to $0x77$.
Byte 3:		
	<chk></chk>	Checksum.

Description:

Move slots.

$\underline{\operatorname{SRC}}$	$\overline{\mathrm{DEST}}$	$\underline{\operatorname{Action}}$
0x00	Don't Care	Dispatch get. Return slot read of dispatch
		slot.
SRC	SRC	Null move. SRC is set to in use.
SRC	0x00	Dispatch put. Mark slot as dispatch.
SRC	DEST	Move slot data from SRC to DEST if not in
		use. Clear SRC.

Response:

OPC_SL_RD_DATA or OPC_LONG_ACK.

Notes:

None.

OPC_PEER_XFER

Operation: Move 8 bytes peer to peer.

Group: Variable-Byte Message

 $\underline{\text{Direction:}} \ \ \text{device} \to \text{device}$

Encoding:

Byte 0:

$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	0xE5	Opcode.
Byte 1:		
$oxed{0\ \ 0\ \ 0\ \ 1\ \ 0\ \ 0\ \ 0\ \ 0}$	0x10	Message length (16 bytes).
Byte 2:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<src></src>	Source id in the range 0x00 to 0x7F.
Byte 3:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<DSTL $>$	Destination id low in the range $0x00$ to $0x7F$.
Byte 4:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<DSTH $>$	Destination id high in the range $0x00$ to $0x7F$.
Byte 5:		
0 d6 d5 d4 d3 d2 d1 d0	<pxct1></pxct1>	Address type code and high bits of D1 to D4.
d6 XC2. Address type code. d5 XC1. Address type code. d4 XC0. Address type code. d3 D4.7. High bit d2 D3.7. High bit d1 D2.7. High bit d0 D1.7. High bit		
	Meaning	11
	7 bit peer to peer	addresses.
	reserved. reserved.	
	reserved.	
Byte 6:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d1></d1>	Data item 1. Low 7 bits.
Byte 7:		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	<d2></d2>	Data item 2. Low 7 bits.

Byte 8:

0 n	n n	n n n	<d3></d3>	Data item 3. Low 7 bits.
Byte 9:				
0 n	n n	n n n	<d4></d4>	Data item 4. Low 7 bits.
Byte 10:				
0 n	n n	n n n	<pxct2></pxct2>	Data type code and high bits for D5 to D8.
d6	XC5. Data	type code.		
d5	XC4. Data	type code.		
d4	XC3. Data	type code.		
d3	D8.7. High	bit		
	D7.7. High			
d1	D6.7. High	bit		
d0	D5.7. High	bit		
$\underline{\text{XC5}}$	$\underline{\text{XC4}}$	$\underline{\text{XC3}}$	$\underline{\text{Meaning}}$	
0	0	0	ANSI text string.	
0	0	1	reserved.	
0	1	0	reserved.	
0	1	1	reserved.	
1	0	0	reserved.	
1	0	1	reserved.	
1	1	0	reserved.	
1	1	1	reserved.	
Byte 11:				
0 n	n n	n n n n	<d5></d5>	Data item 5. Low 7 bits.
Byte 12:				
0 n	n n	n n n	<d6></d6>	Data item 6. Low 7 bits.
Byte 13:				
0 n	n n	n n n	<d7></d7>	Data item 7. Low 7 bits.
Byte 14:				
0 n	n n	n n n	<d8></d8>	Data item 8. Low 7 bits.
Byte 15:				
0 n	n n	n n n	CHK> Ch	necksum.
Descripti	ion:			

This command sends the data from one device to another peer to peer.

$\underline{\operatorname{SRC}}$	$\overline{\mathrm{DSTL}}$	$\overline{\mathrm{DSTH}}$	Comments
0x00			Source is command station.
Don't Care	0x00	0x00	Broadcast Message.
0x70 to $0x7E$			Reserved.
0x7F	0x00	0x00	Broadcast throttle message transfer.
0x7F	ID1	ID2	Throttle message transfer. ID1 and
			ID2 encode ID.

Response:

None

Notes:

None.

$OPC_RQ_SL_DATA$

Operation: Request slot data or status block.

Group: 4-Byte Message

 $\underline{\text{Direction:}} \rightarrow \text{Switch}$

Encoding:

Byte 0:

1	0	1	1	1	0	1	1	0xBB	Opcode
---	---	---	---	---	---	---	---	------	--------

Byte 1:

(0	n	n	n	n	n	n	n	<SLOT $#>$	Slot number in the range 0x00 to 0x7F. 0 re-
										turns the command station status block

Byte 2:

0	0	0	0	0	0	0	0	0x00	Reserved.

Byte 3:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum
---	---	---	---	---	---	---	---	----------	----------

Description:

Request slot data or status block.

Response:

OPC_SL_RD_DATA.

Notes:

None.

$\mathbf{OPC_SL_RD_DATA}$

Operation: Returns slot data.

Group: Variable-Byte Message

 $\underline{\text{Direction:}} \quad \text{Command Station} \rightarrow$

Encoding:

Byte 0:

	1		1	1	0	0	1	1	1	0xE7	Opcode.
--	---	--	---	---	---	---	---	---	---	------	---------

Byte 1:

0	1 1	1 1	1	1	1	0	0x0E	Message length	(14 bytes).
---	-----	-----	---	---	---	---	------	----------------	-------------

Byte 2:

0	n	n	n	n	n	n	n	<slot#></slot#>	Slot number in the range 0x00 to 0x7F. Slot
									0x00 is a special slot, and slots in the range
									0x70 to 0x7F are reserved to Digitrax.

Byte 3:

d7	d6	d5	d4	d3	d2	d1	d0	<stat1></stat1>	Slot status 1.			
	a'	7	46									
	<u>u</u>	<u> </u>	<u>d6</u>									
	0		0]	$\operatorname{Free},$	no e	\cos	ist linking.				
$0 \qquad 1$					Consist sub-member.							
	1		0	(Cons	ist t	op-n	nember.				
	1		1	(Cons	ist 1	Aid-	Consist member.				

Note: d7 is set to 0 in the message by the command station and so may not correctly reflect the actual setting in the slot table.

	<u>uo</u>	<u>u 1</u>	
	0	0	Free slot, no valid data. Not refreshed.
	0	1	Common. Locomotive address in this slot. Refreshed.
	1	0	Idle. Locomotive address in this slot. Not refreshed.
	1	1	In Use. Locomotive address in this slot. Refreshed.
		<u>d3</u>	
		0	No slot consist linked into this slot.
		1	Slot consist linked into this slot.
$\underline{d2}$	<u>d1</u>	<u>d0</u>	
0	0	0	28 step decoder. 3-byte packet regular mode
0	0	1	28 step decoder. Generate trinary packets for this mobile ad-
			dress
0	1	0	14 step decoder.
0	1	1	128 step decoder.
1	0	0	28 step decoder. Allow advanced consisting
1	0	1	reserved
1	1	0	reserved
1	1	1	128 step decoder. Allow advanced consisting
Byte 4	:		

d5

d4

0	$\mid \mathbf{n} \mid$	n	n	n	n	n	n	<ADR $>$	Low address.
---	------------------------	---	---	---	---	---	---	----------	--------------

Byte 5:

0	n	n	n	n	n	n	n	$\langle SPD \rangle$	Speed in the range $0x00$ to $0x7F$. $0x00$ means
							•		inertial stop and $0x01$ means emergency stop.
									Other values mean increasing speed.

Byte 6:

	0	d6	d5	d4	d3	d2	d1	d0	<dirf></dirf>	Locomotive direction and state of functions
_										F0 to F4.

- Reserved. Set to 0. d6
- Locomotive direction. 1 means forward, 0 means backwards. d5
- d4F0 state. 1 means on, and 0 means off.
- d3F4 state. 1 means on, and 0 means off.
- d2F3 state. 1 means on, and 0 means off.
- d1F2 state. 1 means on, and 0 means off.
- d0F1 state. 1 means on, and 0 means off.

Byte 7:

Byte 12:

0	d6 d5 d4 d3 d2 d1 d0	<trk></trk>	Global system track status.
d6	Reserved. Set to 0.		
d5	Reserved. Set to 0.		
d4	Reserved. Set to 0.		
d3	1 means the programming tra	ck is busy.	
d2	1 means this master impleme		rsion 1.1
d1	capability, 0 means the master 0 means the track is paused, 1		morganay
uı	stop.	broadcast an er	nergency
d0	1 means the DCC packets are	on in the maste	er, global
	power up.		
Byte	8:		
0	d6 d5 d4 d3 d2 d1 d0	<ss2></ss2>	Slot status 2.
d6	Reserved. Set to 0.		
d5	Reserved. Set to 0.		
d4	Reserved. Set to 0.		
d3	1 means expansion in $ID1/2$,	0 means encode	ed alias.
d2	1 means expansion ID1/2 is n		
d1	Reserved. Set to 0.	Q	
d0	1 means this slot has suppress	sed advanced co	onsist.
Byte	9:		
0		<adr2></adr2>	High address.
Byte	10:		
0	d6 d5 d4 d3 d2 d1 d0	$\langle SND \rangle$	Slot sound / function mode II packets.
d6	Reserved. Set to 0.		
d5	Reserved. Set to 0.		
d4	Reserved. Set to 0.		
d3	Sound 4 / F8.		
d2	Sound 3 / F7.		
d1	Sound 2 / F6.		
d0	Sound 1 / F5.		
Byte	11:		
0	n n n n n n	<id1></id1>	7-bit ls ID code written by throttle when $STAT2.4 = 1$.

Byte 13:

Description:

This message is sent by the command station in response to a slot data request.

Response:

None.

Notes:

None.

$\mathbf{OPC_SLOT_STAT1}$

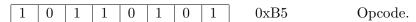
Operation: Set slot status 1.

Group: 4-Byte Message

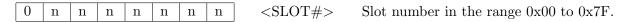
 $\underline{\text{Direction:}} \rightarrow \text{Command Station}$

Encoding:

Byte 0:



Byte 1:



Byte 2:



Byte 3:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum.
---	---	---	---	---	---	---	---	----------	-----------

Description:

This function sets the slot's status 1 values.
Response:
None.
Notes:
None.

$\mathbf{OPC_SW_ACK}$

Operation: Request switch command with acknowledge.

Group: 4-Byte Message

 $\underline{\text{Direction:}} \ \to \text{Turnout controller}$

Encoding:

Byte 0:

1	0	1	1	1	1	0	1	0xBD	Opcode.
---	---	---	---	---	---	---	---	------	---------

Byte 1:

0	d6	d5	d4	d3	d2	d1	d0	<sw1></sw1>	Switch address A6 to A0.
---	----	----	----	----	----	----	----	-------------	--------------------------

- d6 A6.
- d5 A5.
- d4 A4.
- d3 A3.
- d2 A2.
- d1 A1.
- d0 A0.

Byte 2:

0	d6	d5	d4	d3	d2	d1	d0	$\langle SW2 \rangle$	Switch address A10 to A7 and switch control
									hits

- d6 Reserved. Set to 0.
- d5 Direction. 1 means closed/green, and 0 means thrown/red.
- d4 Output. 1 means on, and 0 means off.
- d3 A10.
- d2 A9.
- d1 A8.
- d0 A7.

Byte 3:

0	n	n	n	n	n	n	n	<CHK $>$	Checksum.
---	---	---	---	---	---	---	---	----------	-----------

Description:

Command a turnout controller to a specified state and send acknowledge.

Response:

OPC_LONG_ACK.

Notes:

None.

OPC_SW_REP

Operation: Turnout sensor report.

Group: 4-Byte Message

Direction: Turnout sensor \rightarrow

Encoding:

Byte 0:

1 0 1 1 0 0 0 1 0xB1	1
--------------------------------------	---

Byte 1:

0	d6	d5	d4	d3	d2	d1	d0	<sn1></sn1>	Sensor address.
---	----	----	----	----	----	----	----	-------------	-----------------

SN2.	d6 = 1
d6	A7.
1 -	1.0

- d5A6.
- d4A5.
- d3 A4.
- d2 A3. d1 A2.
- d0 A1.

Byte 2:

0 d6 d5 d4 d3 d2 $d1 \mid d0 \mid$

<SN2>

Sensor address and sensor state.

$\underline{SN2.d6 = 1}$

- Report type. 1 means the report is an input report, and 0 means the report is an output report.
- d5A0.
- d4 Input sensor state, 1 means sensor >= 6V, 0 means sensor = 0V.
- d3 A11.
- d2 A10.
- d1 A9.
- d0 A8.

SN2.d6 = 0

SN2.d6 = 0d6 A6. d5

d4 A4.

d3 A3.

d2 A2.

d1 A1.

A0.

d0

A5.

- d6 Report type. 1 means the report is an input report, and 0 means the report is an output report.
- d5 0 means closed output line is off, 1 means the closed output line is on.
- d4 0 means thrown output line is off, 1 means the thrown output line is on.
- d3 A10.
- d2 A9.
- d1 A8.
- d0 A7.

Byte 3:

0 \mathbf{n} n n \mathbf{n} \mathbf{n} \mathbf{n} n <CHK>

Checksum.

Description:

Turnout sensor report.

Response:

None.

Notes:

None.

OPC_SW_REQ

Operation: Request switch command.

Group: 4-Byte Message

 $\underline{\text{Direction:}} \ \to \text{Turnout controller}$

Encoding:

Byte 0:

		1	0	1	1	0	0	0	0	0xB0	Opcode
--	--	---	---	---	---	---	---	---	---	------	--------

Byte 1:

0	d6 d5	d4	d3	d2	d1	d0	<sw1></sw1>	Switch address A6 to

- d6 A6.
- d5 A5.
- d4 A4.
- d3 A3.
- d2 A2.
- d1 A1.
- d0 A0.

Byte 2:

0	d6	d5	d4	d3	d2	d1	d0	$\langle SW2 \rangle$	Switch address A10 to A7 and switch control
									bits.

- d6 Reserved. Set to 0.
- d5 Direction. 1 means closed/green, and 0 means thrown/red.
- d4 Output. 1 means on, and 0 means off.
- d3 A10.
- d2 A9.
- d1 A8.
- d0 A7.

Byte 3:

0	n	n	n	n	n	n	n	<chk></chk>	Checksum.

Description:

Command a turnout controller to a specified state.

Response:

OPC_LONG_ACK if command failed, otherwise no response.

Notes:

None.

OPC_SW_STATE

Operation: Request state of switch.

Group: 4-Byte Message

 $\underline{\text{Direction:}} \ \to \text{Switch}$

Encoding:

Byte 0:

1	0	1	1	1	1	0	0	0xBC	Opcode
		l .				l .			1

Byte 1:

0	n	n	n	n	n	n	n	<sw1></sw1>	Switch address A6 to A0.
---	---	---	---	---	---	---	---	-------------	--------------------------

Byte 2:



- d6 Reserved. Set to 0.
- d5 Direction. 1 means closed/green, and 0 means thrown/red.
- d4 Output. 1 means on, and 0 means off.
- d3 A10.
- d2 A9.
- d1 A8.
- d0 A7.

Byte 3:

0	n	n	n	n	n	n	n	<chk></chk>	Checksum.
---	---	---	---	---	---	---	---	-------------	-----------

Description:

Request state of switch.

Res	onse:

OPC_LONG_ACK.

Notes:

This needs to be tested to see what the real purpose is.

$\mathbf{OPC_UNLINK_SLOTS}$

Operation: Unlink slots.

Group: Variable-Byte Message

 $\underline{\text{Direction:}} \rightarrow \text{Command Station}$

Encoding:

Byte 0:

		1	0	1	1	1	0	0	0	0xB8	Opcode
--	--	---	---	---	---	---	---	---	---	------	--------

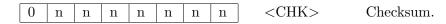
Byte 1:

0	n	n	n	n	n	n	n	<sl1></sl1>	Slot number in the range $0x00$ to $0x7F$.
---	---	---	---	---	---	---	---	-------------	---------------------------------------------

Byte 2:

0	n	n	n	n	n	n	n	<sl2></sl2>	Slot number in the range $0x00$ to $0x7F$.
---	---	---	---	---	---	---	---	-------------	---------------------------------------------

Byte 3:



Description:

This command unlinks slot SL1 from slot SL2.

Response:

Returns OPC_SL_RD_DATA or OPC_LONG_ACK.

Notes:

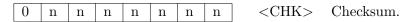
None.

$OPC_WR_SL_DATA$

Operation: Write slot data. Group: Variable-Byte Message Direction: \rightarrow Command Station Encoding: Byte 0: Opcode. 1 1 0 1 0xEF| 1 Byte 1: 0 0x0EMessage length (14 bytes). 1 1 1 1 1 1 0 Byte 2: 0 \mathbf{n} \mathbf{n} \mathbf{n} n n \mathbf{n} \mathbf{n} <SLOT#>Slot number in the range 0x00 to 0x7F. Slot 0x00 is a special slot, and slots in the range 0x70 to 0x7F are reserved to Digitrax.

Bytes 3 to 12 encode the same as bytes 3 to 12 of OPC_SL_RD_DATA.

Byte 13:



Description:

This command sends the slot data to the command station.

Response:

Returns OPC_LONG_ACK.

Notes:

None.