RISC-V®

Security HC

2024-02-06

Agenda and notes

- Disclosures, opens, and updates
- Discussion continuation on Memory safety, tagging, pallet memory, Unified lightweight memory tagging etc…
  - Continuation of the discussion started in the RTI sig. Andy + Ravi Emphasised the need to clearly state the gap, and use-cases that a solution is targeting. A TG isnt fomed to ratify a pre-existing specification, but to address a need.
  - Ravi presented the pertinant questions for the charter to address. Some has been discussed in a gap analysis document already.
  - Critical points for the TG: - use cases that the solution must cover – including production code and debug modes, managed runtimes etc; performance/cost constraints for each targeted case; interaction with existing extensions.
  - Input from SW and tools required

- Updates from Trusted Computing SIG
- Any further comments on Status Roadmap And Priority for 2024?
  - https://docs.google.com/presentation/d/1qHe4eMk0KMVgJFF5JbwLtrWF3_eXmENc7wW9rnm7ATQ/edit?usp=drive_link