

## Address Decoder

### Signals implemented in GAL based on original "piggyback" cart signals

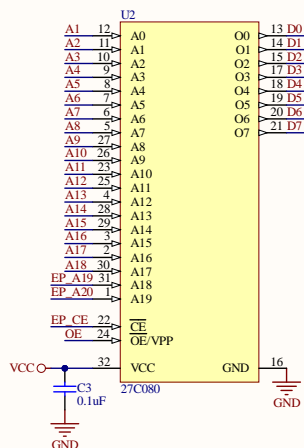
```

1 when /AS=0 and address between either 0x800000 and 0x9DFFFF (A23..A19=100 and A20..A17=1111) or 0xF00000 and 0xF1FFFF (A23..A17=1111000), else 1
0 when /AS=0 and address between 0xF00000 and 0xF1FFFF (A23..A17=1111000), else 100
1 when /AS=0 and address between 0xF00000 and 0xF1FFFF (A23..A17=1111000), else A20      (remaps 0xF00000-0xF1FFFF to 0x980000-0x99FFFF)
0 when /AS=0 and address between 0x9E0000 and 0x9FFFFFF (A23..A17=1001111), else 1
0 when reading (R/W=1), else 1
0 when writing (/AS=0 and R/W=0), else 1

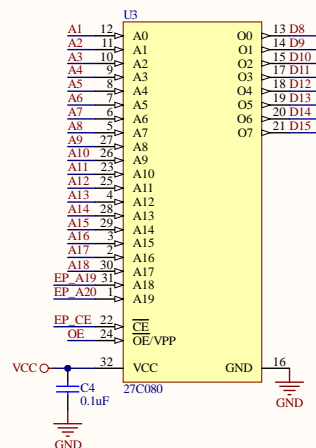
```

Unused outputs unconnected

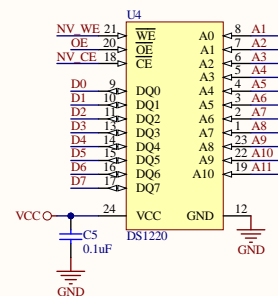
Cut GND trace to use as extra input



## EPRoMs



## NVRAM



Title <b>Arcadia Cart</b>			
Size B	Number		Revision
Date:	12/14/2021	Sheet of	
File:	C:\Users\...\Arcadia_Cart.SchDoc	Drawn By:	