

libIS and SENSEI in transit architecture

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IXPUG In Situ Hackathon

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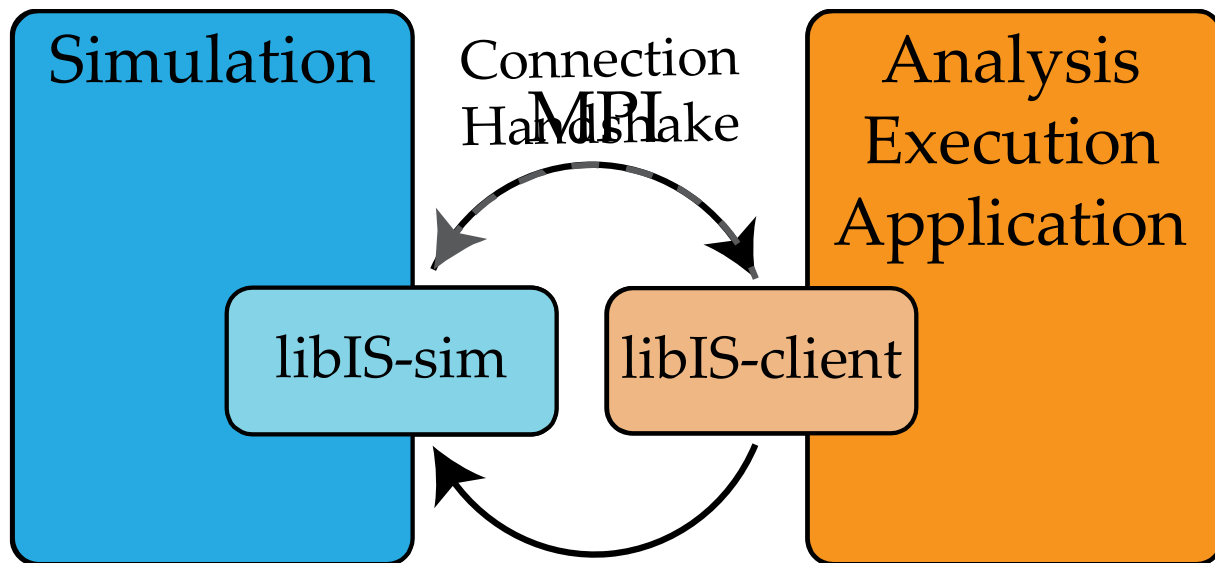
May 2019

Slides courtesy: Will Usher, SCI Institute, Univ. of Utah and Intel Corp.
Burlen Loring, Berkeley Lab and the SENSEI team

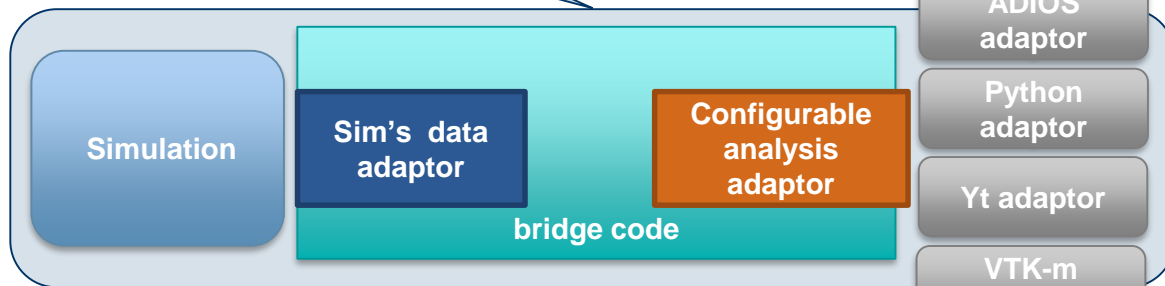
The libIS library

Simulation acts as a data server providing timesteps to clients

Clients can connect/disconnect for on-demand execution



Simulation and analysis
run in the same process



XML selects *one* of
these at *runtime*

Catalyst
adaptor

Lisbim
adaptor

ADIOS
adaptor

Python
adaptor

Yt adaptor

VTK-m
adaptor

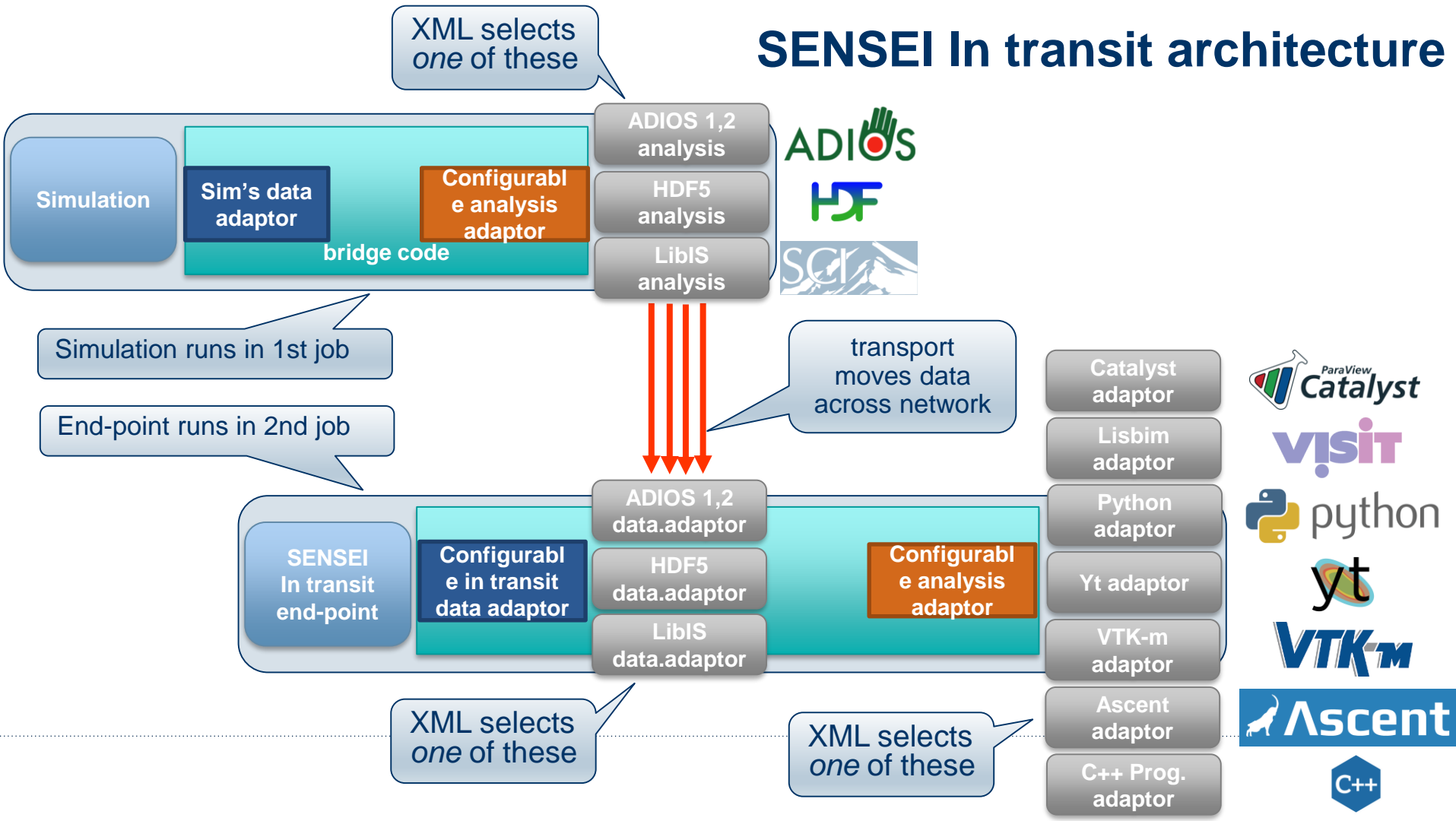
Ascent
adaptor

C++ Prog.
adaptor



SENSEI In situ architecture

SENSEI In transit architecture



Progress

- Based on ADIOS1 implementation by Burlen
- Added libS data adaptor, analysis adaptor and endpoint
- Commits in Kitware Gitlab repo
https://gitlab.kitware.com/sensei/sensei/merge_requests/154
- Still work in progress

To-Do

- Finalize implementation
 - Regression tests
https://gitlab.kitware.com/sensei/sensei/tree/in_transit_data_adaptor/sensei/testing
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