

CS 271 Computer Architecture and Assembly Language
Self-Check for Lecture #3
Solutions are posted

1. How many bits in 35 MiB ? _____ bits

The remaining questions refer to the IA-32 architecture discussed in Lecture #3.

2. What is the width of the internal bus? _____

3. What is the size of the general-purpose registers? _____

4. Which of the following are valid 8-bit register references?

_____ AL
_____ DH
_____ SH
_____ EL

5. Why does protected mode prevent programs from changing the EIP register directly?