Architecture: DE10-Nano based MFM disk Reader/Emulator

USER interface + LEDs

RAM 1 GB

MICRO SD-card

Serial I/O Debug console

Internet

ARM Cortex-A9 CPU

Interface , according to ST506 specification

Command Sequencer + STEP control

Port#2_A

Port#2_B

2 Dual Ported RAM(DPR) 172032 and 43008 Byte

Port#1_A

Port#1_B

MFM Decoder + GAP Recorder

Data-Sequencer + Clock Manager + INDEX-Puls-generator

MFM ENCODER