Sheet: CONTROLLOGIC

File: MZ80A_RFS_CTRL.sch

Sheet: MEMORY

File: MZ80A_RFS_MEMORY.sch

A mechanism to allow the Sharp MZ-80A to utilize different ROM firmware versions (ie. 40/80 character monitors) and ROM based storage.

Sheet: /
File: MZ80-ROMPG.sch

Title: Sharp MZ-80A Rom Filing System

Size: A4	Date: 2020-03-31	**Rev: 2.0**
KiCad E.D.A. kicad (5.1.2-1)-1		Id: 1/3

Title: Sharp MZ80A Rom Filing System

Revised memory of the RFS, now 2 additional devices have been added which are optional, namely an additional 4Mb Flash RAM and a selectable 4Mb static RAM or 4Mb Flash RAM. It is now possible to write into the Flash RAM (except the Monitor ROM Flash RAM which has no write signal from the motherboard).

Sheet: /MEMORY/
File: MZ80A_RFS_MEMORY.sch

Size: A4   Date: 2020-03-29
KiCad E.D.A. kicad (5.1.2-1)-1

Rev: 2.0
Id: 2/3

Title: Sharp MZ80A Rom Filing System

Revised logic of the RFS; addition of a circuit to enable/disable memory/control signals to ensure writes to Flash RAM or static RAM are not affected by writes to control logic; addition of 2 SPI circuits (only 1 needs to be populated), a bitbang SPI and hardware SPI which connects to an SD card.

Sheet: /CONTROLLOGIC/
File: MZ80A_RFS_CTRL.sch

Size: A4   Date: 2020-05-14   Rev: 2.0a
KiCad E.D.A. kicad (5.1.2-1)-1   Id: 3/3