
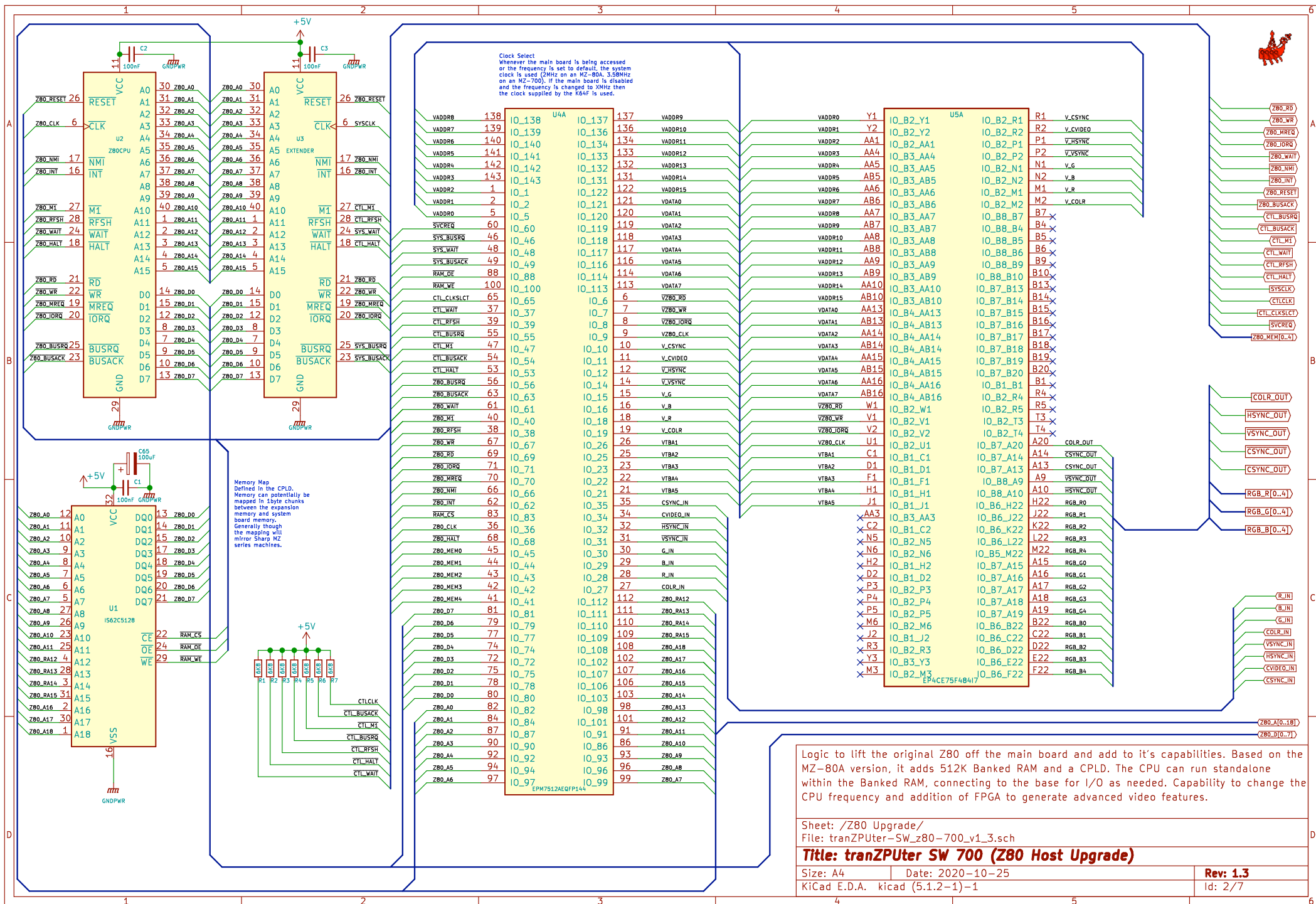
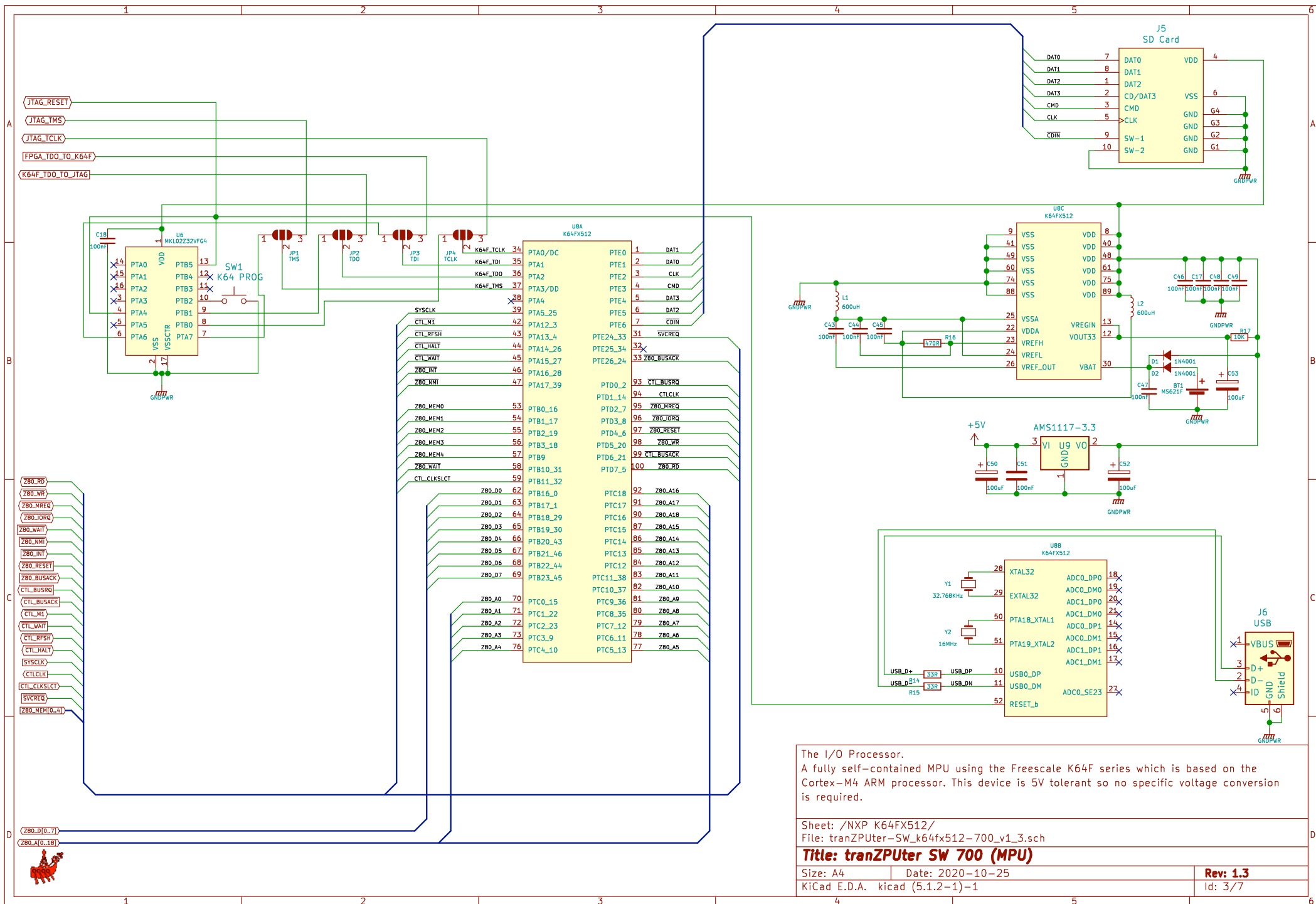
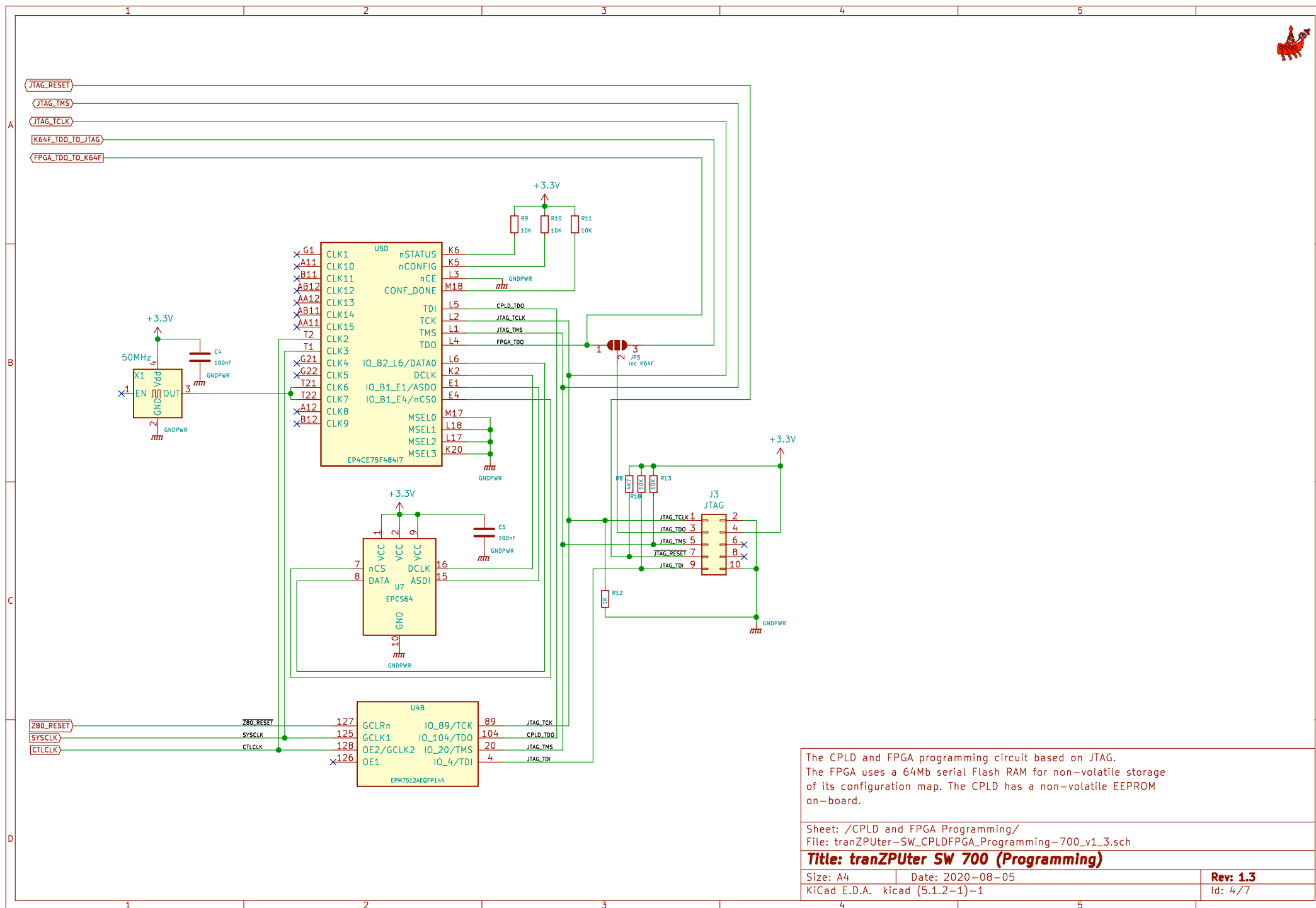


	1	2	3	4	5	6
A	<div>Sheet: Z80 Upgrade</div> <div>File: tranZPUter-SW_z80-700_v1_3.sch</div>	<div>Sheet: NXP K64FX512</div> <div>File: tranZPUter-SW_k64fx512-700_v1_3.sch</div>	<div>Sheet: Video Interface</div> <div>File: tranZPUter-SW_VideoInterface-700_v1_3.sch</div>			
B	<div>Sheet: CPLD and FPGA Programming</div> <div>File: tranZPUter-SW_CPLDFPGA_Programming-700_v1_3.sch</div>	<div>Sheet: Unused FPGA Blocks</div> <div>File: tranZPUter-SW_FPGA2-700_v1_3.sch</div>	<div>Sheet: Power Supply</div> <div>File: tranZPUter-SW_PowerSupply-700_v1_3.sch</div>			
C	<div> <div> <div> <div></div> <div>LG1</div> <div>Argo Logo</div> </div> </div> </div>					
D	<div>  </div>			<div> <div> <div>Capabilities upgrade for the Sharp MZ80A.</div> <div> Providing upgraded hardware and an optional MPU for provision of SD services to host, alternative soft processors, ZPUTA Menu System and additional resources to enhance the Sharp MZ80A. </div> </div> <div> <div>Sheet: /</div> <div>File: tranZPUter-SW-700_v1_3.sch</div> <div> <div>Title: tranZPUter SW 700</div> <div> <div>Size: A4</div> <div>Date: 2020-06-19</div> <div>Rev: 1.3</div> </div> <div> <div>KiCad E.D.A. kicad (5.1.2-1)-1</div> <div>Id: 1/7</div> </div> </div> </div> </div>		
	1	2	3	4	5	6





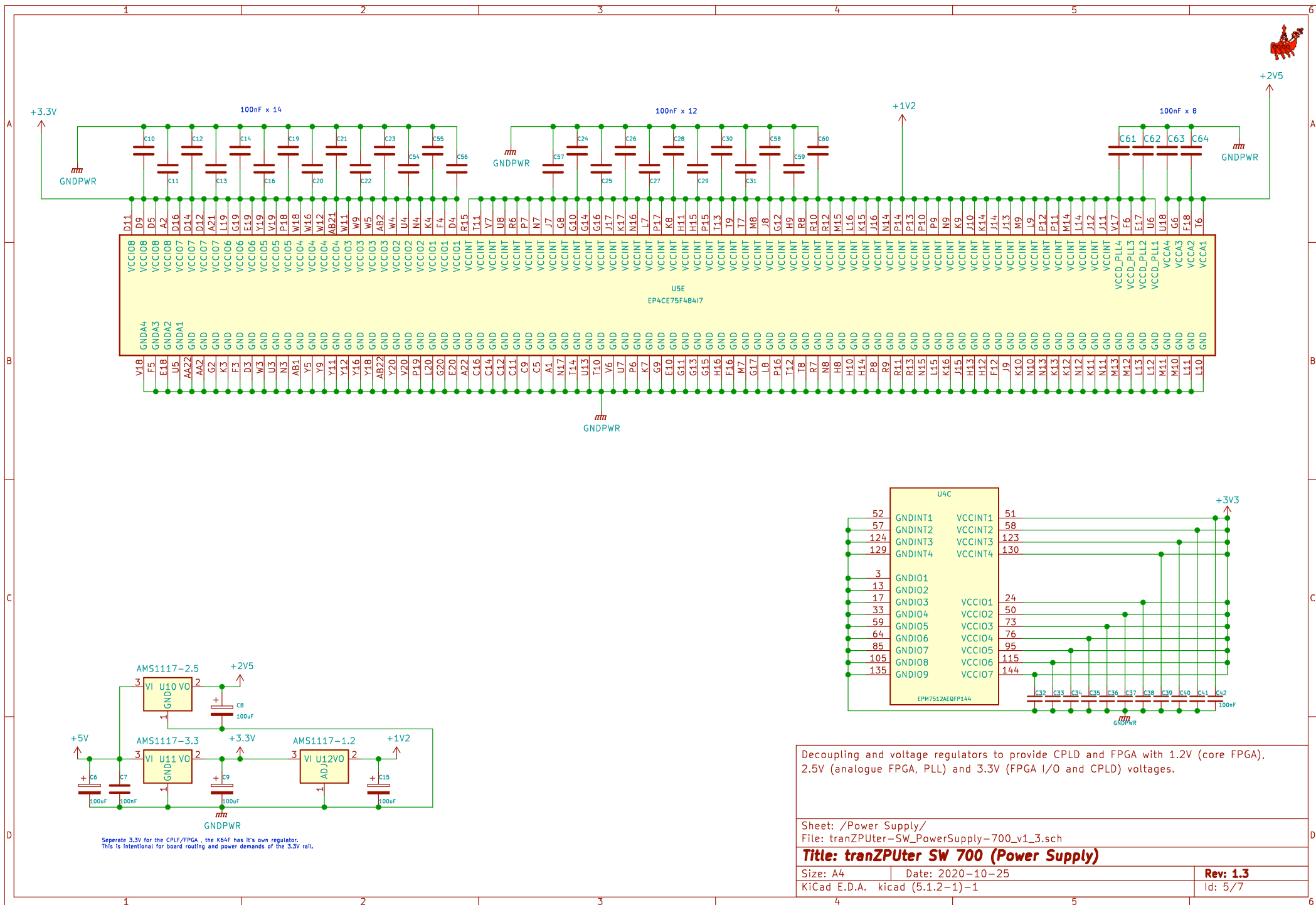


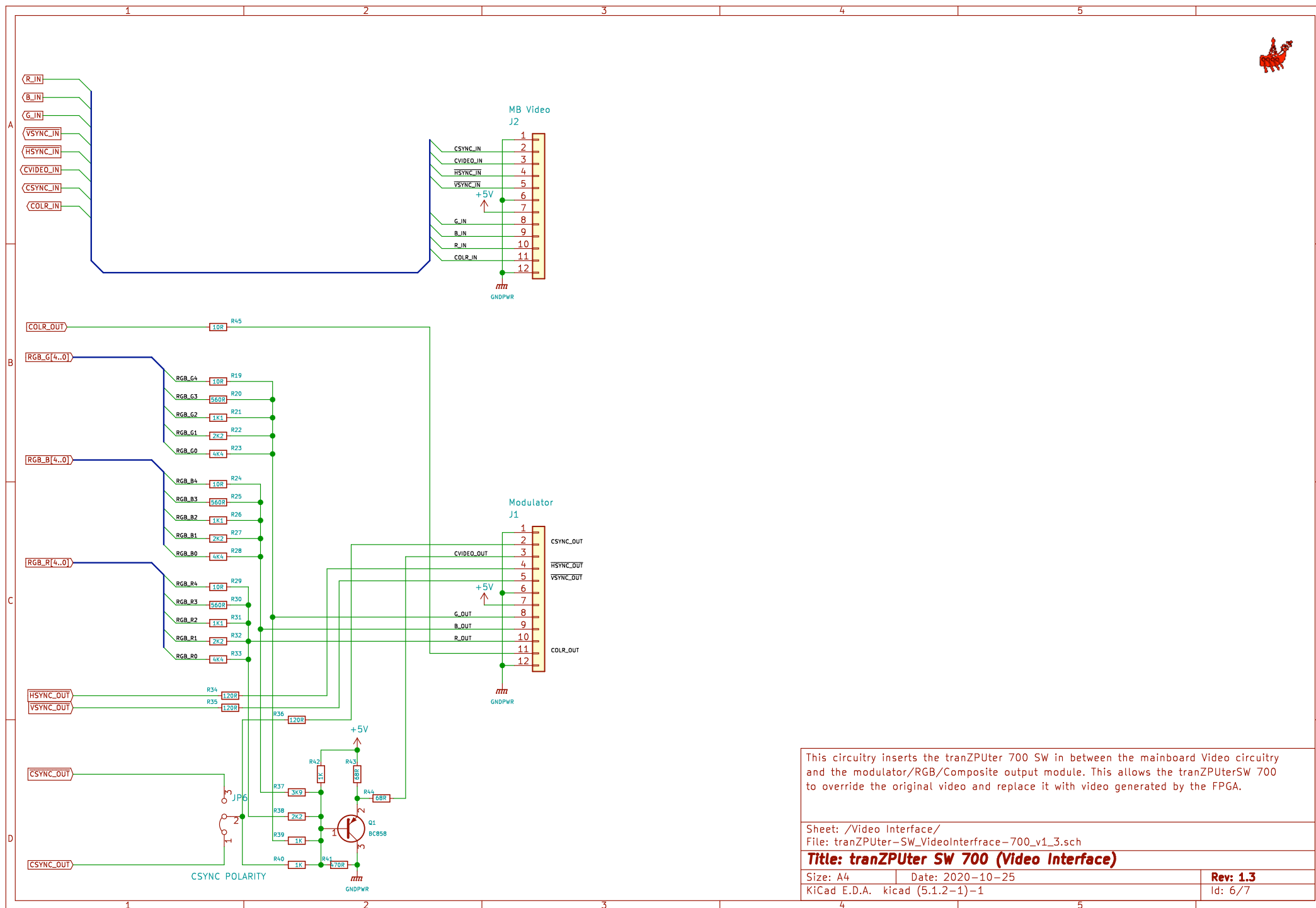
The CPLD and FPGA programming circuit based on JTAG.
The FPGA uses a 64Mb serial Flash RAM for non-volatile storage of its configuration map. The CPLD has a non-volatile EEPROM on-board.

Sheet: /CPLD and FPGA Programming/
File: tranZPUter-SW_CPLDFPGA_Programming-700_v1_3.sch

Title: tranZPUter SW 700 (Programming)

Size: A4	Date: 2020-08-05	Rev: 1.3
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This circuitry inserts the tranZPUter 700 SW in between the mainboard Video circuitry and the modulator/RGB/Composite output module. This allows the tranZPUterSW 700 to override the original video and replace it with video generated by the FPGA.		
Sheet: /Video Interface/ File: tranZPUter-SW_VideoInterface-700_v1_3.sch		
Title: tranZPUter SW 700 (Video Interface)		
Size: A4	Date: 2020-10-25	Rev: 1.3
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X Y7	IO_B3_Y7	U58	IO_B5_N21	N21
X Y8	IO_B3_Y8		IO_B8_A4	A4
X B2	IO_B1_B2		IO_B5_P20	P20
X E2	IO_B1_E2		IO_B5_P21	P21
X E3	IO_B1_E3		IO_B8_A5	A5
X AB4	IO_B3_AB4		IO_B5_R17	R17
X F2	IO_B1_F2		IO_B5_R18	R18
X G3	IO_B1_G3		IO_B5_R19	R19
X G4	IO_B1_G4		IO_B5_R20	R20
X G5	IO_B1_G5		IO_B5_R21	R21
X H3	IO_B1_H3		IO_B8_A6	A6
X H4	IO_B1_H4		IO_B5_T17	T17
X H5	IO_B1_H5		IO_B5_T18	T18
X H6	IO_B1_H6		IO_B5_T19	T19
X H7	IO_B1_H7		IO_B5_T20	T20
X AB3	IO_B3_AB3		IO_B5_U19	U19
X K1	IO_B1_K1		IO_B5_U20	U20
X W2	IO_B2_W2		IO_B5_U21	U21
X R14	IO_B4_R14		IO_B5_U22	U22
X R16	IO_B4_R16		IO_B5_V21	V21
X T15	IO_B4_T15		IO_B5_V22	V22
X T16	IO_B4_T16		IO_B5_W19	W19
X U12	IO_B4_U12		IO_B5_W20	W20
X U14	IO_B4_U14		IO_B5_W21	W21
X U15	IO_B4_U15		IO_B5_W22	W22
X U16	IO_B4_U16		IO_B5_Y21	Y21
X U17	IO_B4_U17		IO_B5_Y22	Y22
X V12	IO_B4_V12		IO_B6_B21	B21
X V13	IO_B4_V13		IO_B6_C20	C20
X V14	IO_B4_V14		IO_B6_C21	C21
X V15	IO_B4_V15		IO_B6_D20	D20
X V16	IO_B4_V16		IO_B6_D21	D21
X W13	IO_B4_W13		IO_B6_E21	E21
X W14	IO_B4_W14		IO_B8_A7	A7
X W15	IO_B4_W15		IO_B2_V3	V3
X W17	IO_B4_W17		IO_B2_V4	V4
X Y13	IO_B4_Y13		IO_B2_T5	T5
X Y14	IO_B4_Y14		IO_B6_F17	F17
X Y15	IO_B4_Y15		IO_B6_F19	F19
X Y17	IO_B4_Y17		IO_B6_F20	F20
X AA21	IO_B5_AA21		IO_B6_F21	F21
X M16	IO_B5_M16		IO_B8_A8	A8
X M19	IO_B5_M19		IO_B6_G18	G18
X M20	IO_B5_M20		IO_B6_H17	H17
X M21	IO_B5_M21		IO_B6_H18	H18
X P22	IO_B5_P22		IO_B6_H19	H19
X N18	IO_B5_N18		IO_B6_H20	H20
X N19	IO_B5_N19		IO_B6_H21	H21
X N20	IO_B5_N20		IO_B5_N22	N22

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X J18	IO_B6_J18	U5C	IO_B7_E15	E15
X J19	IO_B6_J19		IO_B7_E16	E16
X J20	IO_B6_J20		IO_B7_F11	F11
X J21	IO_B6_J21		IO_B7_F13	F13
X R22	IO_B5_R22		IO_B7_F14	F14
X K18	IO_B6_K18		IO_B7_F15	F15
X K19	IO_B6_K19		IO_B8_B8	B8
X K21	IO_B6_K21		IO_B3_U10	U10
X A3	IO_B8_A3		IO_B3_U11	U11
X L21	IO_B6_L21		IO_B3_U9	U9
X B3	IO_B8_B3		IO_B3_V10	V10
X U2	IO_B2_U2		IO_B3_V11	V11
X AB17	IO_B4_AB17		IO_B3_V5	V5
X AB18	IO_B4_AB18		IO_B3_V8	V8
X AB19	IO_B4_AB19		IO_B3_V9	V9
X AB20	IO_B4_AB20		IO_B3_W10	W10
X M4	IO_B2_M4		IO_B3_W6	W6
X M5	IO_B2_M5		IO_B3_W7	W7
X AA17	IO_B4_AA17		IO_B3_W8	W8
X AA18	IO_B4_AA18		IO_B3_Y10	Y10
X AA19	IO_B4_AA19		IO_B3_Y4	Y4
X AA20	IO_B4_AA20		IO_B3_Y6	Y6
X J3	IO_B1_J3		IO_B8_C10	C10
X J4	IO_B1_J4		IO_B8_C3	C3
X J5	IO_B1_J5		IO_B8_C4	C4
X J6	IO_B1_J6		IO_B8_C6	C6
X C13	IO_B7_C13		IO_B8_C7	C7
X C15	IO_B7_C15		IO_B8_C8	C8
X C17	IO_B7_C17		IO_B8_D10	D10
X C18	IO_B7_C18		IO_B8_D6	D6
X C19	IO_B7_C19		IO_B8_D7	D7
X D13	IO_B7_D13		IO_B8_D8	D8
X D15	IO_B7_D15		IO_B8_E5	E5
X D17	IO_B7_D17		IO_B8_E6	E6
X D18	IO_B7_D18		IO_B8_E7	E7
X D19	IO_B7_D19		IO_B8_E8	E8
X E11	IO_B7_E11		IO_B8_E9	E9
X E12	IO_B7_E12		IO_B8_F10	F10
X E13	IO_B7_E13		IO_B8_F7	F7
X E14	IO_B7_E14		IO_B8_F8	F8
X			IO_B8_F9	F9
X			IO_B8_G7	G7

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Unused components of the Cyclone IV FPGA.

Sheet: /Unused FPGA Blocks/
File: tranZPUter-SW_FPGA2-700_v1_3.sch

Title: tranZPUter SW 700 (FPGA 2)

Size: A4

Date: 2020-10-25

Rev: 1.3

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Id: 7/7