

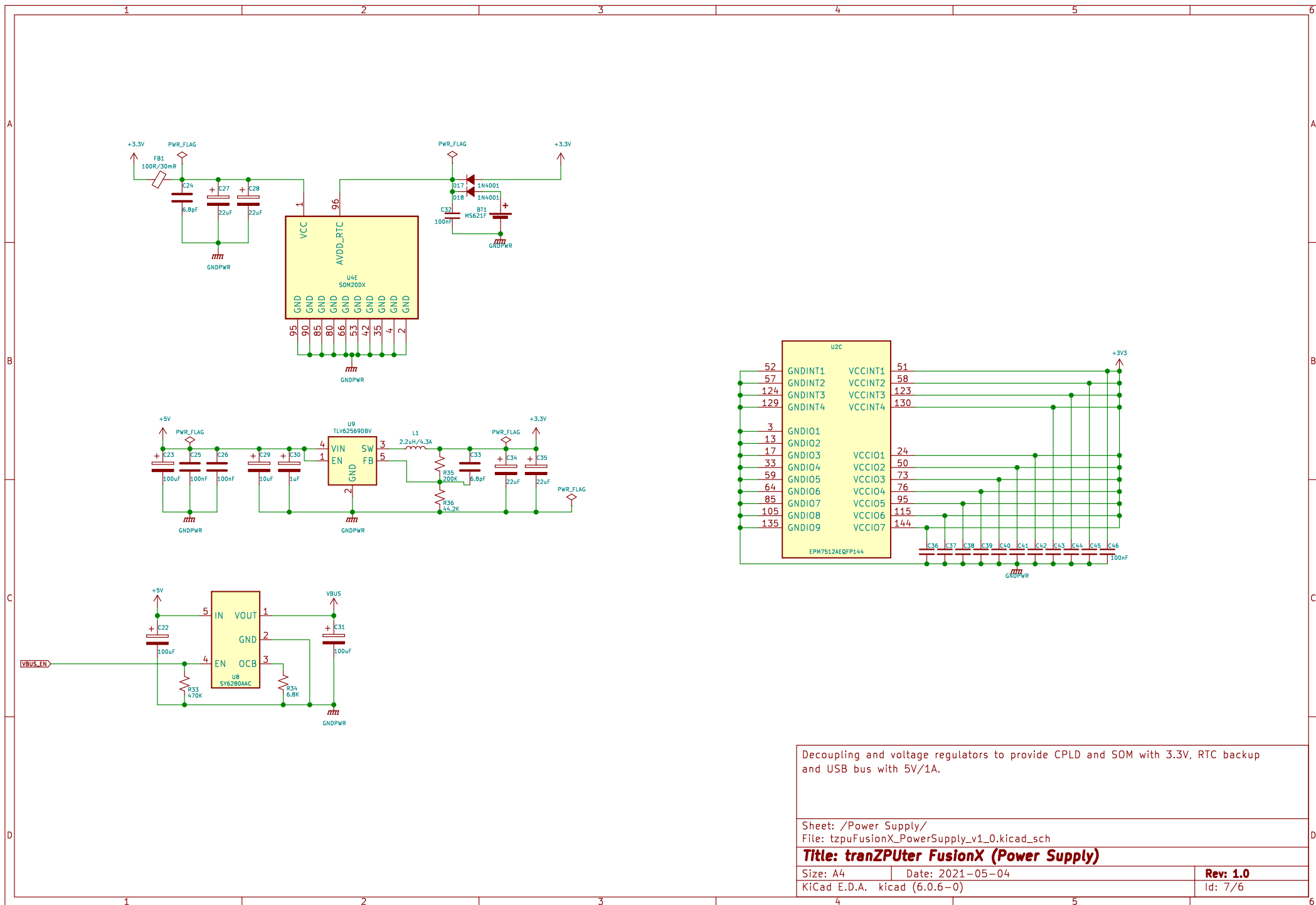
Video Processor logic.
The SOM2D02 outputs a RGB888 signal from its internal video processor which is used to replace the video generated by the host. The mono circuit is based on the VGA with output in range 4v–5v to allow for contrast shading on monochrome CRT's.

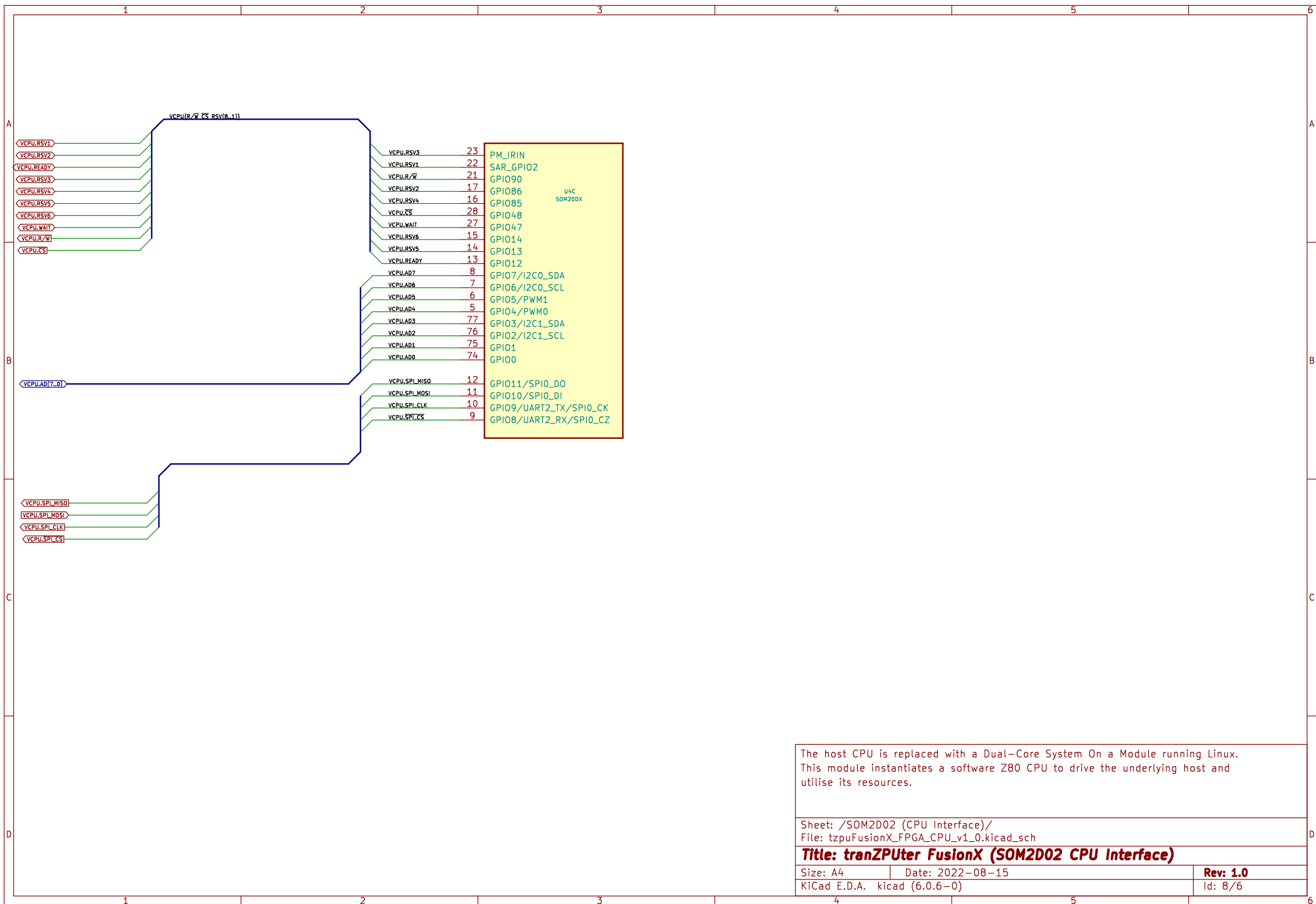
Sheet: /SOM2D02 (Video)/
File: tzpuFusionX_FPGA_Video_v1_0.kicad_sch

Title: tranZPUter FusionX (Video Interface)

Size: A4 Date: 2021–05–04
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Rev: 1.0
Id: 6/6





The host CPU is replaced with a Dual-Core System On a Module running Linux. This module instantiates a software Z80 CPU to drive the underlying host and utilise its resources.

Sheet: /SOM2D02 (CPU Interface)/
File: tzpuFusionX_FPGA_CPU_v1_0.kicad_sch

Title: tranZPUter FusionX (SOM2D02 CPU Interface)

Size: A4 Date: 2022-08-15

Rev: 1.0

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Id: 8/6