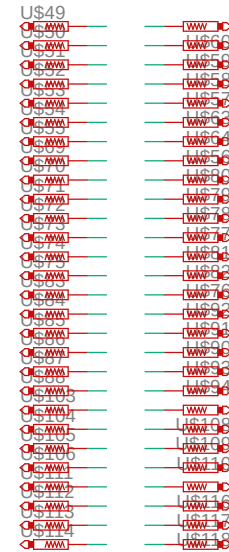
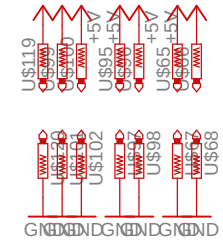


Interrupt Controller

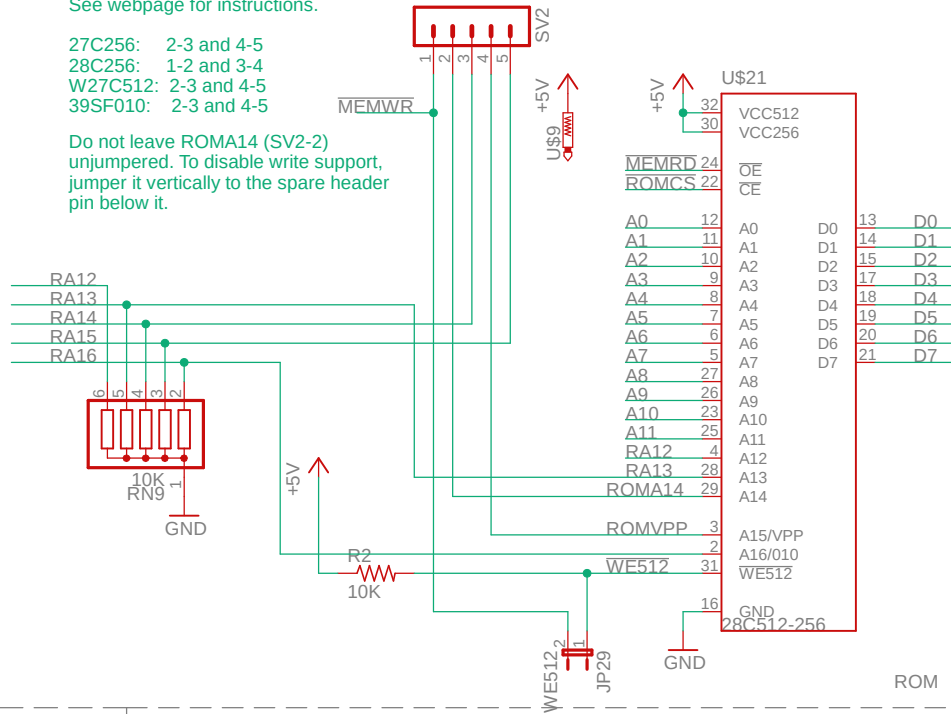


The footprints over here make up a general purpose breadboard area

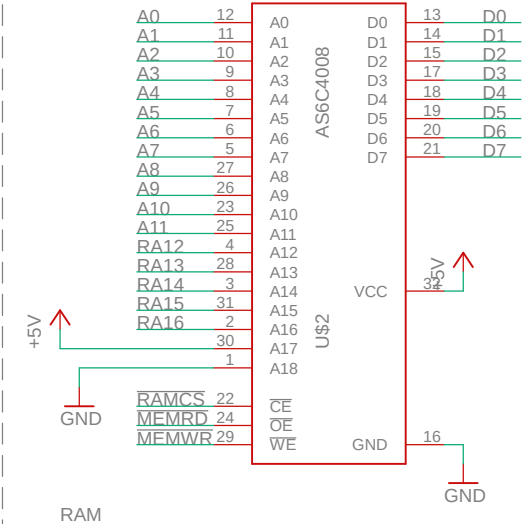
See webpage for instructions.

27C256: 2-3 and 4-5  
 28C256: 1-2 and 3-4  
 W27C512: 2-3 and 4-5  
 39SF010: 2-3 and 4-5

Do not leave ROMA14 (SV2-2) unjumped. To disable write support, jumper it vertically to the spare header pin below it.



AS6C1008 and AS6C4008 should both work.



NOTE: In this config, PLD is responsible for generating RAMCS by watching ROMCS.

RA12, RA13, RA14, ROMCS pulled down elsewhere.

