2018-2019

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7. ACSL Assembly Language

PROBLEM: Write an ACSL Assembly Language interpreter.

INPUT: The input for this program will be 10 valid ACSL Assembly Language programs. Each program will be preceded by a line with one or more integers separated by one or more spaces. These integers are input to the program if and when the **READ** opcode is executed. Each program will end with a blank line, or the end of the input file. Please review the ACSL Assembly Language reference guide attached. The fields on each line are separated by a combination of spaces and tabs.

OUTPUT: Print the value of the ACC when the program ends.

SAMPLE INPUT: (2 programs; the Test Data will have 10 programs):

```
Α
      DC
             8
В
      DC
            -2
С
      DC
            3
      LOAD
            В
      MULT
            С
      ADD
            Α
      DIV
            В
      SUB
      END
2 3
Α
      DC
            1
      READ
            в
      READ
            С
      LOAD =1
      MULT C
      ΒE
            D
D
      STORE A
      DIV
      BL
            DONE
      STORE C
      SUB
            Α
      ВG
            D
      STORE B
      ADD
DONE
      END
```

SAMPLE OUTPUT:

- 1. -9
- 2. 1

ACSL Assembly Language Reference Guide

Each line of an ACSL Assembly Language program has 3 components: a **label** (usually optional), an **opcode** (always required), and a **loc** field (nearly always required).

- The **label**, if present, is an alphanumeric character string beginning in the first column. A label must begin with an alphabetic character(A through Z, or a through z), and labels are case-sensitive. The label field is required for the **DC** opcode; it is optional for all other opcodes.
- Valid **opcodes** are listed in the chart below; they are uppercase and case-sensitive. Opcodes are reserved words of the language and may not be used as a label.
- The loc field is either a reference to a label (e.g., "ADD A") or immediate data (e.g., "LOAD =123"). Only those opcodes with an asterisk in the following chart are allowed to use the immediate data format of the loc field. The loc field is required for all opcodes, except for the END opcode. It is prohibited on the END opcode.

OP CODE	DESCRIPTION
*LOAD	The contents of LOC are placed in the ACC. LOC is unchanged.
STORE	The contents of the ACC are placed in the LOC. ACC is unchanged.
*ADD	The contents of LOC are added to the contents of the ACC. The sum is stored in the ACC. LOC is unchanged. Addition is modulo 1,000,000.
*SUB	The contents of LOC are subtracted from the contents of the ACC. The difference is stored in the ACC. LOC is unchanged. Subtraction is modulo 1,000,000.
*MULT	The contents of LOC are multiplied by the contents of the ACC. The product is stored in the ACC. LOC is unchanged. Multiplication is modulo 1,000,000.
*DIV	The contents of LOC are divided into the contents of the ACC. The signed integer part of the quotient is stored in the ACC. LOC is unchanged.
BG	Branch to the instruction labeled with LOC if ACC>0.
BE	Branch to the instruction labeled with LOC if ACC=0.
BL	Branch to the instruction labeled with LOC if ACC<0.
BU	Branch to the instruction labeled with LOC.
READ	Read a signed integer (modulo 1,000,000) into LOC.
PRINT	Print the contents of LOC.
DC	The value of the memory slot defined by the LABEL field is defined to contain the specified constant. The LABEL field is mandatory; the ACC is not modified.
END	Program terminates. LOC field is ignored and must be blank.

TEST DATA

TEST INPUT:

O TME A B	Ď			DC DC LOAD MULT STORE LOAD MULT DIV END		0 -1 213 A =12 TMP B A
-3 A B D	2	1	-4	DC DC READ LOAD BE BG LOAD ADD STORE	0	7 0 0 N N E C B
С				BU LOAD ADD		D A N
				STORE		A
				BU		D
Ε				LOAD		В
				SUB		А
				END		

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0		
A	DC	5
В	DC	4
C	DC	12
	LOAD	A
	ADD	В
	SUB	С
	BG	ONE
	BL	TWO
ONE	DIV	В
	END	
TWO	STORE	X
	LOAD	С
	MULT	X
	BU	ONE
0		
NSUB	DC	3
TOTAL	DC	210
CNT	DC	0
TOP	LOAD	CNT
	ADD	=1
	STORE	CNT
	LOAD	NSUB
	MULT	=2
	STORE	NSUB
	LOAD	TOTAL
	SUB	NSUB
	STORE	TOTAL
	BG	TOP
	LOAD	CNT
	END	

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5 -3 6 2 SUM J TOP	-9 7 1 4 DC DC READ LOAD MULT ADD STORE LOAD SUB STORE BE BU	-8 0 6 VALUE VALUE VALUE SUM SUM J =1 J STOP TOP
STOP	LOAD END	SUM
15 10 A	DC READ	0 B
T ST	READ READ DC LOAD ADD STORE LOAD ADD STORE LOAD ADD STORE SUB BL	C 0 T =1 T B C A B A B =1000 ST
	SUB	=1000

2018-2019

7. ACSL Assembly Language

1 -1	-12	1 2	10	3	-5	1	1	-14	49	0	8	100
S		DC			()						
P		DC			()						
OWT		RE.	AD		E	?						
		RE.	AD		N	1						
		RE.	AD		I	_						
		LO.	AD		Ε	?						
		ΒE			Ι		Ν					
		LO.	AD		N	1						
		MU	$_{ m LT}$		N	1						
		ST	ORE		S	3						
		LO.	AD		I							
		MU	LT		Ε	?						
		MU	LT		=	=4						
		ST	ORE		Ε	-						
		LO.	AD		S	3						
		SU	В		Ε	<u> </u>						
		ВG			Γ	ľWC)					
		BL			Γ	ľWC)					
		LO.	AD		N	1						
		MU	LT		=	=-1						
		DI	V		=	=2						
		DI	V		Ε	7						
DOWN		EN	D									

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4213		
	READ	NUM
В	DC	16
SUM	DC	0
	LOAD	NUM
TOP	DIV	В
	STORE	С
	BE	DOWN
	LOAD	С
	MULT	В
	STORE	E
	LOAD	NUM
	SUB	E
	STORE	F
	ADD	SUM
	STORE	SUM
	LOAD	С
	STORE	NUM
	BU	TOP
DOWN	LOAD	SUM
	ADD	NUM
	END	

2018-2019			
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-16 4		
X	DC	4
Y	DC	6
А	READ	R
	ADD	Χ
	STORE	Z
	SUB	Y
	BG	А
	ADD	Y
W	DC	1
	LOAD	Χ
	ADD	W
	STORE	Χ
	LOAD	Y
	DIV	X
	STORE	Z
	LOAD	=-3
V	ADD	W
	BE	Т
	BL	V
T	STORE	Z
	READ	S
	LOAD	=-10
	ADD	S
	SUB	R
	DIV	Χ
	MULT	Y
	END	

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7. ACSL Assembly Language

100 999		
A	DC	0
	READ	В
	READ	E
YELLOW	LOAD	В
	DIV	=10
	STORE	С
	MULT	=10
	STORE	F
	LOAD	В
	SUB	F
	STORE	D
	LOAD	В
	DIV	=100
	SUB	D
	STORE	G
	BE	RED
BLUE	LOAD	В
	SUB	E
	BL	GREEN
	LOAD	A
	END	
RED	LOAD	A
	ADD	=1
	STORE	A
	BU	BLUE
GREEN	LOAD	В
	ADD	=1
	STORE	В
	BU	YELLOW

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TEST OUTPUT:

- 1. 17
- 2. -32
- -9 3.
- 6 4.
- 5. 204
- 6. 590
- 7. 7
- 8. 13
- 9. 12
- 90 10.