From input port 8 x 8 AWGR $\lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7$ $O_0 \Rightarrow |\lambda_0| \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7$ $\lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \Rightarrow 1$ $O_1 \Rightarrow \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \lambda_0$ $\lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \Longrightarrow$ $O_2 \Rightarrow \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \lambda_0 \lambda_1$ $\lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \Longrightarrow$ $O_3 \Rightarrow \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \lambda_0 \lambda_1 \lambda_2$ $\lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \Longrightarrow$ $O_4 \Rightarrow \lambda_4 \lambda_5 \lambda_6 \lambda_7 \lambda_0 \lambda_1 \lambda_2 \lambda_3$ $\lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \Longrightarrow$ $O_5 \rightleftharpoons \lambda_5 \lambda_6 \lambda_7 \lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4$ $\lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \Longrightarrow I$ $O_6 \Rightarrow \lambda_6 \lambda_7 \lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5$ $\lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6 \lambda_7 \Longrightarrow$ $O_7 \Rightarrow \lambda_7 \lambda_0 \lambda_1 \lambda_2 \lambda_3 \lambda_4 \lambda_5 \lambda_6$ Input Ports **Output Ports**