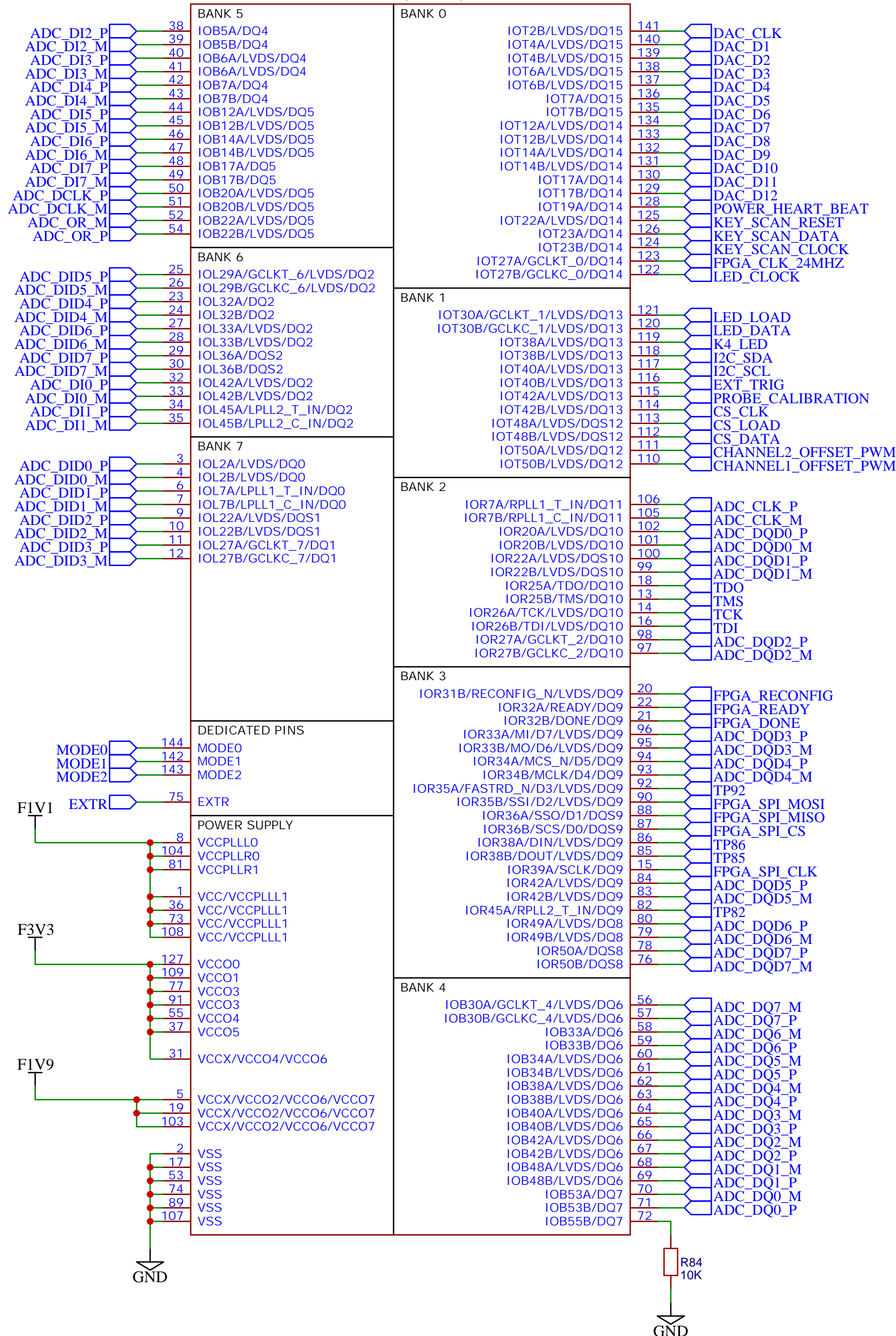
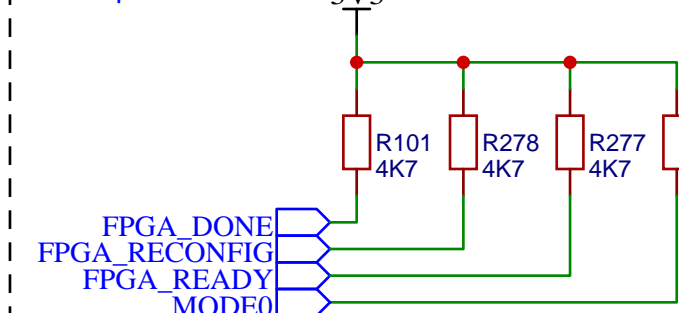


FPGA with embedded PSRAM

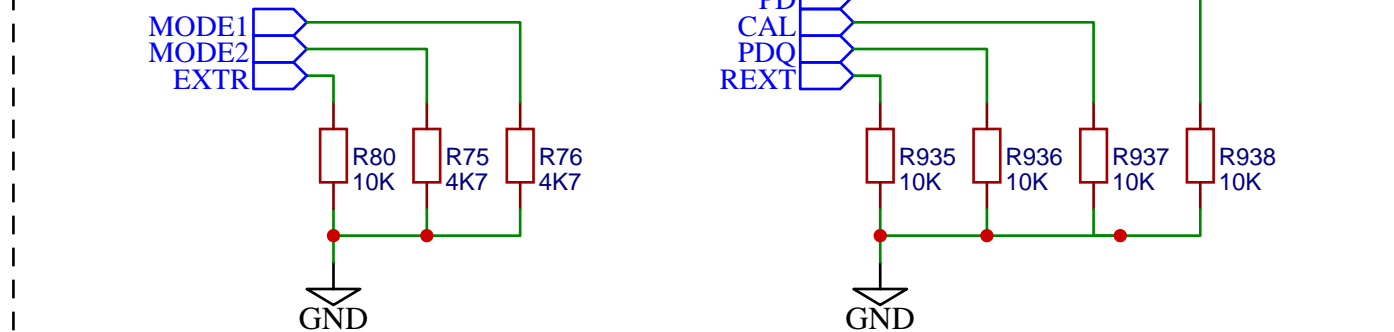
U7
GW2AR-18-144P



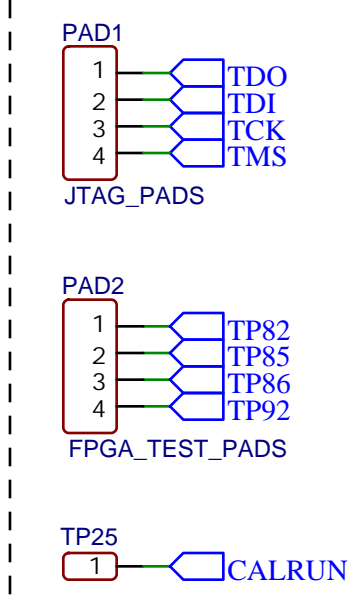
Pull Up's



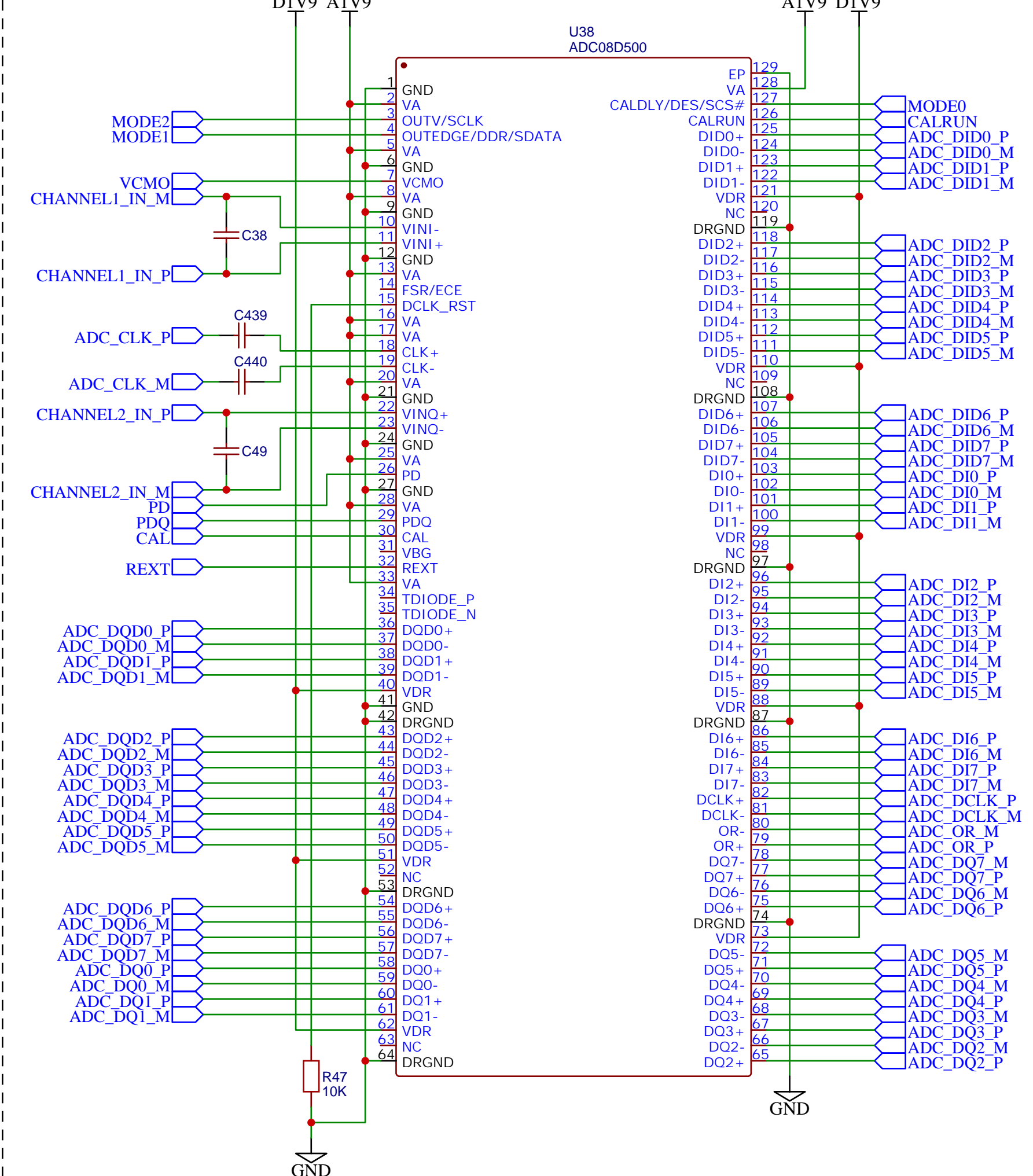
Pull Down's



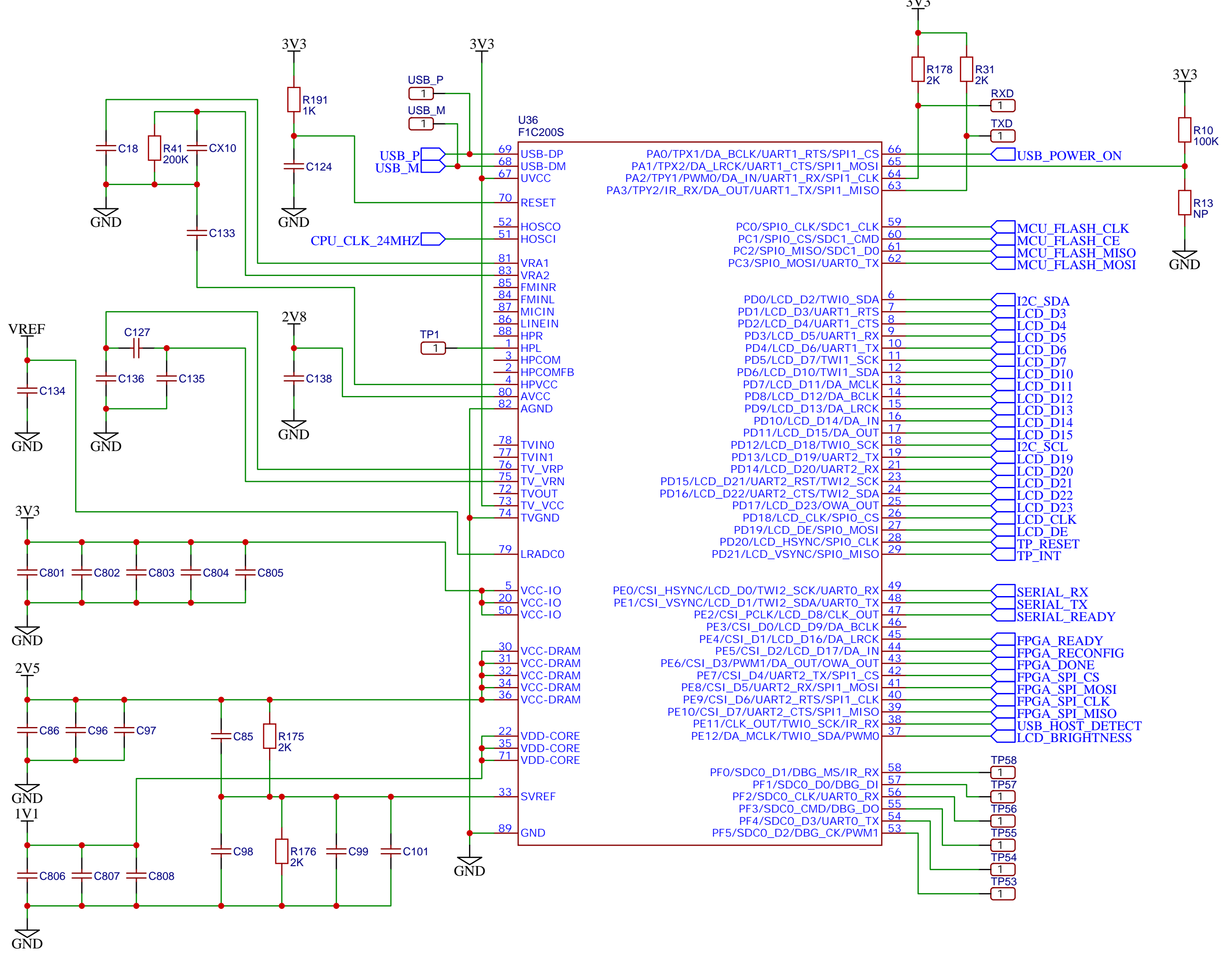
Test Pads



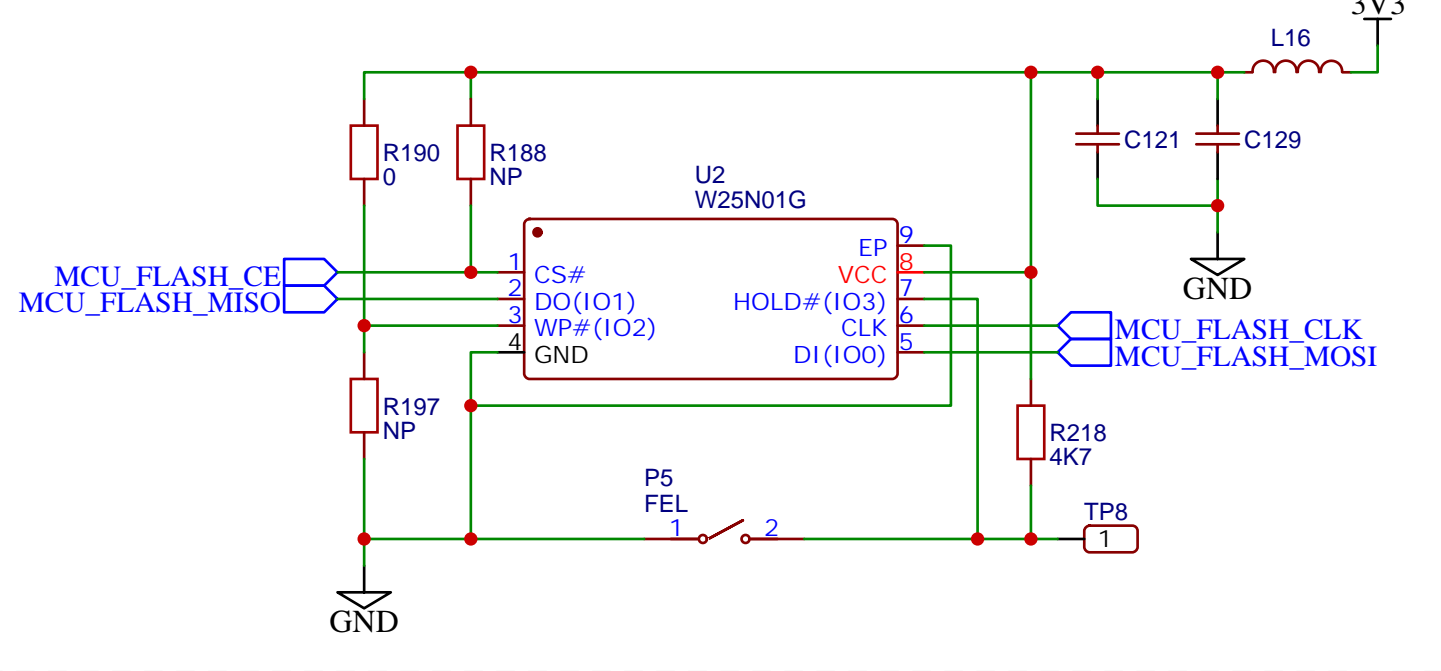
ADC



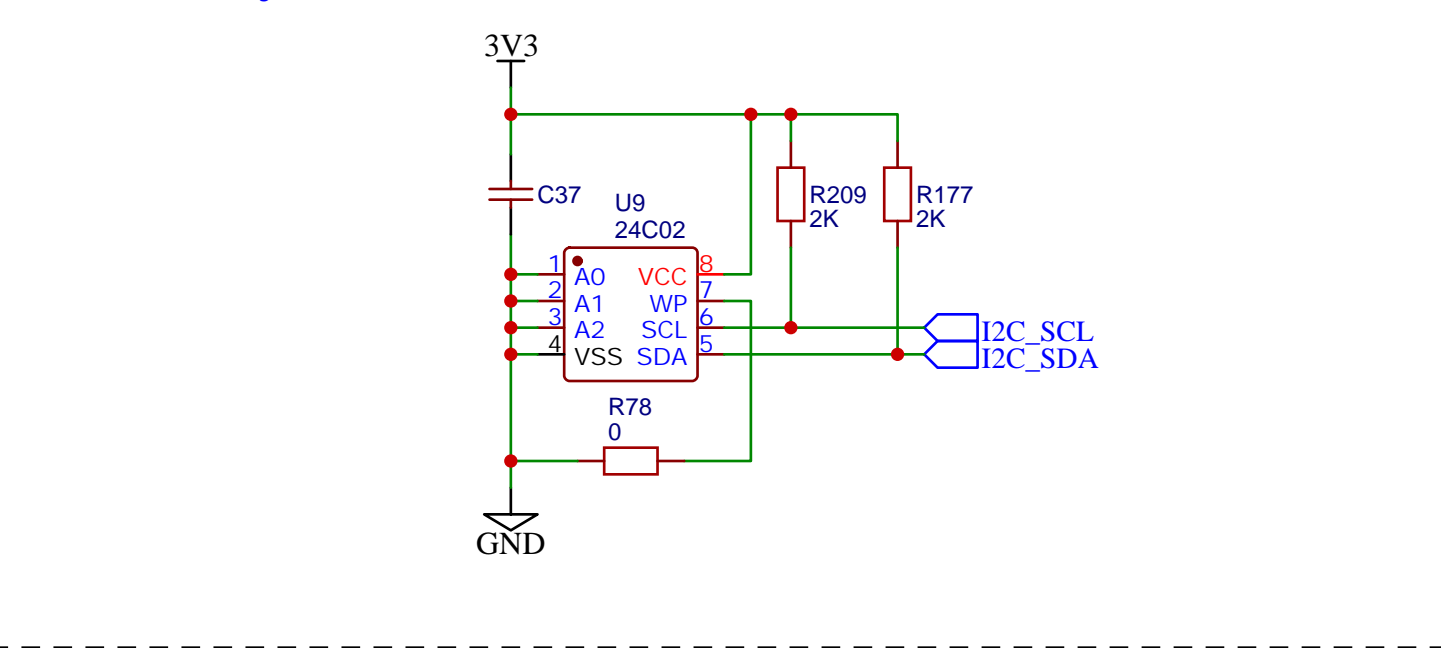
Central Processing Unit



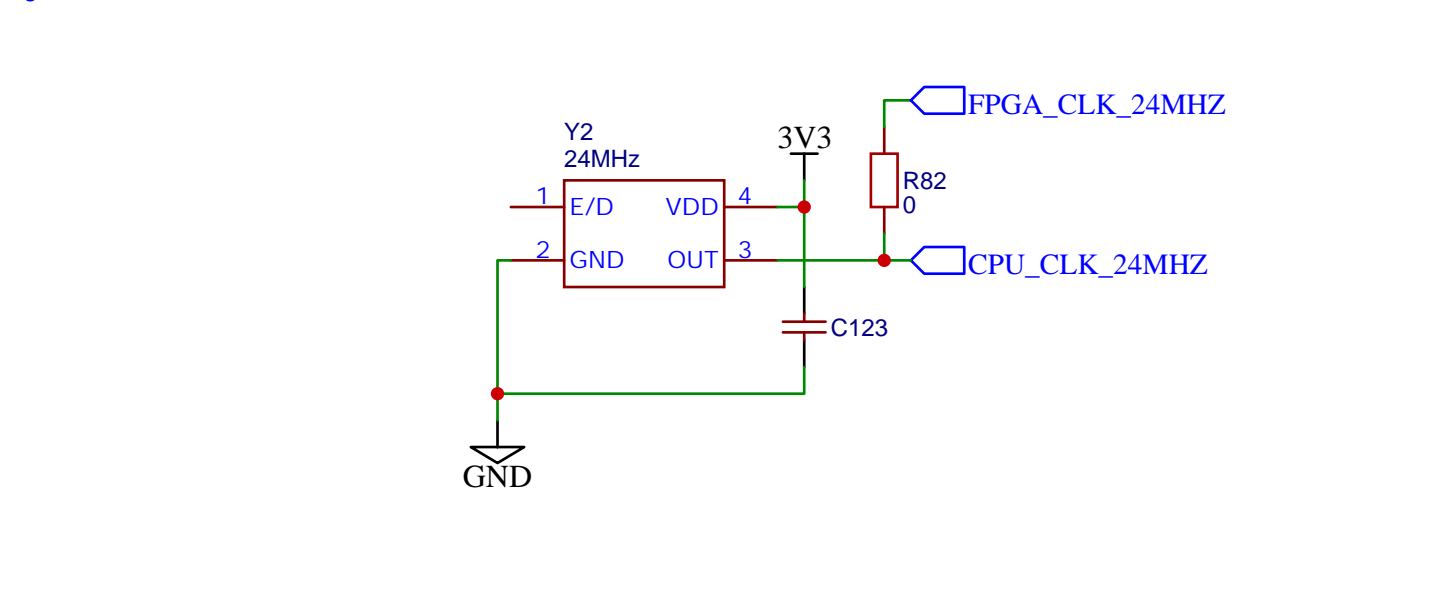
FLASH Memory

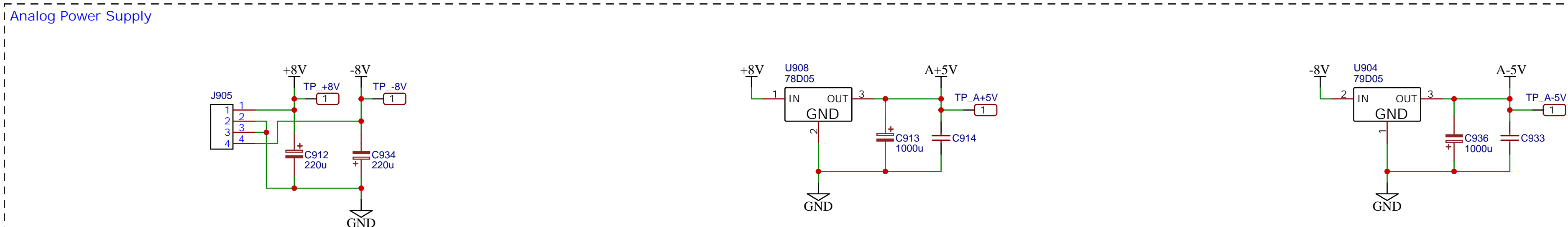
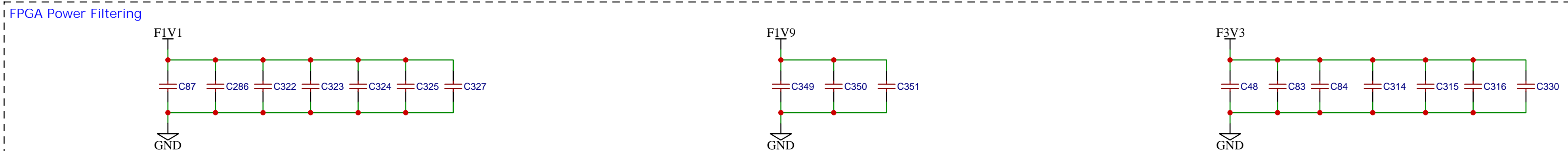
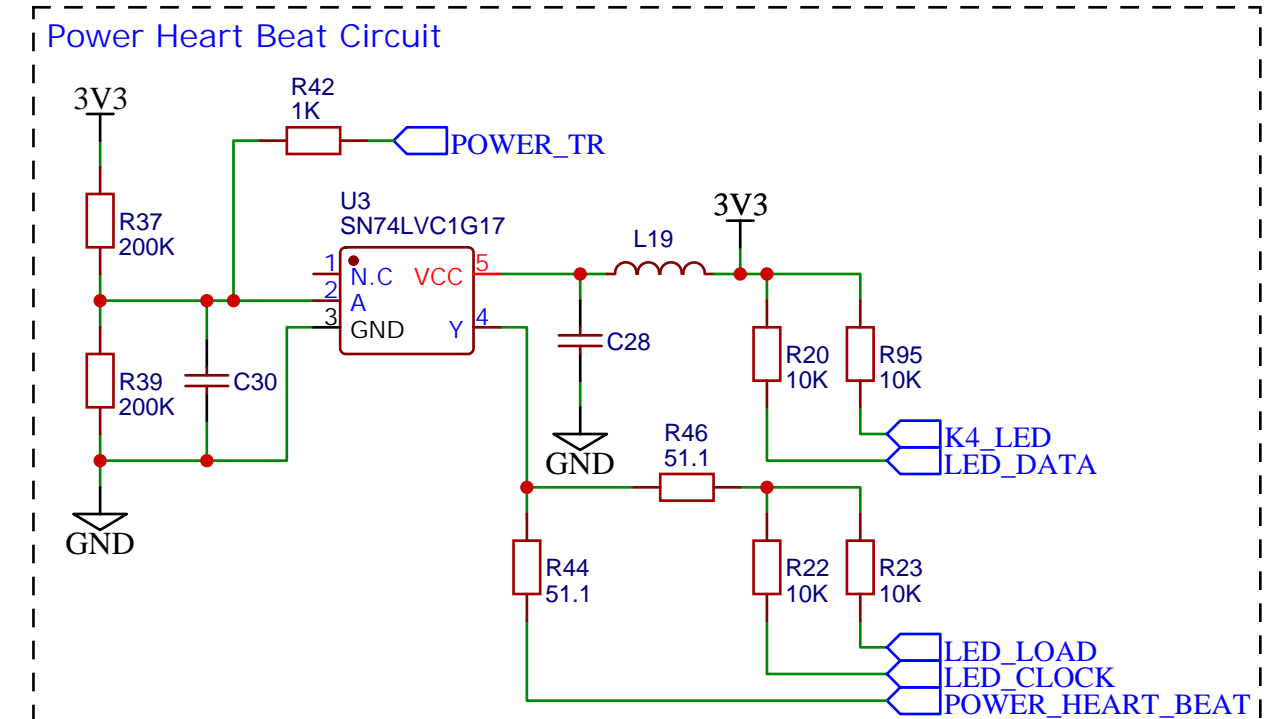
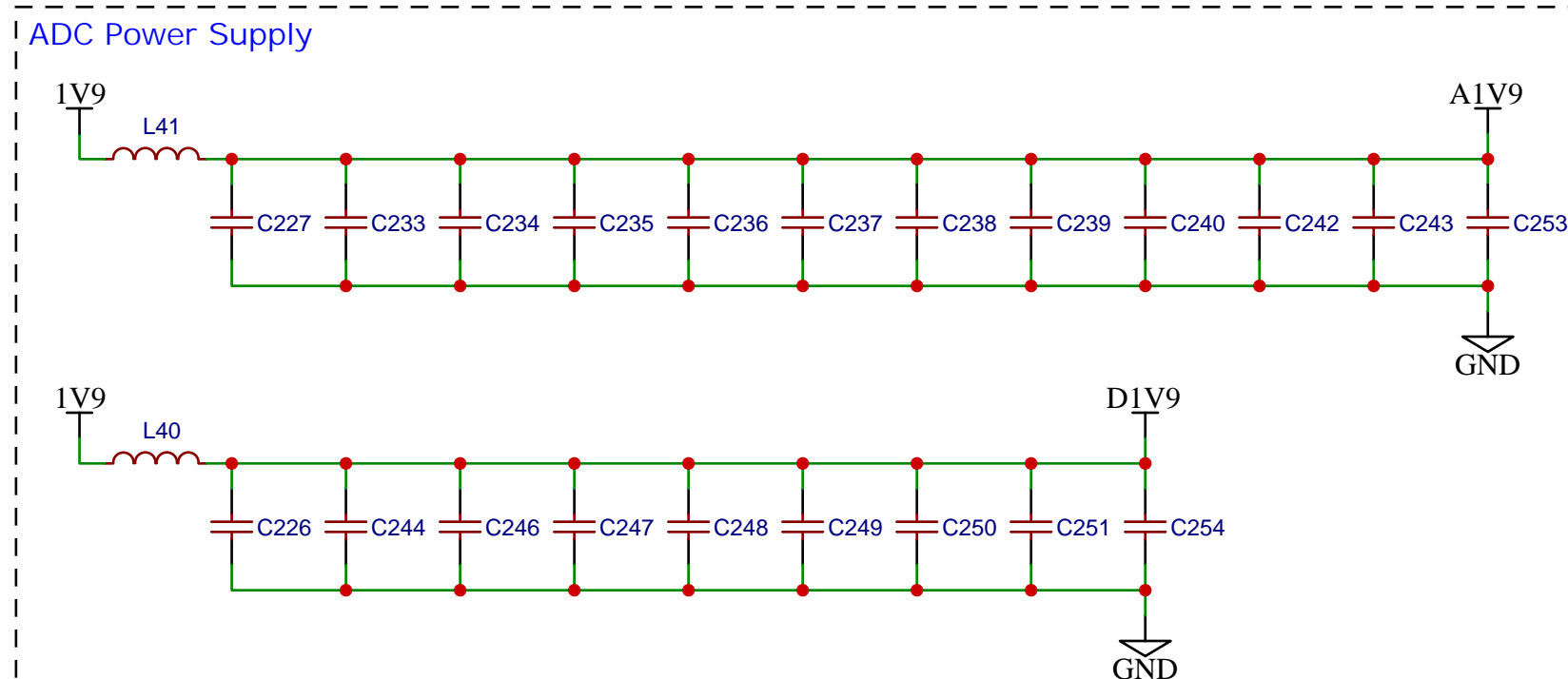
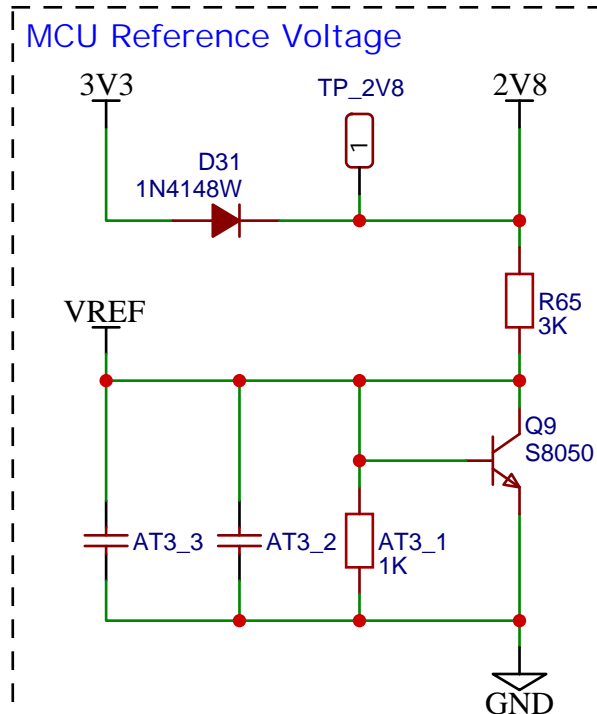
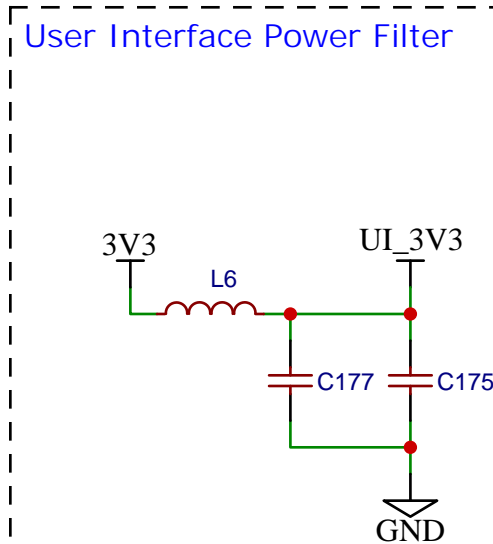
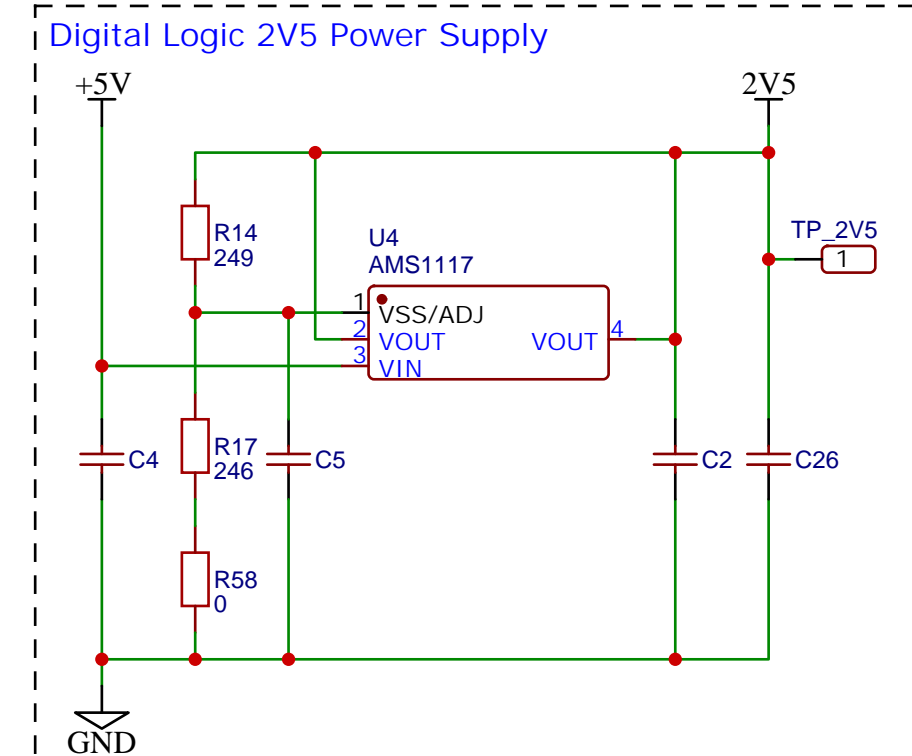
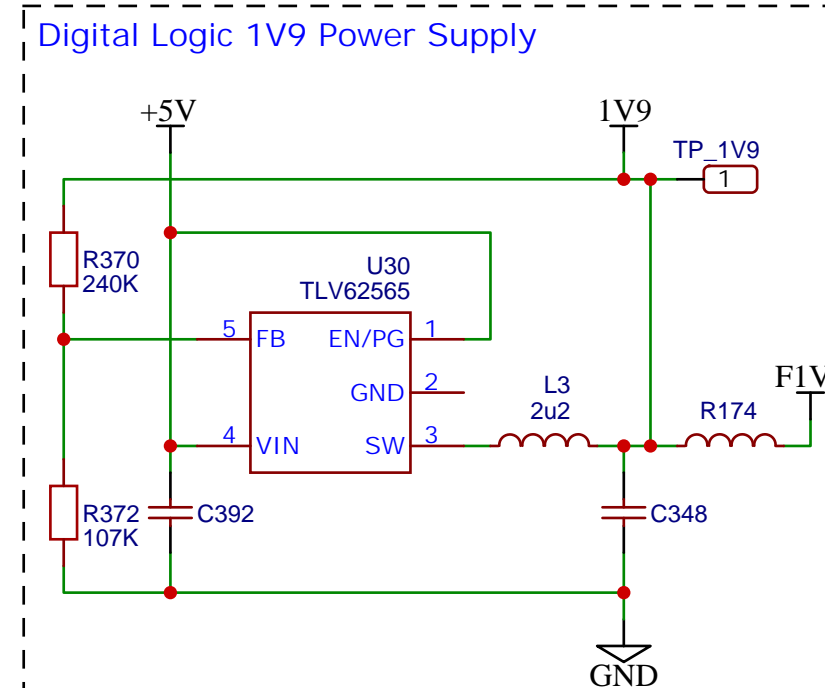
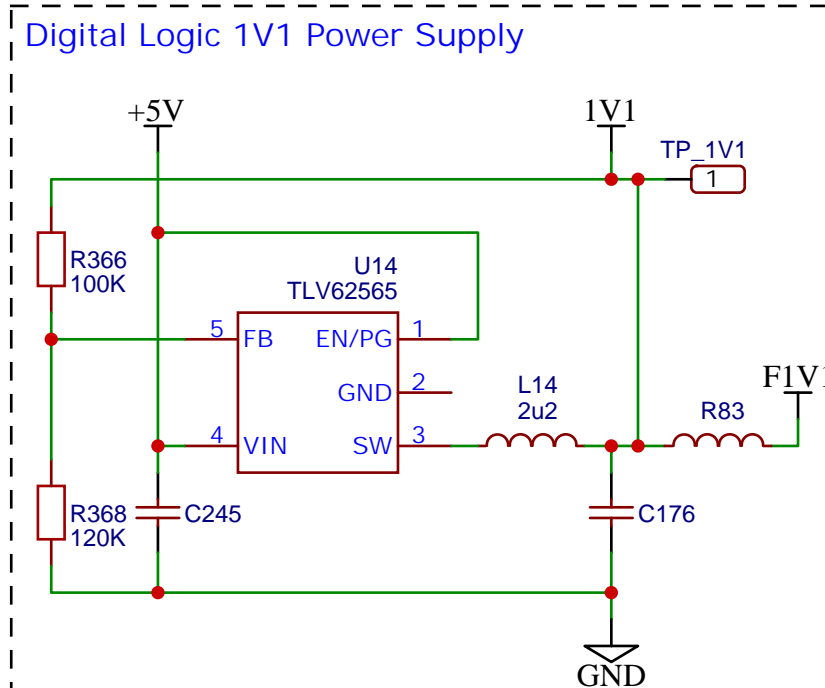
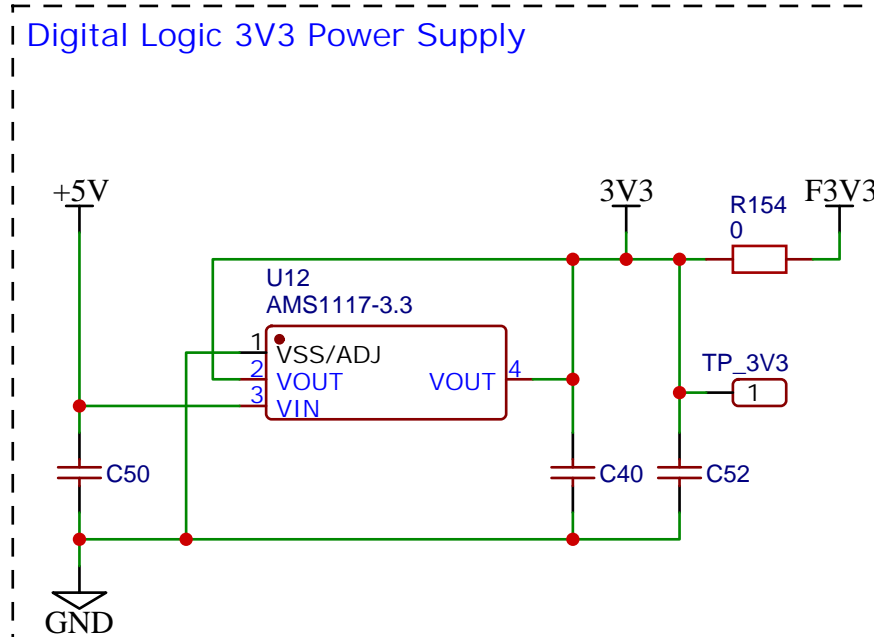
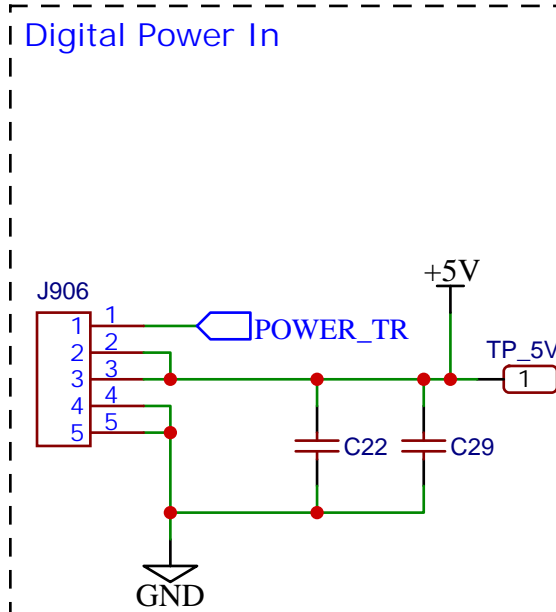


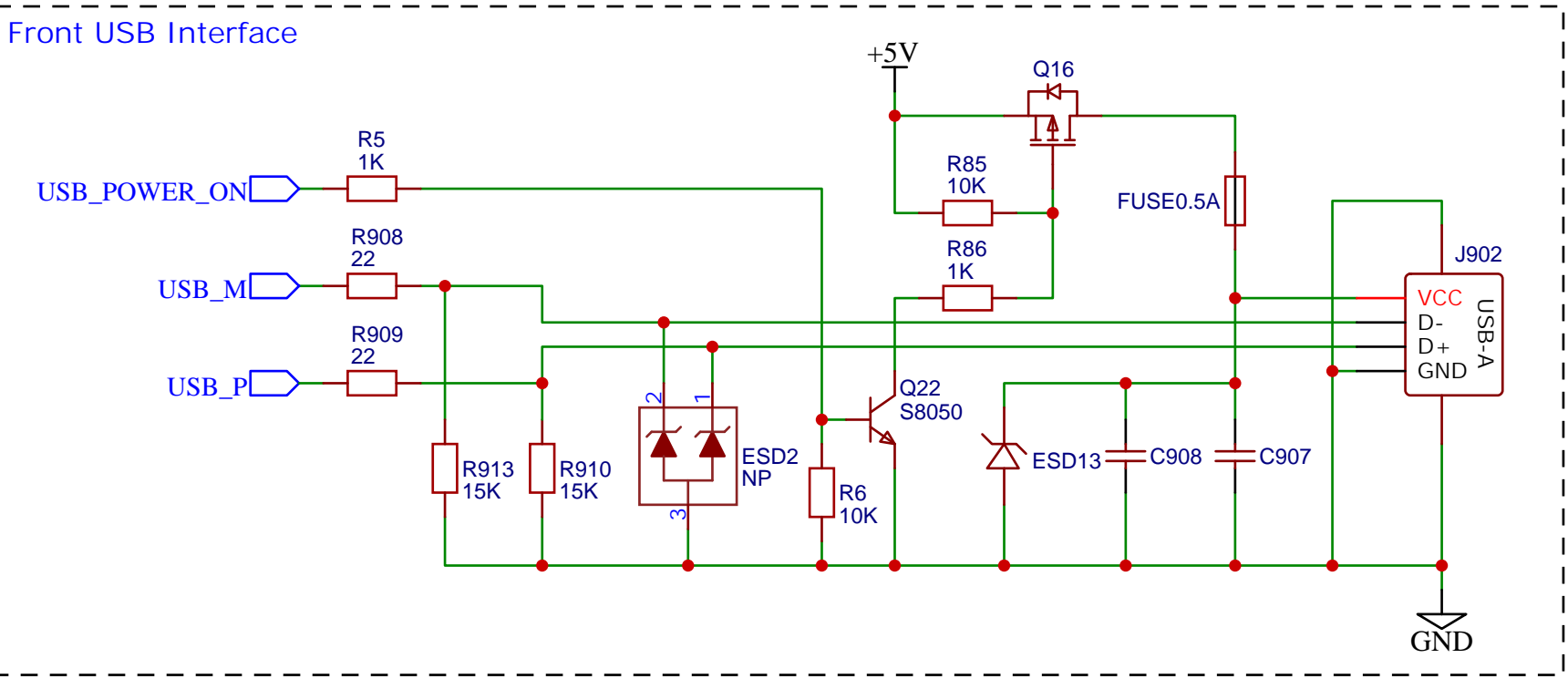
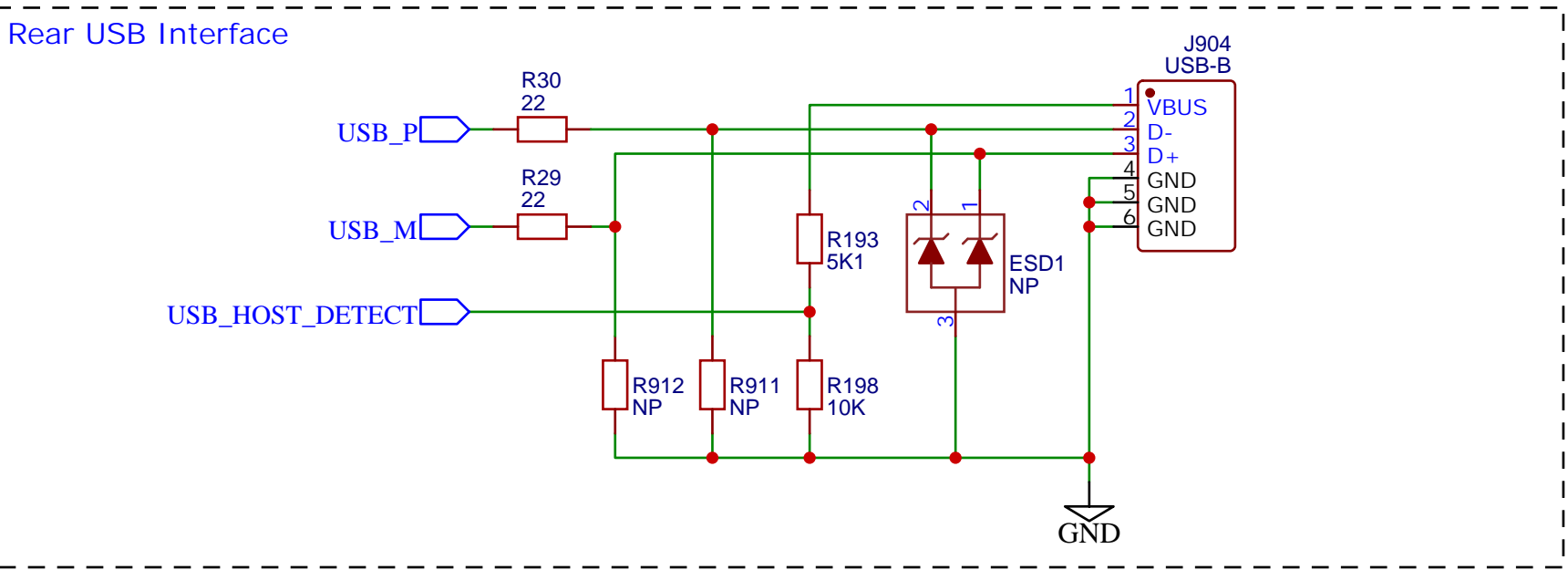
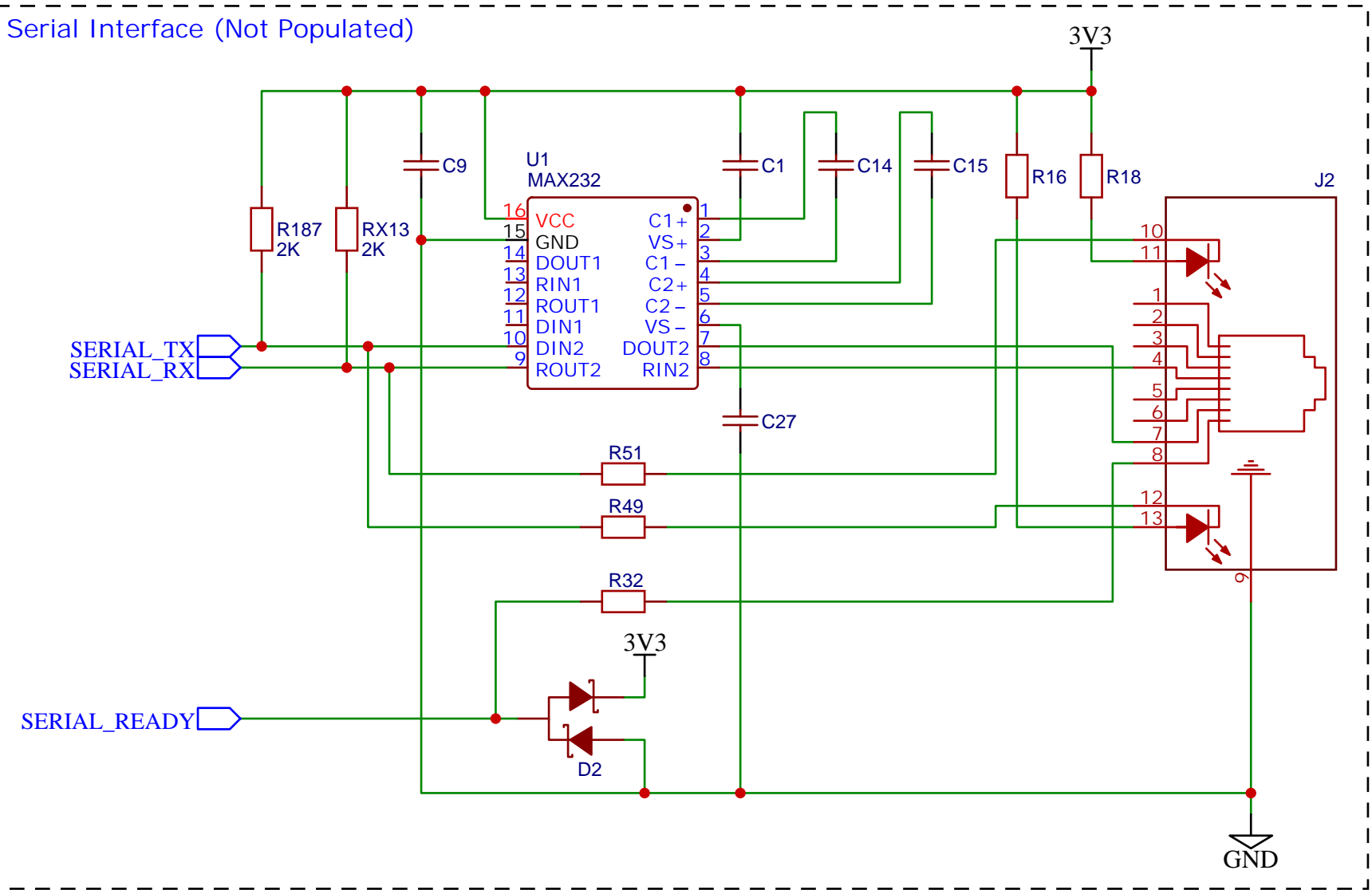
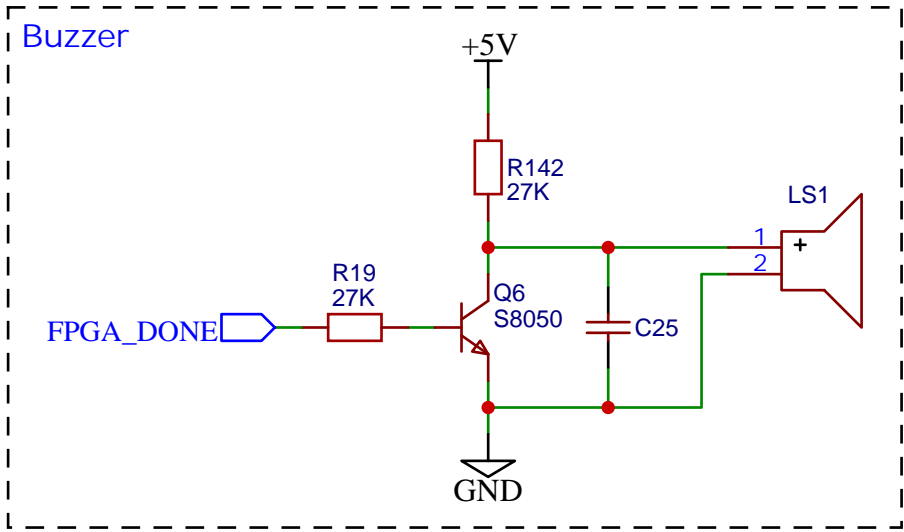
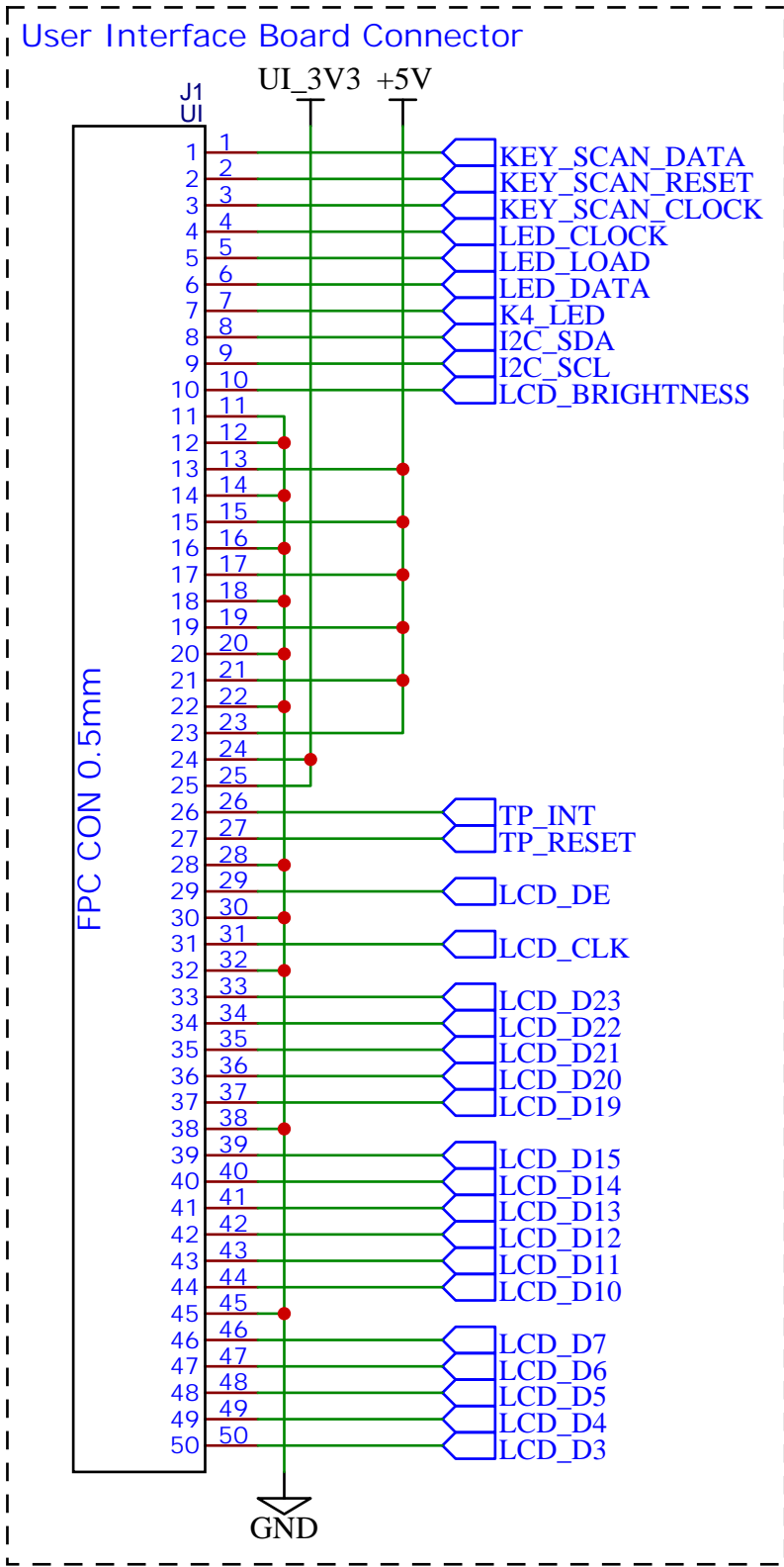
EEPROM Memory



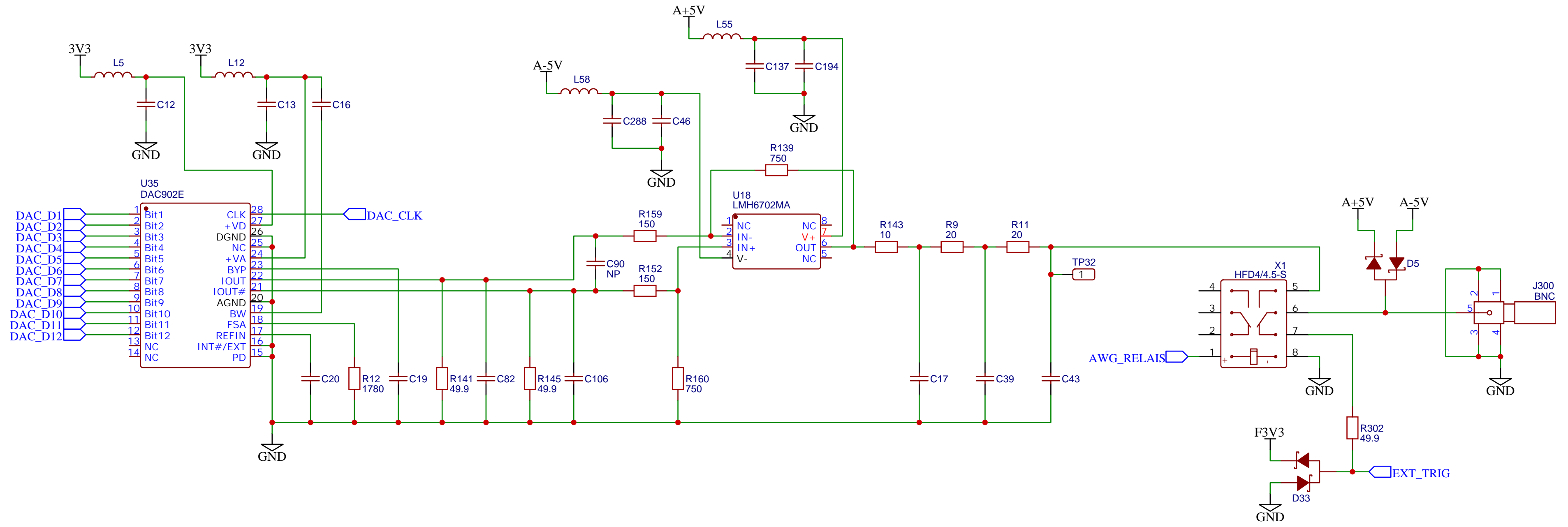
System Clock Generation



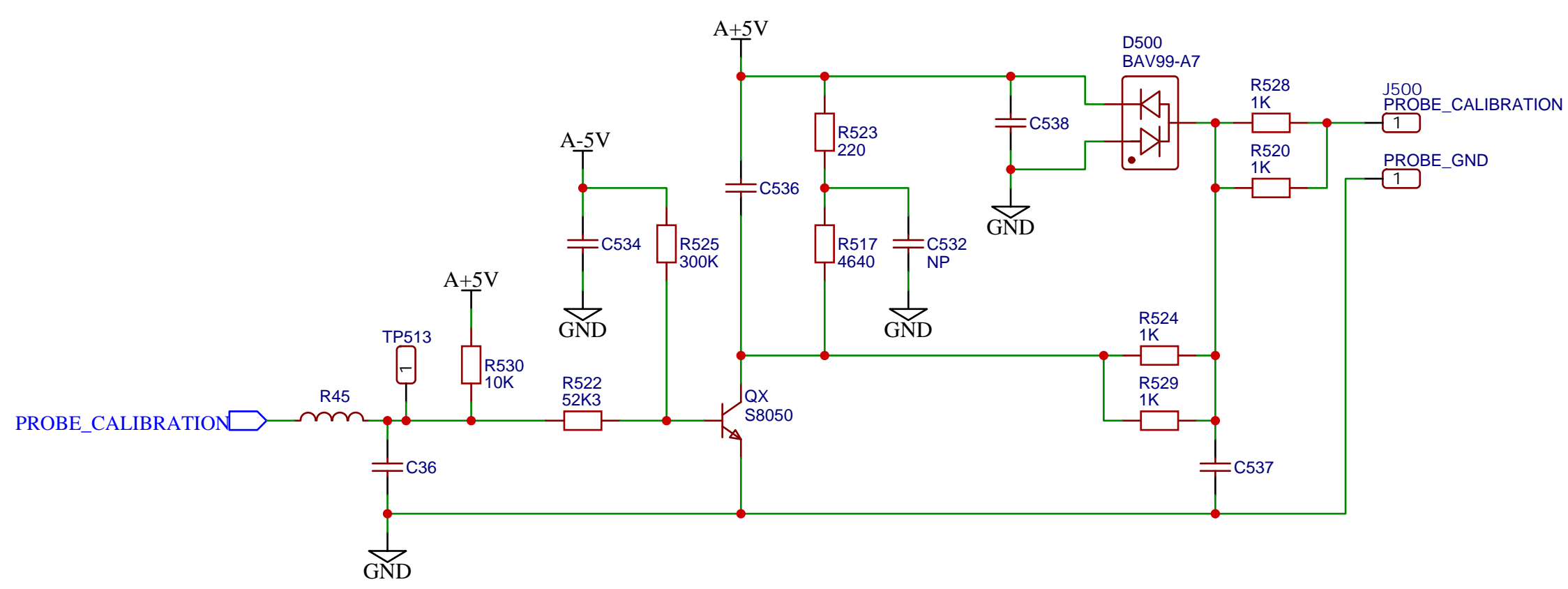




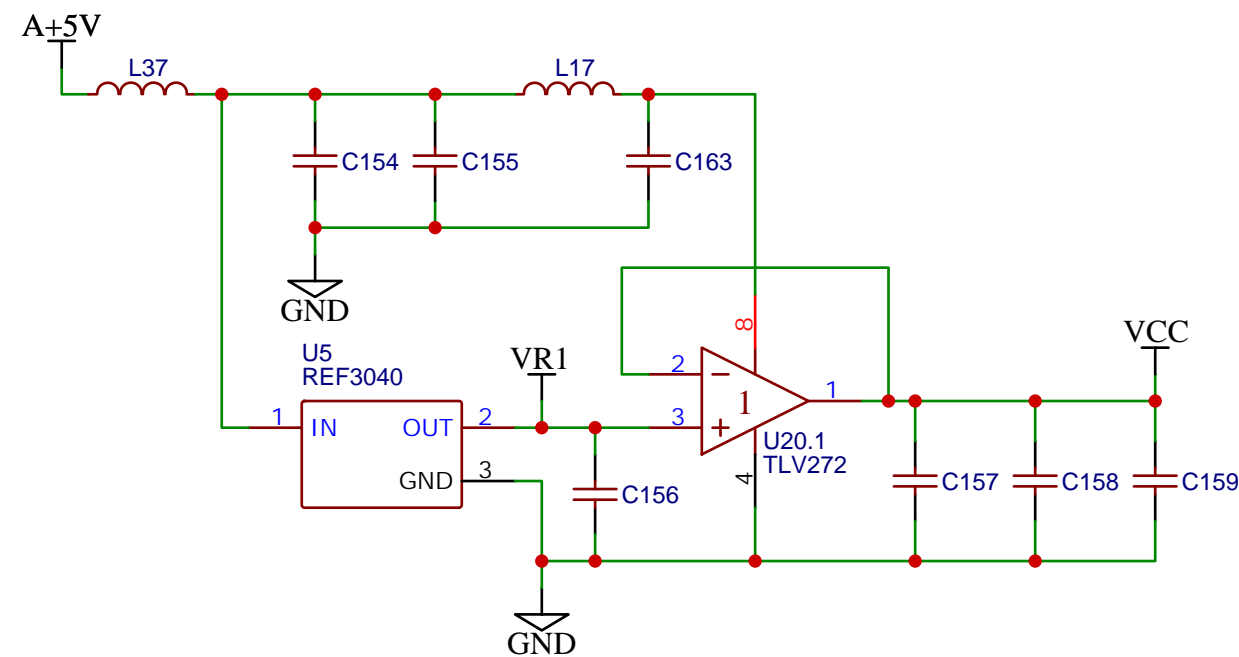
Waveform Generator And Trigger Input



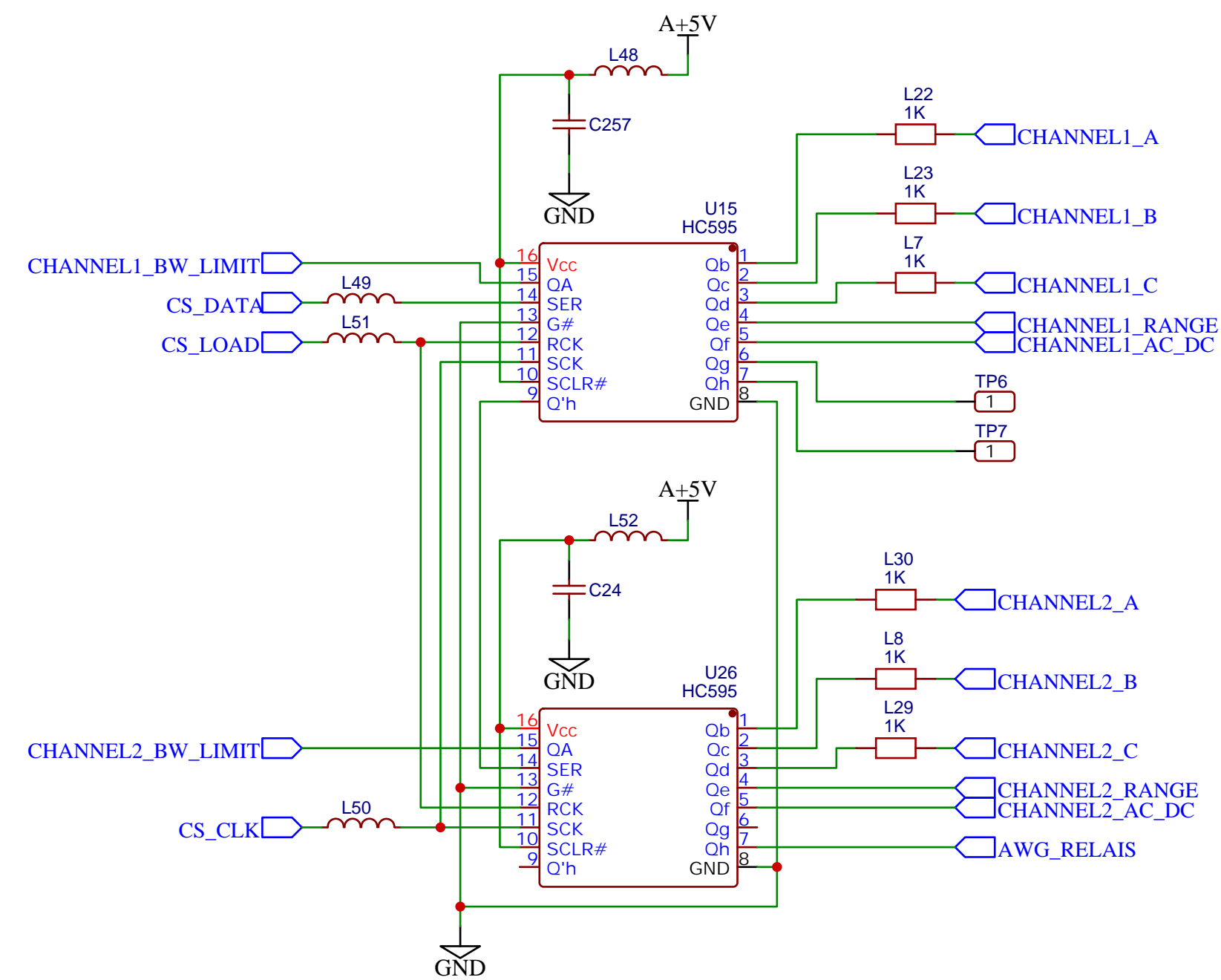
Probe Calibration Signal



Offset Signal Buffers Power Supply



Channel Control Signals



Offset Signals

