Need a special component and footprint for this.
Has to have 6 rows of pads, with 22 pads on the right and left side for the 1000 mil ESP32-S3-WROOM board
The schematic symbol for this needs 89 pins

S2F Has a 700mil spacing on the inner pins
Option is to overlay the pads such that the 3V3 of the S2F is on top of the 3V3 of the 4 super mini boards
Only issue is with the C6 super mini that GPIO8 is overlapped by the EN pin and should thus not be used.
The USB connectors can than be on the same distance from the carrier board edge

C6_SM These four super mini boards all have a 600mil spacing on the pins, and only overlap with the S2F with one row of pins
S3_SM The three supply pins are on the same location so no problem in overlap.
H2_SM The GPIO matrix allows for full re allocation of the peripherals in software.
C3_SM

C6 UART 3x, SPI 3x, I2C 1x, I2S 1x
C3 UART 2x, SPI 3x, I2C 1x, I2S 1x
H2 UART 2x, SPI 3x, I2C 1x, I2S 1x
S3 UART 3x, SPI 4x, I2C 2x, I2S 2x, SDIO host 1x
S2 UART 2x, SPI 4x, I2C 2x, I2S 1x

ESP32 UART 3x, SPI 4x, I2C 2x, I2S 1x, SDIO host 1x