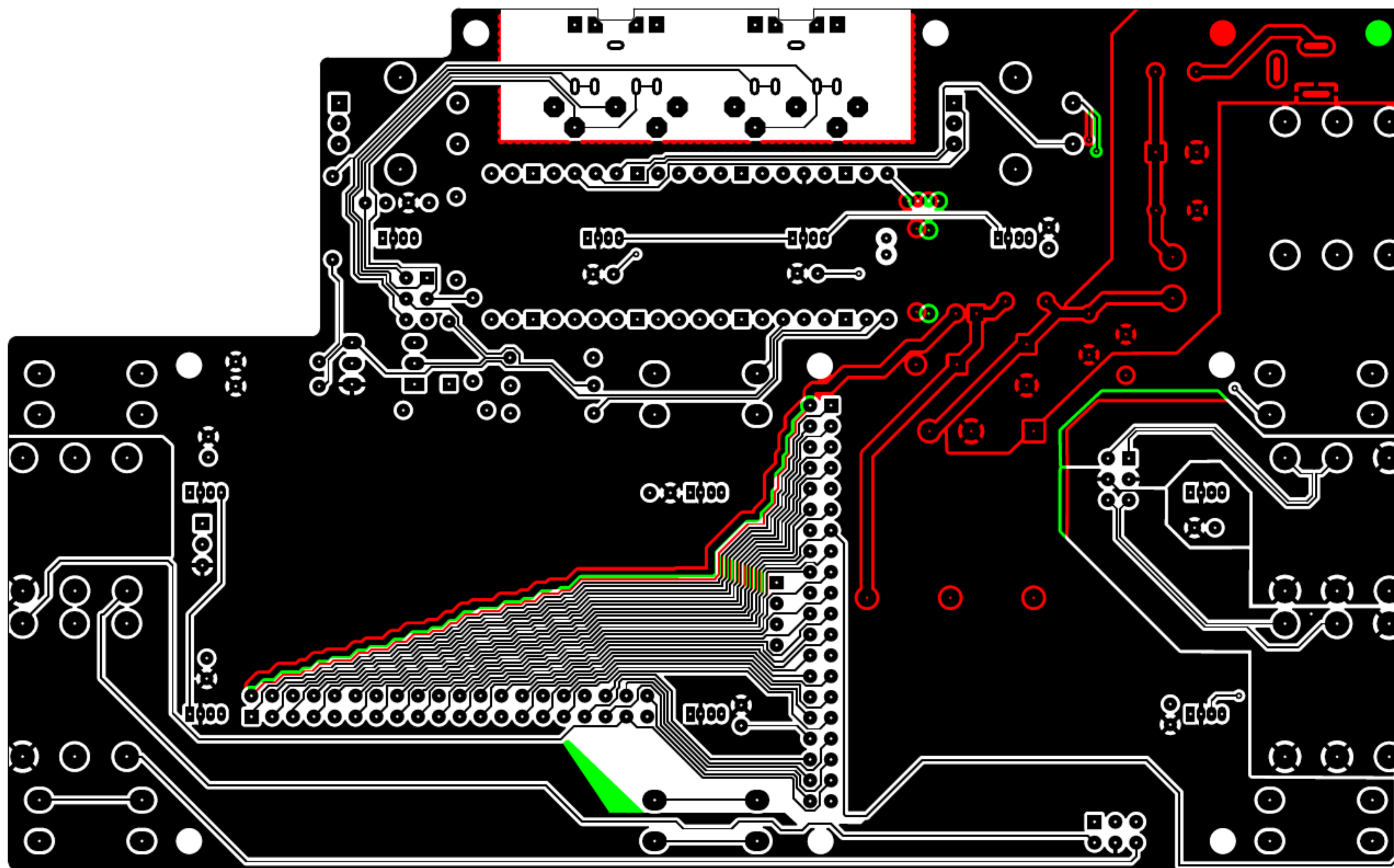
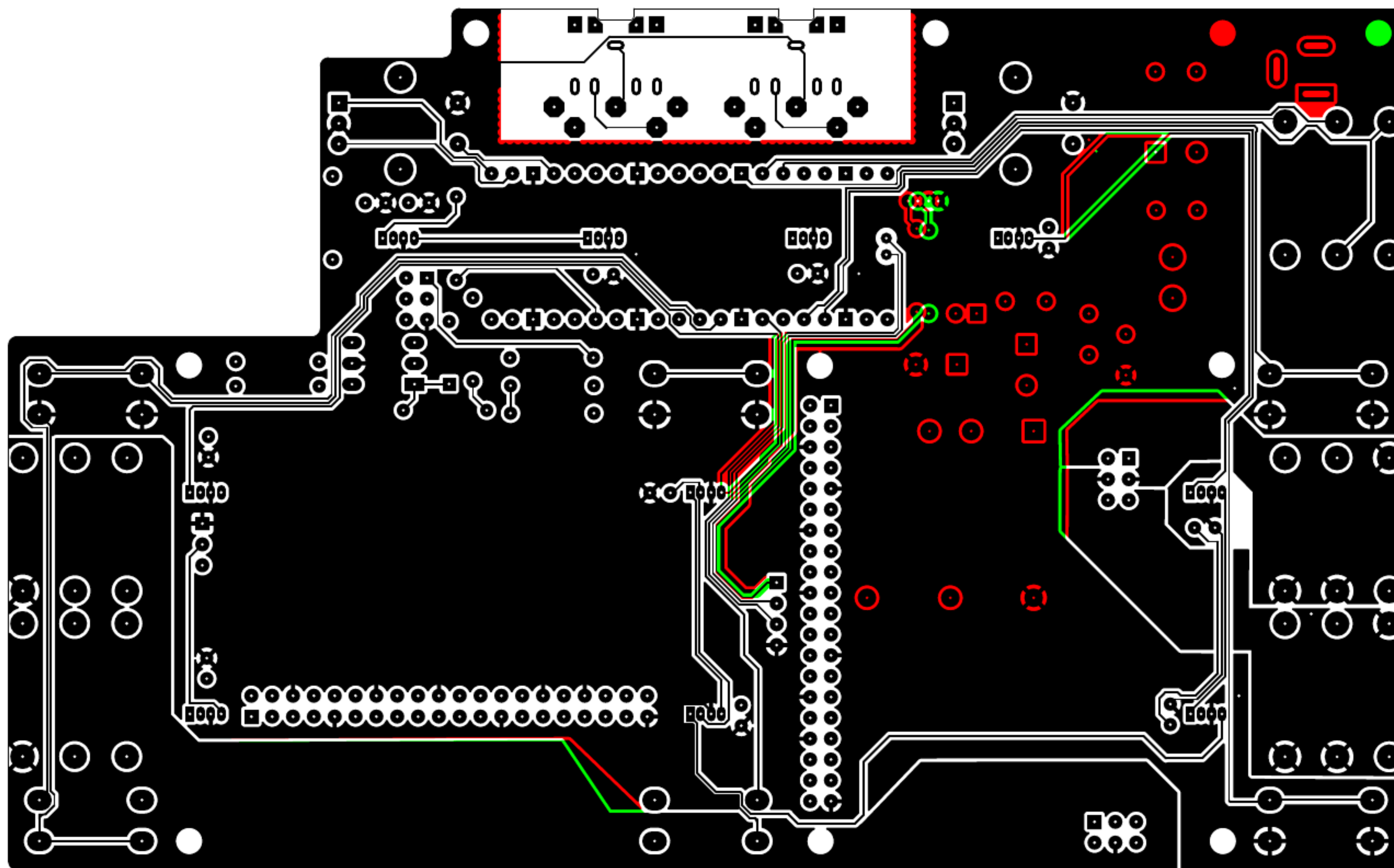
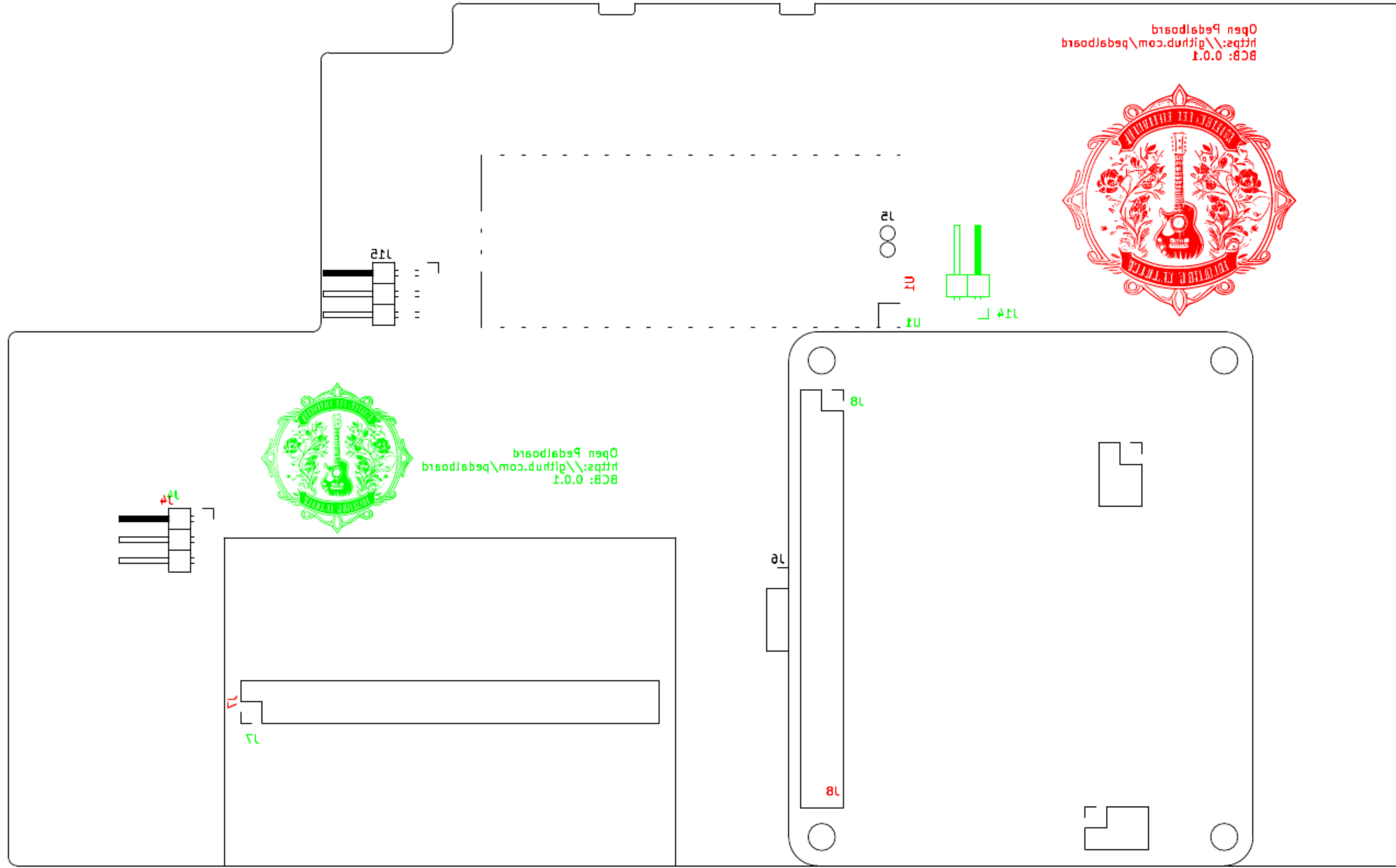


Layer: F.Cu

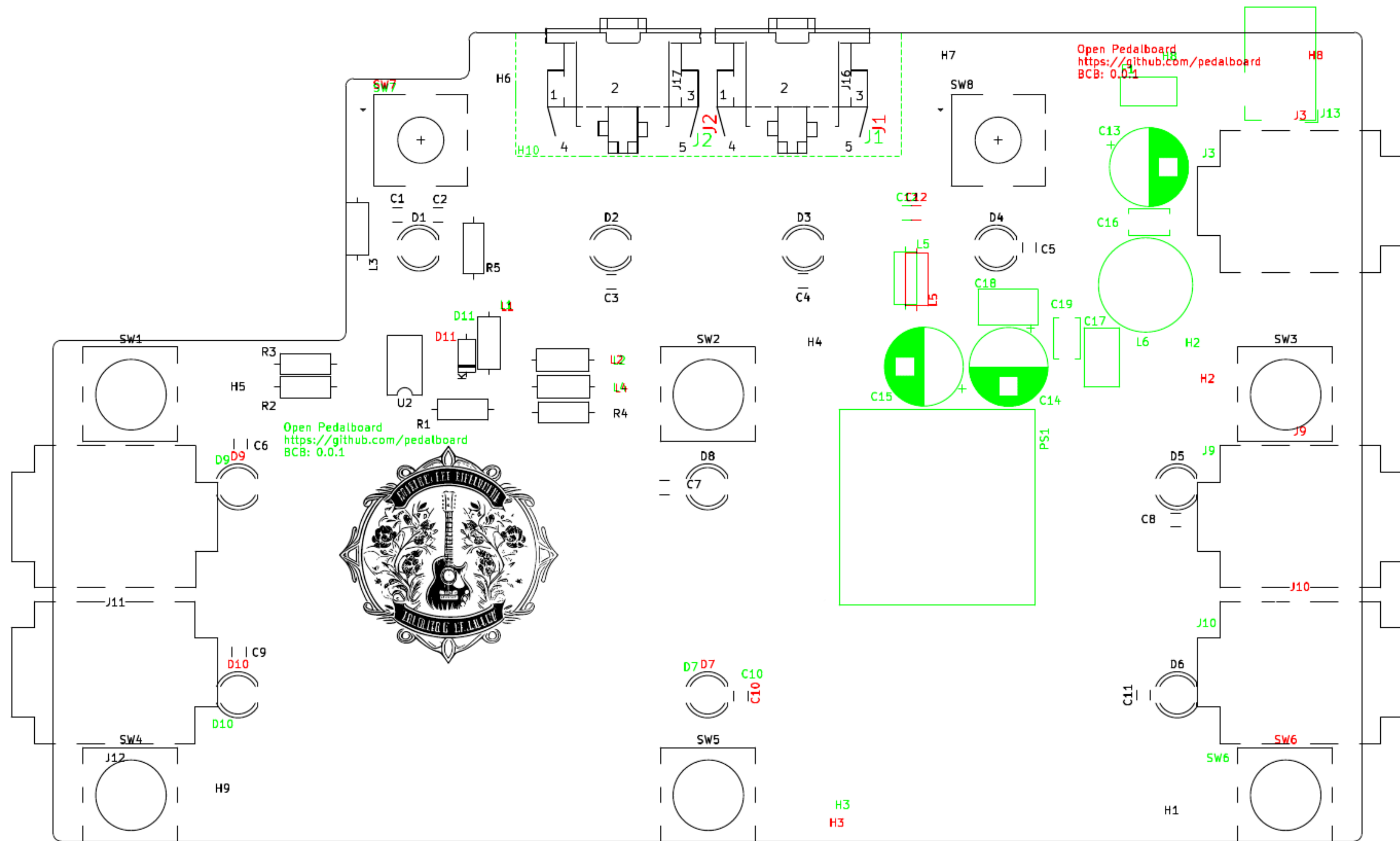


Layer: B.Cu

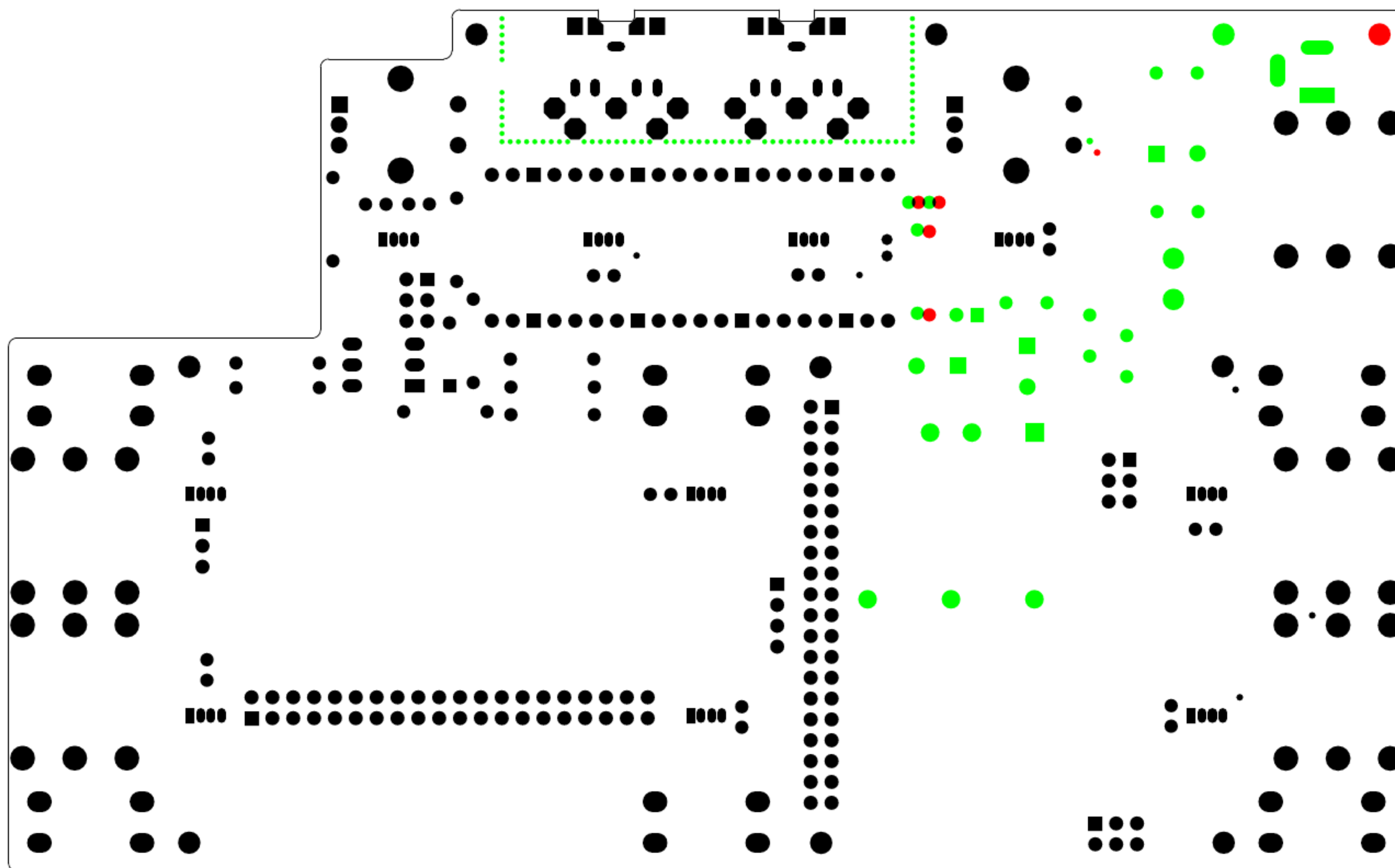




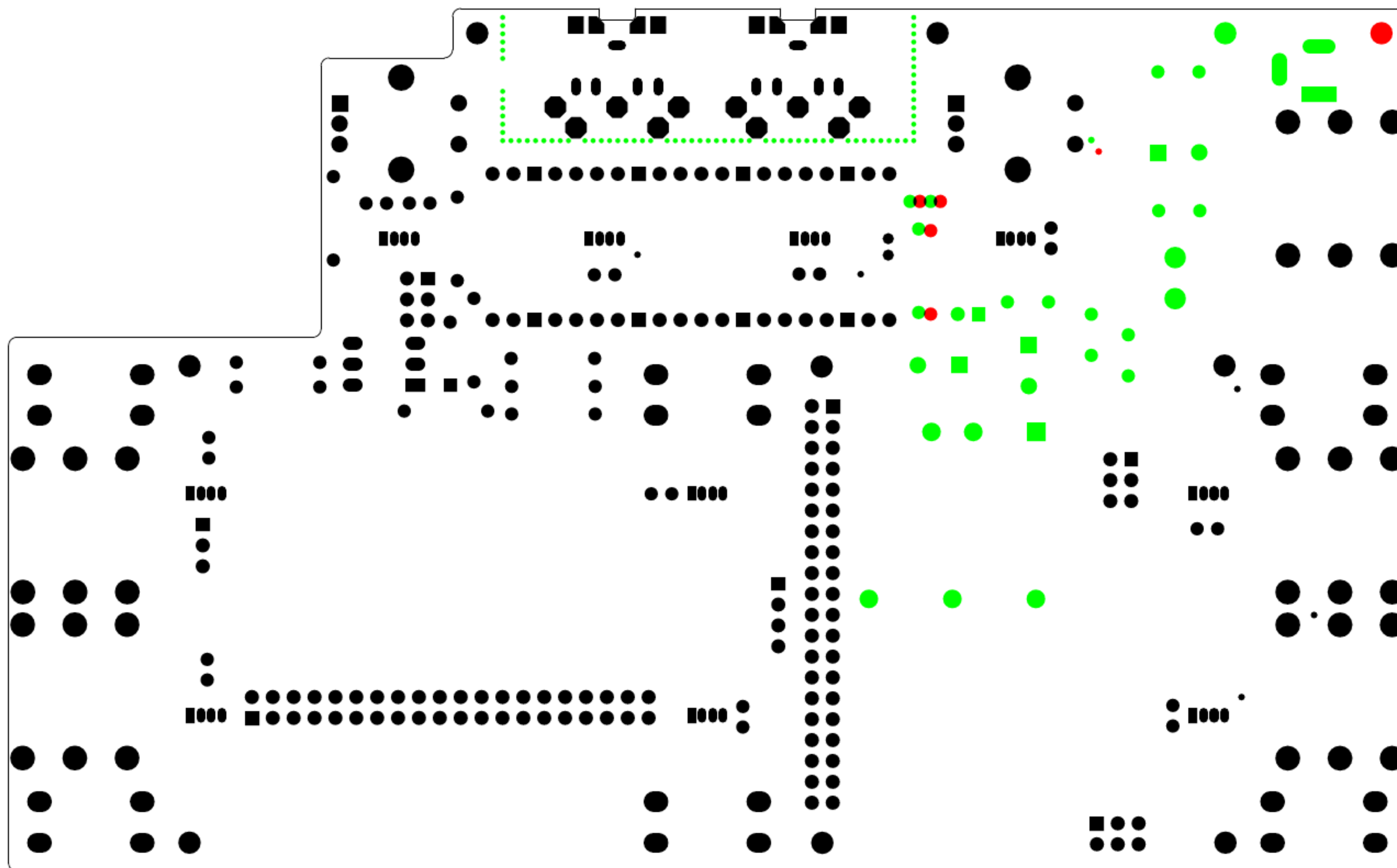


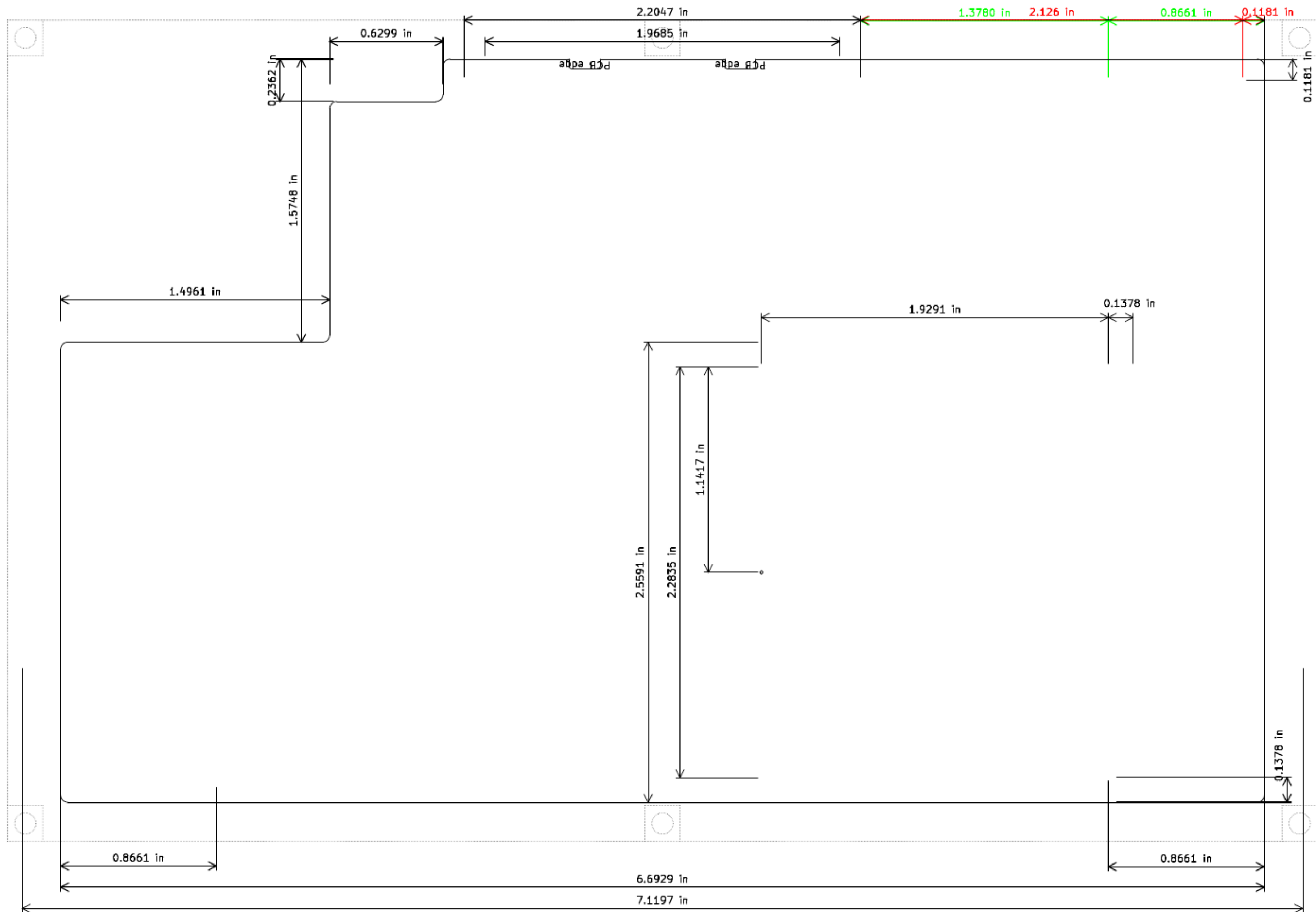


Layer: B.Mask



Layer: F.Mask





The image shows a PCB layout for the STM32F407VGT6 microcontroller. The chip is represented by a central rectangle with pins extending from its top and bottom edges. The top pins are labeled as follows:

- GP16, GP17, GND, GP18, GP19, GP20, GP21, GND, GP22, RUN, GP26, GP27, AGND, GP28, ADC\_VREF, VV3, VV3\_EN, GND, VSYS, VBUS

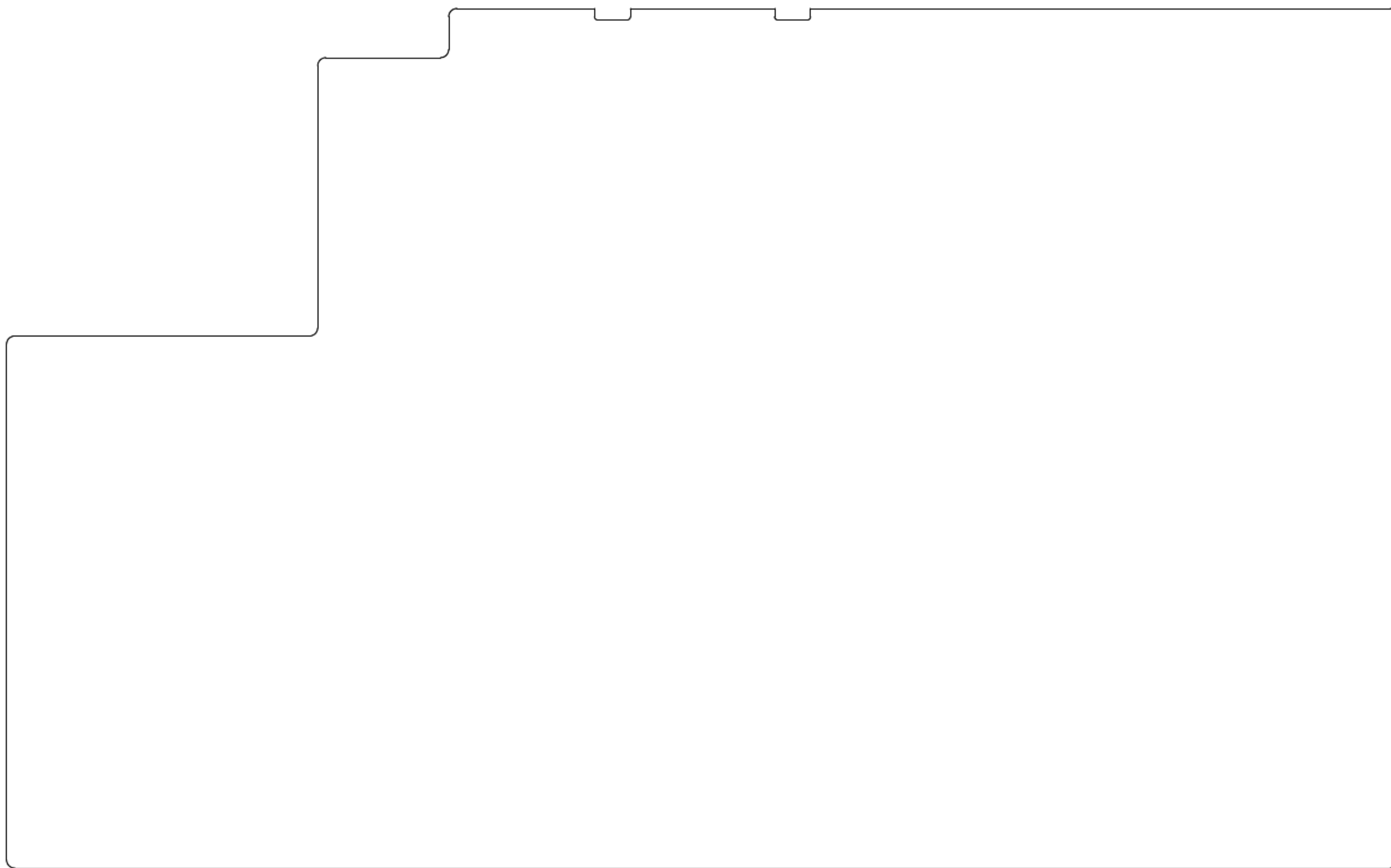
The bottom pins are labeled as follows:

- GP15, GP14, GND, GP13, GP12, GP11, GP10, GND, GP9, GP8, GP7, GP6, GND, GP5, GP4, GP3, GP2, GND, GP1, GP0

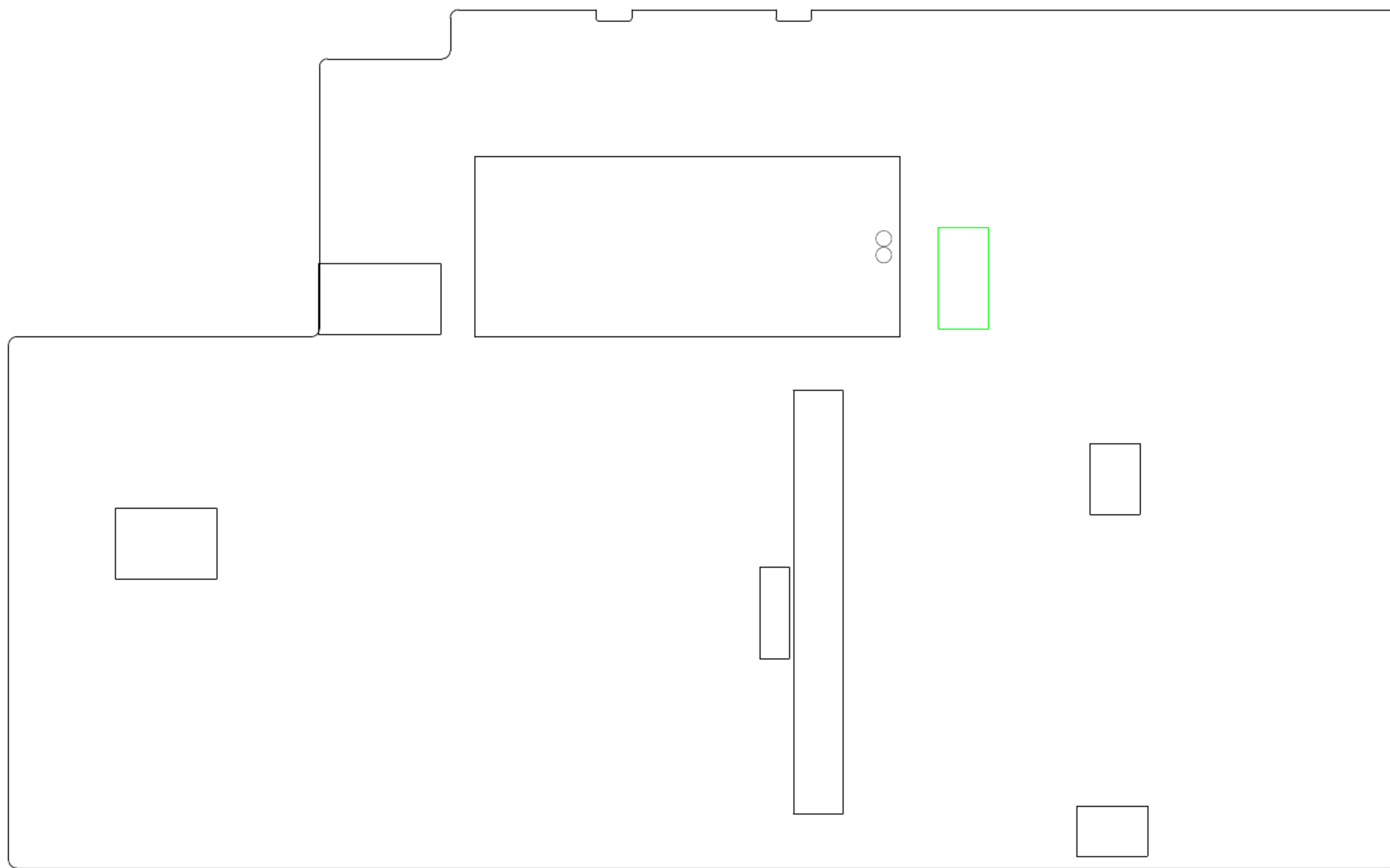
Two specific pins are highlighted with red circles: GP2 (bottom) and GP28 (top). The PCB layout includes a top layer (blue) and a bottom layer (green). A large rectangular area on the left side of the chip is labeled "SWCLK" and "SWDIO", indicating the location of the SWD pins. A large rectangular area on the right side of the chip is labeled "VDD", indicating the location of the power pins. The PCB is populated with various components, including resistors, capacitors, and a microcontroller unit (MCU). The MCU is located in the center of the board, with its pins connected to the PCB pads. The board is populated with various components, including resistors, capacitors, and a microcontroller unit (MCU). The MCU is located in the center of the board, with its pins connected to the PCB pads. The board is populated with various components, including resistors, capacitors, and a microcontroller unit (MCU). The MCU is located in the center of the board, with its pins connected to the PCB pads.



Layer: Edge.Cuts



Layer: B.CrtYd (B.Courtyard)



Layer: F.CrtYd (F.Courtyard)

