



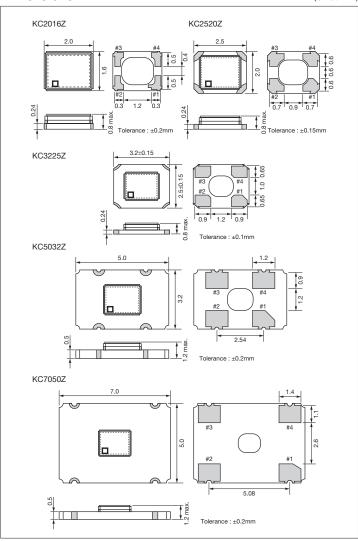
CMOS/ 1.8V, 2.5V, 3.3V/ 2.0×1.6, 2.5×2.0, 3.2×2.5, 5.0×3.2, 7.0×5.0mm



Dimensions

(Unit:mm) Recommended Land Patterns

(Unit : mm)



Recommen	ded Land Patterns	(Unit : mm)
KC2016Z	KC2520Z	
0.6	1.2	960
KC3225Z		
	960	
KC5032Z	2.5	
	Z Z Z	
KC7050Z	5.08	
	2.5	
	1.8	

Pad Connections		
#1	INH	
#2	Case GND	
#3	Output	
#4	Vcc	

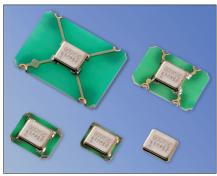
INH Function		
Pad1	Pad3 (Output)	
Open	Active	
"H" Level	Active	
"L" Level	High Z (No-Oscillation)	

Clock Oscillators Surface Mount Type Clock Z-Series "X" type (STD, Short LT type)





CMOS/ 1.8V, 2.5V, 3.3V / 2.0×1.6, 2.5×2.0, 3.2×2.5, 5.0×3.2, 7.0×5.0mm



Features

- Frequency Range 0.5 to 170 MHz
- CMOS Output
- Short Lead Time
- Heat resistant up to +125°C

Applications

• Consumer/ Networking/ Industrial/ Amuse

Table 1

Freq. Tol.		Operating Temperature	Note		
Code	× 10 ⁻⁶	Range (°C)	Note		
S	± 30				
U	± 25	-10 to +70			
W	± 20				
G	± 50	-40 to +85	For additional		
Н	± 30				
J	± 25				
K	± 20		stabillity, please		
L	± 15		contact us.		
6	± 50				
5	± 30	-40 to + 103			
X	± 100	-40 to +125			
Z	± 50				
9	± 30				

How to Order

 $\mathsf{KC} \underline{\square} \underline{\square} \underline{\square} \underline{Z} \quad \underline{25.0000} \quad \underline{C} \quad \underline{1} \quad \underline{\square} \quad \underline{X} \quad \underline{00}$ 3 4 5 6 7

①Series

KC2016Z	2016 Size	KC2520Z	2520 Size
KC3225Z	3225 Size	KC5032Z	5032 Size
KC7050Z	7050 Size		

②Output Frequency (25.0000 : 25MHz) ③Output Type (C : CMOS)

Supply Voltage

(1:1.8V/ 2.5V/ 3.3V Compatible)

⑤Frequency Tolerance (See Table 1)

©Symmetry/ INH Function

STD 45/ 55%

OIndividual Specification (STD Specification is "00".)

Packaging Tape&Reel

KC7050Z/ KC5032Z	1000 pcs./ reel
KC3225Z/ KC2520Z/ KC2016Z	2000 pcs./ reel

Specifications

Item	Symbol	Conditions	Min.	Max.	Unit		
Output Frequency Range	fo		0.5	170	MHz		
Frequency Tolerance	f_tol	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	See Table 1.				
Storage Temperature Range	T_stg		-55 150 °C		°C		
Operating Temperature Range	T_use		See Table 1.				
Max. Supply Voltage	_		-0.3	4.5	V		
Supply Voltage	Vcc		1.71	3.63	V		
		0.5≤fo<5MHz	_	5.2			
		5≤fo<15MHz	_	5.8			
		15≤fo<30MHz	_	6.2			
		30≤fo<50MHz	_	6.8			
Current Consumption	Icc	50≤fo≤60MHz		6.8			
(Noload/ 1.71≤Vcc≤2.25)		60 <fo<75mhz< td=""><td>_</td><td>9</td><td></td></fo<75mhz<>	_	9			
		75≤fo<105MHz	_	10			
		105≤fo<130MHz	_	10.5			
		130≤fo<160MHz	_	11.5			
		160≤fo≤170MHz	_	12.5			
		0.5≤fo<5MHz	_	5.5	_		
		5≤fo<15MHz		6			
		15≤fo<30MHz	_		6.5		
Current Consumption		30≤fo<50MHz 50≤fo≤60MHz		7.2 7.4			
	lcc	60 <fo<75mhz< td=""><td></td><td>10</td><td>mA</td></fo<75mhz<>		10	mA		
(Noload/ 2.25 < Vcc ≤ 2.8)		60<10<75MHz 75≤fo<105MHz		11.5			
				12.5			
		105≤fo<130MHz — 130≤fo<160MHz —		14.5			
		160≤fo≤170MHz	_	15			
		0.5≤fo<5MHz		5.8			
	lcc	5≤fo<15MHz		6.5			
		15≤fo<30MHz	 	7.3			
		30≤fo<50MHz		8			
Current Consumption		50≤fo≤60MHz	_	8.5			
(Noload/ 2.8 <vcc≤3.63)< td=""><td>60<fo<75mhz< td=""><td>_</td><td>12.5</td></fo<75mhz<></td></vcc≤3.63)<>		60 <fo<75mhz< td=""><td>_</td><td>12.5</td></fo<75mhz<>	_	12.5			
(14010dd/ 2.0 14cc=3.03)		75≤fo<105MHz	_	14.5			
		105≤fo<130MHz	_	15.5			
		130≤fo<160MHz	_	18			
		160≤fo≤170MHz		19.5			
Stand-by Current	I_std		_	5	μΑ		
Symmetry	SYM	@50% Vcc	45	55	%		
		Loaded/ 1.71≤Vcc≤2.25	_	4			
		0.5≤fo≤60MHz Loaded/ 2.25 <vcc≤2.8< td=""><td>_</td><td>3</td><td rowspan="2">ns</td></vcc≤2.8<>	_	3	ns		
Rise/ Fall Time	Tr/ Tf	Loaded/ 2.8 < Vcc ≤ 3.63	_	2.5			
(20% to 80% Output Level)	117 11	Loaded/ 1.71≤Vcc≤2.25	_	1.5	115		
		60 < fo ≤ 170 MHz Loaded / 2.25 < Vcc ≤ 2.8	_	1.3]		
		Loaded/ 2.8 < Vcc ≤ 3.63		1			
Low Level Output Voltage	Vol	IoL = 4mA		10% Vcc	V		
High Level Output Voltage	Voн	IOH = -4mA	90% Vcc		V		
Output Load (CMOS)	L_CMOS			15	pF_		
Low Level Input Voltage	VIL			30% Vcc	V		
High Level Input Voltage	ViH			200	V		
Disable Time	t_dis			200	ns		
Enable Time	t_ena	@Minimum aparating voltage to be 0 ass	_	5	ms		
Start-up Time	Time t_str @Minimum operating voltage to be 0 sec. — 5 ms				ms		



