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Introduction to Computers

Notebook - Lab 3

(1) State diagram		(3) Truth table - State transition function																																																	
		<table border="1"><thead><tr><th>s_1</th><th>s_0</th><th>x</th><th>s_1'</th><th>s_0'</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>0</td><td></td><td></td></tr><tr><td>0</td><td>0</td><td>1</td><td></td><td></td></tr><tr><td>0</td><td>1</td><td>0</td><td></td><td></td></tr><tr><td>0</td><td>1</td><td>1</td><td></td><td></td></tr><tr><td>1</td><td>0</td><td>0</td><td></td><td></td></tr><tr><td>1</td><td>0</td><td>1</td><td></td><td></td></tr><tr><td>1</td><td>1</td><td>0</td><td></td><td></td></tr><tr><td>1</td><td>1</td><td>1</td><td></td><td></td></tr></tbody></table>					s_1	s_0	x	s_1'	s_0'	0	0	0			0	0	1			0	1	0			0	1	1			1	0	0			1	0	1			1	1	0			1	1	1		
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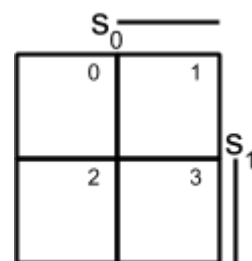
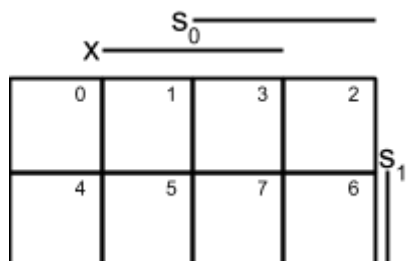
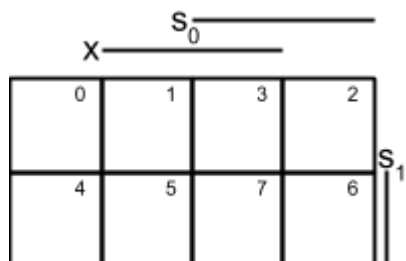


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(5) Karnaugh Maps



$S_1' =$	$S_0' =$	$Z =$
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Design

Define for each element and port the associated IC number and the corresponding pin