

WCAS 2012
Second Workshop on Circuits and System Design

August 30th to September 2nd, 2012
Brasília - Brazil

<http://chipinbsb.cic.unb.br/wcas.html>

The Second Workshop on Circuits and System Design - WCAS 2012 is devoted to the presentation and discussion of design experiences with a high degree of relevance in industrial and educational contexts, as well as innovative design methodologies and applications of specific design technologies in an industrial context.

The workshop is organized within two tracks: industrial and academic. Contributions should illustrate state-of-the-art designs, design methodologies or tools, which will provide viable solutions in tomorrow's silicon and embedded systems. Designs that achieve a specific record in terms of performance, power management or any other concrete advantage compared to the state-of-the-art for a given application domain should also be submitted to this workshop. Validated methodologies and techniques through demonstration are welcome.

The authors are invited to submit 6-page double column version of the paper. The accepted papers will be presented in poster sessions or in oral sessions.

The main idea of the workshop is to offer the chance (primarily to industry) of pointing out to the community real-life design and technology challenges that should be addressed in the short-to-medium term.

Important dates

- Submission deadline (6-page paper): June 3rd, 2012
- Notification of Acceptance: July 6th, 2012
- Camera-Ready Deadline (6-page paper): July 23rd, 2012

Topics of interest

The areas of interest include, but are not limited to:

- Industrial track:
 - A1 - Computing Systems
 - A2 - Communication, Consumer and Multimedia Systems
 - A3 - Transportation Systems
 - A4 - Medical and Healthcare Systems
 - A5 - Energy Generation, Recovery and Management Systems
 - A6 - Secure, Dependable and Adaptive Systems

- Academic Track:
 - B1 - Analog & RF & Mixed Signal
 - B2 - CAD, Verification and Test
 - B3 - Digital, Reconfigurable & Applications
 - B4 - SoC, NoC, Embedded