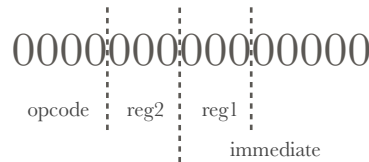


Codificação Assembly



Opcodes

| | |
|--------------------|---------|
| ADD reg2,reg1 | => 0001 |
| ADD reg2,immediate | => 0010 |
| SUB reg2,reg1 | => 0011 |
| SUB reg2,immediate | => 0100 |
| MOV reg2,reg1 | => 0101 |
| SWITH reg2. | => 0110 |