

ModelSim - INTEL FPGA STARTER EDITION 10.5b

File Edit View Compile Simulate Add Transcript Tools Layout Bookmarks Window Help

Layout: NoDesign ColumnLayout: AllColumns

Transcript

```
# Loading work.bloco_controle
VSI8> run -all
# 0:: Code[xxxxxxx] PC[ 0]
# 1:: Code[00110000] PC[ 0]
# Registradores :: Work[ 0] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 2:: Code[00110000] PC[ 1]
# Registradores :: Work[ 0] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 3:: Code[00100010] PC[ 1]
# Registradores :: Work[ 0] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 4:: Code[00100010] PC[ 2]
# Registradores :: Work[ 0] R1[ 0] R2[ 0] R3[00000001] R4[zzzzzzzz]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 5:: Code[00000000] PC[ 2]
# Registradores :: Work[ 0] R1[ 0] R2[ 0] R3[00000001] R4[zzzzzzzz]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 6:: Code[00000000] PC[ 3]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[zzzzzzzz]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 7:: Code[00110000] PC[ 3]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[zzzzzzzz]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 8:: Code[00110000] PC[ 4]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 9:: Code[01000000] PC[ 4]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 10:: Code[01000000] PC[ 5]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 11:: Code[00110000] PC[ 5]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 12:: Code[00110000] PC[ 6]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 13:: Code[00101000] PC[ 6]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 14:: Code[00101000] PC[ 7]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[zzzzzzzz]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 15:: Code[00000000] PC[ 7]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[zzzzzzzz]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 16:: Code[00000000] PC[ 8]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[zzzzzzzz]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 17:: Code[00110000] PC[ 8]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[zzzzzzzz]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 18:: Code[00110000] PC[ 9]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
# 19:: Code[00100001] PC[ 9]
# Registradores :: Work[ 2] R1[ 0] R2[ 0] R3[00000001] R4[00000000]
# MemoriaDados :: 0[ x] 1[ x] 2[ x] 3[ x] 4[ x] 5[ x] 6[ x] 7[ x] 8[ x] 9[ x] 10[ x] 11[ x] 12[ x]
```

Transcript | Code_MemoryProgram.v | Code_control.v | Code_Outhers.v | Code_REG.v | Library | Project | nRisc.v

Project : Processador_jaoc <No Design Loaded> <No Context>