

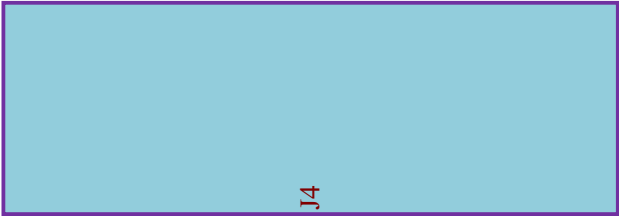
A

B

C

D

Control Board



J4

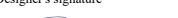



Power Board



J2



Designer's signature 	Sheet title: Main			<div>Tecnologías de Circuitos Impresos Grado en Ingeniería de Tecnologías de Telecomunicación ETSIT. University of Granada C/ Periodista Daniel Saucedo Aranda, s/n, 18014, Granada, Granada, Spain</div> 
	Project title: Balancing Robot			
Supervisor's signature 	Desginer: Pedro Javier Belmonte Miñano			
	Date: 29/12/2021	Revision: v 1	Sheet 1 of 1	