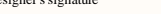


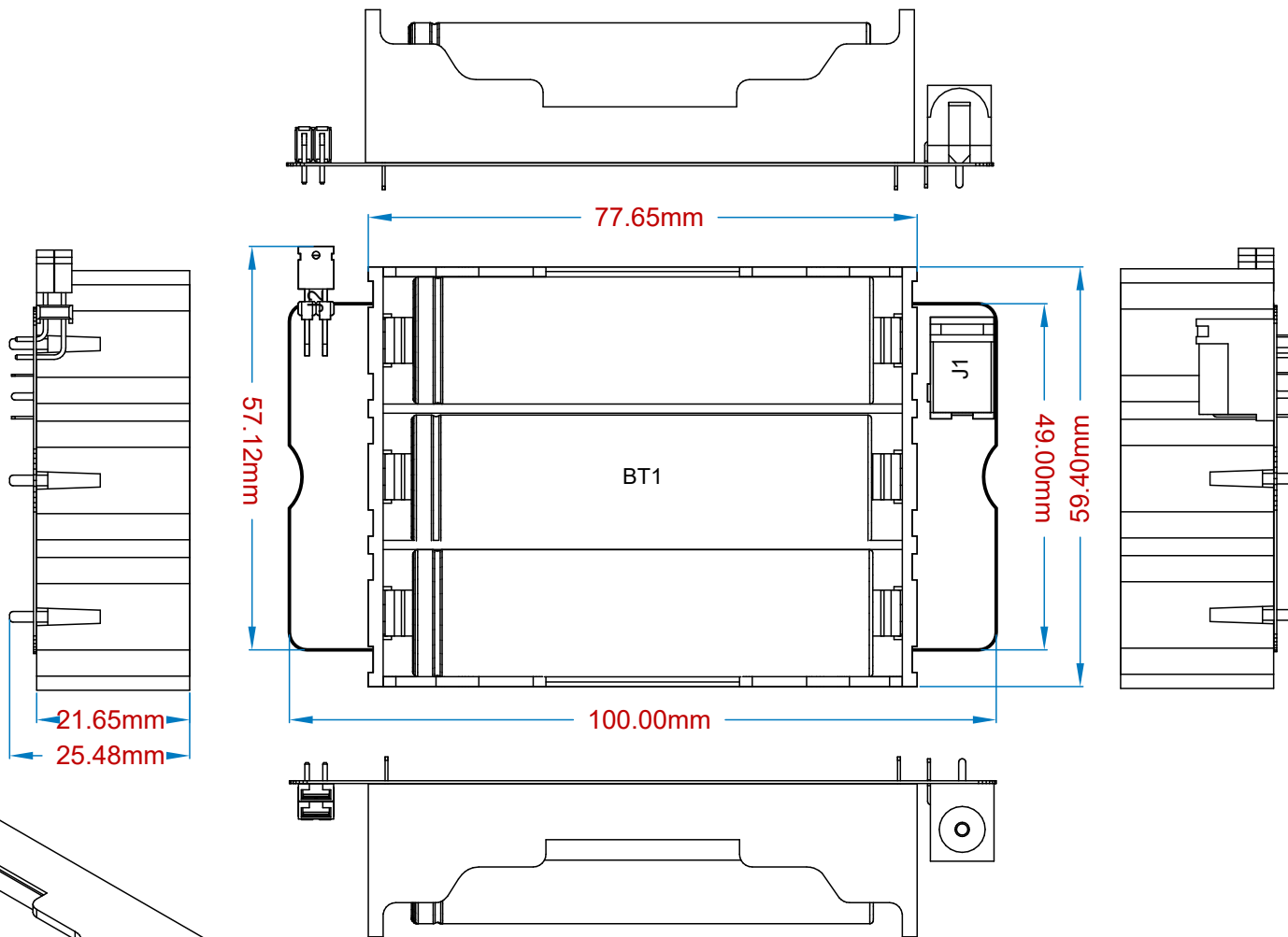
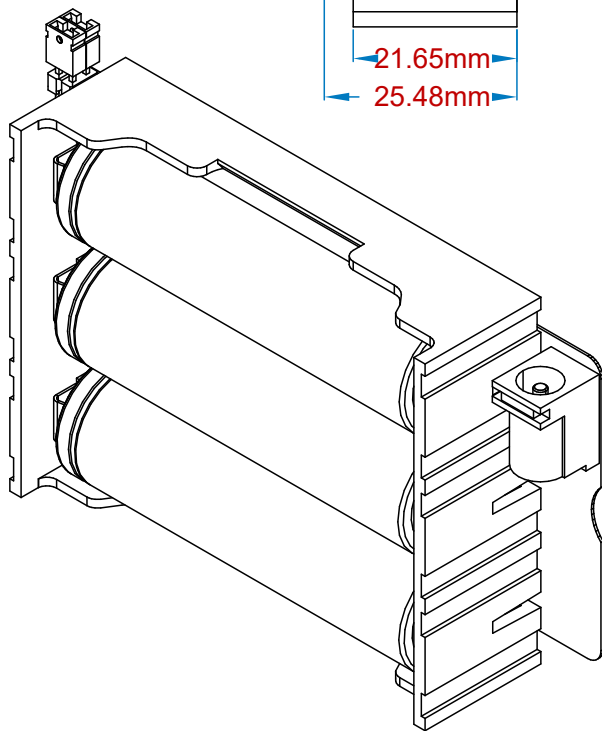





Designer's signature 	Sheet title: <b>Bottom Board</b>			<div>Tecnologías de Circuitos Impresos Grado en Ingeniería de Tecnologías de Telecomunicación ETSIT. University of Granada C/ Periodista Daniel Saucedo Aranda, s/n, 18014, Granada, Granada, Spain</div> 
	Project title: <b>Balancing Robot</b>			
Supervisor's signature 	Designer: <b>Pedro Javier Belmoonte Miñano</b>			
	Date: <b>29/12/2021</b>	Revision: <b>V 1</b>	Sheet 1 of 1	



#### Bill Of Materials

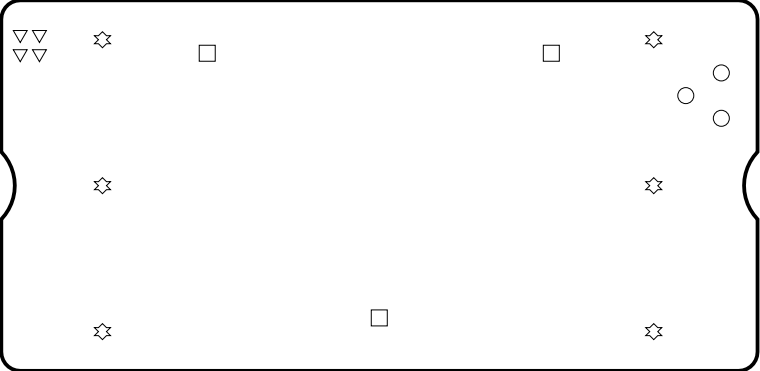
Line #	Designator	Comment	Quantity
1	BT1	BK-18650-PC6	1
2	J1	PJ-102AH	1
3	J2	PIN HEADER 2x1	1
4	P1	BMS3S10A	1

Designer's signature: 	Sheet title:	Board Assembly View					Tecnologías de Circuitos Impresos Grado en Ingeniería de Tecnologías de Telecomunicación ETSIT. University of Granada C/ Periodista Daniel Saucedo Aranda, s/n, 18014, Granada, Granada, Spain	
	Project title:	PowerBoard.PrjPcb						
Supervisor's signature: 	Designer:	Pedro Javier Belmonte Miñano					Sheet 1 of 2	
	Size: A4	Scale: 1:1	Date: 29/12/2021	Revision: V 1				

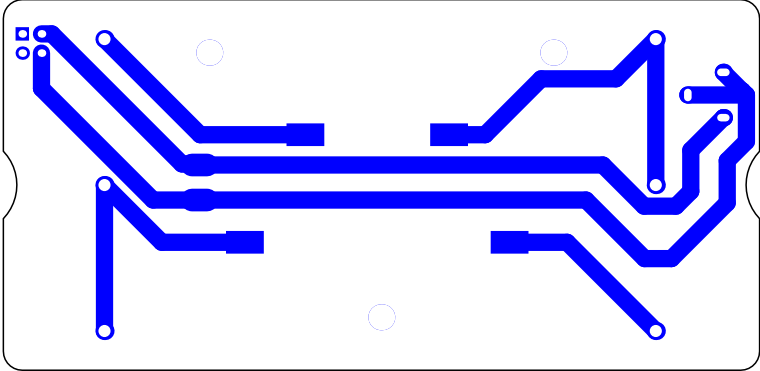
Top Layer



Drill Drawing View



Bottom Layer



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
○	3	1.00mm	Plated	
▽	4	1.02mm	Plated	
☆	6	1.60mm	Plated	
□	3	3.44mm	Plated	
	16 Total			

