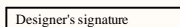




Designer's signature 	Sheet title: Bottom Board			<div>Tecnologías de Circuitos Impresos Grado en Ingeniería de Tecnologías de Telecomunicación ETSIT. University of Granada C/ Periodista Daniel Saucedo Aranda, s/n, 18014, Granada, Granada, Spain</div> 
	Project title: Balancing Robot			
Supervisor's signature 	Desginer: Pedro Javier Belmonte Miñano			
	Date: 16/01/2022	Revision: V 1	Sheet 1 of 1	

A

B

C

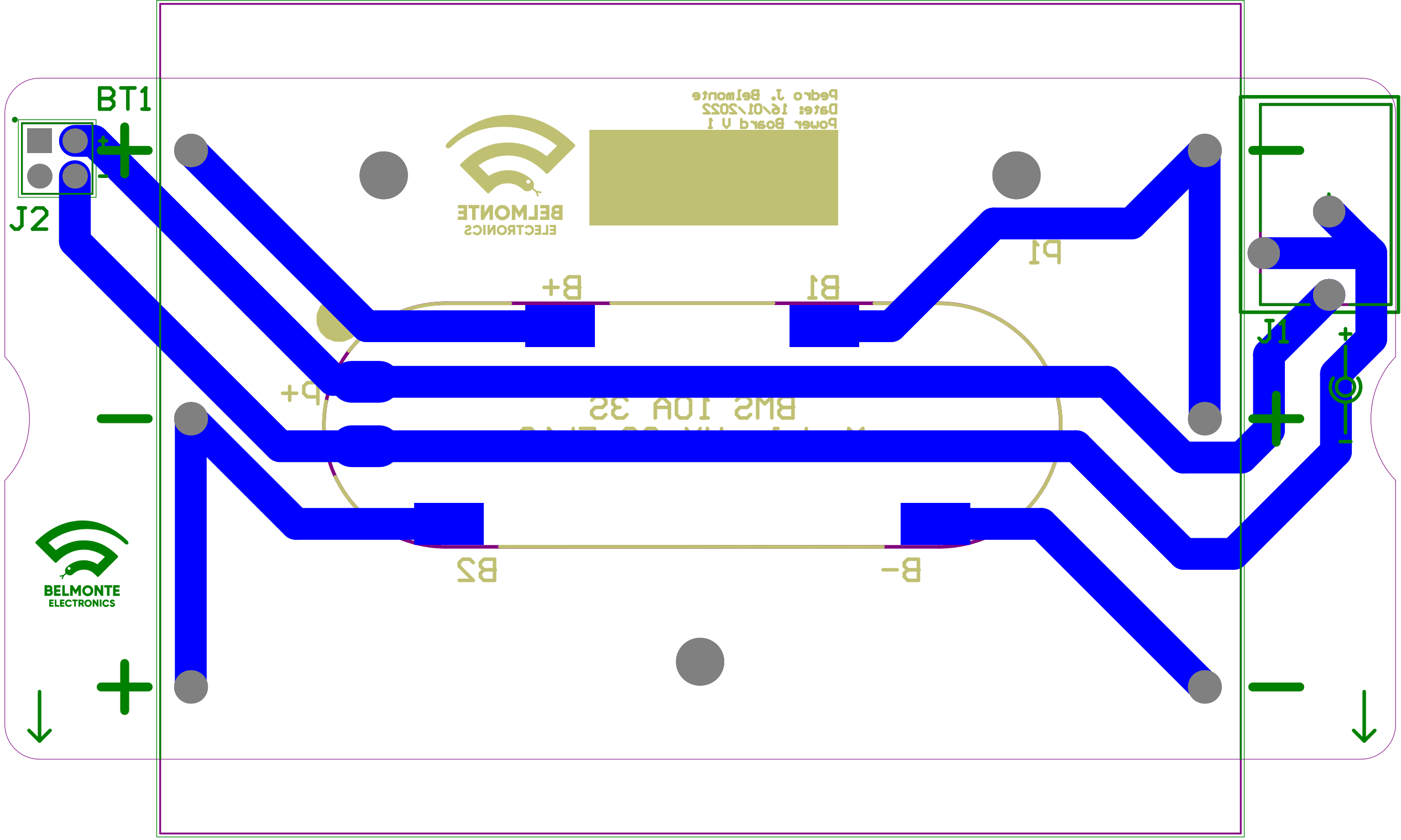
D




A

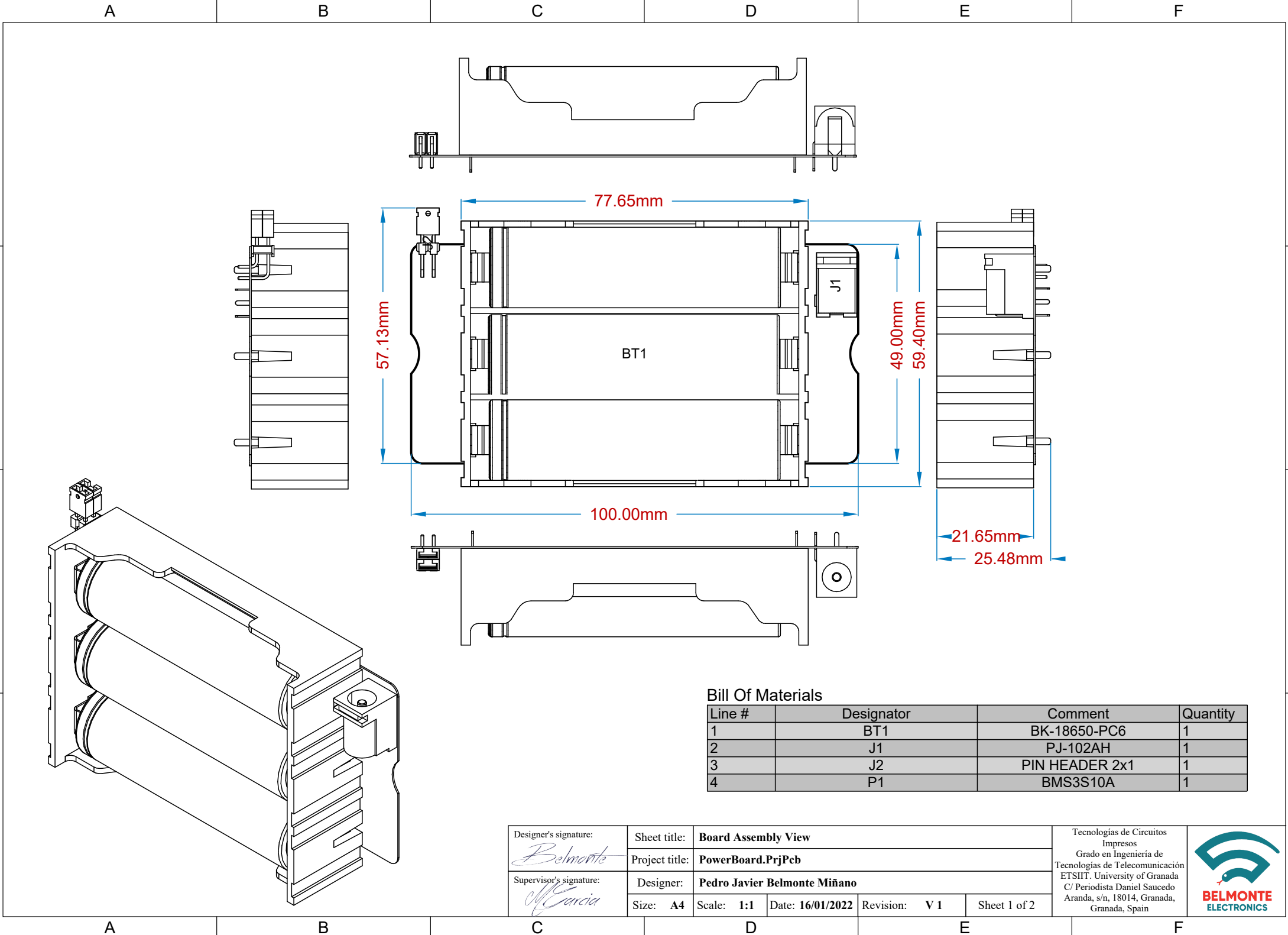
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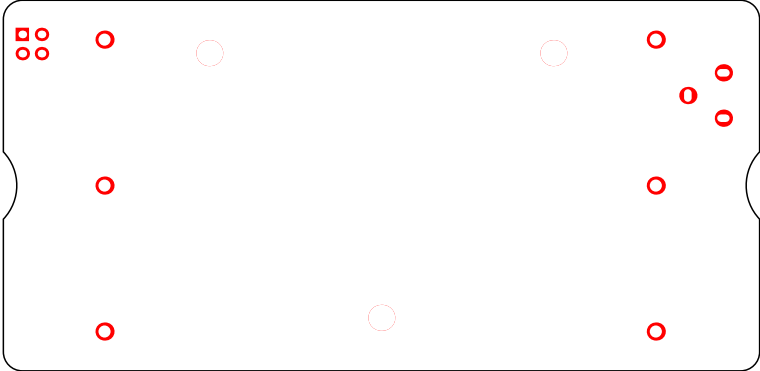
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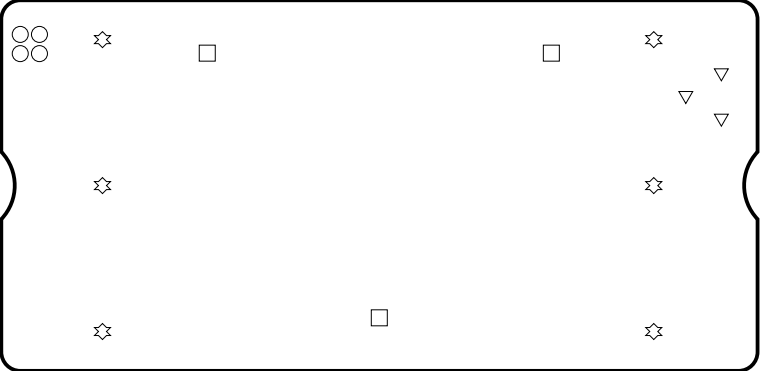
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	Project title: Balancing Robot			
Supervisor's signature:	Designer: Pedro Javier Belmonte			
	Date: 16-01-2020	Revision: V1	Sheet 1	



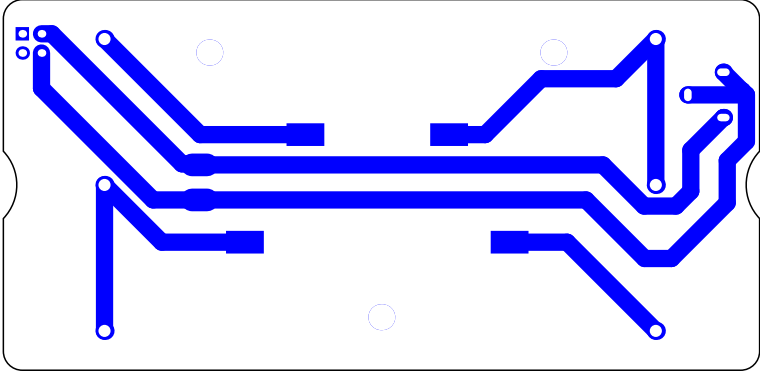
Top Layer



Drill Drawing View



Bottom Layer



Drill Table

Symbol	Count	Hole Size	Plated
▽	3	1.00mm	Plated
○	4	1.02mm	Plated
☆	6	1.60mm	Plated
□	3	3.44mm	Plated
16 Total			

