

ExercSC (3) - Figura 7.11 - Datapath

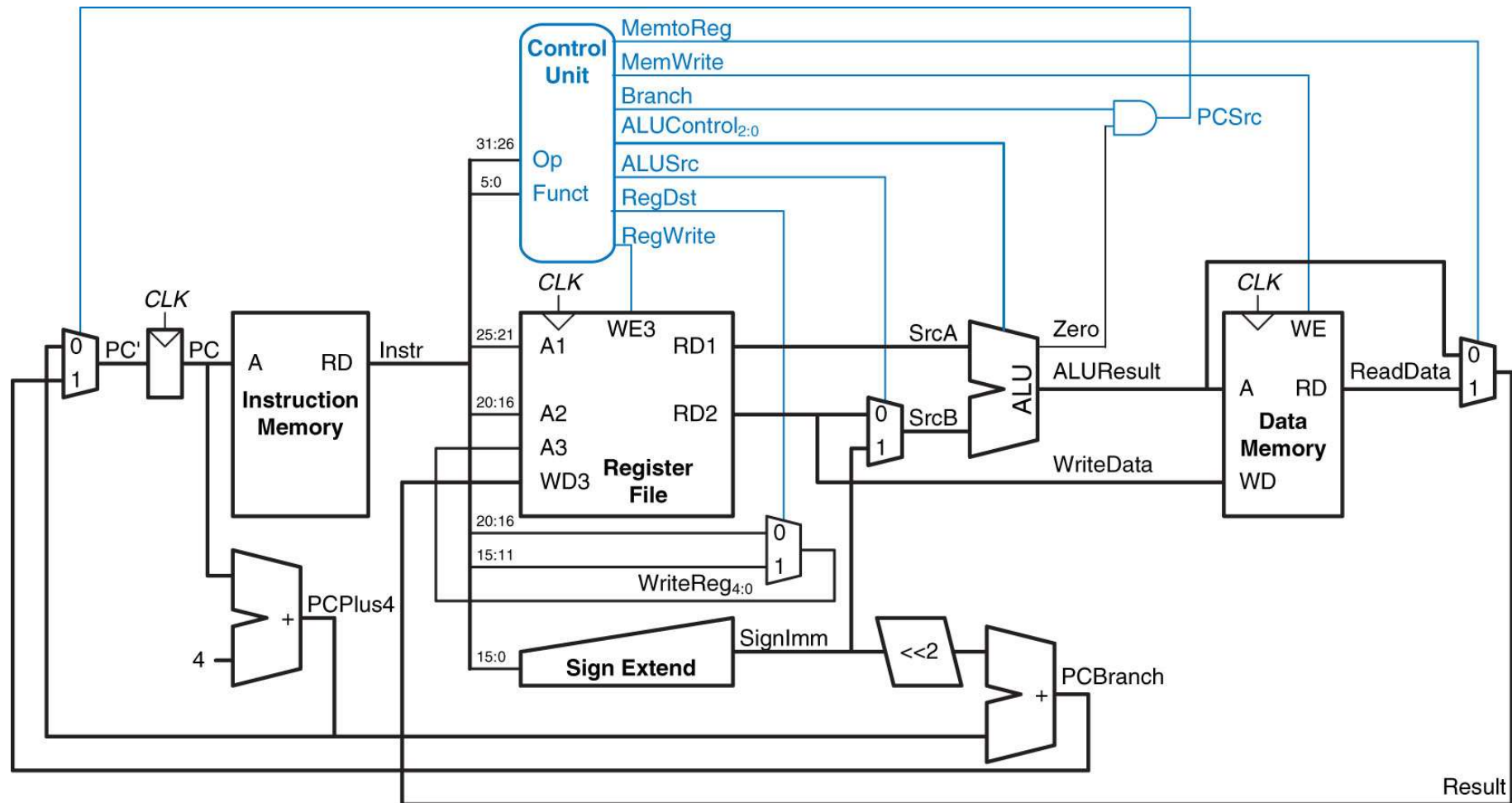


Figura 7.11 Processador MIPS Single-cycle completo

