

ESP-Ariane first-stage boot loader

Scanning device tree...

[probe] sld,mriq\_stratus.0 registered

Address : 0x60010000

Interrupt : 6

\*\*\*\*\* sld,mriq\_stratus.0 \*\*\*\*\*

memory buffer base-address = 0xa0100b60

ptable = 0xa0100d10

nchunk = 1

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Generate input...

-> Non-coherent DMA

Start...

Done

validating...

... PASS