

```
ncsim> Info: dma-controller: reset done  
  
Info: testbench: reset done  
  
Info: testbench: load memory completed  
  
Info: testbench: config done  
  
Info: testbench: @310 ns BEGIN - mriq  
  
Info: testbench: @4330 ns END - mriq  
  
Info: testbench: dump memory completed  
  
Info: testbench: validation passed!
```