

Start End		Monday, April 2, 2018 Riddel Hall, Queen's University Belfast
7:30	8:00	Registration Opens (CR1 & 2 Lobby)
8:00	9:30	Workshop: FastPath 2018 International Workshop on Performance Analysis of Machine Learning Systems (Conference Room 1) Tutorials: Improving Energy-Efficiency in Arm-based Systems (Lecture Room 3) Benchmarking modern online services on real and simulated hardware (Conference Room 2) GenSim: A Toolset for Binary Translation and Emulation (Lecture Room 2)
9:30	10:00	Tea and Coffee (CR1 & 2 Lobby)
10:00	12:00	Morning workshops and tutorials continue
12:00	13:30	Lunch (Courtyard)
13:30	15:00	Workshop: FastPath 2018 International Workshop on Performance Analysis of Machine Learning Systems (Conference Room 1) Tutorials: Cache-aware Roofline Model: Performance, Power and Energy-Efficiency Modeling of Parallel Processors (Lecture Room 1) Lift - performance portable code generation on parallel accelerators (Syndicate Room 6)
15:00	15:30	Tea and Coffee (CR1 & 2 Lobby)
15:30	17:00	Afternoon workshops and tutorials continue

Start End		Tuesday, April 3, 2018 Riddel Hall Conference Rooms 1 & 2, Queen's University Belfast
7:30	8:00	Registration Opens (CR1 & 2 Lobby)
8:00	9:30	Welcome and Keynote I <i>Session Chair: Bronis de Supinski, LLNL</i> Reconciling Software to a Heterogeneous World Mike O'Boyle, <i>University of Edinburgh</i>
9:30	10:00	Tea and Coffee (CR1 & 2 Lobby)
10:00	12:00	Paper Session I: Best Paper Nominees - <i>Session Chair: Thomas Wenisch, University of Michigan</i> Behind the Scenes: Memory Analysis of Graphical Workloads on Tile-based GPUs Germán Ceballos, Andreas Sembrant (Uppsala University), Trevor E. Carlson (National University of Singapore), David Black-Schaffer (Uppsala University) Performance Analysis of NVMe SSD-based All-flash Array Systems Young Tack Jin, Sungjoon Ahn (CircuitBlvd), Sungjin Lee (DGIST) Doppio: I/O-Aware Performance Analysis, Modeling and Optimization for In-Memory Computing Framework Peipei Zhou (University of California, Los Angeles), Zhenyuan Ruan (University of California, Los Angeles), Zhenman Fang (UCLA/Xilinx), Megan Shand, David Roazen (Broad Institute), Jason Cong (University of California, Los Angeles)

		MAPS: Understanding Metadata Access Patterns in Secure Memory Tamara Silbergleit Lehman, Andrew D. Hilton, Benjamin C. Lee (Duke University)
12:00	13:30	Lunch (Courtyard)
13:30	15:00	Paper Session II: Energy and Power <i>Session Chair: Mark Hempstead, Tufts University</i> Hardware-Validated CPU Performance and Energy Modelling Matthew J. Walker (University of Southampton), Sascha Bischoff, Stephan Diestelhorst (ARM), Geoff Merrett, Bashir Al-Hashimi (University of Southampton) Micro-Viruses for Fast System-Level Voltage Margins Characterization in Multicore CPUs George Papadimitriou, Athanasios Chatzidimitriou, Manolis Kaliorakis, Yannis Vastakis, Dimitris Gizopoulos (University of Athens) DORA: Optimizing Smartphone Energy Efficiency and Web Browser Performance under Interference Daves Shingari (Arizona State University/Marvell), Akhil Arunkumar, Benjamin Gaudette, Sarma Vrudhula, Carole-Jean Wu (Arizona State University)
15:00	15:30	Tea and Coffee (CR1 & 2 Lobby)
15:30	17:00	Paper Session III: Data Centers and Interconnects <i>Session Chair: Andrew Hilton, Duke University</i> Proctor: Identifying and Mitigating Interference in Shared Datacenters Ram Srivatsa Kannan, Mike Laurenzano, Animesh Jain, Lingjia Tang, Jason Mars (University of Michigan) Extensible Flit-Level Simulation of Large-Scale Interconnection Networks Nic McDonald, Adriana Flores, Al Davis (Hewlett-Packard Enterprise), Mikhail Isaev (Georgia Institute of Technology), John Kim (Korea Advanced Institute of Technology), Doug Gibson (Hewlett-Packard Enterprise) Performance Implications of NoCs on 3D-Stacked Memories: Insights From the Hybrid Memory Cube Ramyad Hadidi, Bahar Asgari, Jeffrey Young, Burhan Ahmad Mudassar, Kartikay Garg, Tushar Krishna, Hyesoon Kim (Georgia Institute of Technology)
17:00	18:00	Poster Session (outside Conference Rooms 1 & 2 at Riddel Hall) <ul style="list-style-type: none"> Characterizing the Runtime Effects of Object-Oriented Workloads on GPUs Mengchi Zhang, Roland Green, Timothy G. Rogers (Purdue University) PowerSensor 2: a Fast Power Measurement Tool John W. Romein, Bram Veenboer (Netherlands Institute for Radio Astronomy) HMCSP: Reducing Transaction Latency of CSR-based SPMV in Hybrid Memory Cube Cheng Qian (National University of Defense Technology), Bruce Childers (Univ. of Pittsburgh), Libo Huang and Zhiying Wang (National University of Defense Technology) Evaluating Memory Performance of Emerging Scale-Out Applications Using C-AMAT Qi Yu, Libo Huang (National University of Defense Technology)

		<ul style="list-style-type: none"> • Shifting the barrier: Extending the boundaries of the BarrierPoint methodology Miguel Tairum Cruz, Sascha Bischoff and Roxana Rusitoru (ARM) • Algorithmic Performance-Accuracy Trade-off in 3D Vision Applications Bruno Bodin (University of Edinburgh), Luigi Nardi (Stanford), Harry Wagstaff (University of Edinburgh), Paul Kelly (ICL), Michael O'Boyle (University of Edinburgh) • Impact of System Resources on Performance of Deep Neural Network Parijat Dube and Zehra Sura (IBM)
18:00	18:15	Bus to Belfast City Hall
18:15	onward	Tour, Drinks Reception, and Dinner (City Hall)

Start End		Wednesday, April 4, 2018 Riddel Hall Conference Rooms 1 & 2, Queen's University Belfast
7:30	8:00	Registration Opens
8:00	9:30	Welcome and Keynote II <i>Session Chair: Dimitris Nikopoulos, Queen's University Belfast</i>