

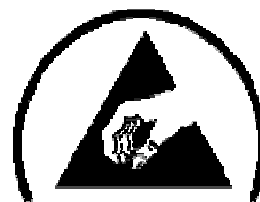
RailClamp -- Low Capacitance TVS Diode Array

Brightking's UDT26A05L05 are low capacitance TVS arrays designed to protect high speed data interfaces. This device has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from overvoltage caused by ESD (electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients).

The device has a typical capacitance of only 3pF (typ.).

This means it can be used on circuits operating in excess of 1GHz without signal attenuation.

The UDT26A05L05 will meet the immunity requirement of IEC61000. Level 4. (15kV air, 8kV contact discharge).

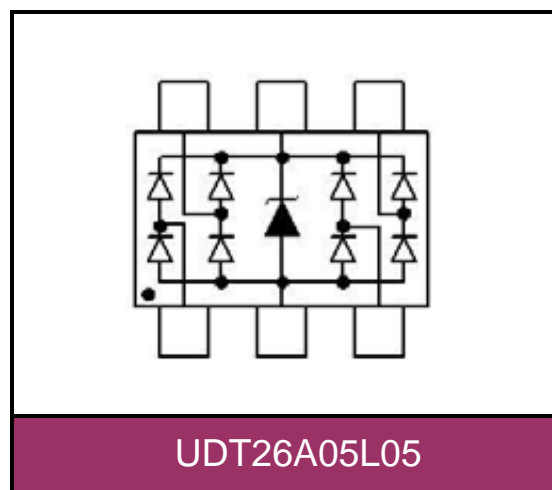


HBM : $\pm 8\text{kV}$
Air Mode : $\pm 15\text{kV}$



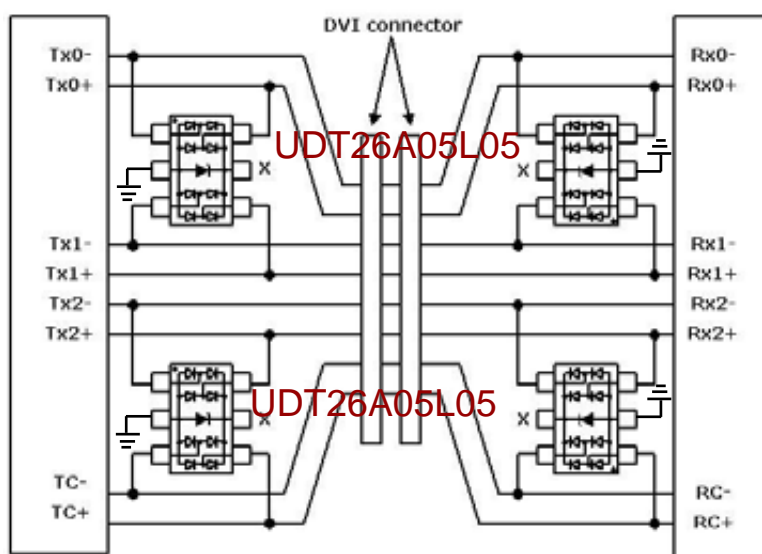
SPECIFICATION FEATURES

- IEC61000-4-2 ESD 15kV Air, 8kV contact compliance
- SOT-23 6L surface mount package
- Array of surge rated diodes with internal TVS Diode
- Peak power dissipation of 350W under 8/20 μs waveform
- Protects four I/O lines & power line to ground
- Low leakage current
- Low capacitance and clamping voltages
- Solid-state silicon avalanche technology
- Lead Free/ RoHS Compliant
- Solder Reflow Temperature:
Pure-Tin - Sn, 260-270°C
- Weight 16 milligrams (Approximate)
- Flammability rating UL 94V-0



APPLICATIONS

- USB Power and Data line Protection
- 10/100/1000 Ethernet
- Digital Video Interface(DVI)
- I²C Bus Protection
- WAN/LAN Equipment
- ISDN S/T Interface
- Microcontroller Input Protection
- Portable Electronics
- SIM Ports
- IEEE 1394 Firewire Ports



DVI Data Line Protection

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Pulse Power (8/20μs waveform)	P_{PP}	350	W
ESD Voltage (HBM Contact)	V_{ESD}	±8	kV
ESD Voltage (AIR Contact)		±15	
Storage & Operating Temperature Range	T_{STG}, T_J	-55~+150	°C

ELECTRICAL CHARACTERISTICS (T_J=25°C)

UDT26A05L05 (Marking :B 05B OR 054)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse Stand-Off Voltage	V_{WRM}				5	V
Reverse Breakdown Voltage	V_{BR}	$I_{BR}=1mA$	6			V
Reverse Leakage Current	I_R	$V_R=5V$			5	μA
Clamping Voltage (tp=8/20μs)	V_C	$I_{PP}=1A$			9.8	V
Clamping Voltage (tp=8/20μs)	V_C	$I_{PP}=10A$			12	V
Clamping Voltage (tp=8/20μs)	V_C	$I_{PP}=25A$			20	V
Off State Junction Capacitance	C_J	0Vdc, f=1MHZ between I/O pins and GND		3		pF

TYPICAL CHARACTERISTICS CURVES

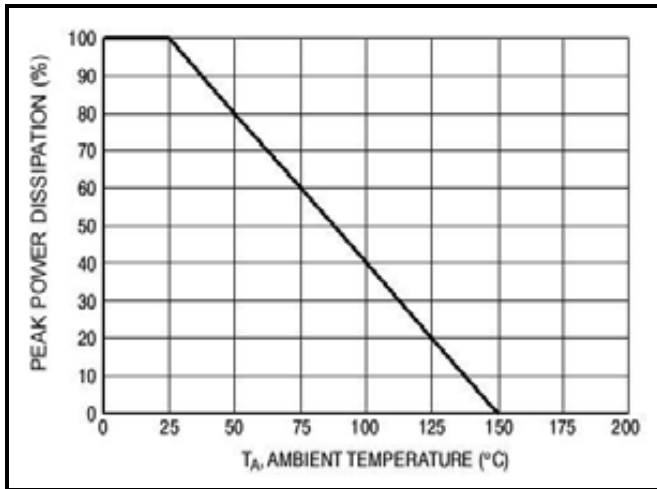


Figure 1. Power Derating Curve

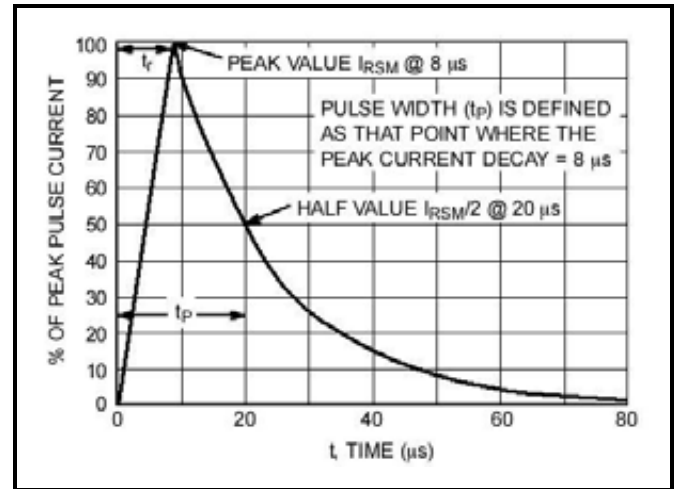


Figure 2. 8/20 μ s Pulse Waveform

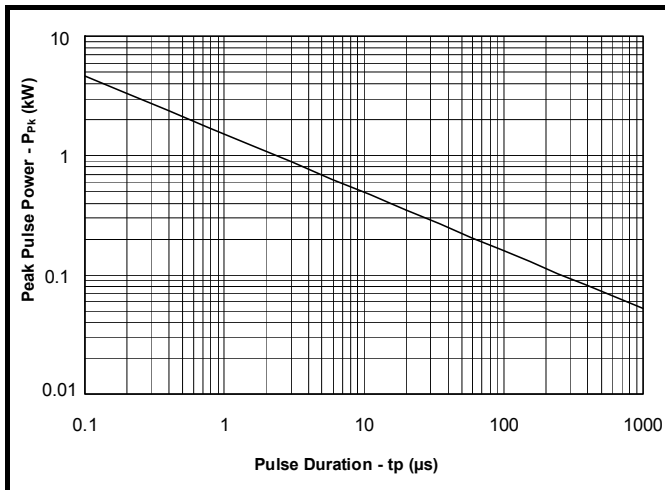


Figure 3. Non-Repetitive Peak Pulse vs. Pulse Time

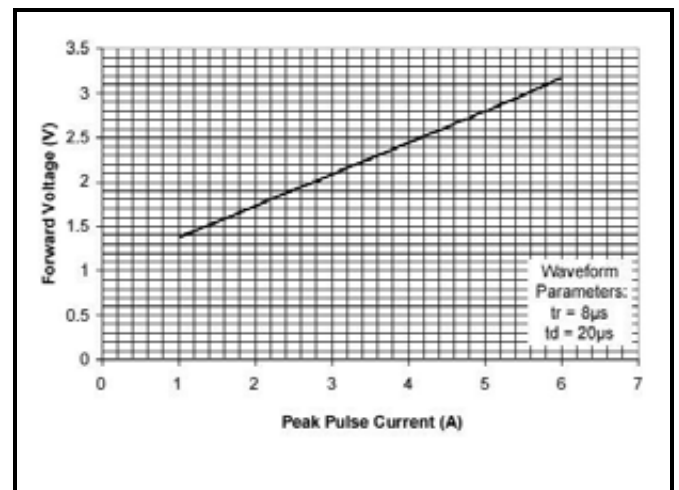


Figure 4. Forward Voltage vs. Forward Current

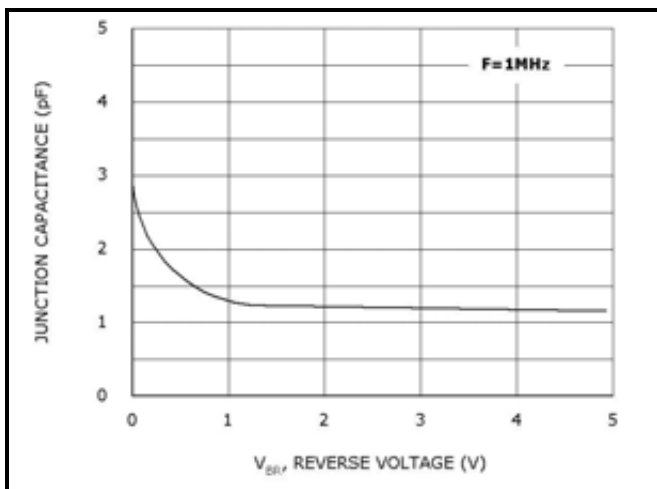


Figure 5. Capacitance vs. Reverse Voltage

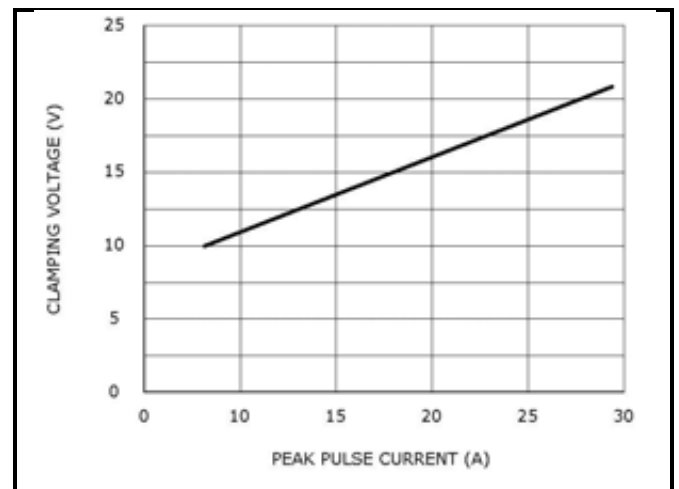


Figure 6. Clamping voltage vs Peak Pulse Current

PACKAGE AND SUGGESTED PAD LAYOUT DIMENSION

SOT-26

