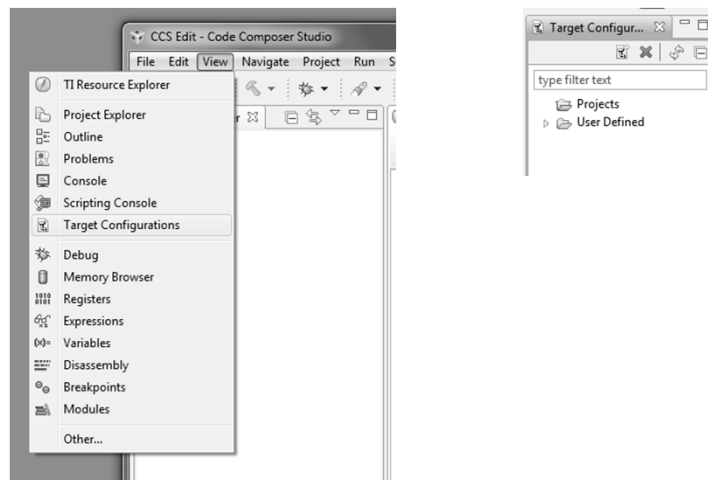
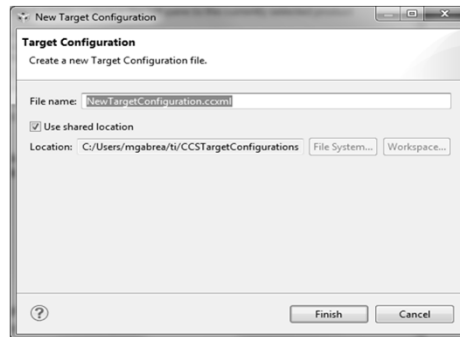
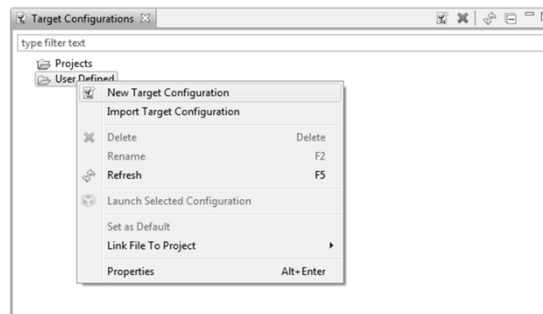


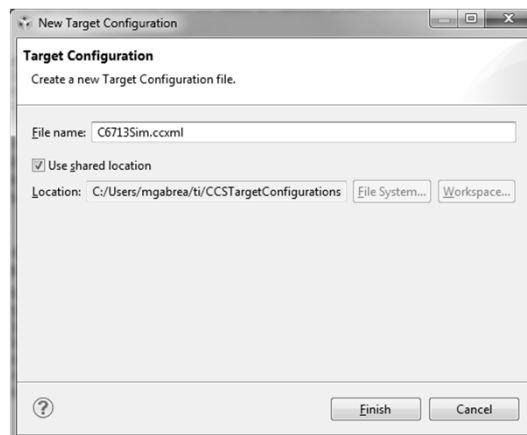


Code Composer Studio v5 – Ex01a



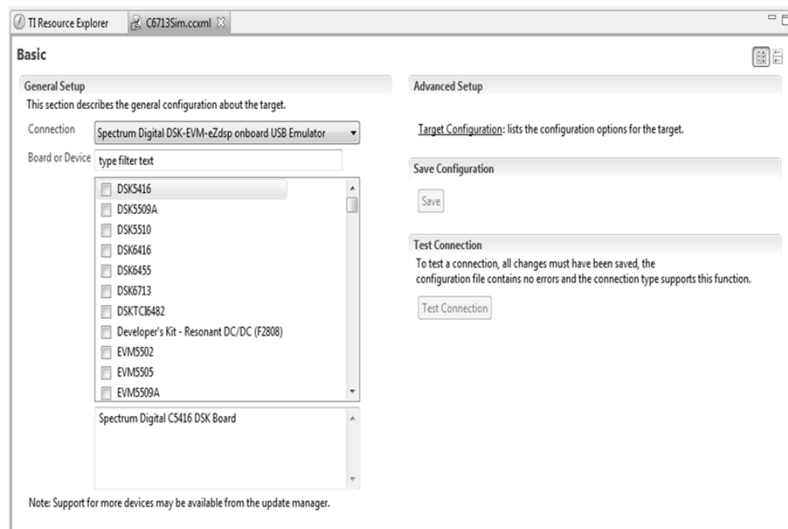


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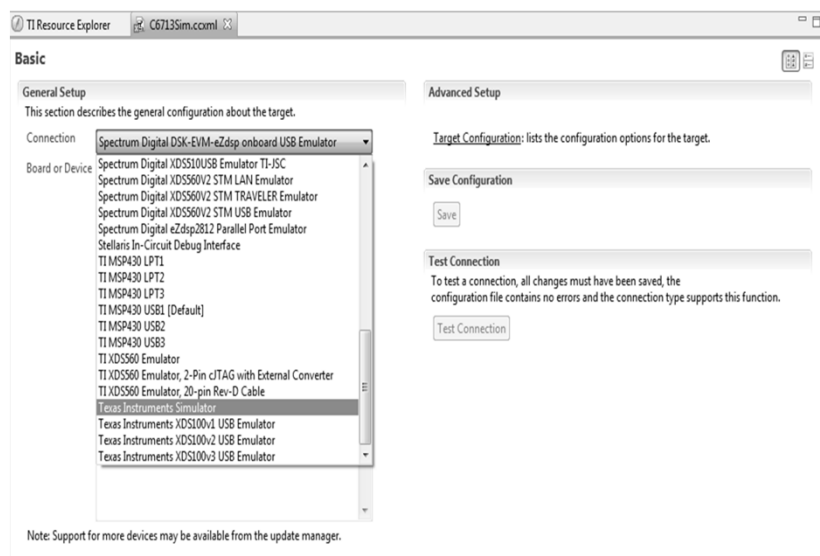
SYS-835 Processeur numérique du
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Basic

General Setup
This section describes the general configuration about the target.

Connection: **Texas Instruments Simulator**

Board or Device: type filter text

- ☐ C6670 Device Functional Simulator, Big Endian
- ☐ C6670 Device Functional Simulator, Little Endian
- ☐ C6678 Device Cycle Approximate Simulator, Big Endian
- ☐ C6678 Device Cycle Approximate Simulator, Little Endian
- ☐ C6678 Device Functional Simulator, Big Endian
- ☐ C6678 Device Functional Simulator, Little Endian
- ☐ C6713 Device Cycle Accurate Simulator, Big Endian
- ☒ C6713 Device Cycle Accurate Simulator, Little Endian
- ☐ C672x CPU Cycle Accurate Simulator, Little Endian
- ☐ C6745 Device Cycle Accurate Simulator, Little Endian

Simulates C67x core, L1D, L1P, L2 Caches, EDMA, QDMA, Timer(2), EMIF interfacing with async & SDRAM memory models, McBSP(2), McASP(2) and interrupt selector. Does not model HPI and I2C.
This is a cycle accurate device configuration and hence

Note: Support for more devices may be available from the update manager.

Advanced Setup

Target Configuration: lists the configuration options for the target.

Save Configuration

Save

Test Connection

To test a connection, all changes must have been saved, the configuration file contains no errors and the connection type supports this function.

Test Connection

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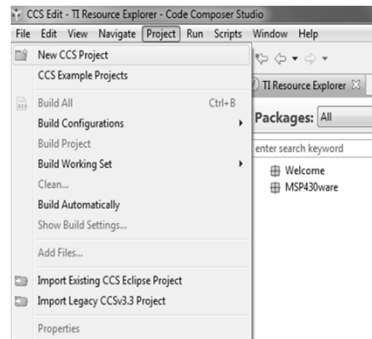
Target Configurations

type filter text

- Projects
- User Defined
 - C6713Sim.ccxml [Default]

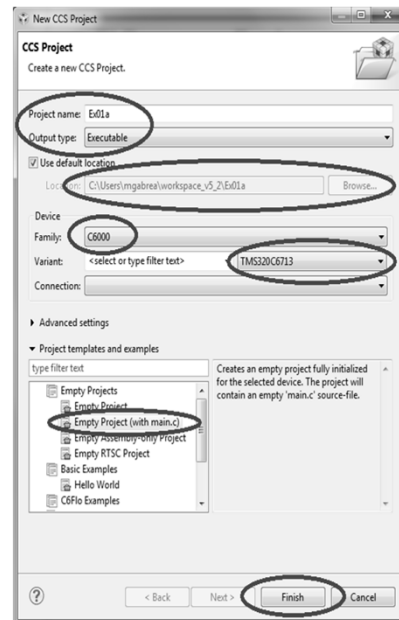
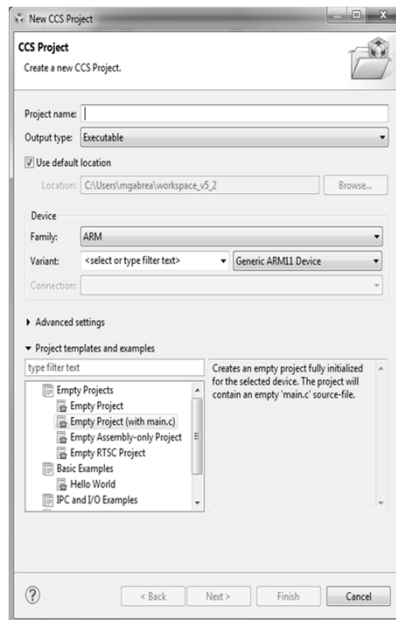
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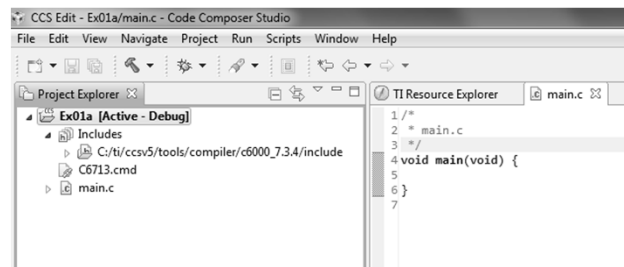
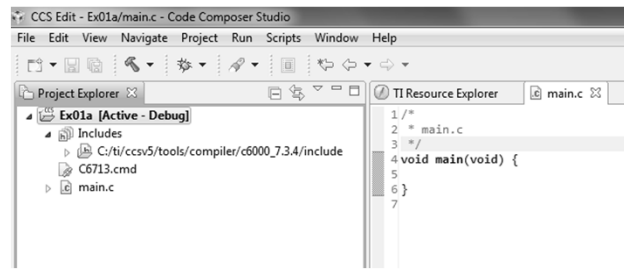
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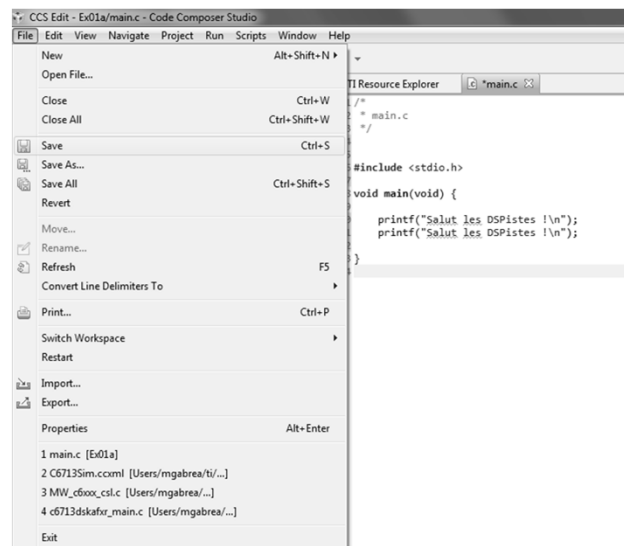
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signal et ses applications

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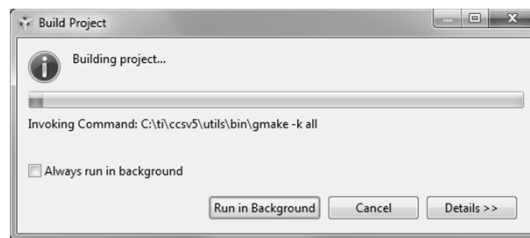
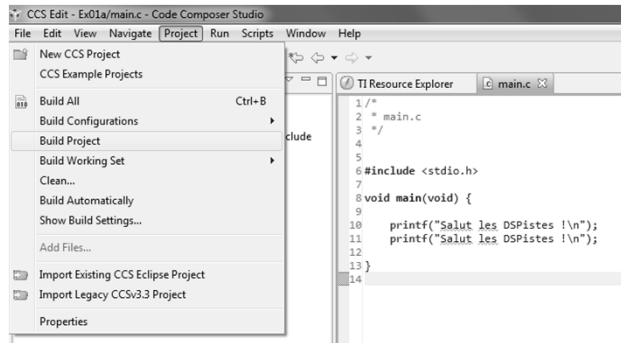
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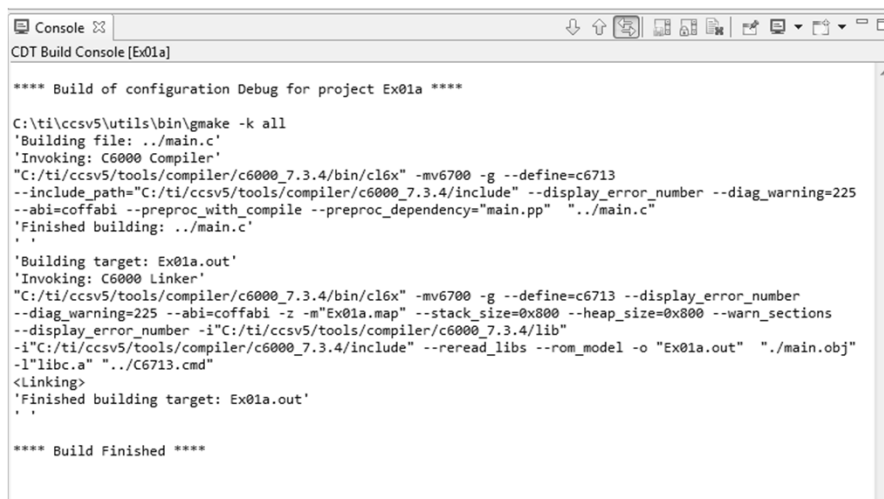
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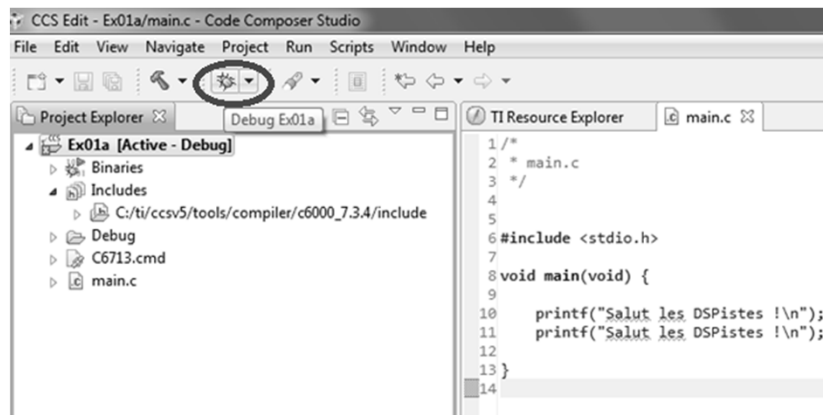
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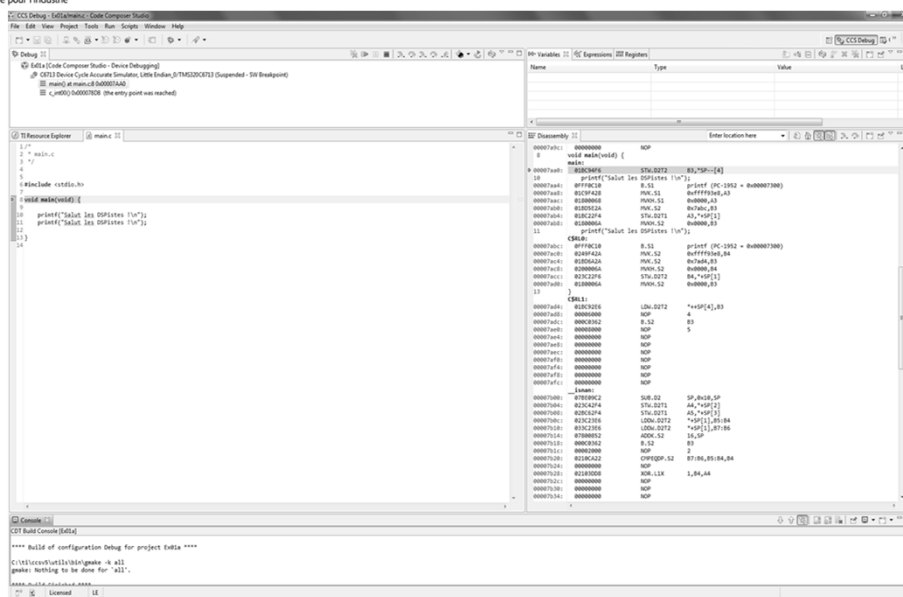
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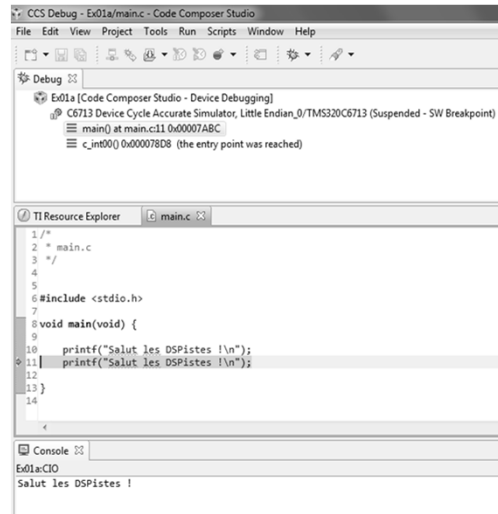
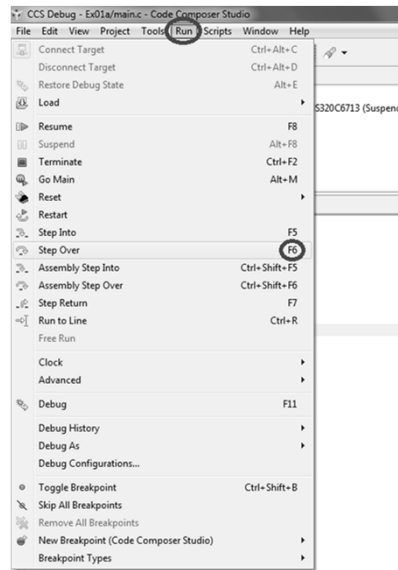
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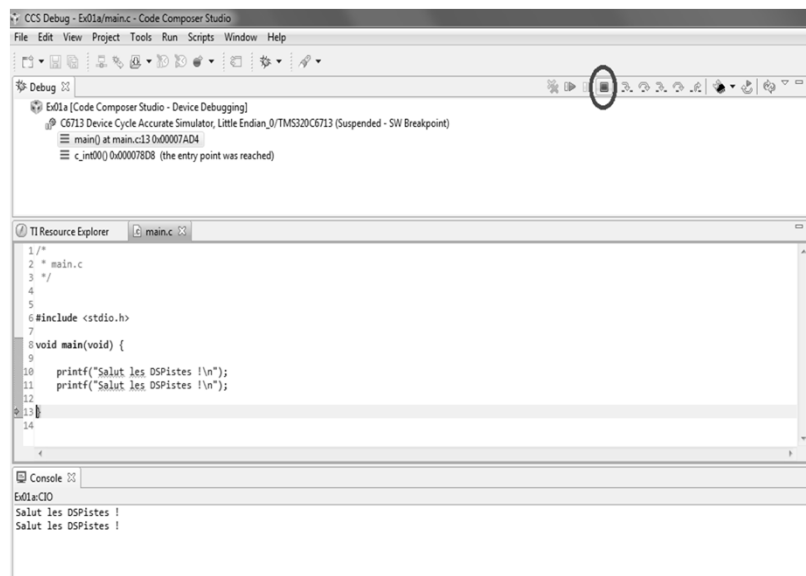
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SYS-835 Processeur numérique du signal et ses applications

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SYS-835 Processeur numérique du signal et ses applications

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