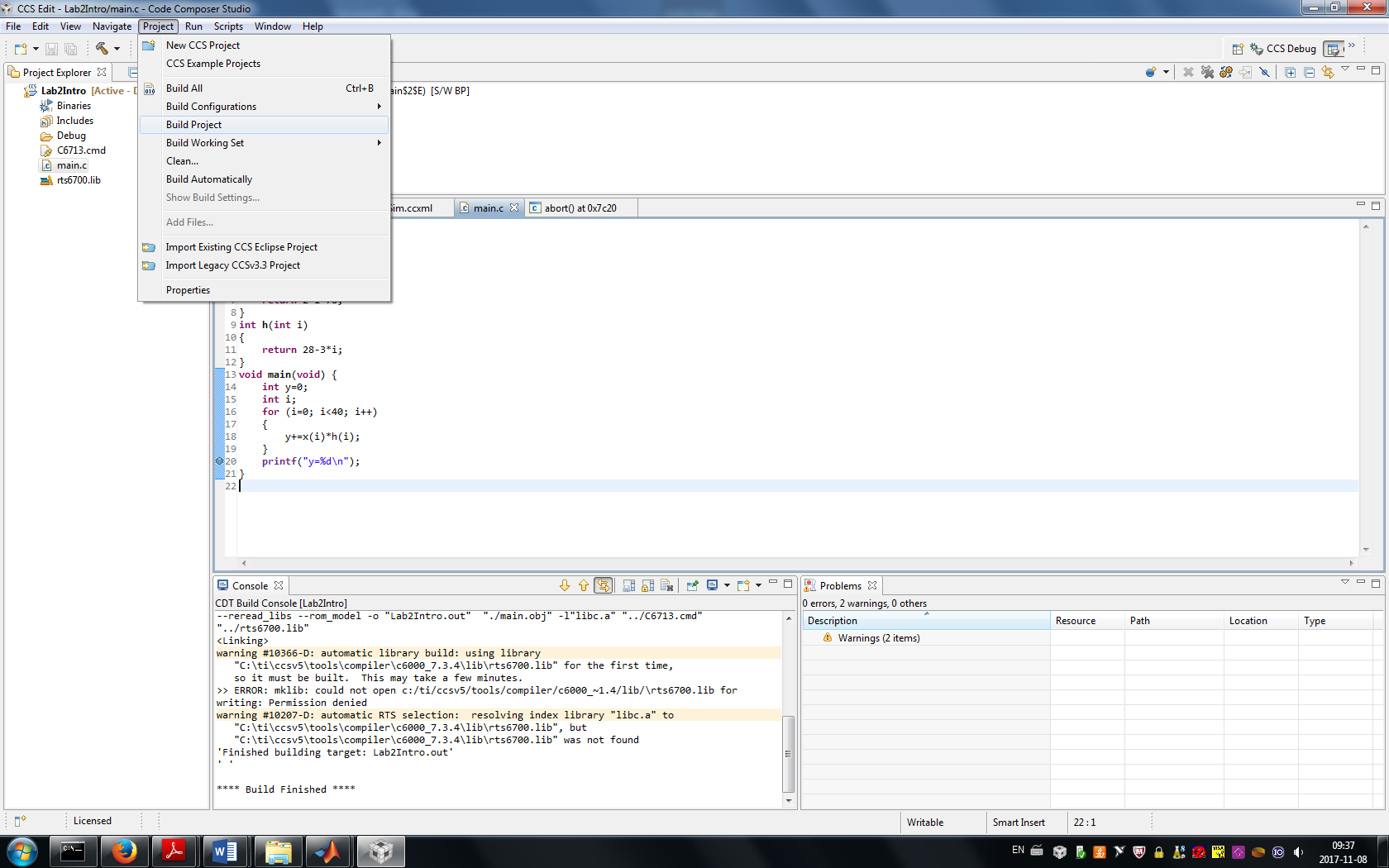
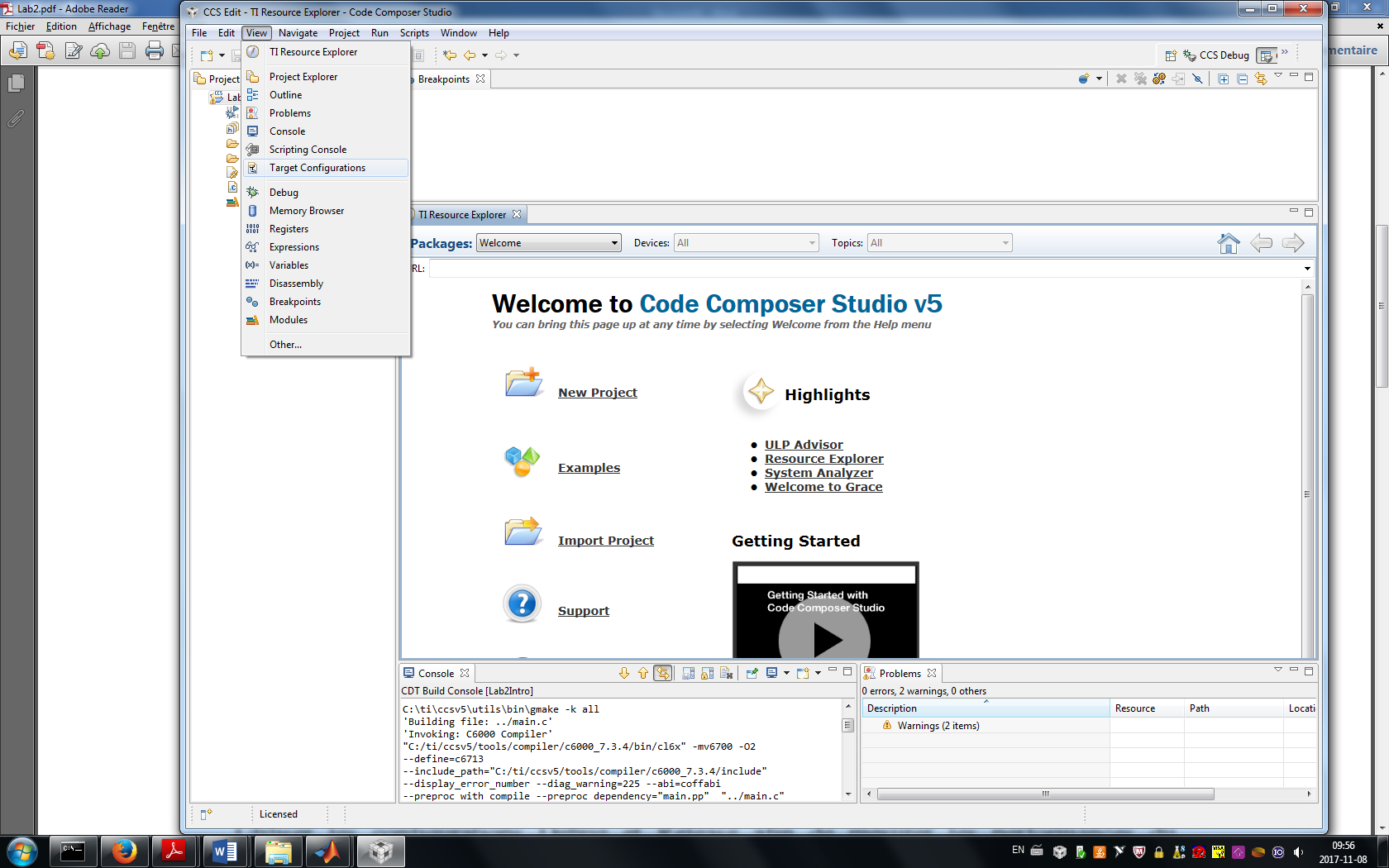
Compilation du projet

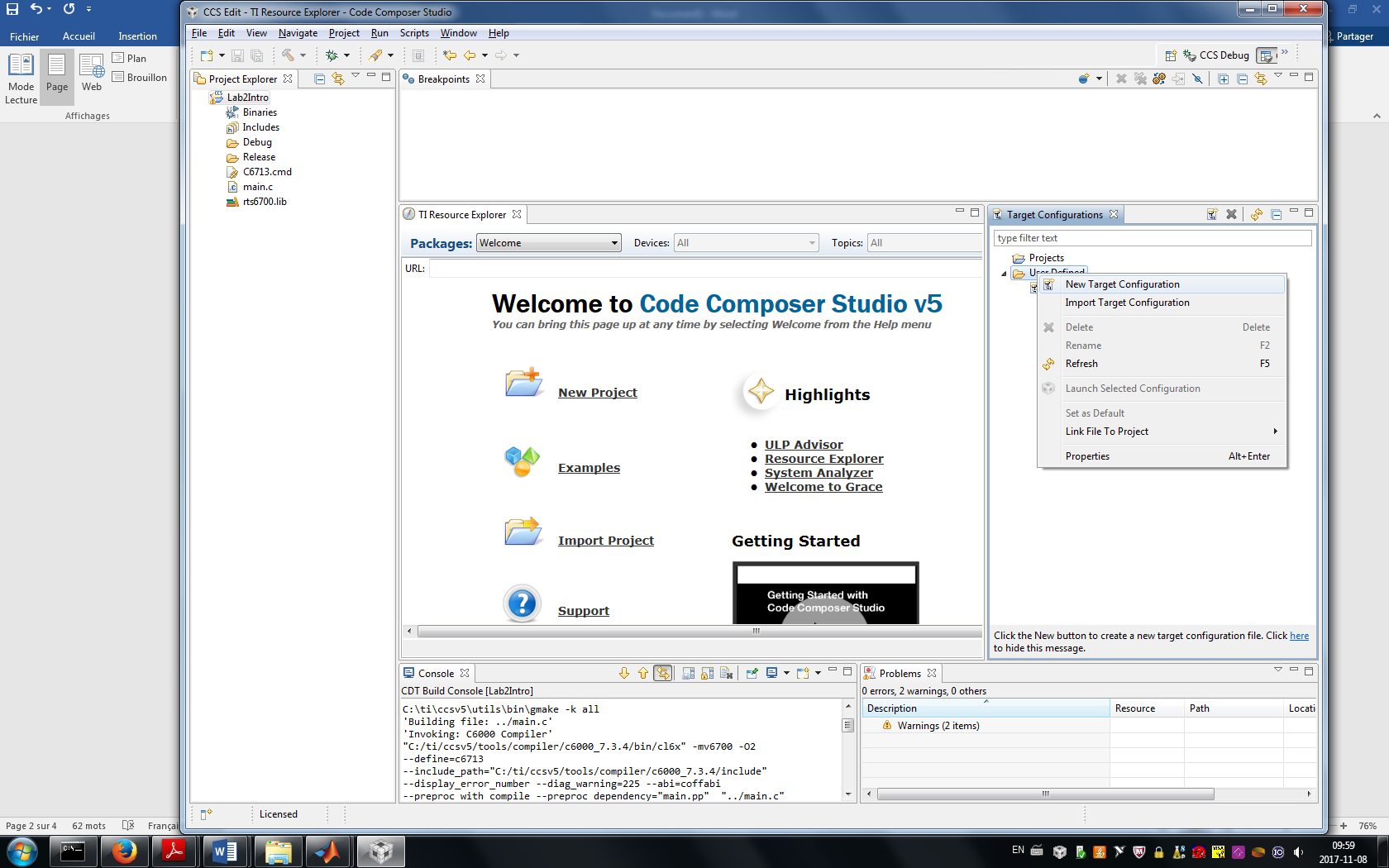


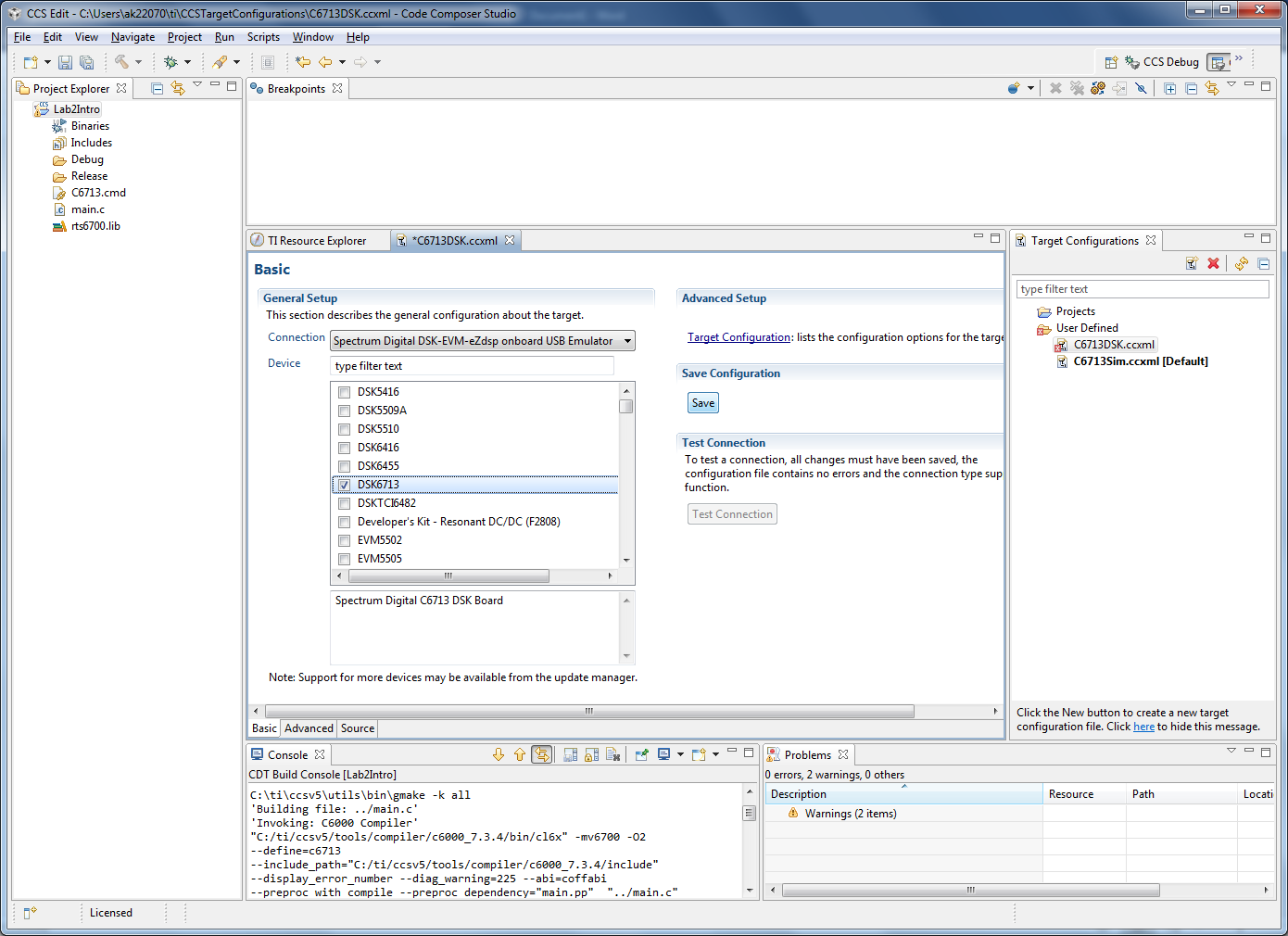
Exécution en Debug sur le simulateur avec valeur finale de y.

(Capture)

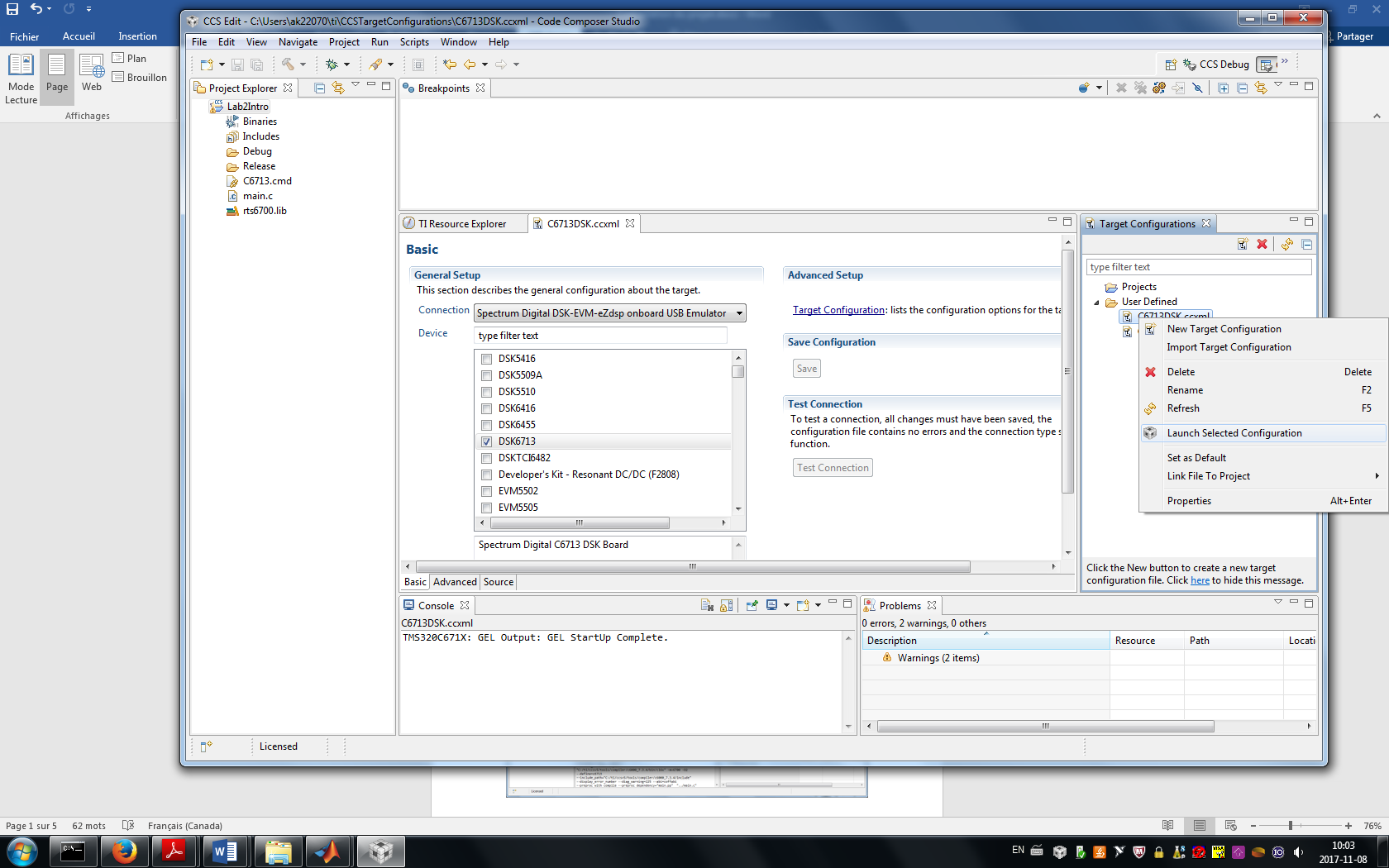
Configuration du « Target » de la carte :



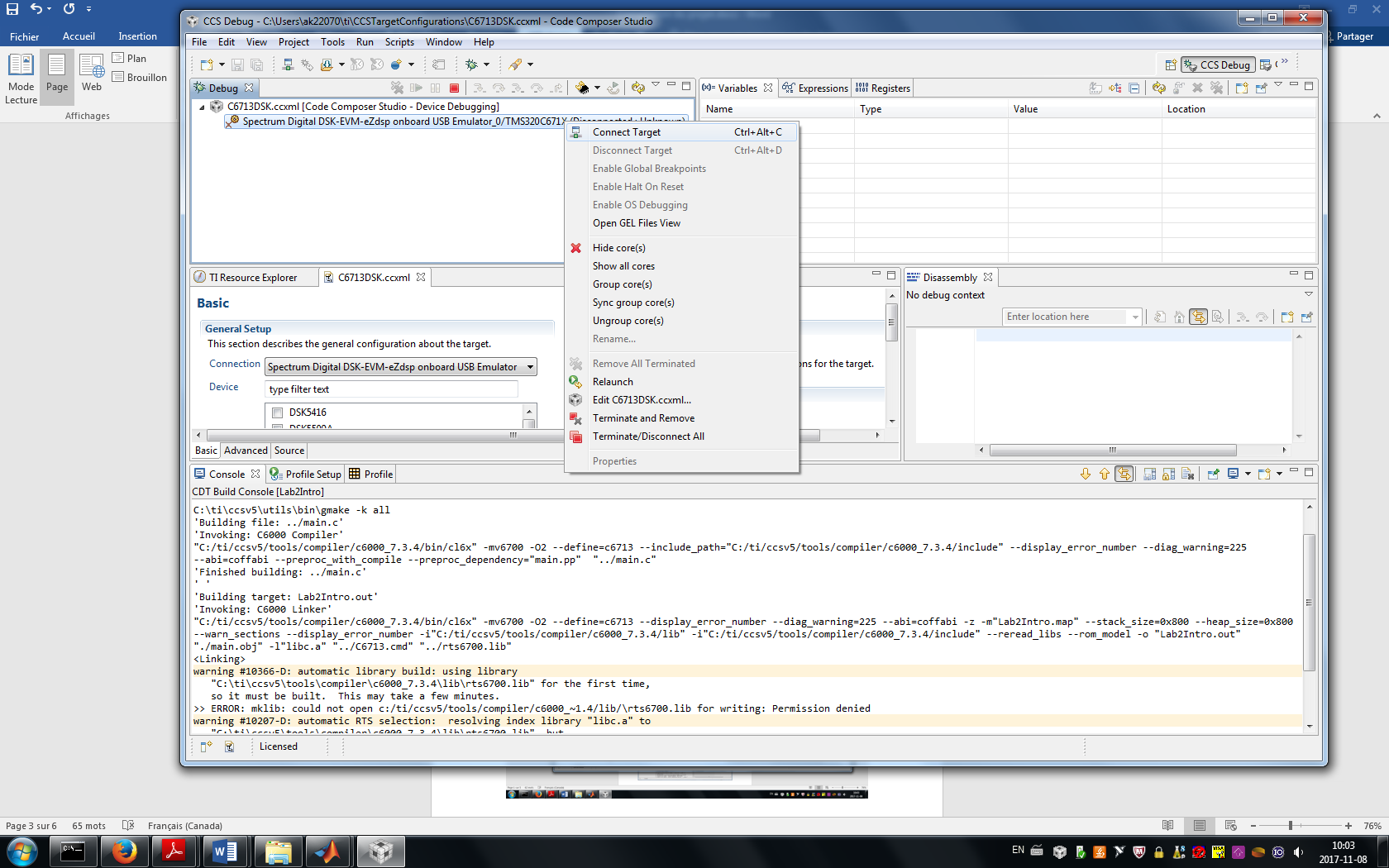




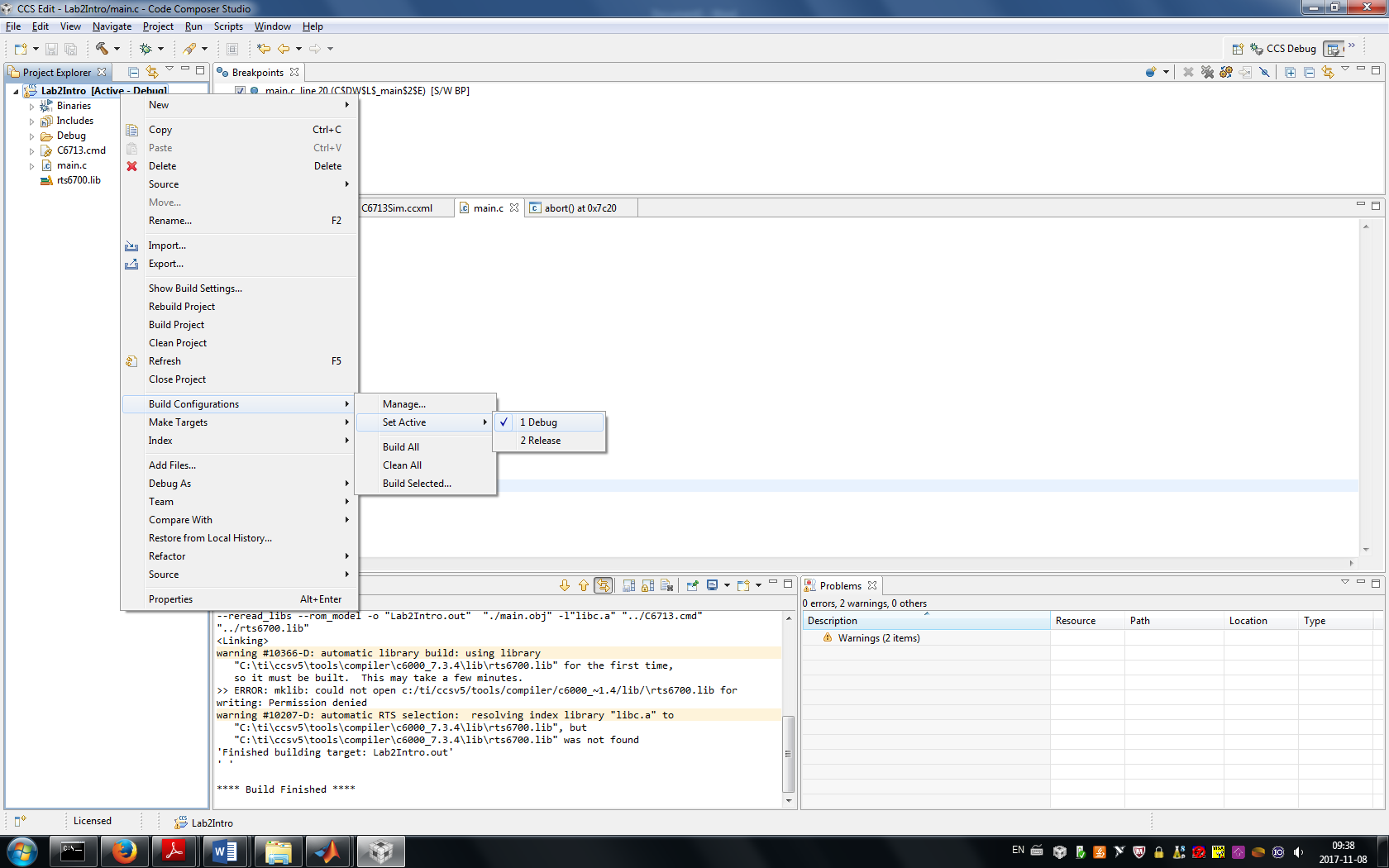
Launch selected configuration



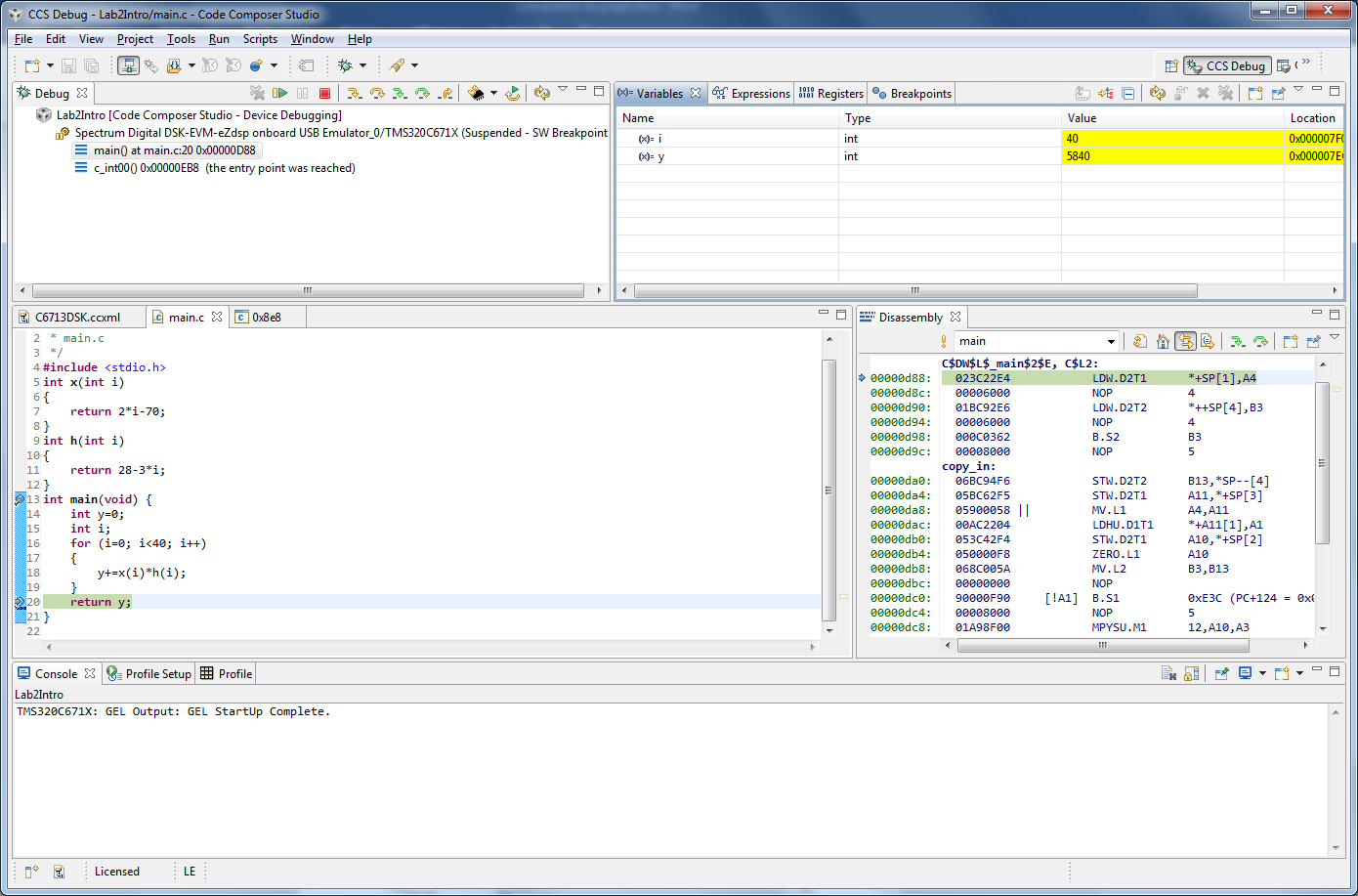
Connect target :



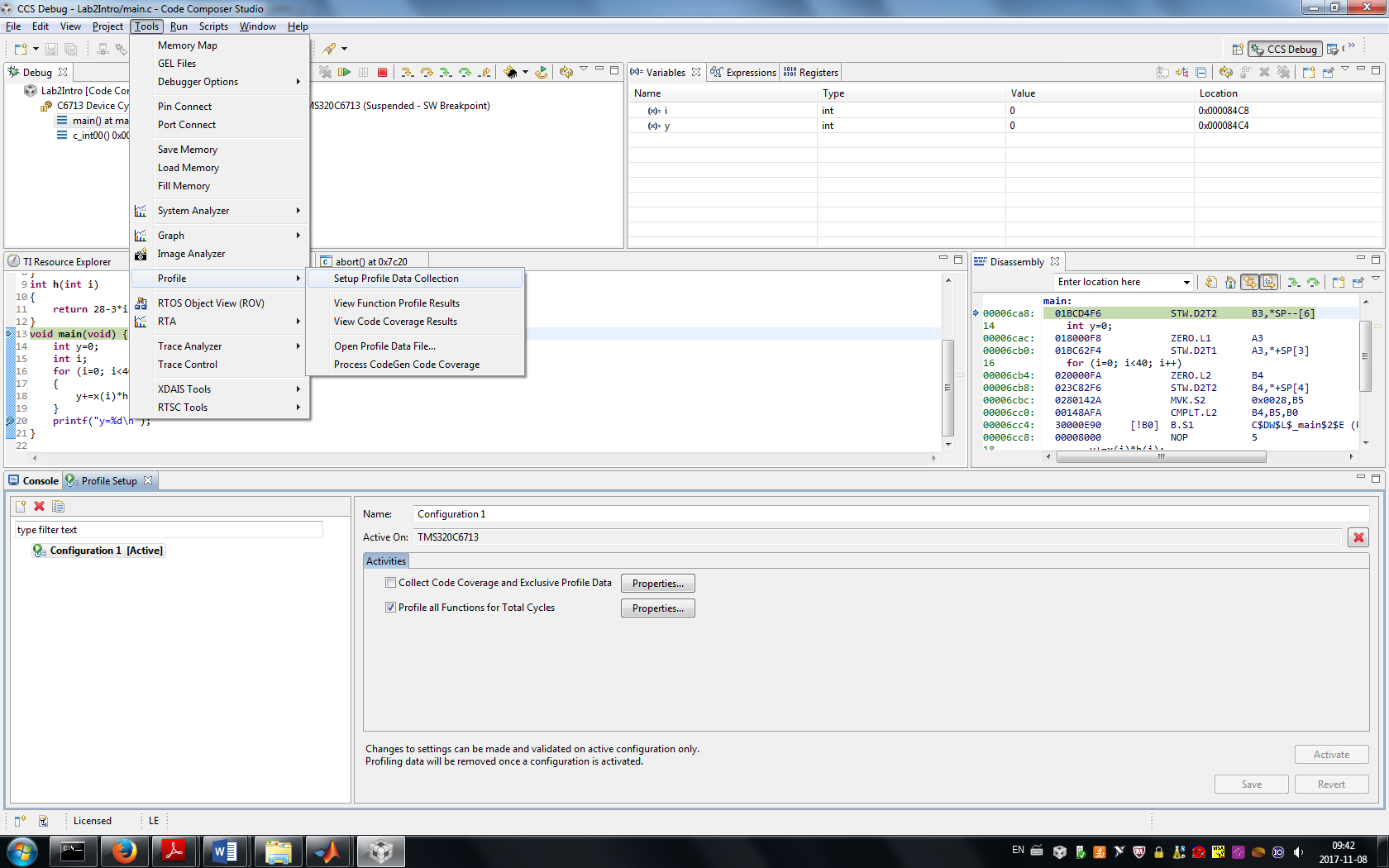
Sélection de la configuration Debug :



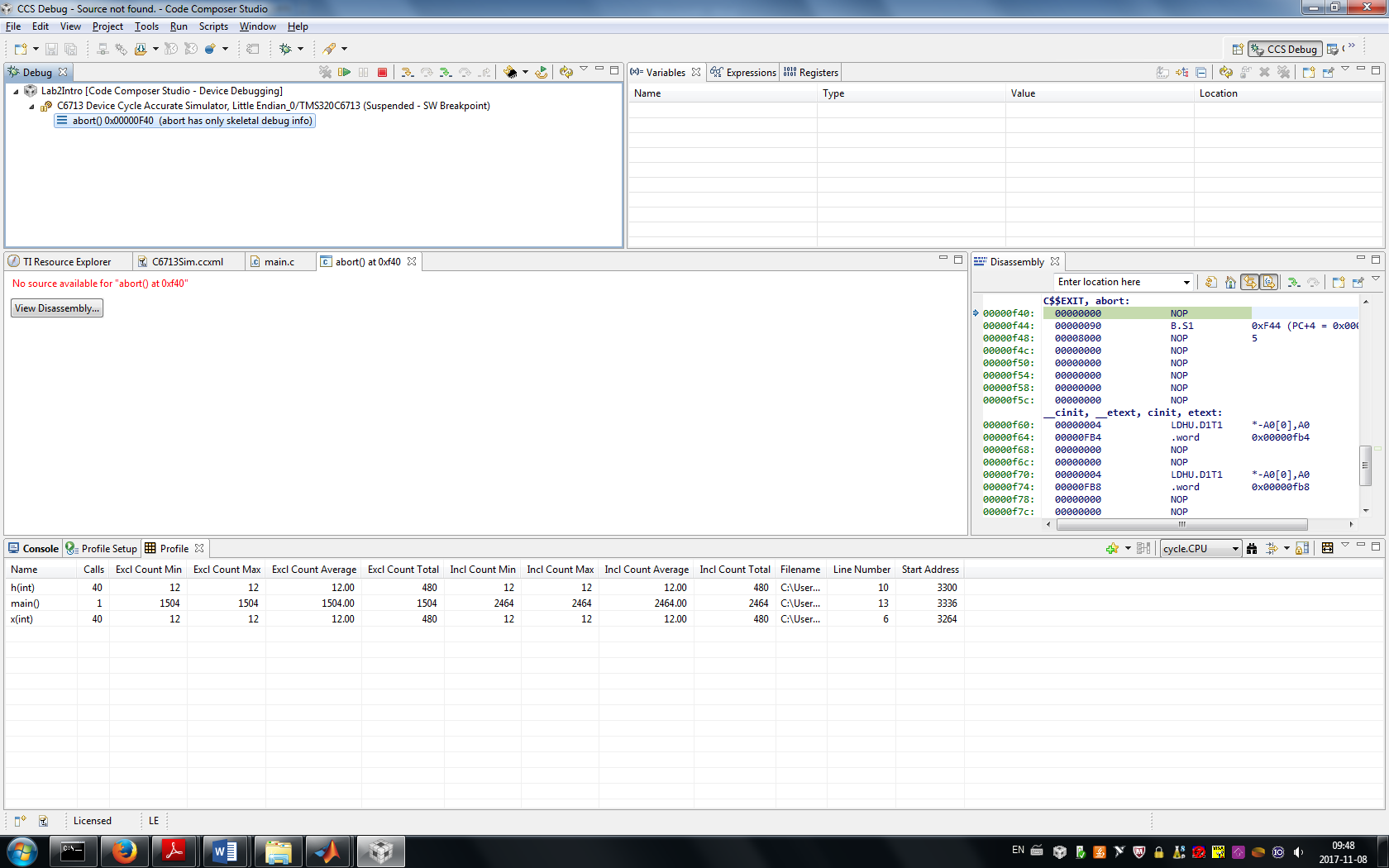
Execution sur la carte :



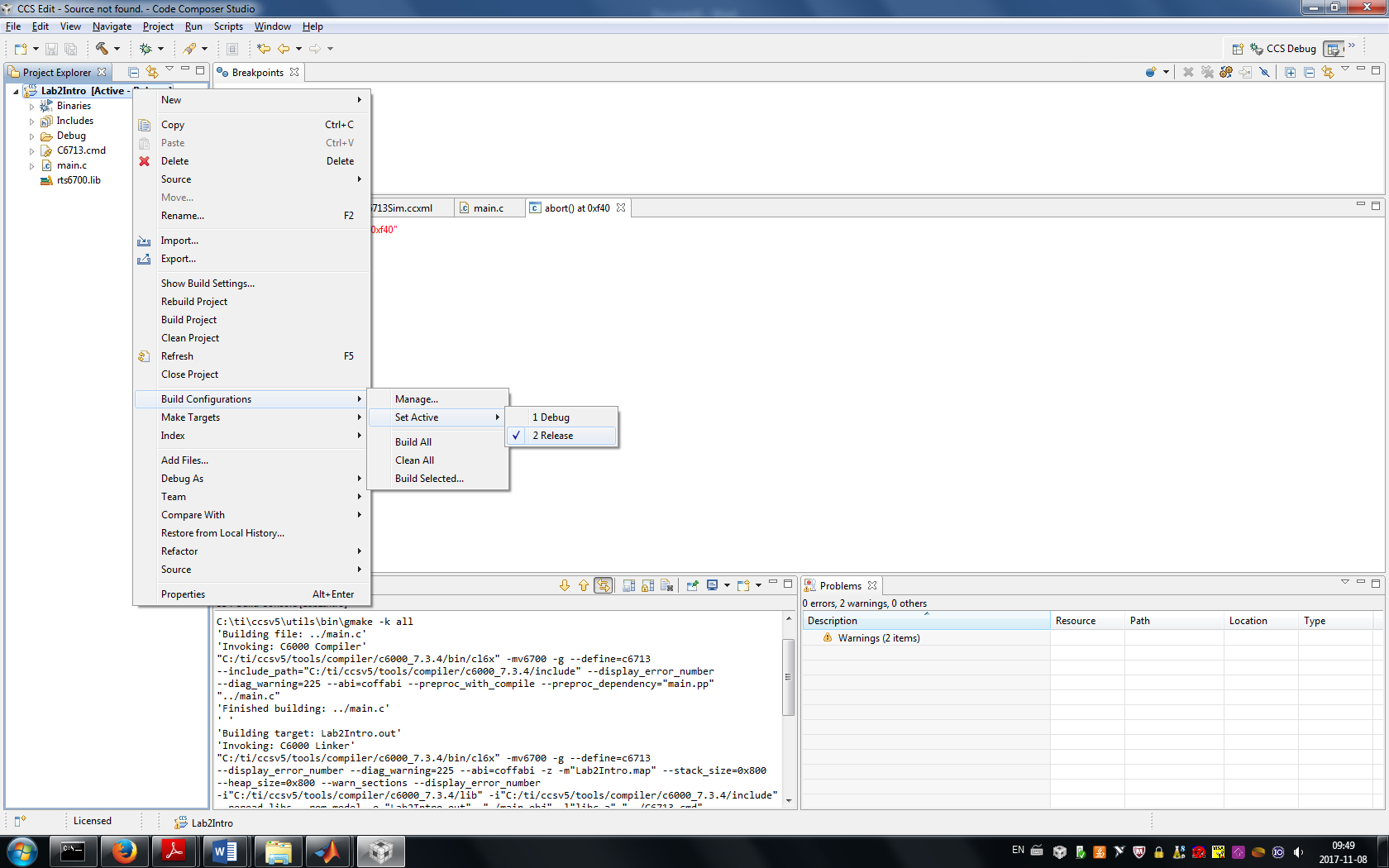
Configurer et activer le profilage :



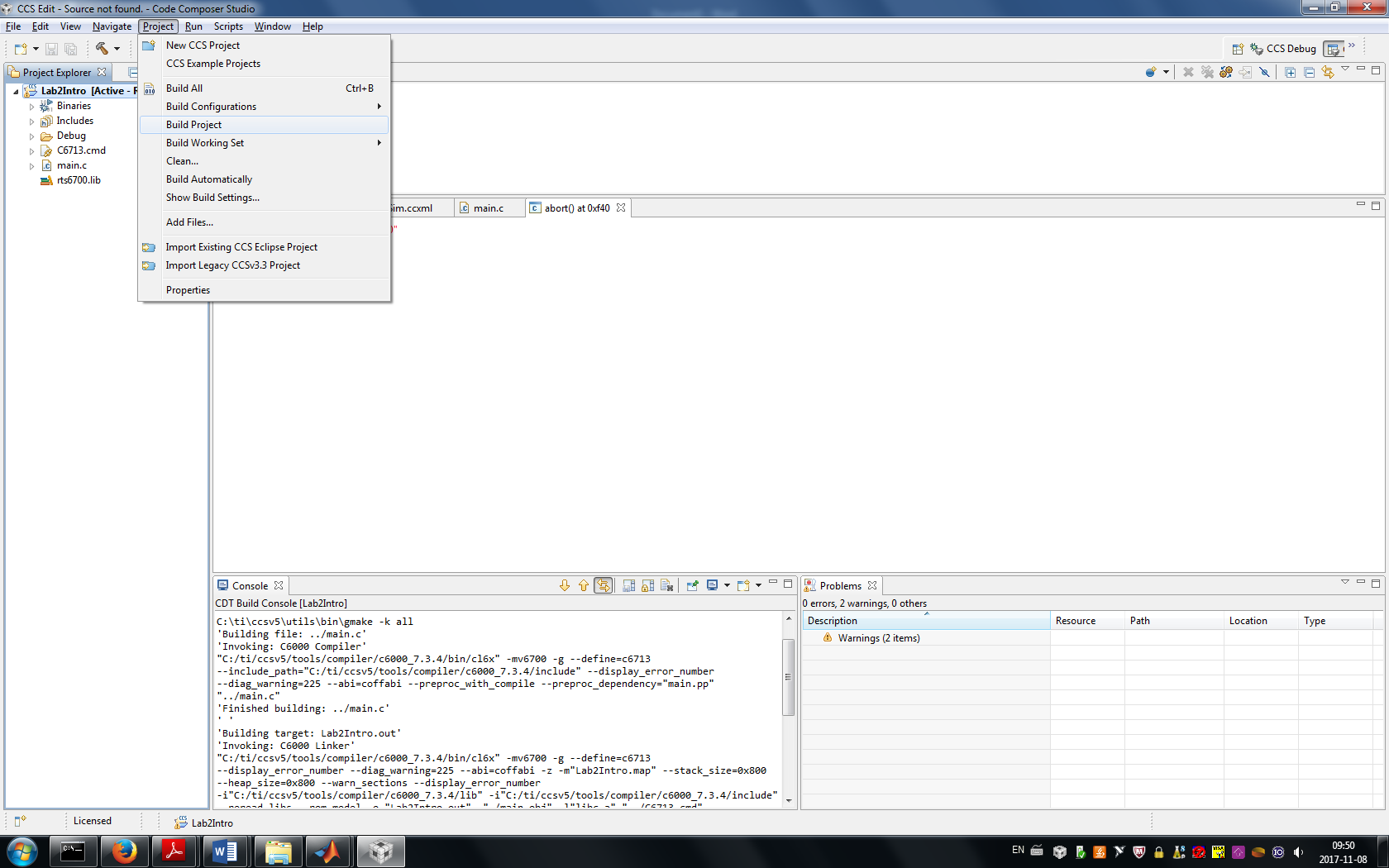
Résultats profilage Debug, 2e exécution pour éliminer effet de la cache :



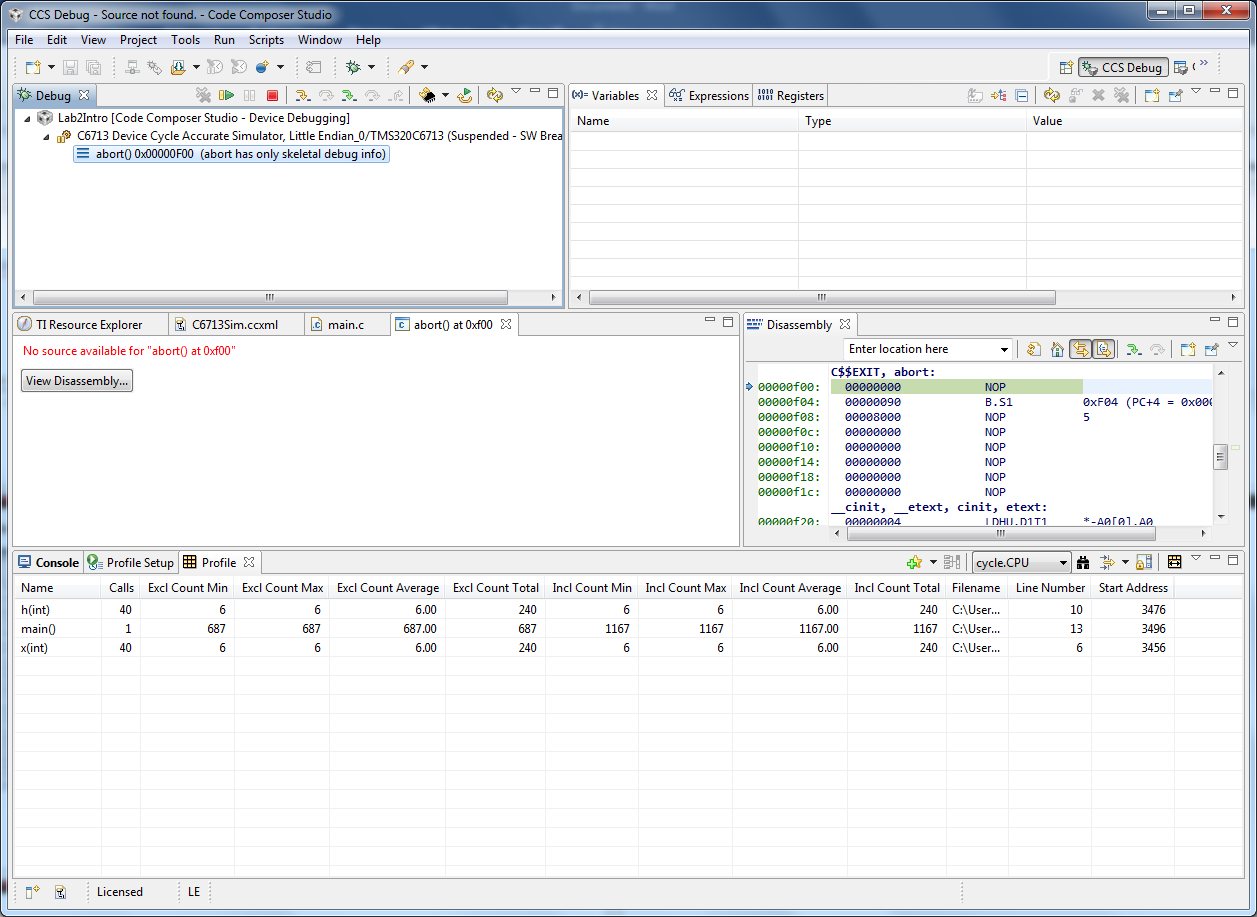
Sélection de la configuration Release :

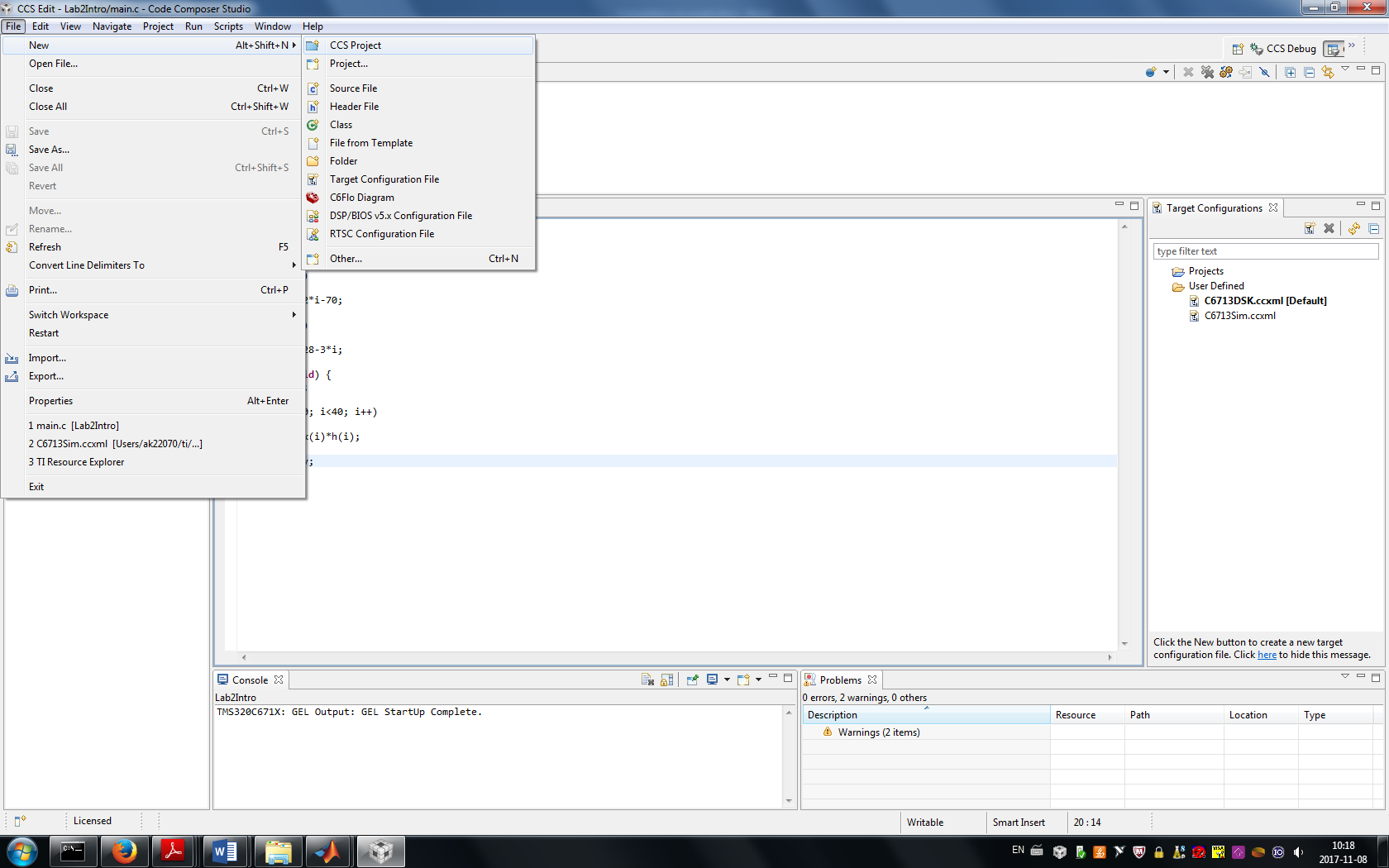


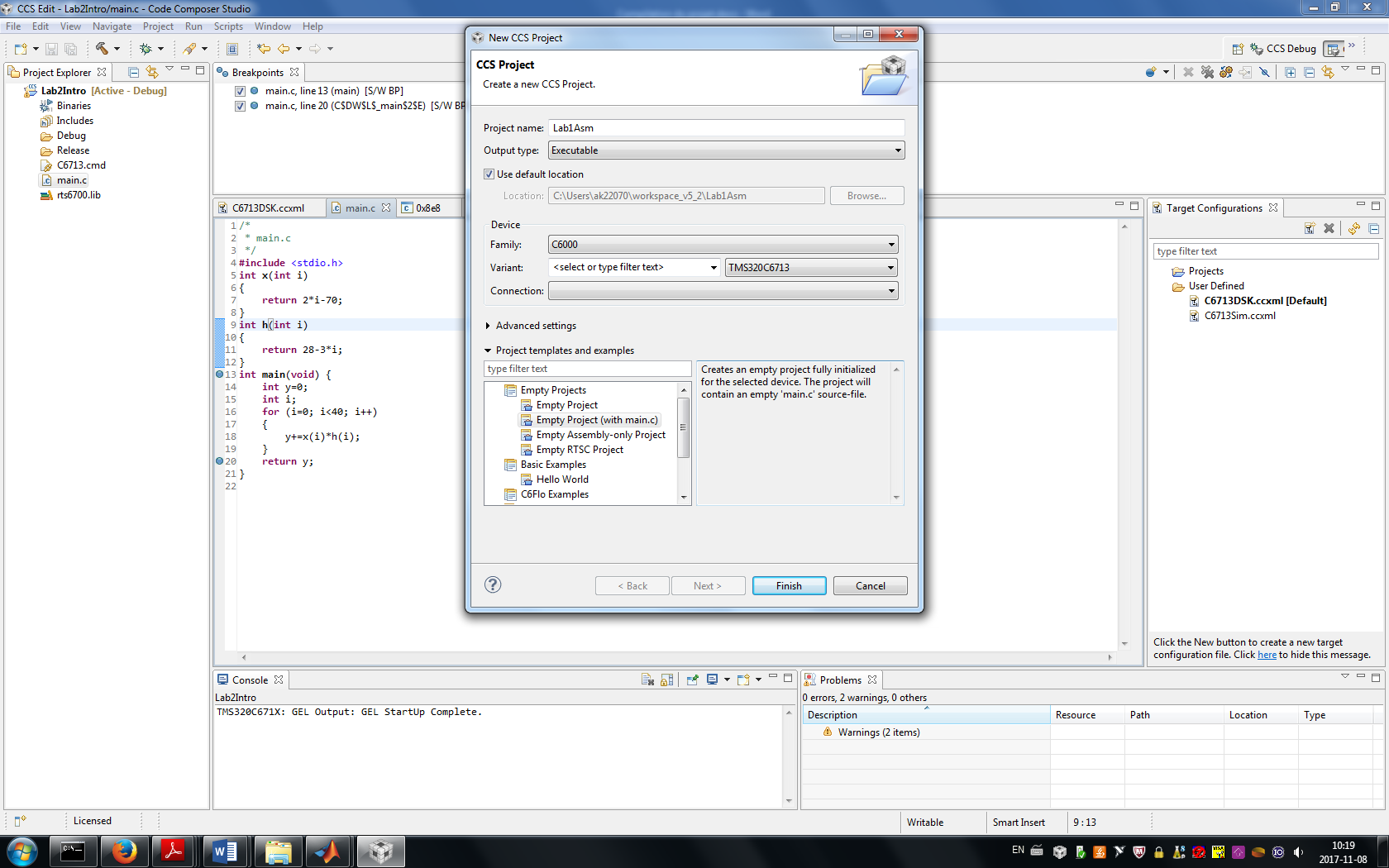
Build



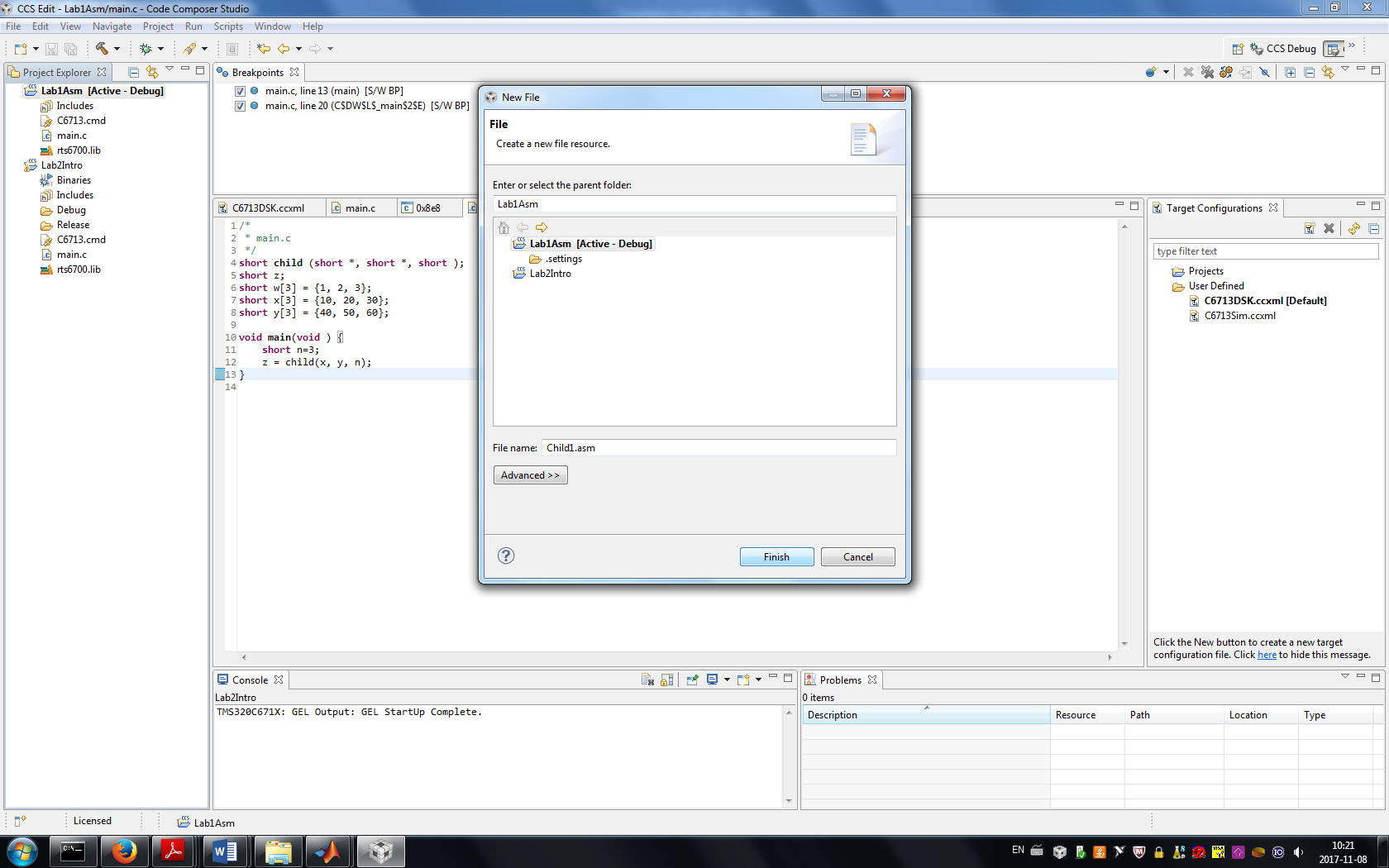
Résultats profilage configuration Release, 2e exécution :



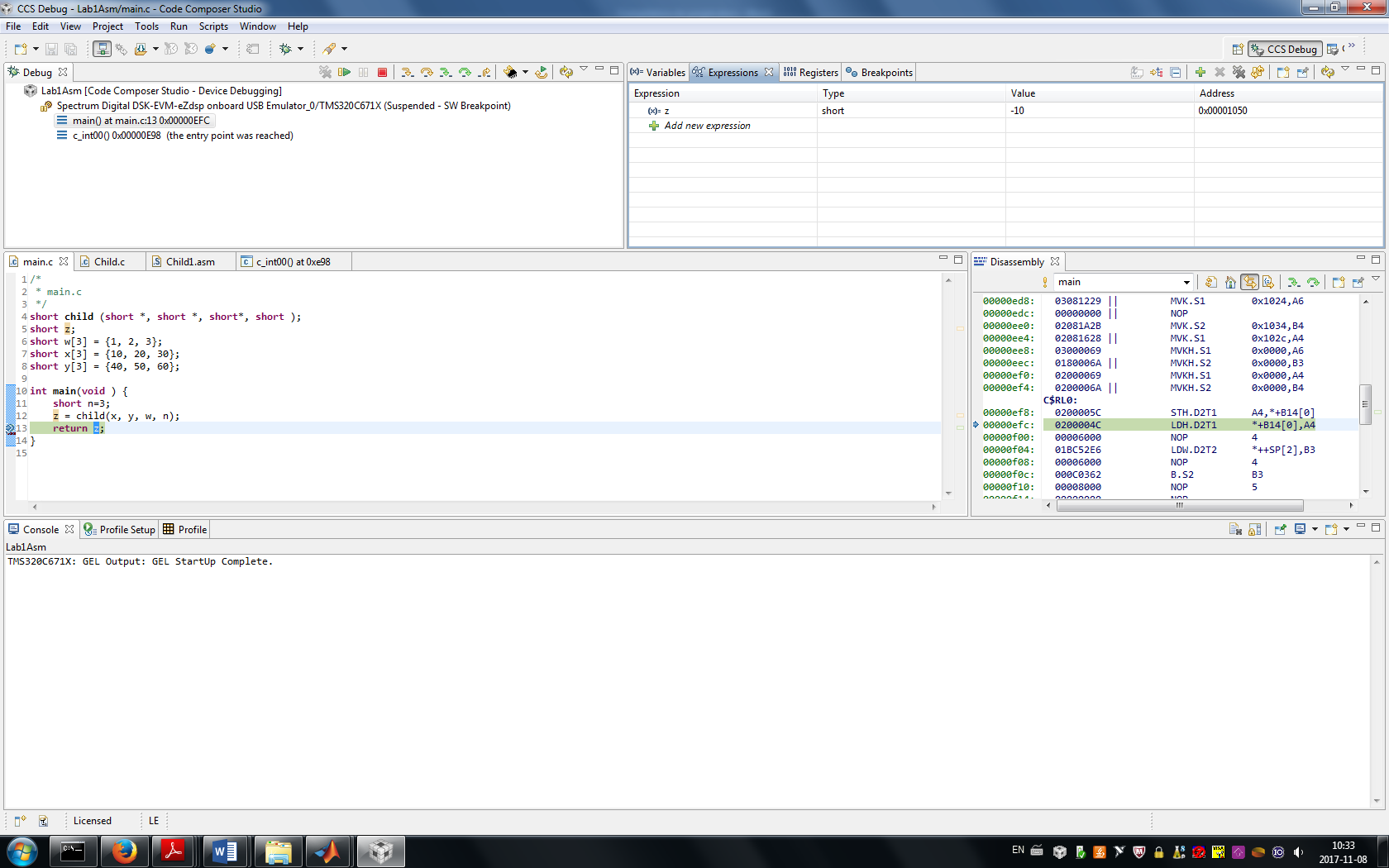
Create new project 



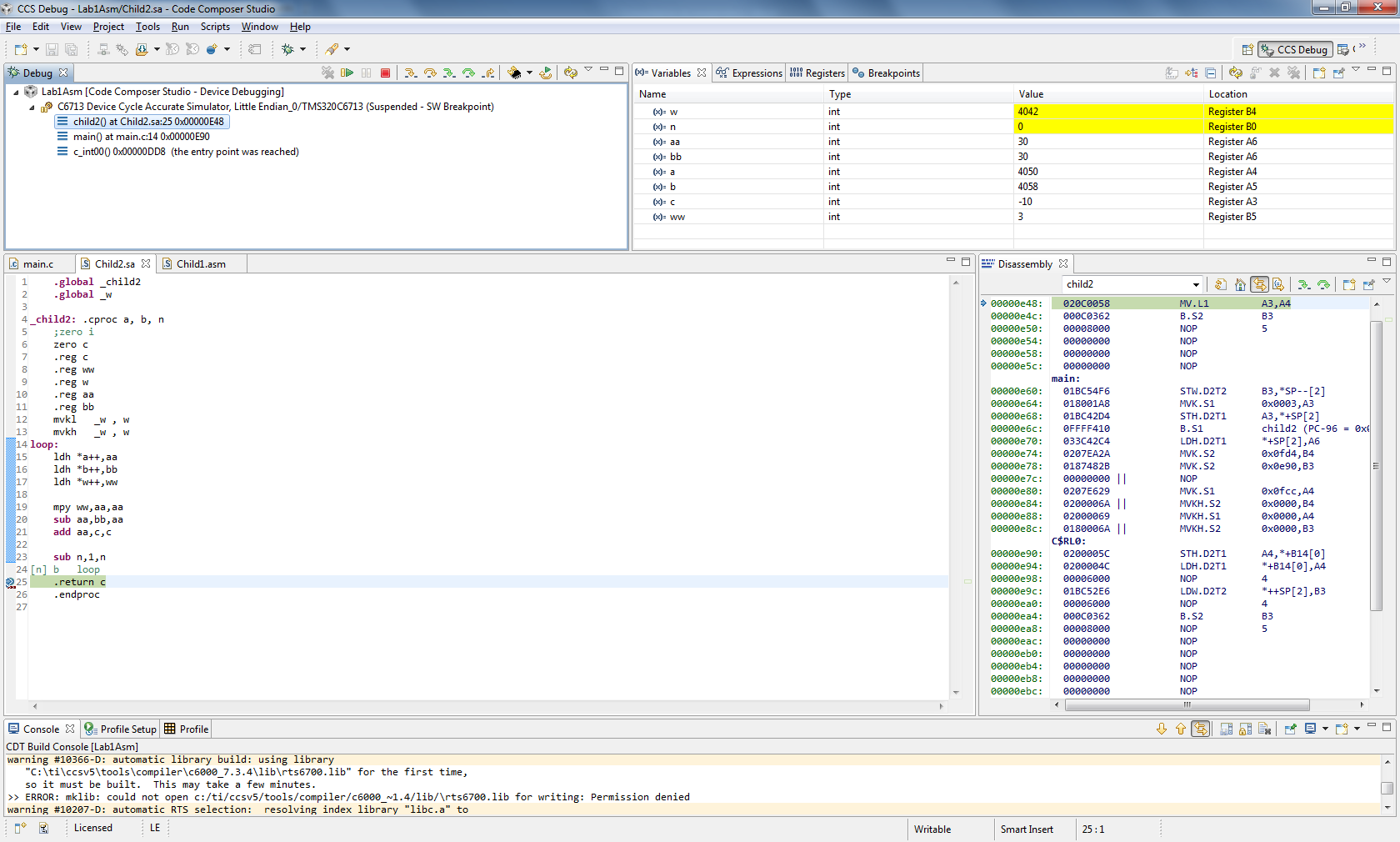
Create child.asm



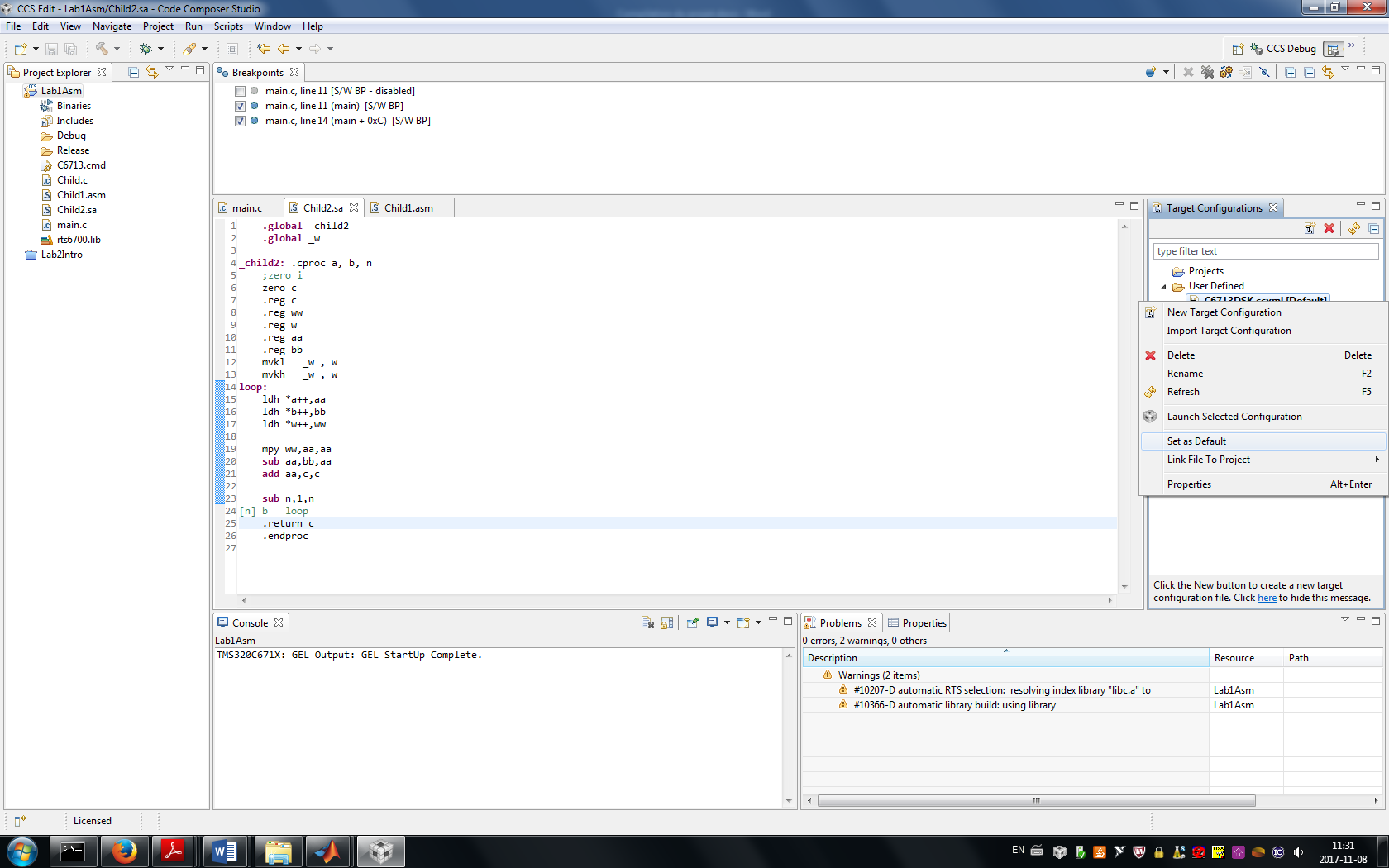
Run child.c



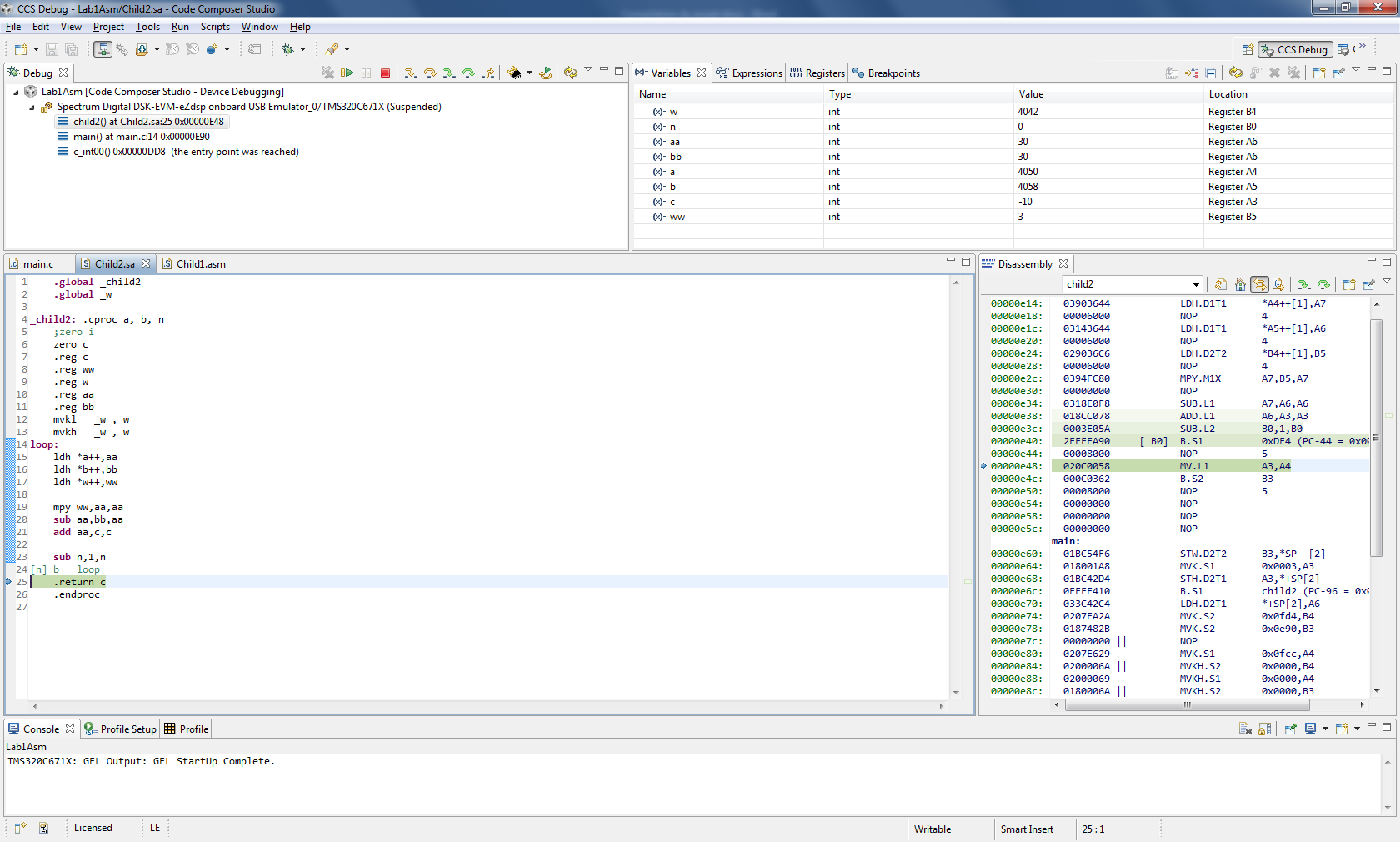
Run child2.sa (linear assembly) on simulator



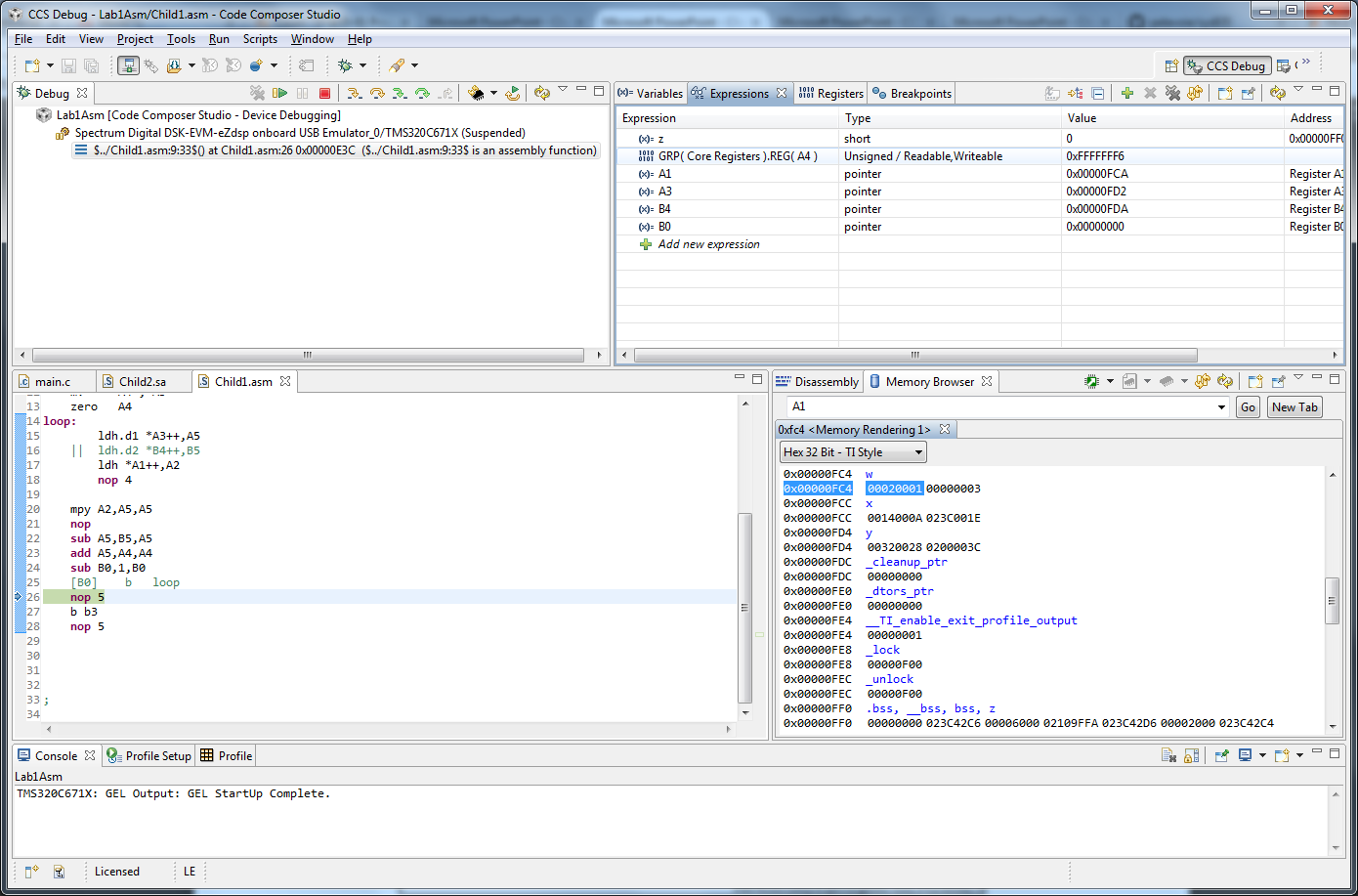
“Set default target “ sur DSK



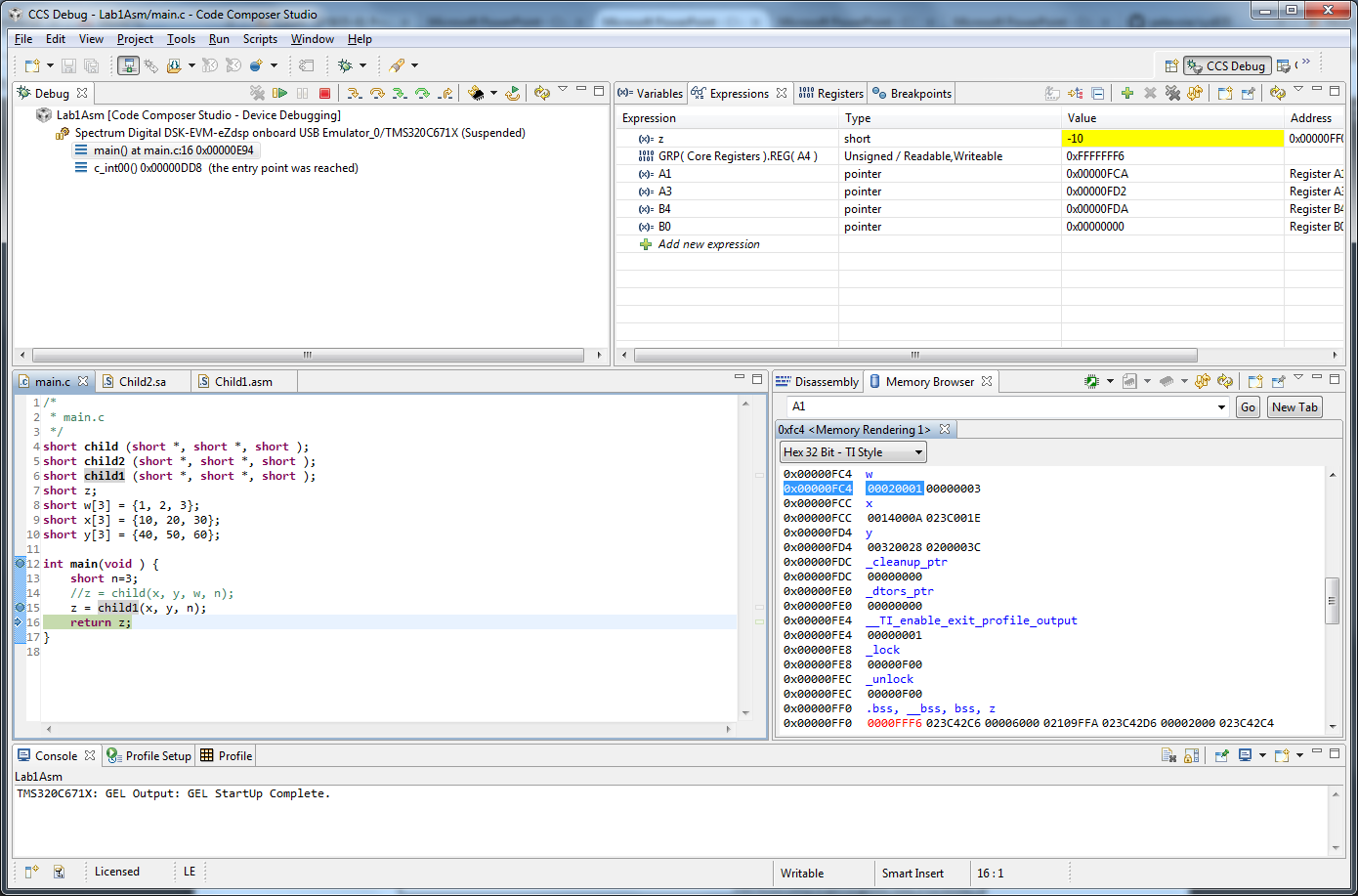
Run child2.sa on DSK (Set as default)



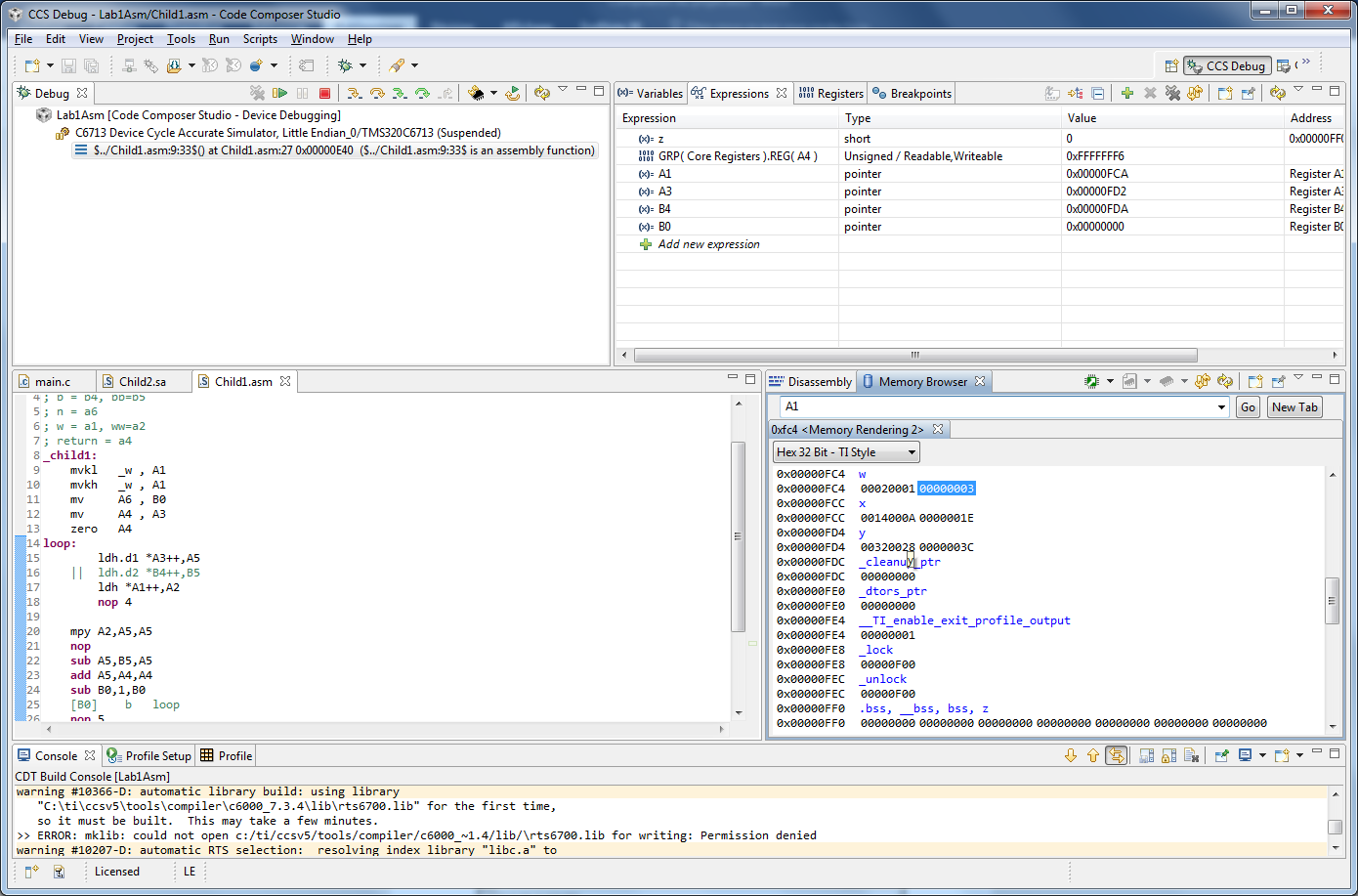
Run child1.asm DSK



Résultat



Run child1.asm SIM



Résultat

