

#### **Department of Electronics & Communication Engineering**

21<sup>st</sup> November, 2018

# Final Lab Examination Schedule for Batch 2015, Batch 2016 (ECE) Semester I, Session 2018-19

#### **IMPORTANT INSTRUCTIONS:**

- 1. During the Lab exam students are required to carry their ID card failing which student will not be allowed to take the exam.
- 2. Students are NOT allowed to carry mobile phones even in switched off mode.
- 3. Possession of Pen Drives, CDs etc. will be treated as Unfair Means Case even if the said items do not have any material of the course.
- 4. No exchange of Stationary items, Calculators is permissible during the course of the exam.
- 5. Students are supposed to be in their respective labs in time. Late comers will not be allowed to take the exam.
- 6. Students are allowed to leave the Exam venue ONLY AFTER all the students in the group are done with their written exam, performance and Viva.
- 7. All students are compulsorily required to be in complete university uniform during the lab exams. Any kind of excuses in this regard will not be accepted. The students who will not be the in proper uniform will be liable for penalty of marks in their lab exam.

### **Batch 2015 (ECE)**

Date	Course Name	Time	Section	Venue
	(Course Code)			
29.11.2018	Embedded System Design (ECP2401)	9:30 AM to 11:00 AM	9L	1303 Kalam-Block
29.11.2018	Digital Image Processing Lab (ECP1404)	1:30 PM to 3:15 PM	9L	1303 Kalam-Block
01.12.2018	Major Project* (ECP6401)	2:30PM to 4:00 PM	9L	1303 Kalam-Block

<sup>\*</sup>Notice with additional instructions for project display will be uploaded separately.



## **Department of Electronics & Communication Engineering**

## **Batch 2016 (ECE)**

Date	Course Name	Time	Section	Venue
	(Course Code)			
01.12.2018	Microprocessor and Microcontroller Lab (ECP1315)	9:00 AM to 11:00 AM	10L	1303 Kalam-Block
01.12.2018	Analog & Digital Communication Lab (ECP1206)	1:30 PM to 3:15 PM	10L	1304 Kalam-Block
30.11.2018	Data Structures Lab (CSL2210)	2:30PM to 4:00 PM	10L	CL-1
30.11.2018	Microelectronics Lab (ECP2303)	9:00 AM to 11:00 AM	10L	Cadence Lab- Kalam- Block

In case of observance of any discrepancy or error in the above schedule, kindly write a mail at lipika.gupta@chitkarauniversity.edu.in latest by 24<sup>th</sup> November, 2018.

Prof. Lipika Gupta HOD (ECED)

Cc:

PS to VC and Registrar for their kind information
Dean (Administration) for his kind information
HODs of all departments
COE

**Chalkpad Notice board** 



# **Department of Electronics & Communication Engineering**

# Annexure 1

# **Batch 2015 (ECE):**

S. No.	Univ. Roll	NAME
1	1511981503	AMIT S PAUNIKAR
2	1511981504	ASHWIN SHARMA
3	1511981505	BHANU SHARMA
4	1511981507	HITESH SHARMA
5	1511981509	JASMEET KAUR
6	1511981513	PANKAJ CHAUHAN
7	1511981516	SHIVAKSH SHARMA
8	1511981520	UZZWAL GUPTA
9	1511981522	RAJNEESH GULATI
10	1511981524	ISHANT KAMAL
11	1511980601	CHANDAN DEV SINGH
12	1511980605	ANAMIKA SHARMA

# **Batch 2016 (ECE):**

Sr. No.	ID No.	Name
1	1611980601	Kailash Kumar
2	1611980602	Kumari Neha
3	1611980603	Amit Kumar
4	1611980604	Maya Kashyap
5	1611982001	Abhishek
6	1611982002	Aditya Grewal
7	1611982003	Ajay C Rajan
8	1611982004	Anshul Rana
9	1611982005	Anunay Srivastava
10	1611982006	Ayush Gupta
11	1611982007	Dushant
12	1611982008	Manu
13	1611982010	Nishant Koundal
14	1611982012	Shubham Singh
15	1611982013	Suraj Singh
16	1611982014	Vedant Rohatgi
17	1611982015	Vikas Thakur
18	1611982016	Vivek Samul
19	1611982019	Gurinder Singh
20	1611982020	Rohan Dhaload