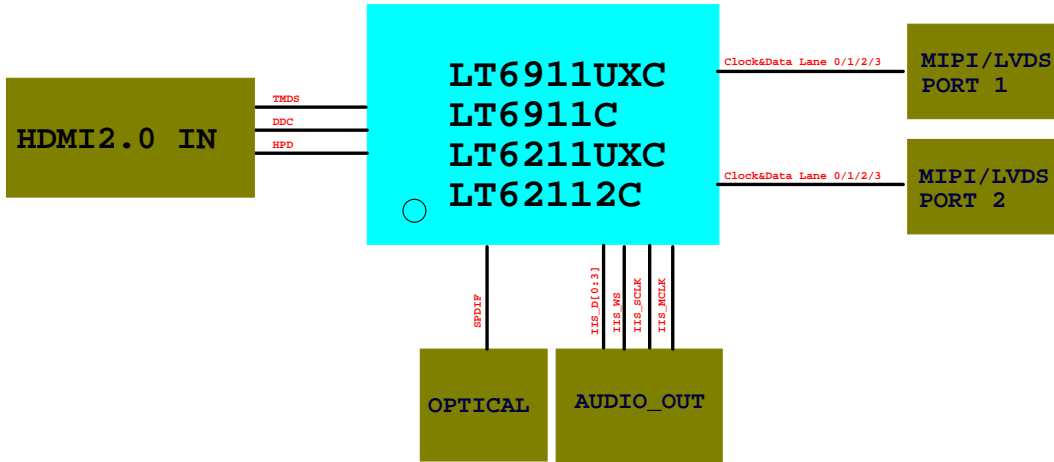


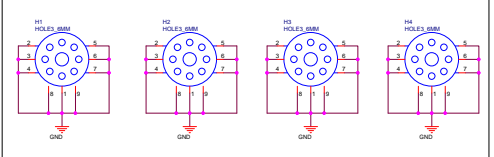
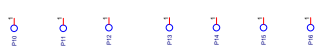
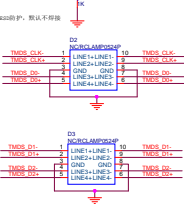
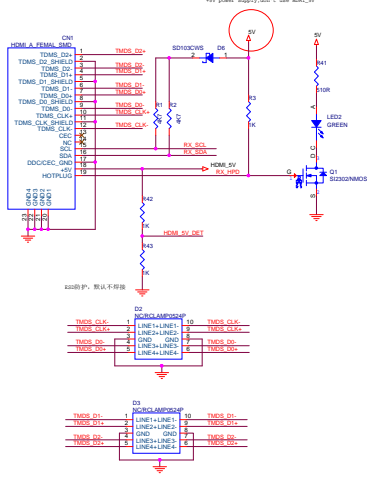
# Block Diagram



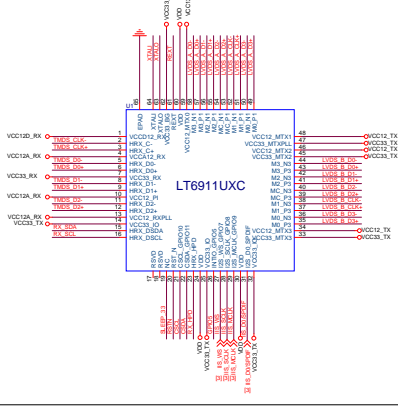
File		
LT6911UXC_EVB_HDMI2.0_TO_MIPI&LVDS_V1_0		
Size	Document Number	Rev
Custom	Block Diagram	V1_0
Date:	Thursday, January 16, 2020	Sheet 1 of 4

HDMI INPUT

\*1V power supply not use HDMI\_V1

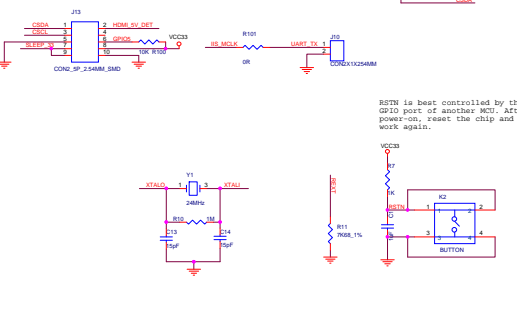


LT6911UXC/LT6911C/LT6211UXC/LT6211C



EXTERNAL

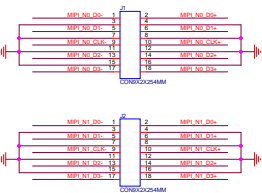
SLEEP\_33: This pin can be left floating, and the SLEEP function is temporarily not supported.
CSDA/CSDC: Test points can be used to upgrade firmware.
The upgrade method can use the adapter or the I2C interface of the MCU/SOC.



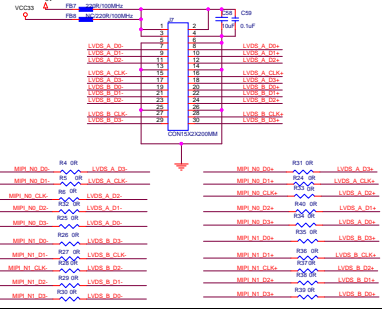
MIPI OUT

If the MIPI output signal is connected to the SOC, the GPIO5 and I2C interfaces need to be connected to the SOC. GPIO5 can output high level signal within 200ms. Within 200ms, SOC can read the registers of the chip via I2C, SOC can get the resolution and other informations.

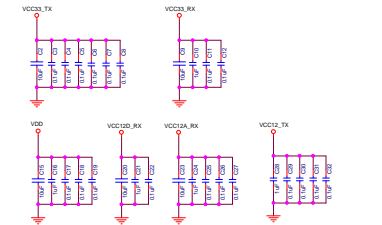
You can control 2d/3d switch via GPIO or I2C.



LVDS OUT

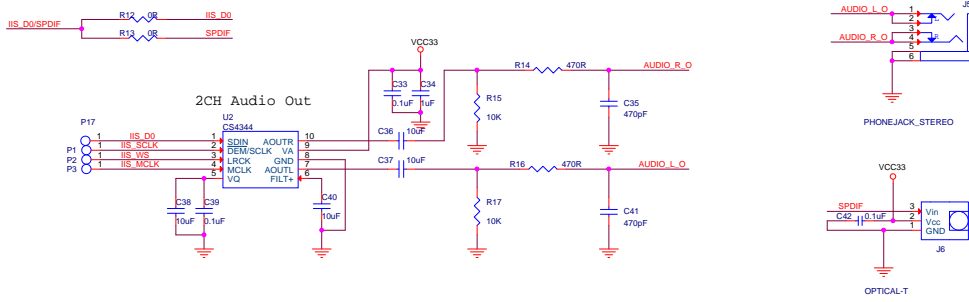


Decouple capacitance: Close IC power pin



# AUDIO

- [2] IIS\_DO/SPOIF >> IIS\_DO/SPOIF
- [2] IIS\_SCLK >> IIS\_SCLK
- [2] IIS\_WS >> IIS\_WS
- [2] IIS\_MCLK >> IIS\_MCLK



# POWER

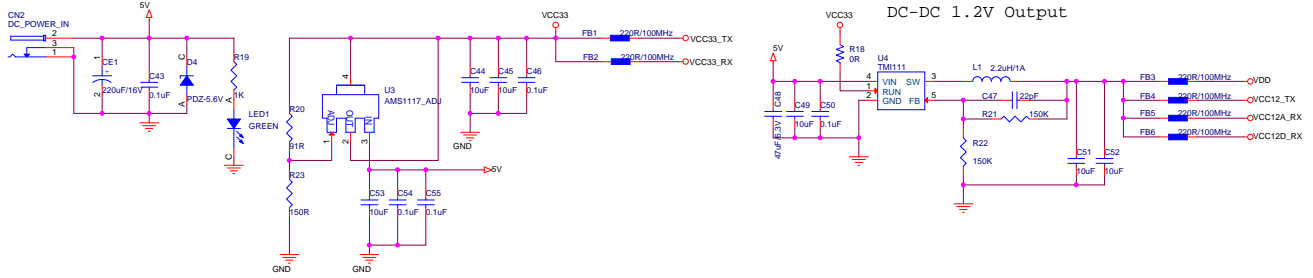
Note:

- (1) 1.2V power should be set up at least 1ms later than 3.3V power, the internal reset signal should be released after 1.2V power is ready.
- (2) External RESET# signal should be set up at least 1ms later than 1.2V. The 1.2V power supply enable pin can be connected to the 3.3V power supply.

DC 5V Input

LDO 3.3V Output

DC-DC 1.2V Output



File	LT8911UXC_EVB_HDMI2.0_TO_MIP&LVDS_V1_0		
Size	Document Number	AUDIO&POWER	Rev
A3			V1_0
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