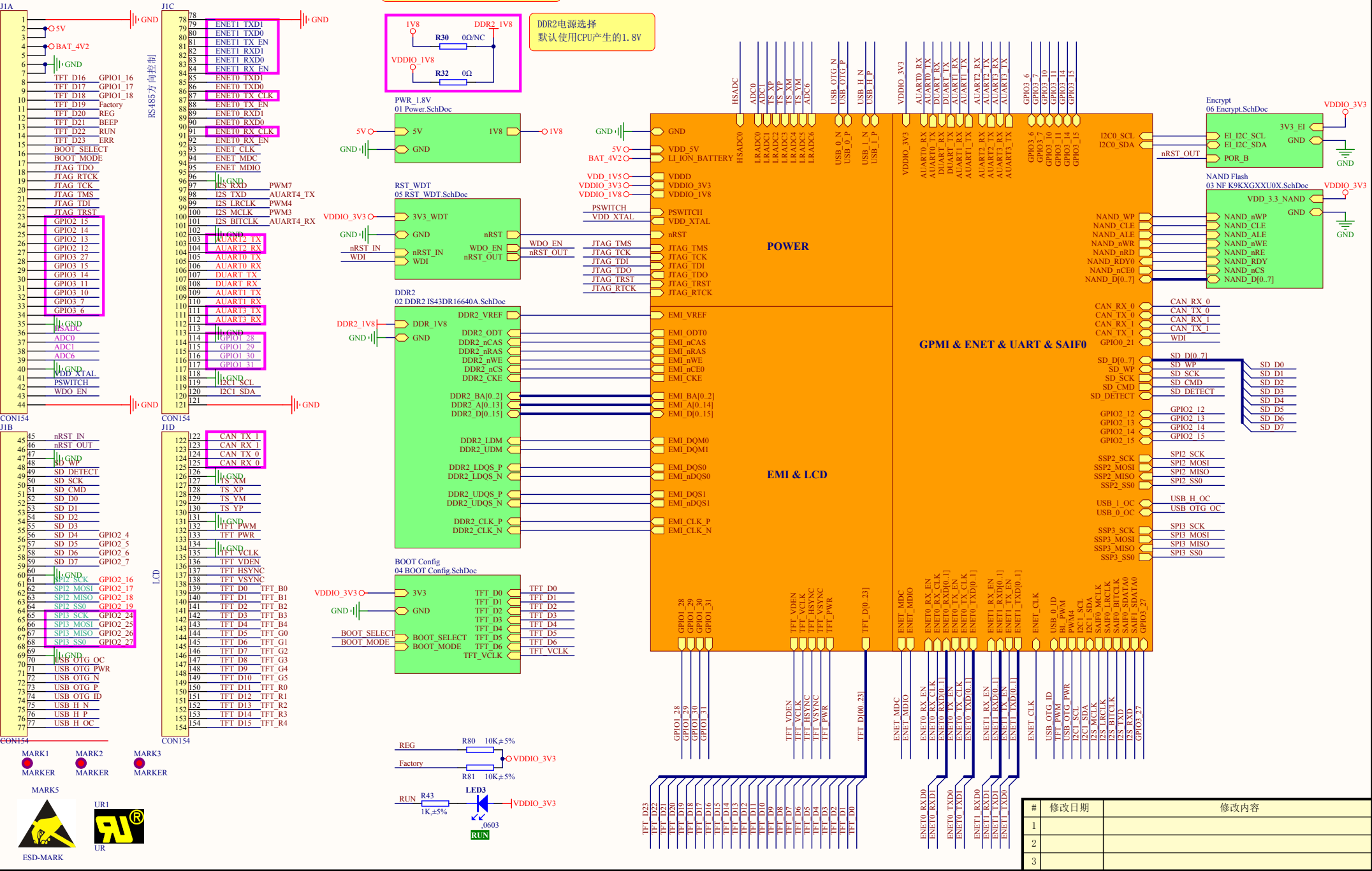
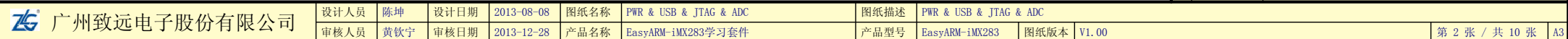


(1) EasyARM-iMX283工控核心板

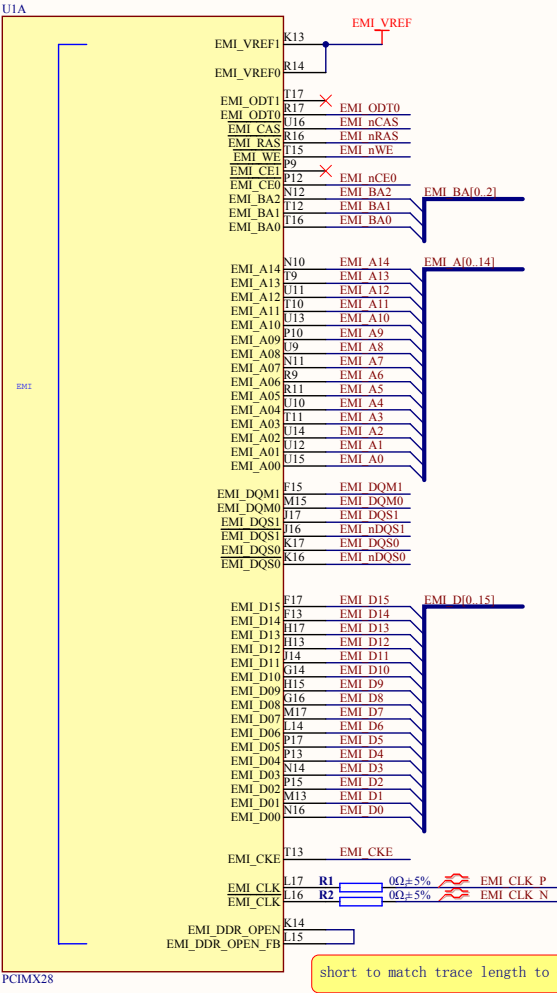
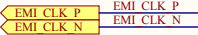
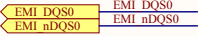
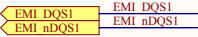
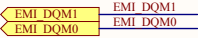
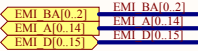
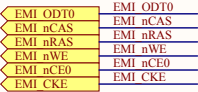
注：J1红框内的信号，i.MX287均支持
i.MX286支持CAN、GPIO3_27
i.MX283均不支持



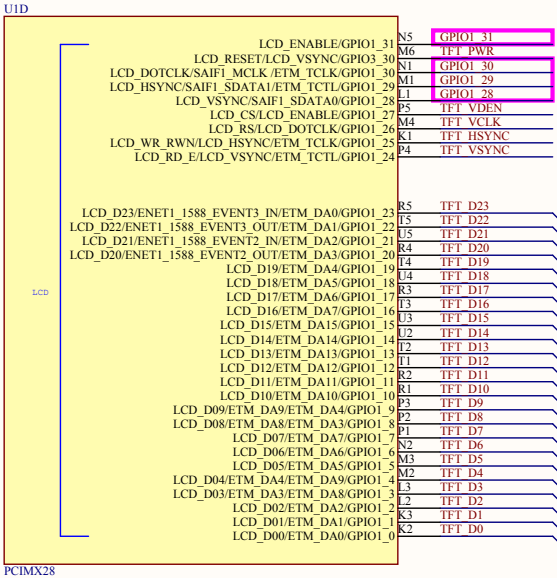


(3)CPU EMI和LCD接口

EMI_VREF



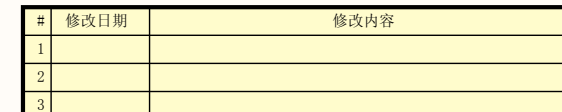
short to match trace length to CLK + DATA signals



i. MX283没有这4个GPIO

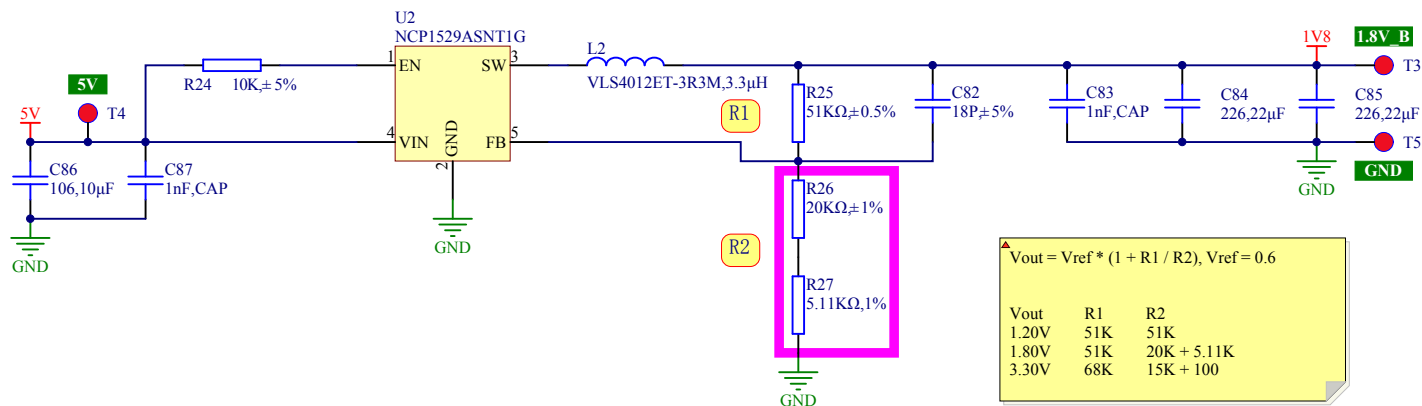
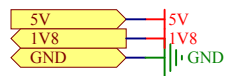
#	修改日期	修改内容
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(4) ENET & UART & PWM & SAIF



5. POWER

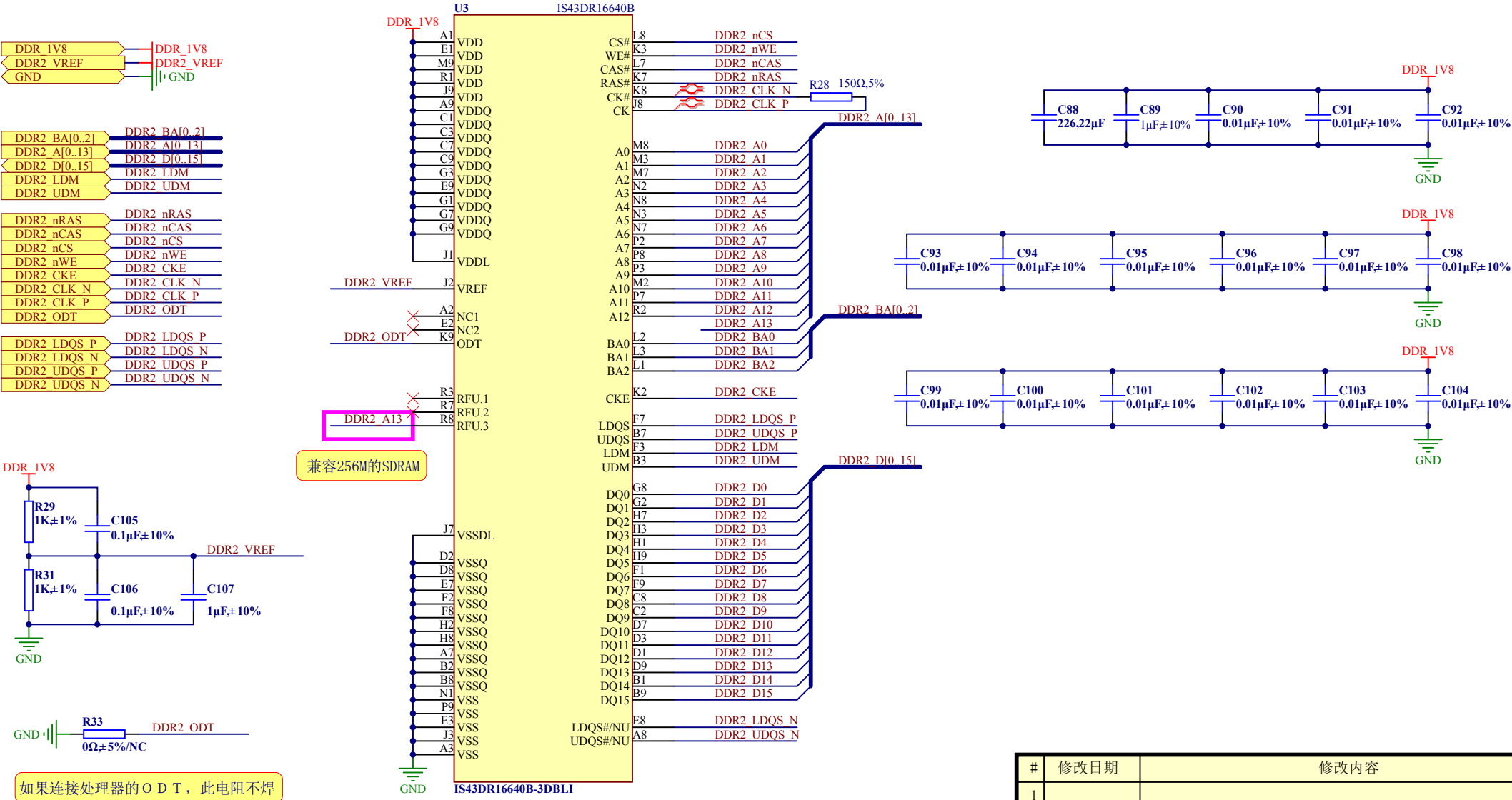
(5) 电源电路



#	修改日期	修改内容
1		
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3		

6. DDR2_IS43DR16640B

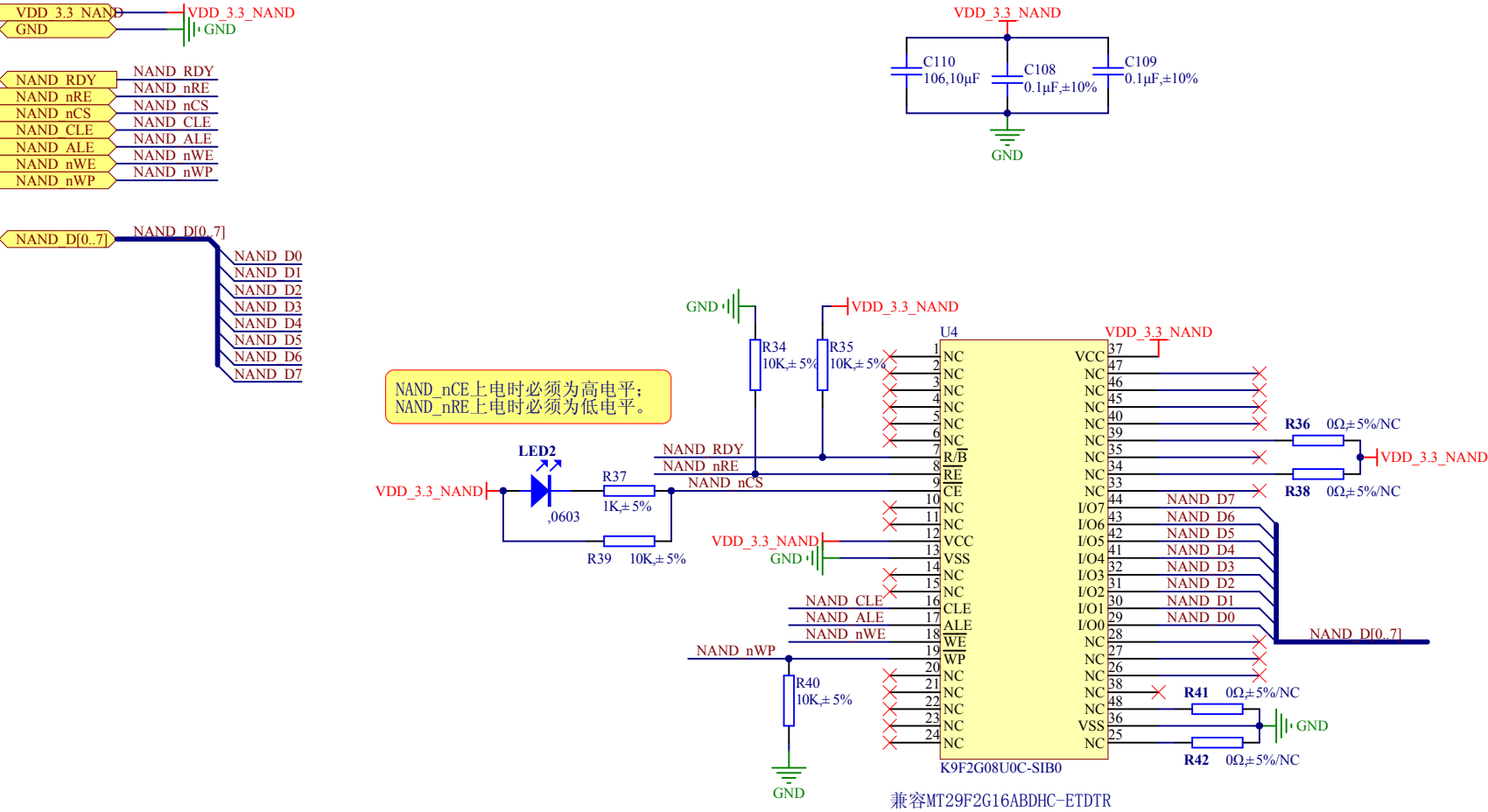
(6)DDR2_IS43DR16640B



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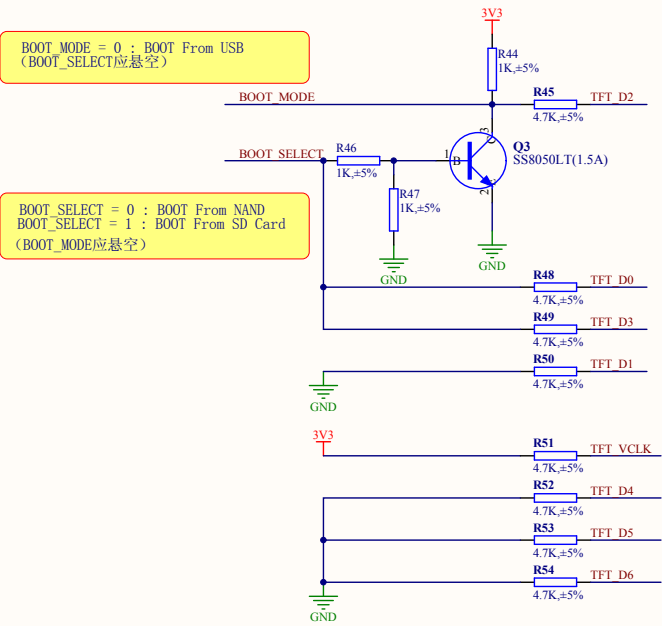
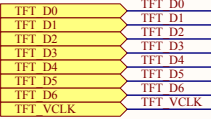
7. NAND Flash

(7)Nand Flash



#	修改日期	修改内容
1		
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(8)BOOT Config



BOOT Configuration				
	D3	D2	D1	D0
USB0	0	0	0	0
NAND	0	1	0	0
SD0	1	0	0	1

LCD_RS needs to be pulled HIGH to enable Resistor Boot Mode. if LCD_RS is pulled low, thei.MX28 will attempt to boot from OTP.

LCD_D04 for Voltage Select
Low = 3.3V
High = 1.8V

LCD_D05 = ETM Enable Bit:
Low = ETM disabled
High = ETM enabled

LCD_D06 for test mode

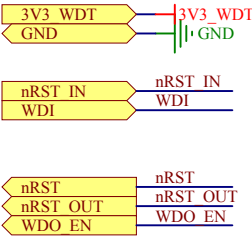
Table 2-1 Boot Mode Select

B3	B2	B1	B0	Boot Mode
0	0	0	0	USB0
0	0	0	1	I2C0
0	0	1	0	SPI2
0	0	1	1	SPI3(Flash)
0	1	0	0	GPMI(NAND)
0	1	0	1	Reserve
0	1	1	0	JTAG
0	1	1	1	Reserve
1	0	0	0	SPI3(EEPROM)
1	0	0	1	SSP0(SD0)
1	0	1	0	SSP1(SD1)
1	0	1	1	Reserve
1	1	0	0	Reserve
1	1	0	1	Reserve
1	1	1	0	Reserve
1	1	1	1	Test mode

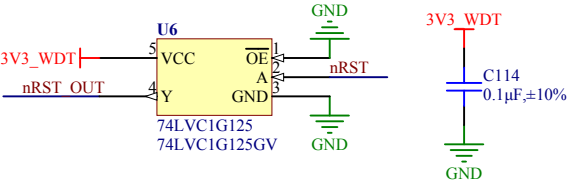
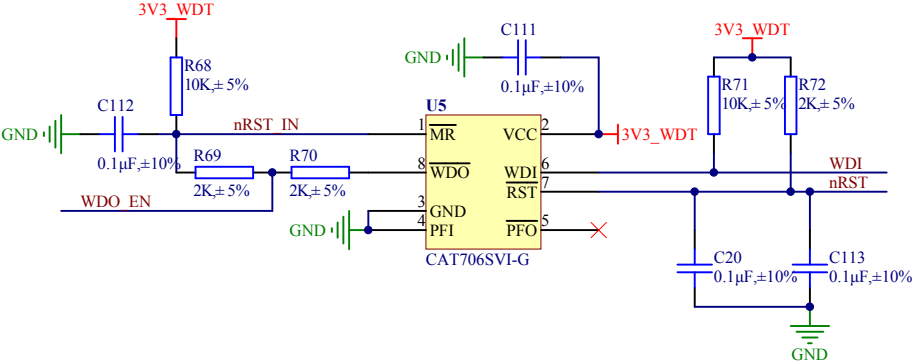
#	修改日期	修改内容
1		
2		
3		

9. RST_WDT

(9) 看门狗复位



3. 3V -> 看门狗输出禁止
悬空 -> 看门狗使能

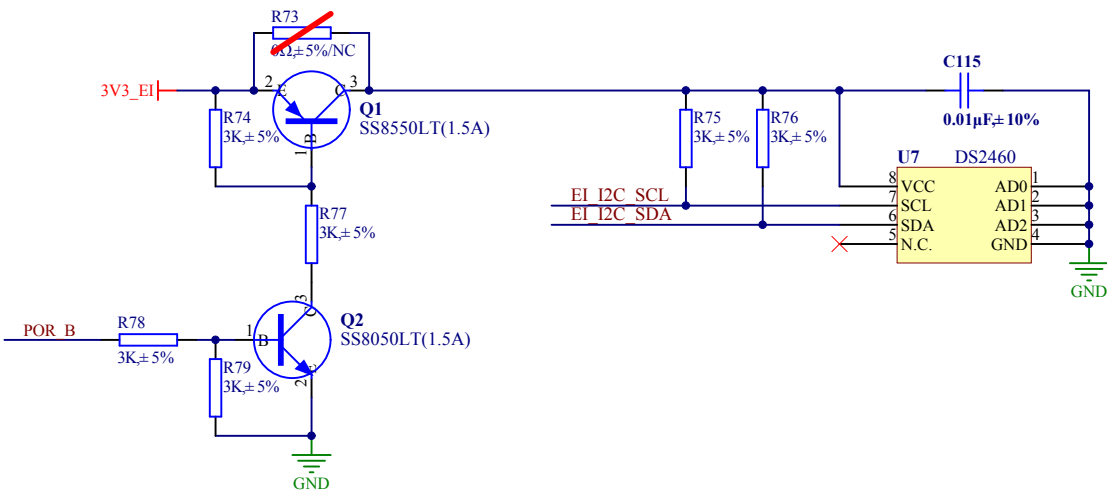
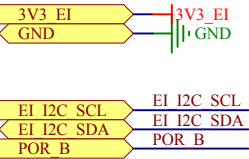


做核心板的时候，往底板输出复位信号，
如果不需要，可以删除该部分

#	修改日期	修改内容
1		
2		
3		

10. Encrypt

(10) 加密电路



#	修改日期	修改内容
1		
2		
3		