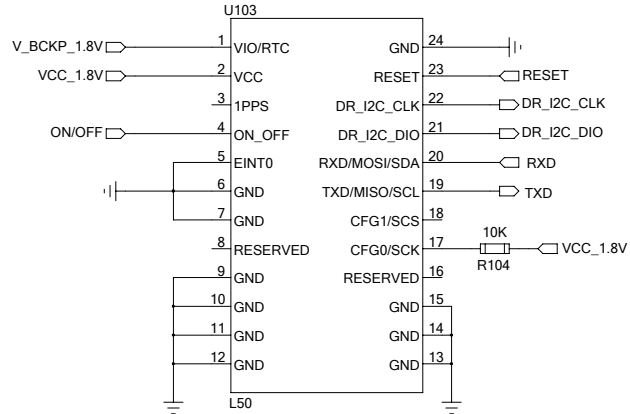
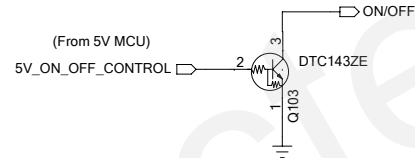


MODULE



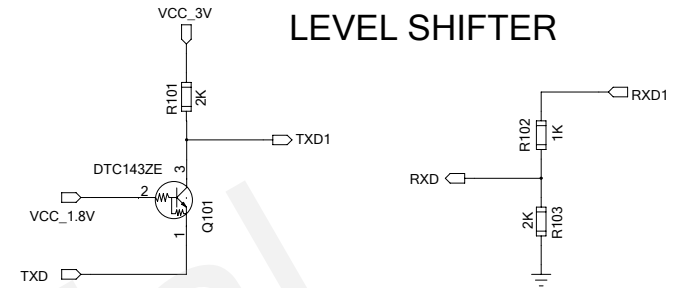
- 1.For UART mode,CFG0/SCK pin should be pulled up to VCC_1.8V.
- 2.The RXD pin must be used in UART mode.

ON_OFF



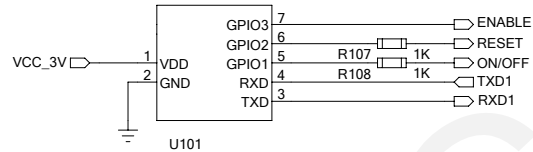
If the control signal for ON_OFF pin is 5V level, this circuit should be used.

LEVEL SHIFTER



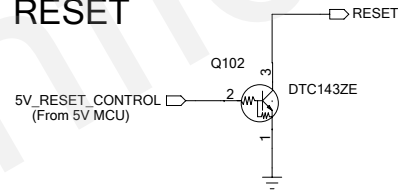
VOH is 1.8V and VIH is 3.6V tolerant,so level shifter circuit should be used sometimes.For different level system,the voltage divider should be adjusted.

MCU



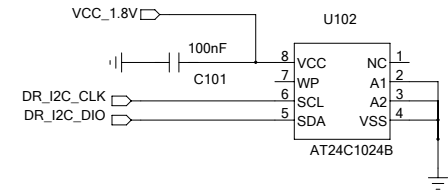
- 1.The circuit above symbolizes MCU,of which voltage is 3V or 3.3V.
- 2.If pull down ON/OFF pin for about 1ms and then release it,L50 module will switch working mode between Full_on and Hibernate.
- 3.L50 module provides a RESET pin for emergency reset,which is low active. It is only used in the event of a malfunction.

RESET



If the control signal for RESET pin is 5V level, this circuit should be used.

EEPROM

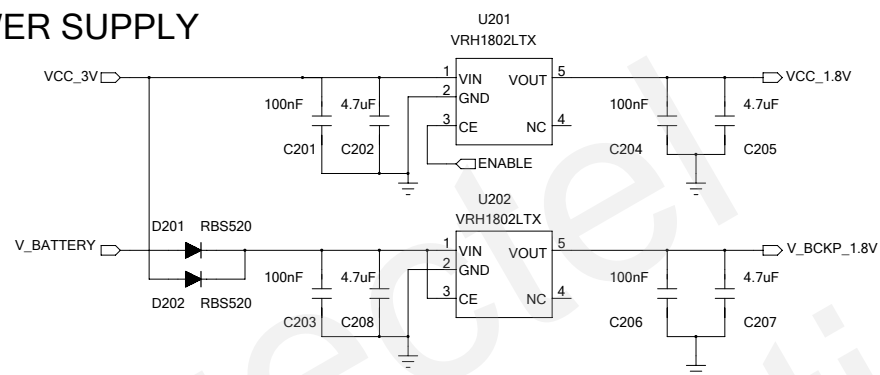


If customers want to use CGEE function,a 1M-bit EEPROM must be used.For detailed information about CGEE,please refer to L50 Haredware Design.

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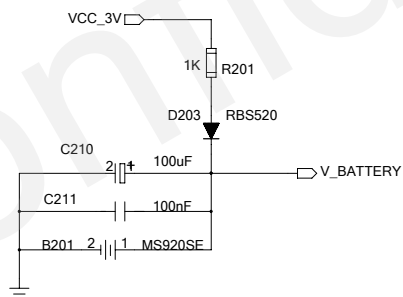
DRAWN BY Baly BAO	PROJECT L50	TITLE L50-UART Reference Design
CHECKED BY Yong AN	SIZE A2	VER V2.0
SHEET 1 of 2		2013.04.11

POWER SUPPLY



1. Current of VCC_1.8V should be no less than 100mA.
2. V_BCKP_1.8V supplies power for RTC and CMOS I/O.

BATTERY



MS920SE is a rechargeable battery, which supplies power for RTC and CMOS I/O, when VCC_3V is cut off.

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DRAWN BY Baly BAO	PROJECT L50	TITLE L50-UART Reference Design
CHECKED BY Yong AN	SIZE A2	VER V2.0
SHEET 2 of 2	2013.04.11	