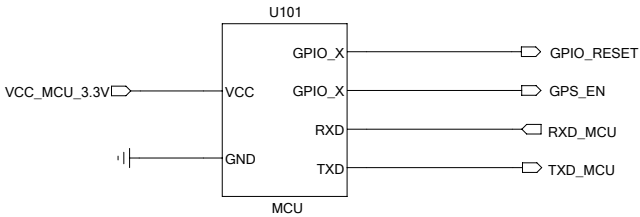
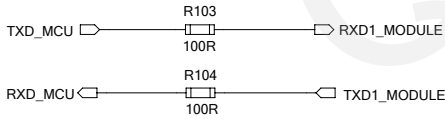


# Power Suppply and UART Circuit

## Customer's MCU

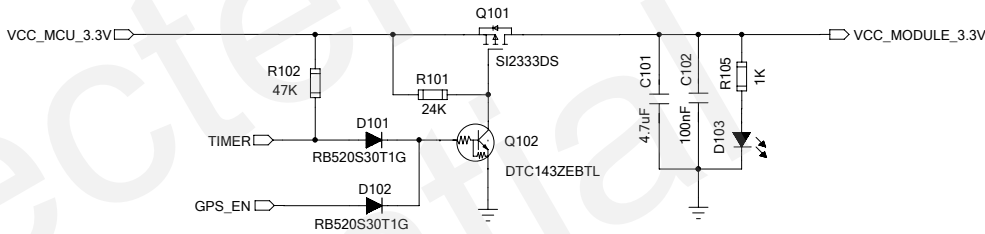


## UART Circuit



R103,R104 are rserved for debugging the waveform of UART, and they can prevent L80 module from ESD interference. In general, 100R for R103 and R104 is recommended, but 0R also works well.

## Power Management Circuit



TIMER: An open drain output signal can be used to control L80's main power on/off. Please Note that when TIMER function is used in L80 module, ensure that V\_BCKP is constantly supplied with power.

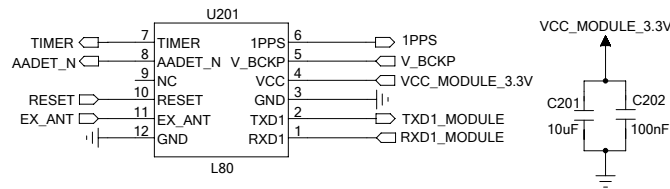
For more details about TIMER, please refer to L80 Hardware Design.

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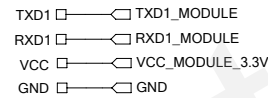
DRAWN BY <Tony GAO & Rain ZHOU>	PROJECT <L80>	TITLE <L80_Reference_Design>
CHECKED BY <Ray XU>	SIZE A2	VER <1.02>
SHEET 1 of 3		<2013.08>

# Module Interface

## Module Interface

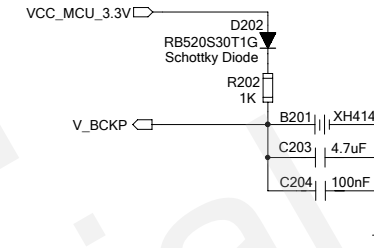


## Test Points



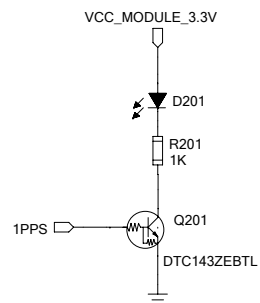
1. UART can be used to output NMEA message as well as to upgrade firmware.
2. The test points are reserved for debugging the GPS module.

## Charging Circuit for RTC Domain



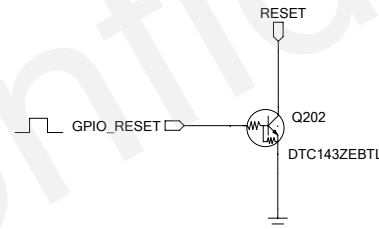
The V\_BCKP pin can be directly supplied power by an external rechargeable battery. Furthermore, it is necessary to add an external charging circuit for rechargeable battery.

## Indicating Circuit



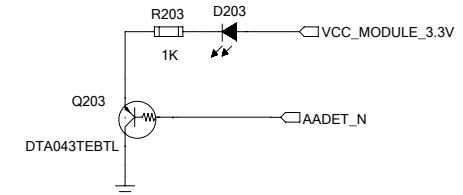
The 1PPS indicator will blink at 1Hz frequency after fixing the position.

## Reset Circuit



1. If the reset function is unused, the RESET pin can be connected to the VCC directly.
2. RESET has been pulled up internally.

## Indicating Circuit for Active Antenna



1. The pin "AADET\_N" also can be used to indicate the status of active antenna. When active antenna is not connected to EX\_ANT or has poor contact with antenna feeding point, AADET\_N will keep a high level to indicate the active antenna absent. AADET\_N will change to a low level when active antenna is connected well.
2. If unused, please keep AADET\_N pin open.

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# Antenna Interface

## Inner Patch Antenna

1. Keep at least 10mm distance to the nearest edge of the mother board. It will be better for L80 to be placed in the center of the mother board.

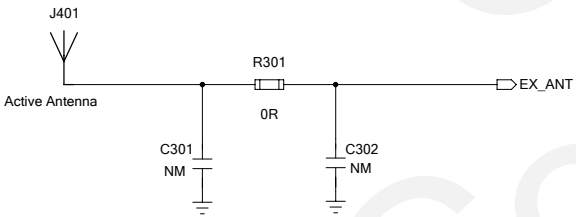
2. Keep enough distance between L80 antenna and tall components ( $h>6\text{mm}$ ) and the minimum  $d$  is 10mm.

3. Put L80 on the top of the device, which can guarantee antenna to face to open sky and achieve good receiving performance during operation.
4. Device enclosure should be made of non-metal materials especially around antenna area.  
The minimum distance between antenna and enclosure is 1mm.

5. It is recommended that the mother board is bigger than  $80\text{mm}\times 40\text{mm}$  for the better performance, and pour ground copper on the whole mother board

6. Other antennas such as BT/WIFI/GSM should be kept minimum 10mm distance far away from the embedded patch antenna in L80.  
For more details, please refer to L80 Hardware Design.

## EXT Active Antenna



- By default, C301 and C302 are not mounted, R301 is 0R.
1. Pi circuit (C301,R301,C302) is reserved for impedance matching for antenna.

2. R301 must not be a capacitance, because DC will flow from the R301 to the antenna.

3. Impedance of RF trace should be controlled by 50 ohm and the length should be kept as short as possible.
- For more details, please refer to L80 Hardware Design.

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