

MBCC-IO		(Micro Board Carrier Card)	
Avnet Design Services		HKADS-13-04-HW-0400	
www.microzed.org			
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MBCC-IO


10 Dec 2013

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 Avnet Engineering Services		
Title: 01 Cover		
Size: B	Document Number: MBCC-IO	Rev: D
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U_01-Cover
01-Cover.SchDoc

U_06-Revision
06-Revision.SchDoc

U_03-uHeader_JX1
03-uHeader_JX1.SchDoc

U_04-uHeader_JX2
04-uHeader_JX2.SchDoc

U_05-Power
05-Power.SchDoc

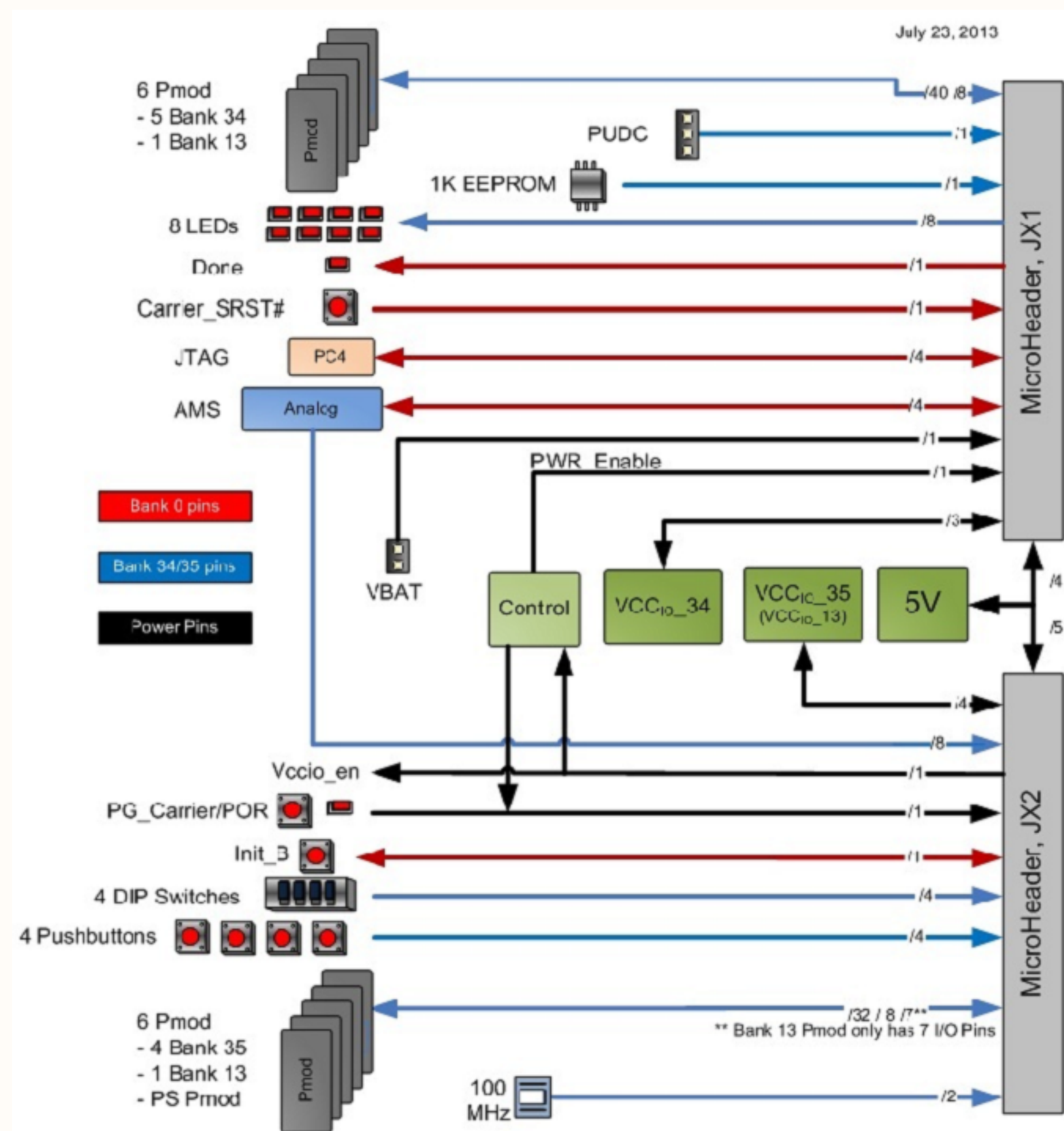
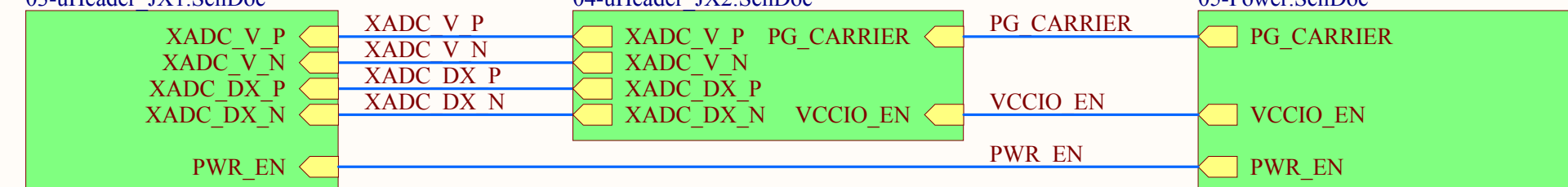


Figure 1 - Digilent I/O Expansion™ compatible interface Carrier Block Diagram

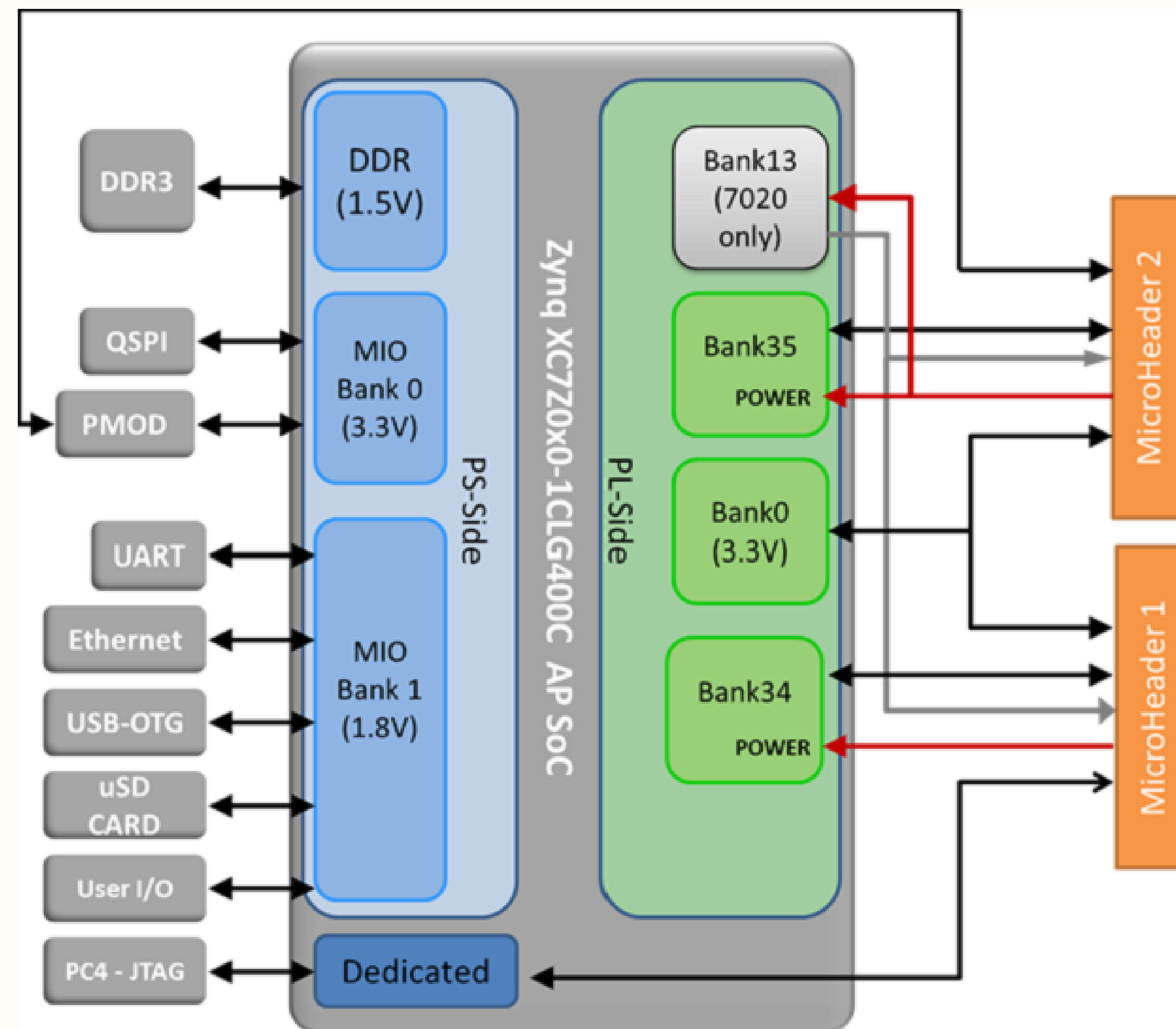
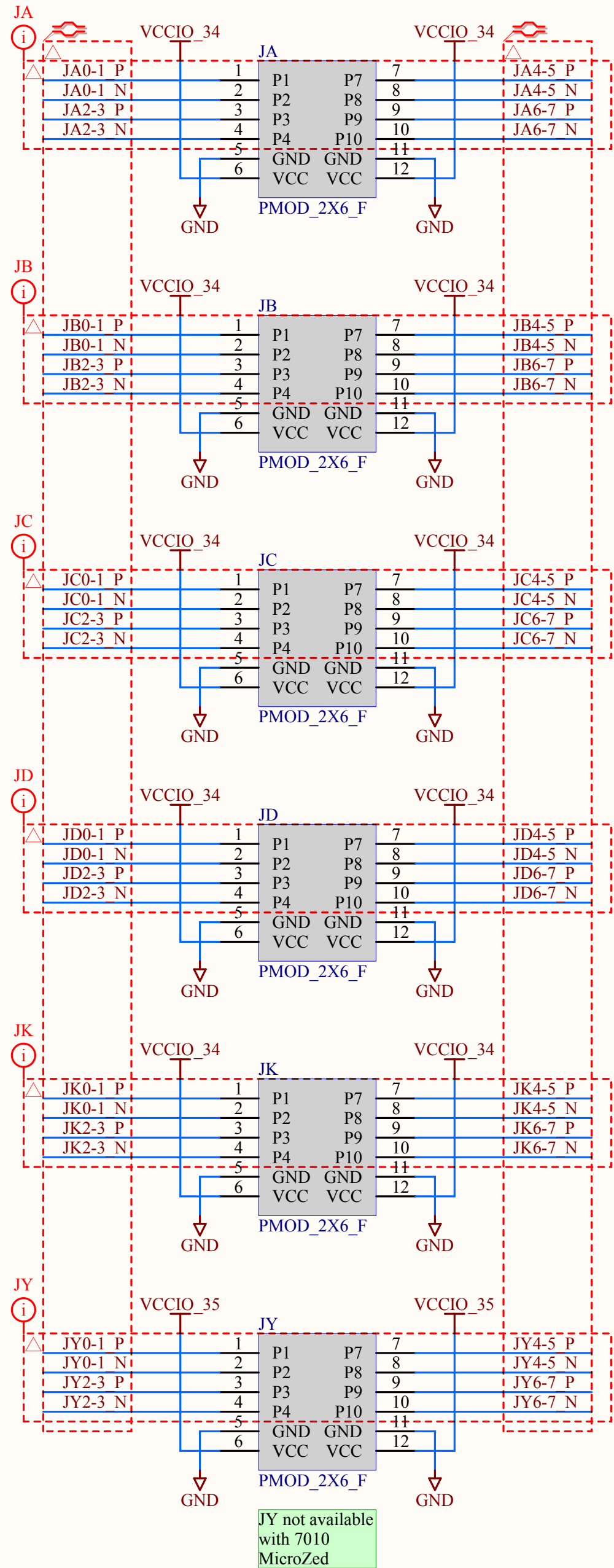
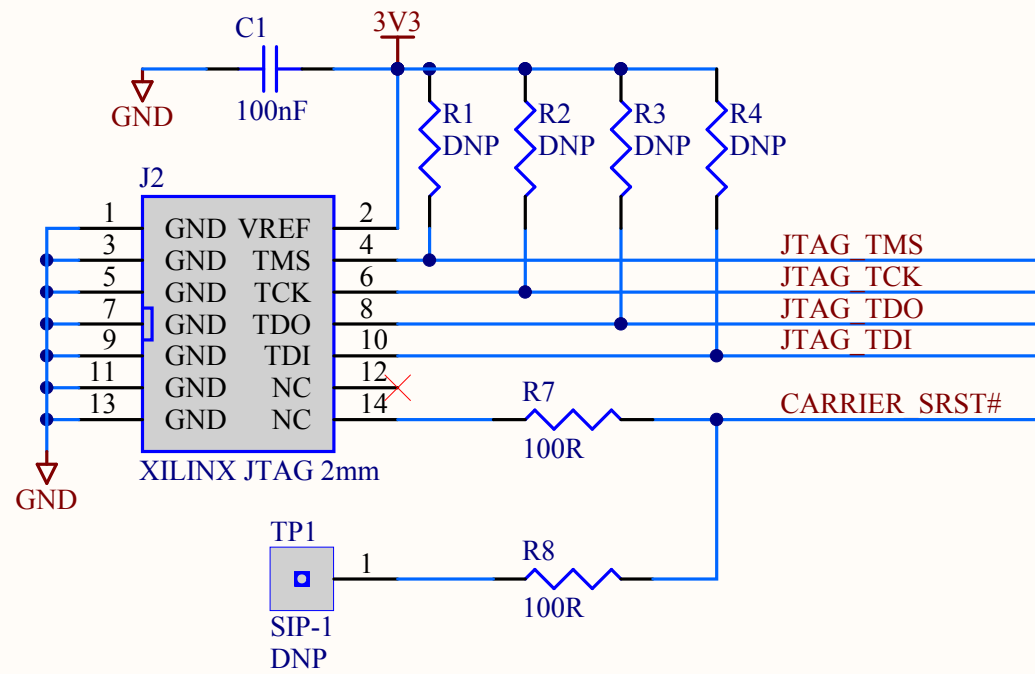


Figure 2 - Zynq Z70x0 CLG400 Bank Assignments

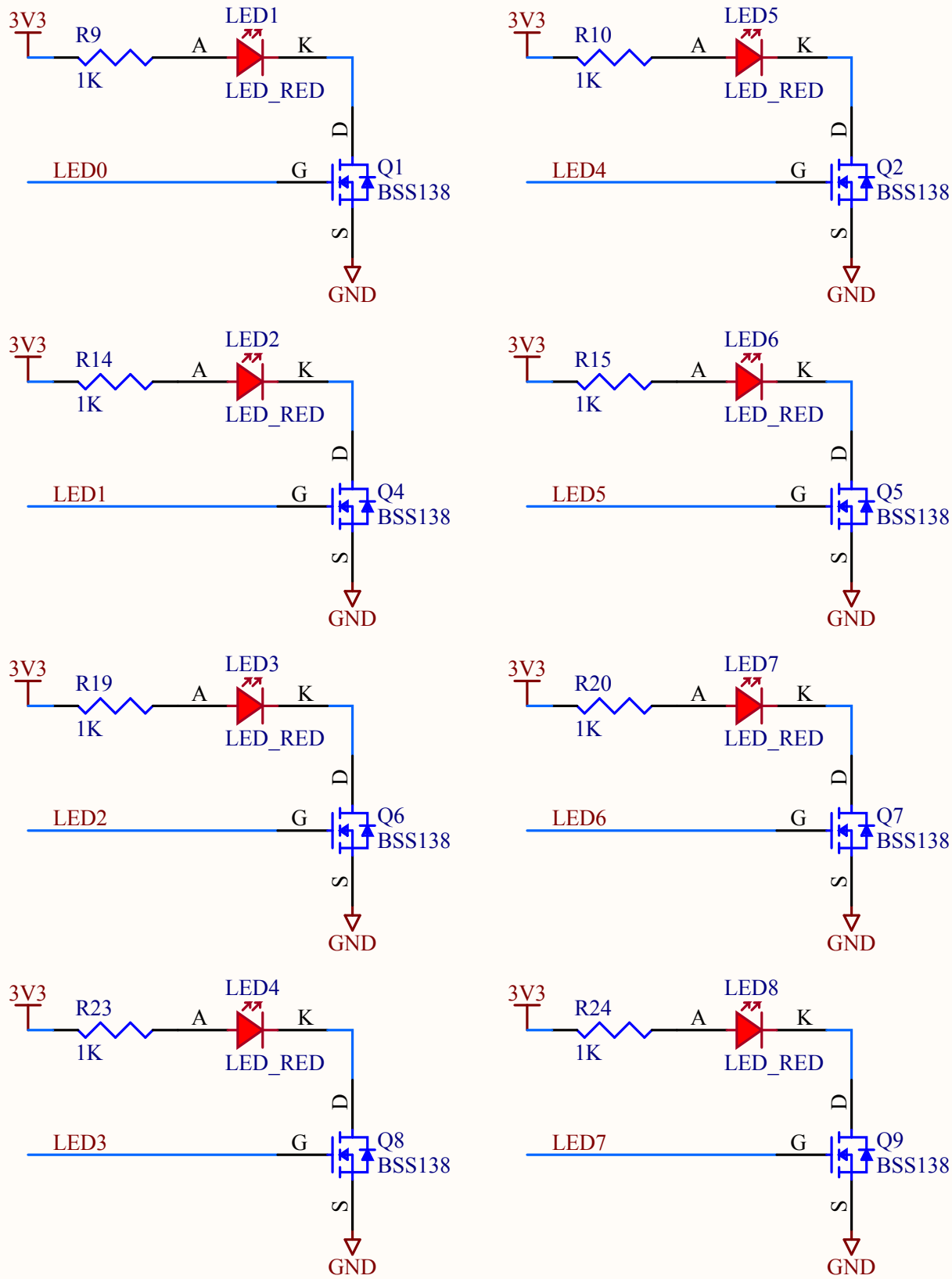
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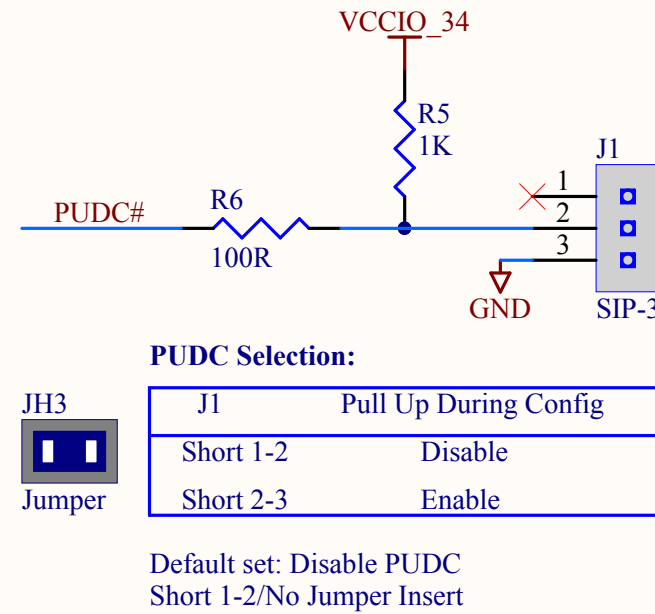
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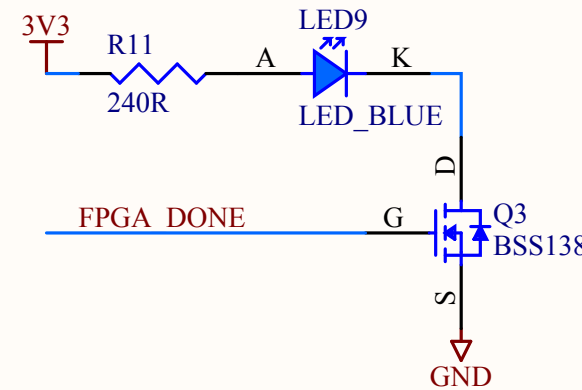
User LEDs



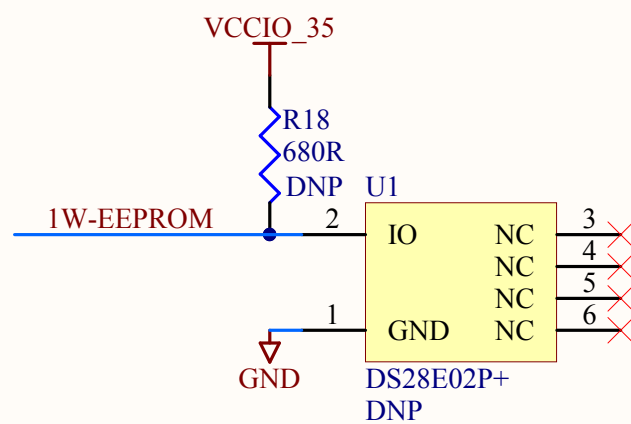
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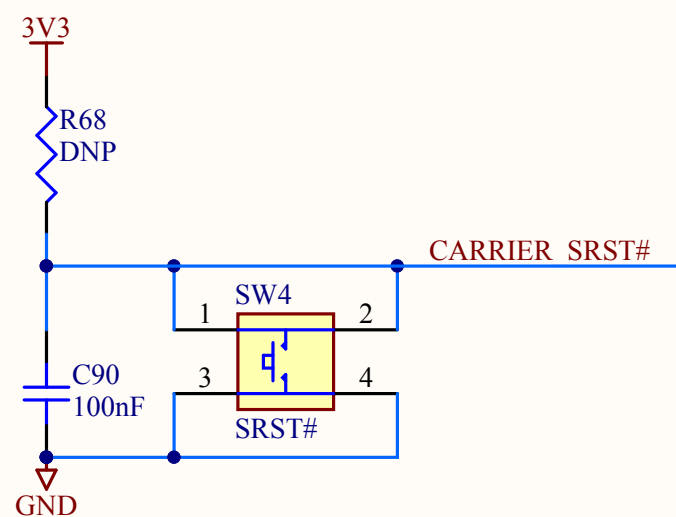
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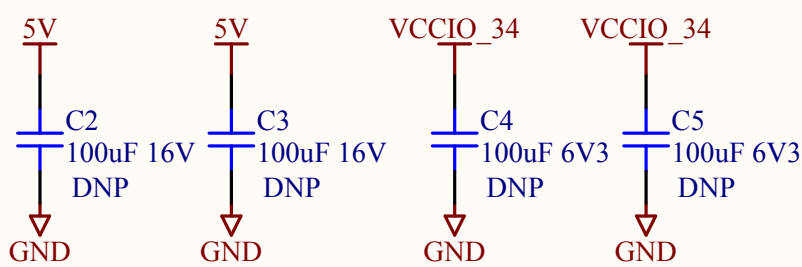
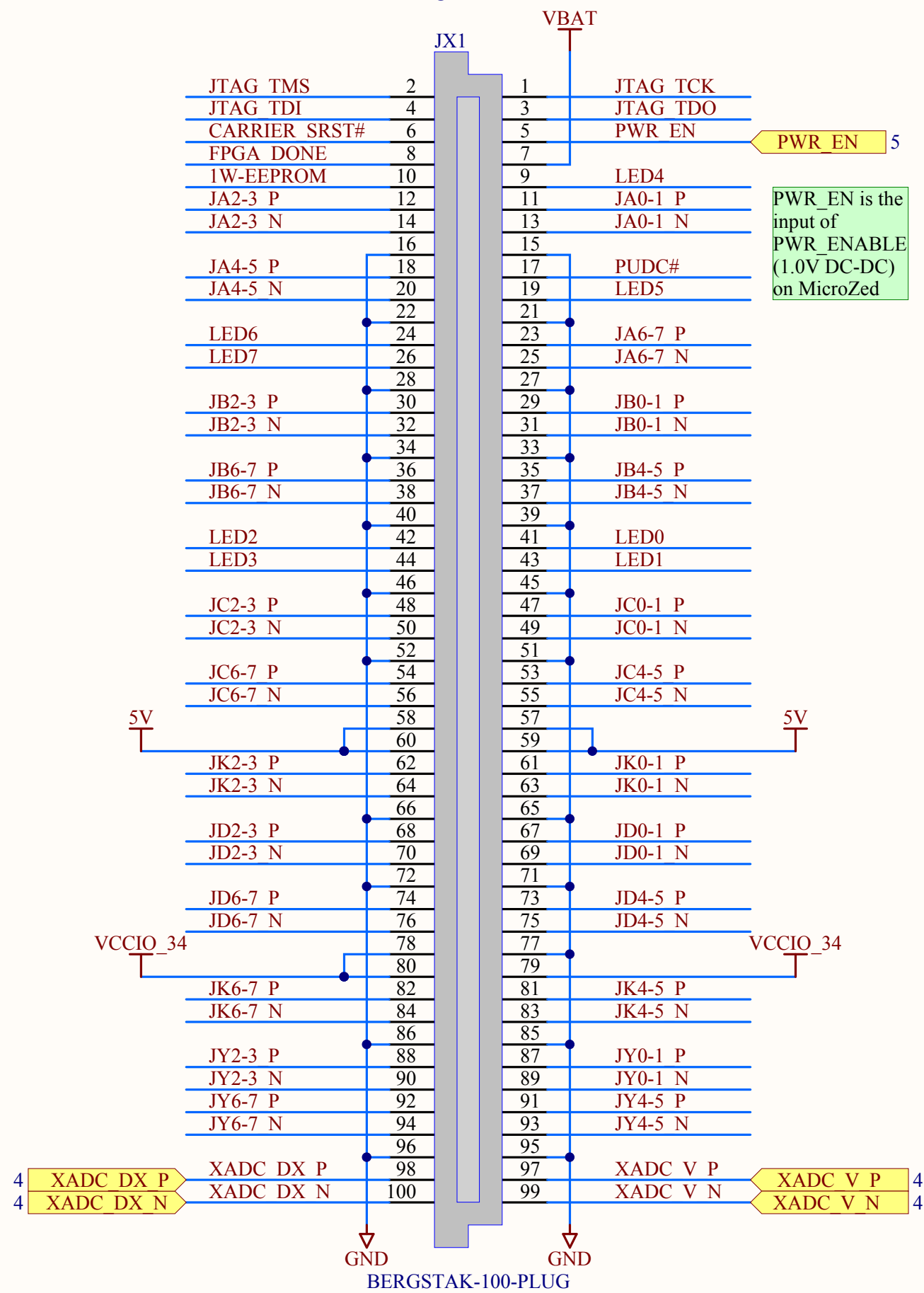
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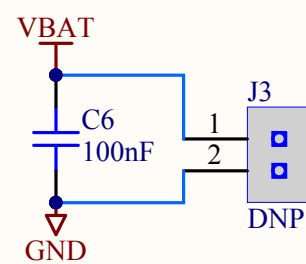
Reset Ctrl



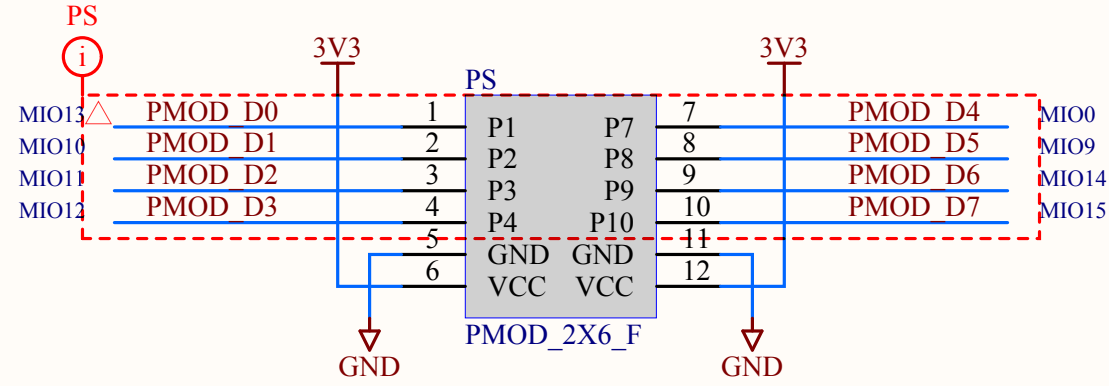
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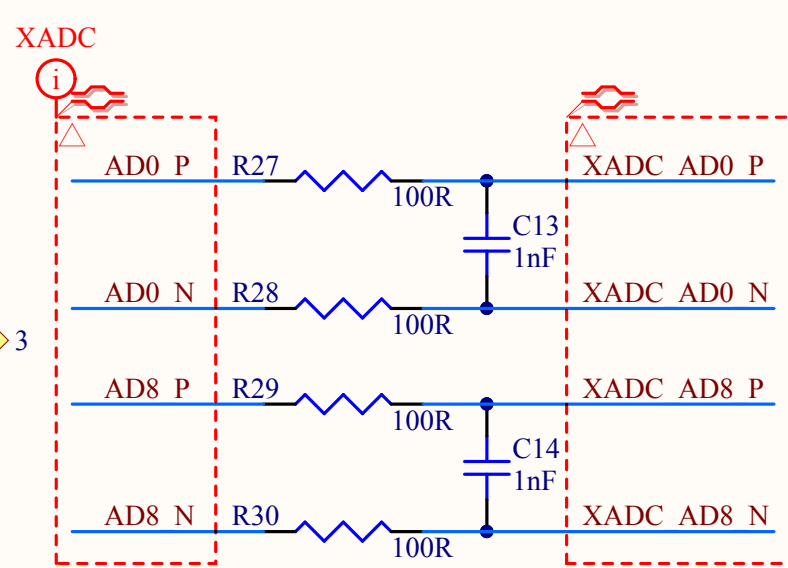
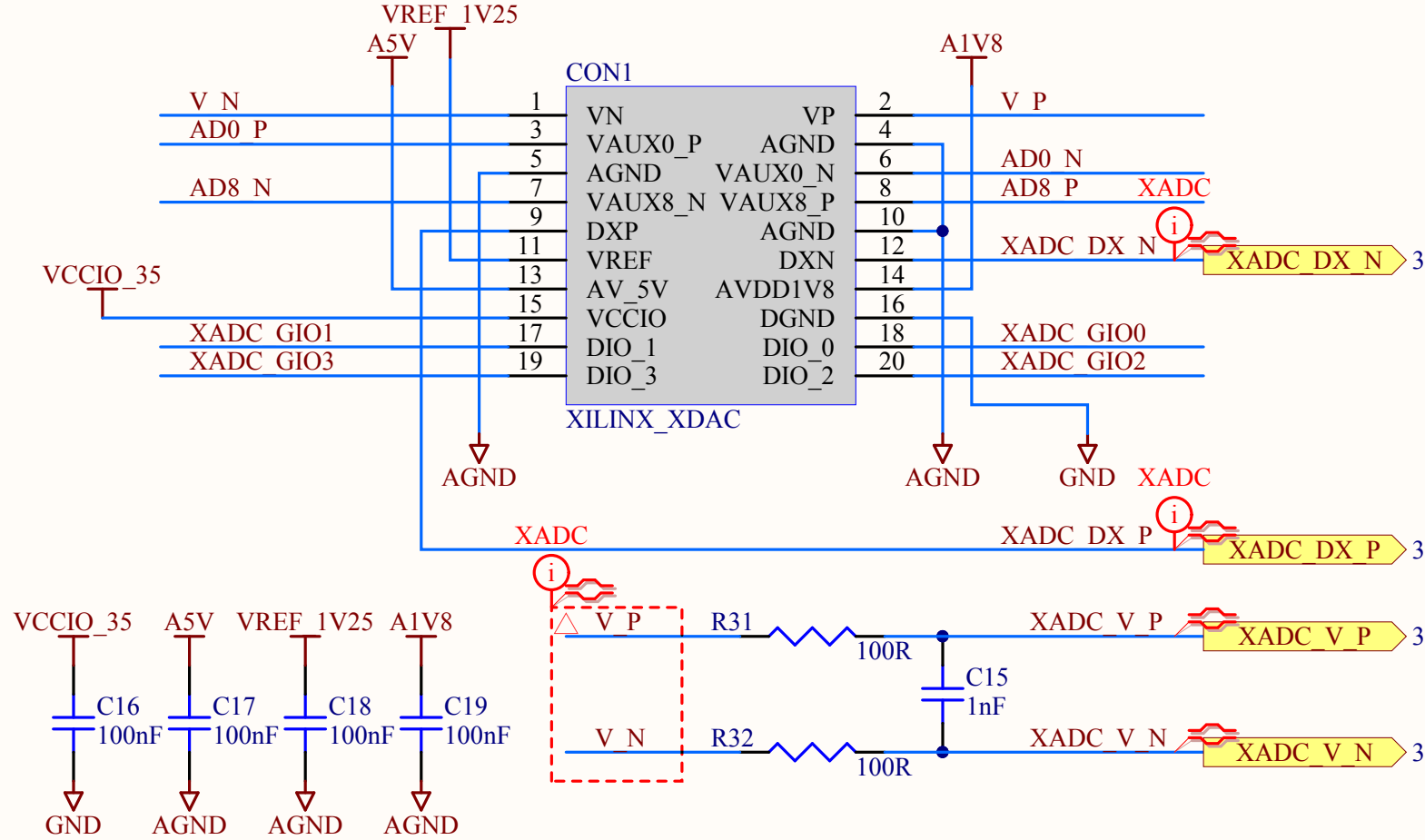
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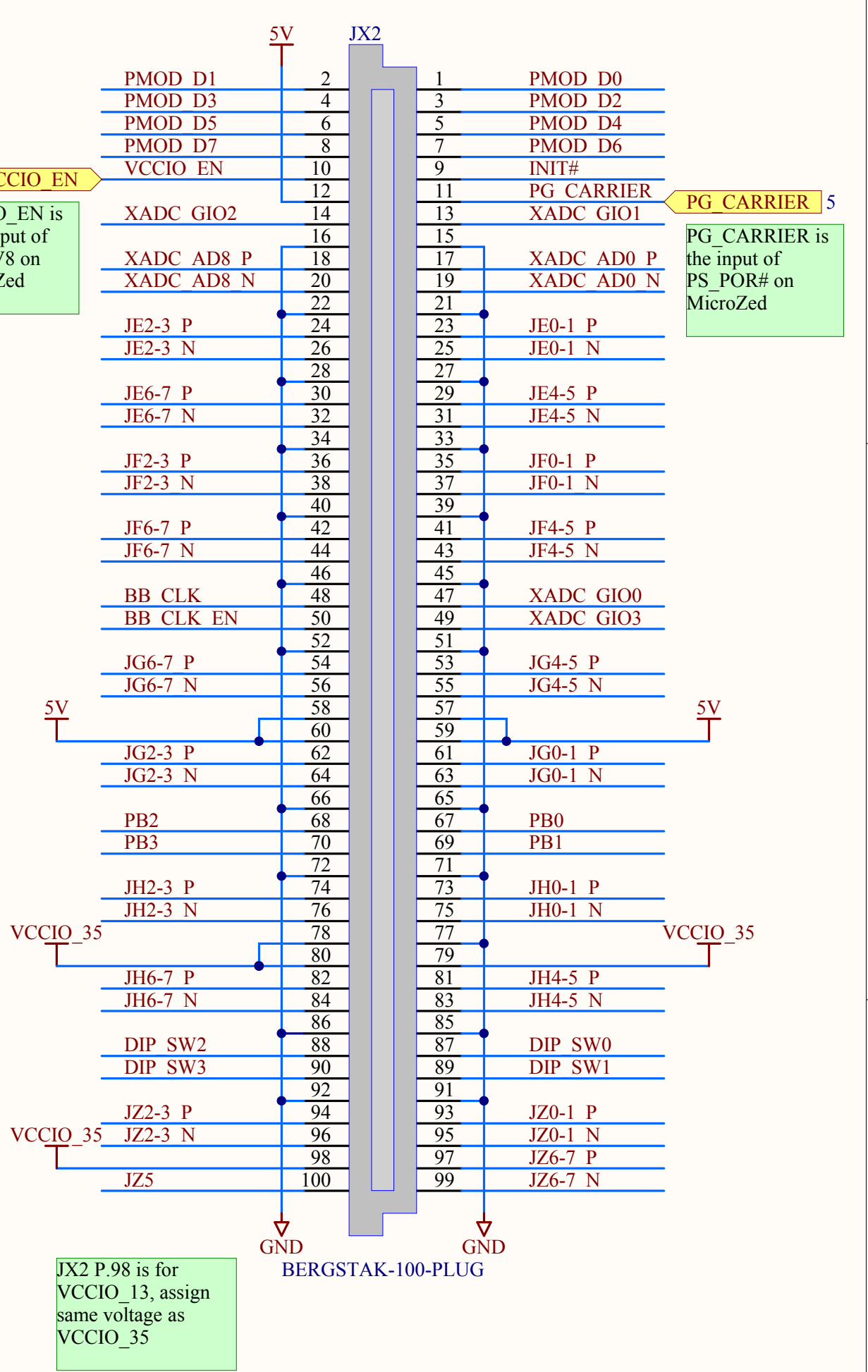
PMODs



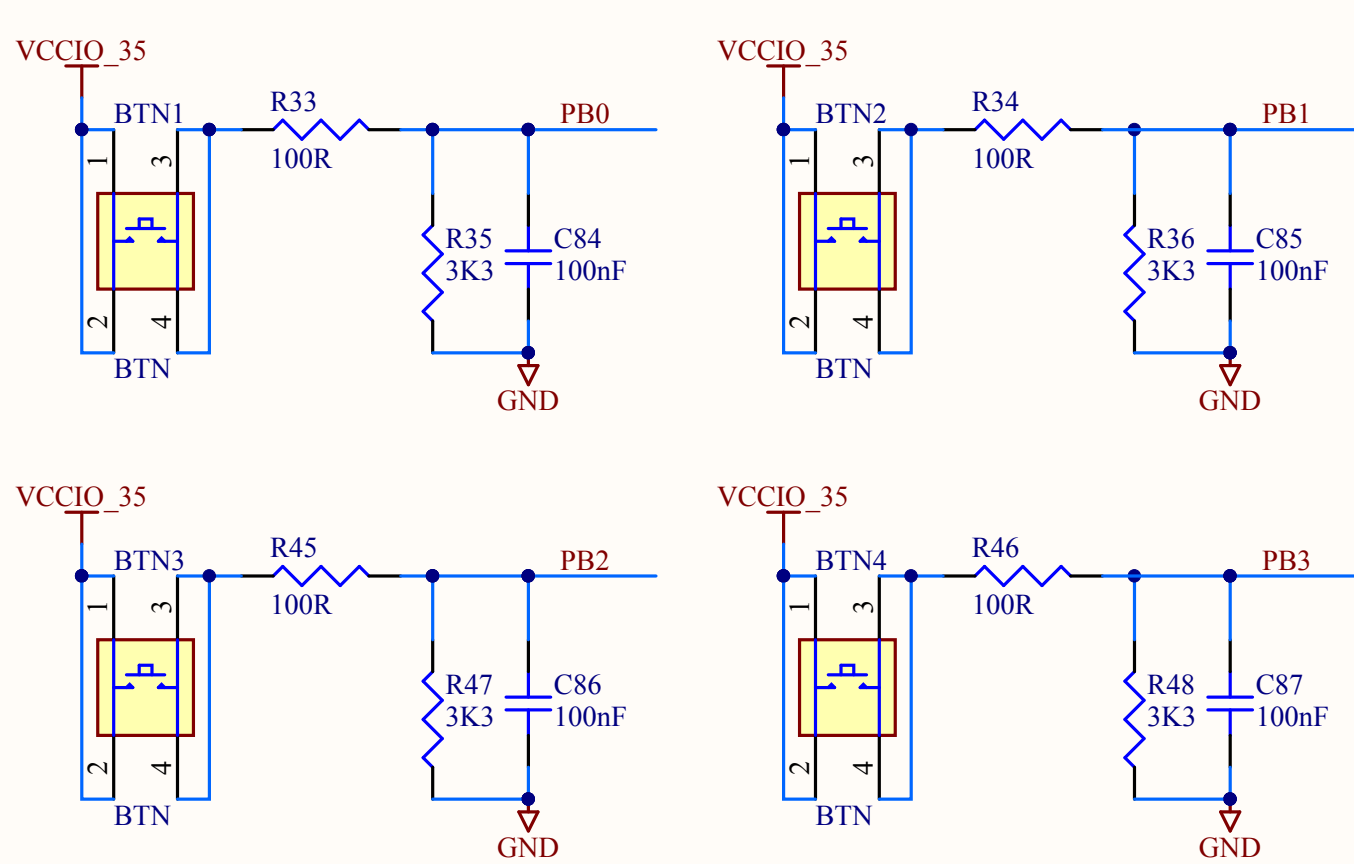
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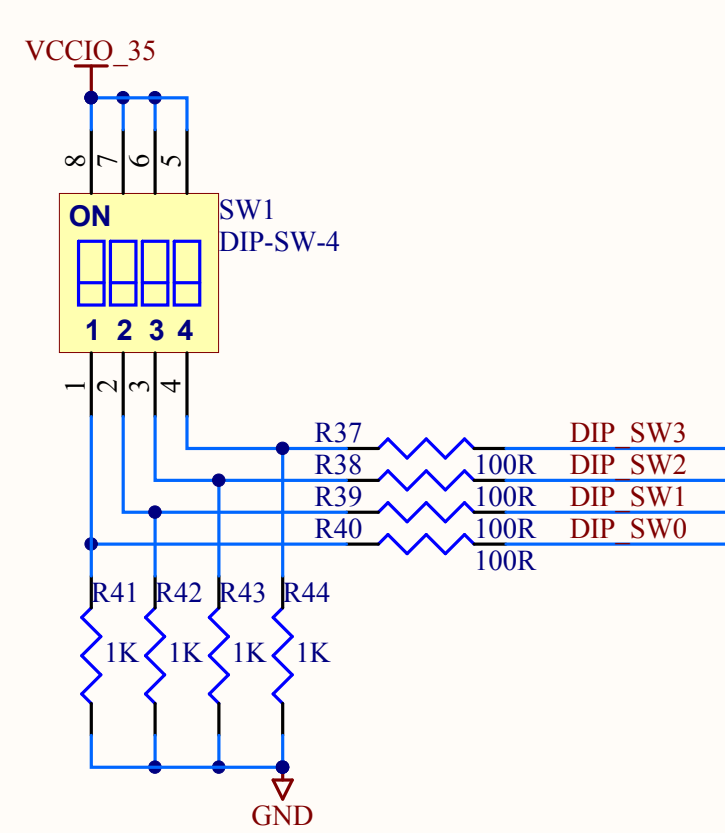
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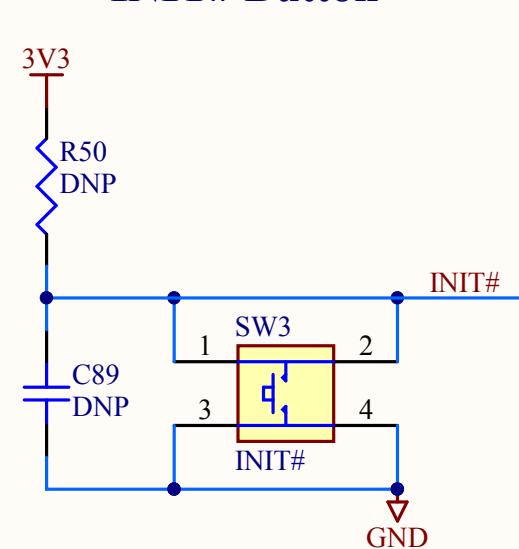
User Push Buttons



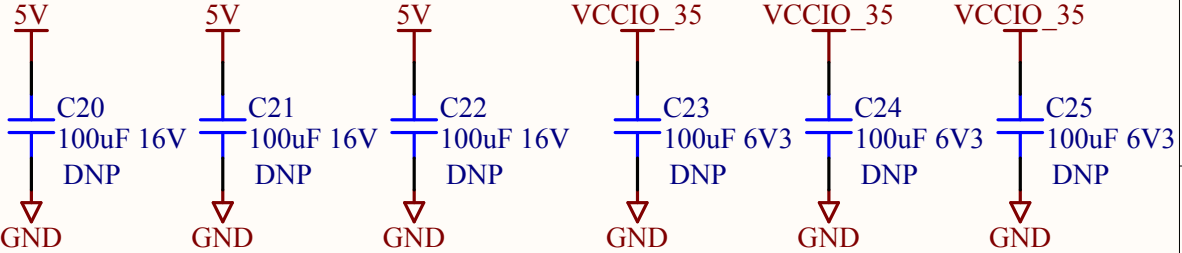
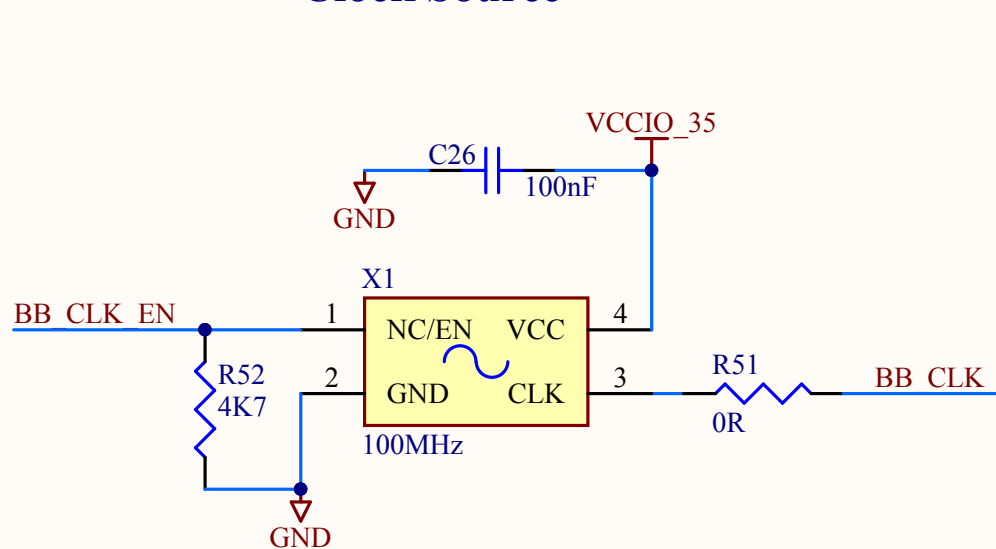
DIP Switches



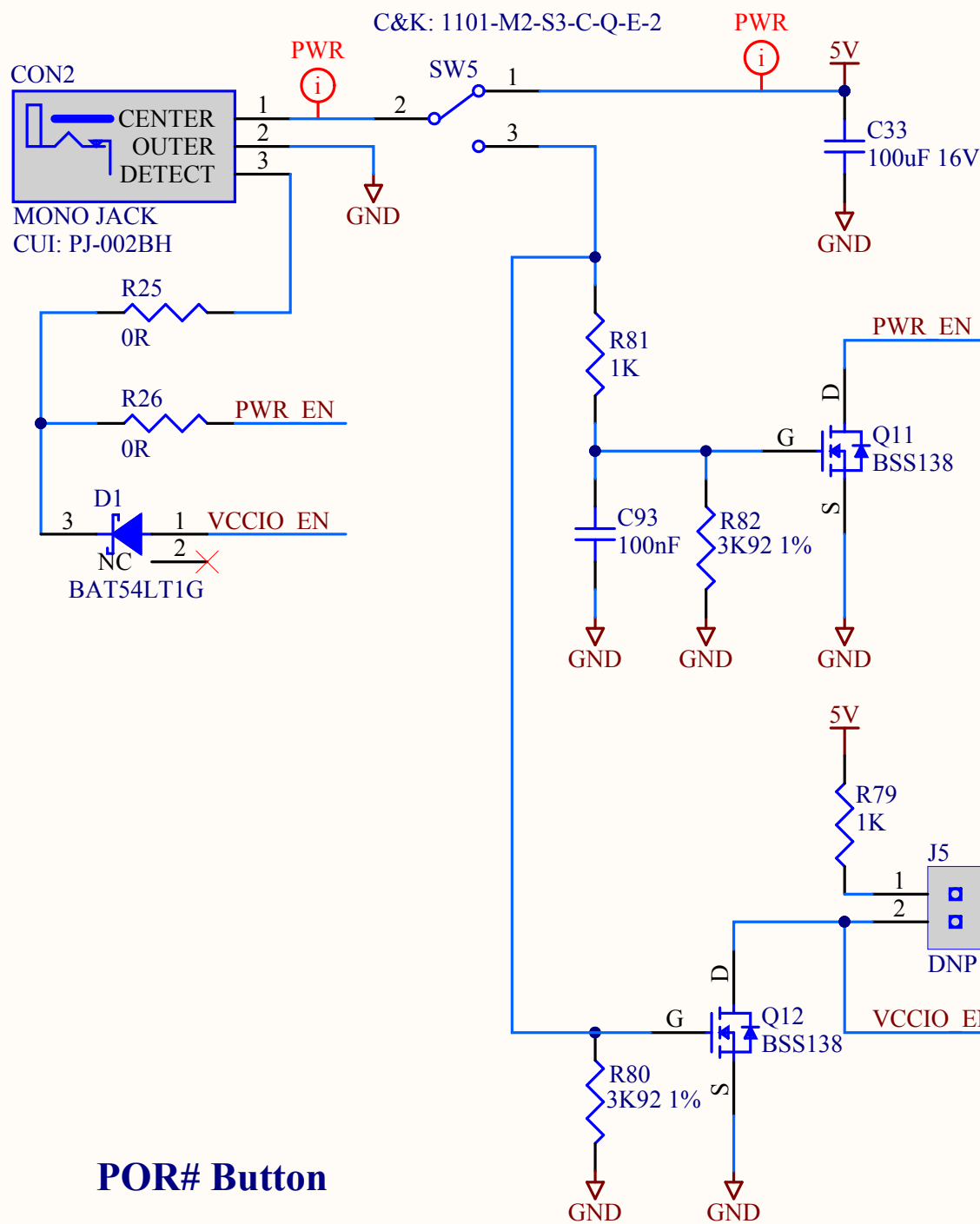
INIT# Button



Clock Source

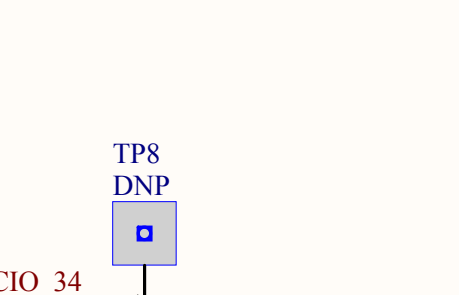
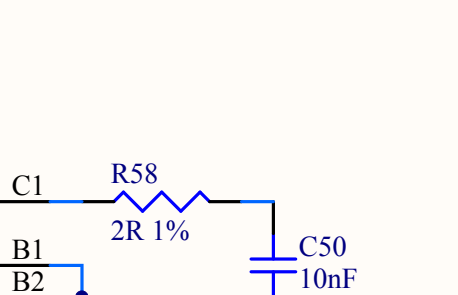
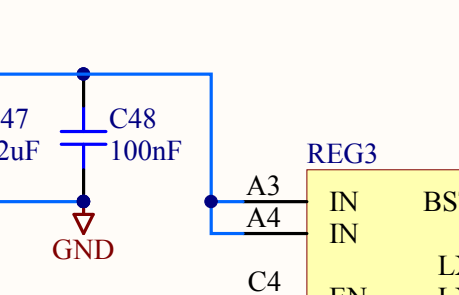
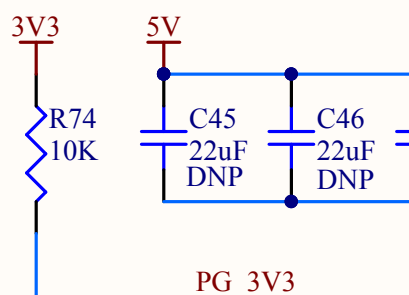
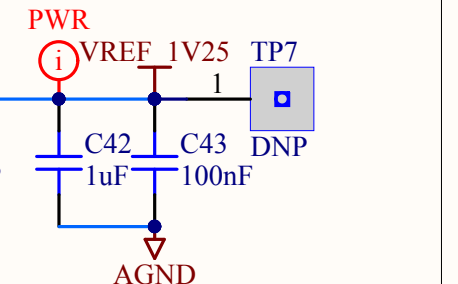
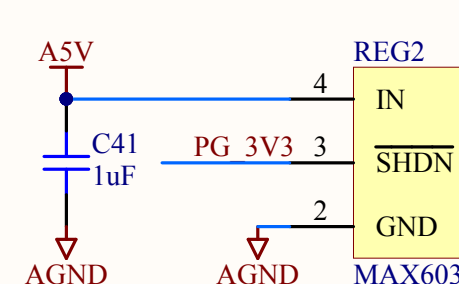
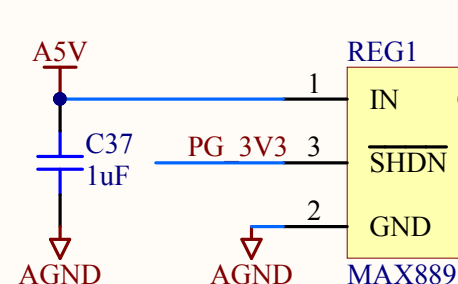
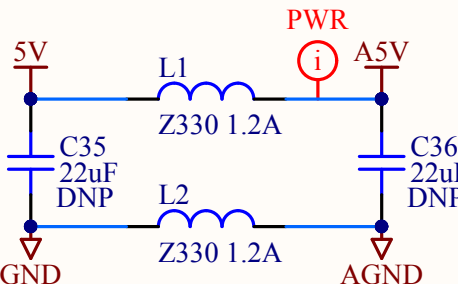
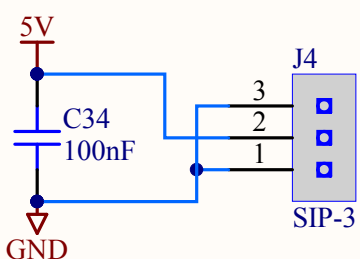


Center 5V Only

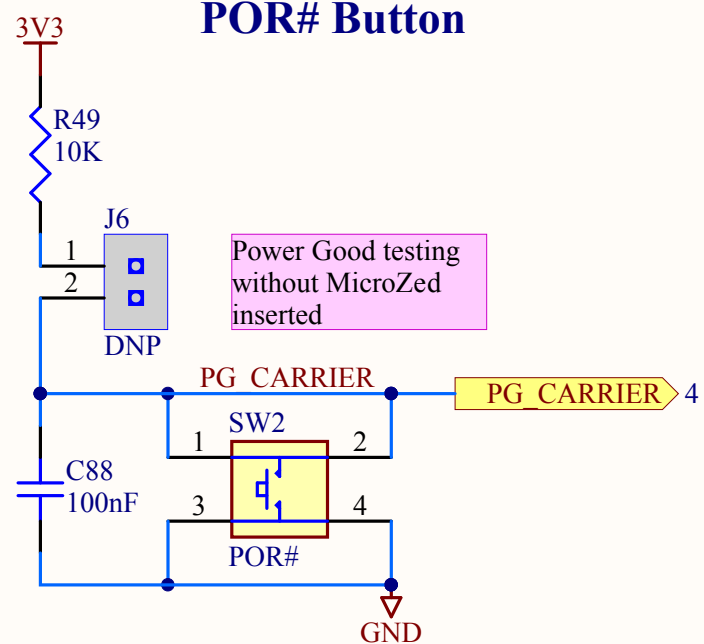


MicroZed PWR Ctrl

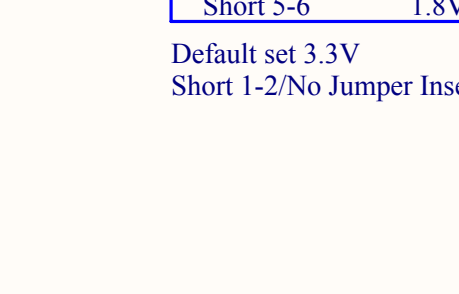
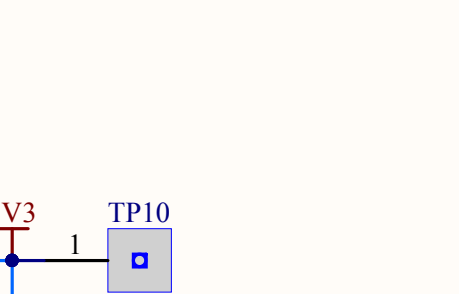
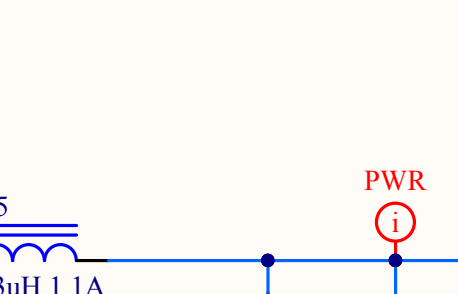
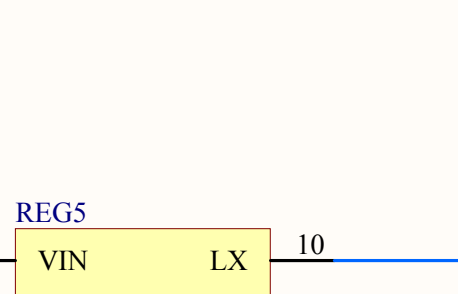
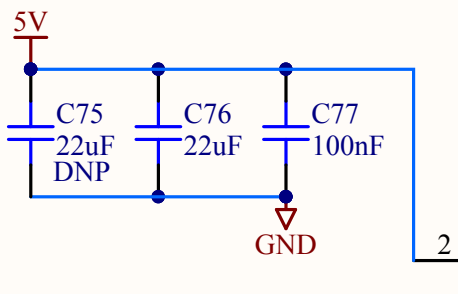
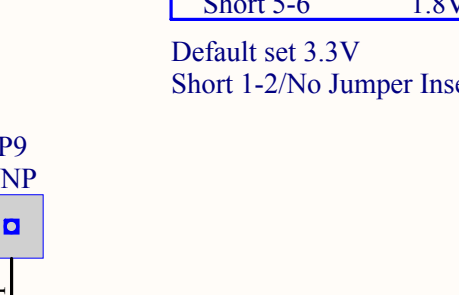
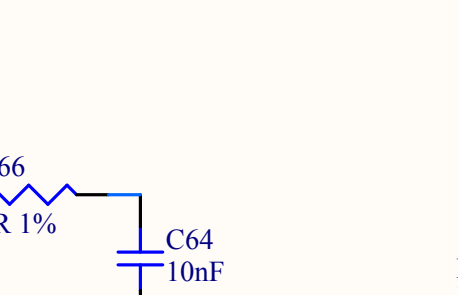
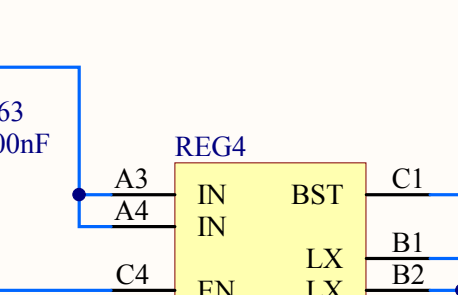
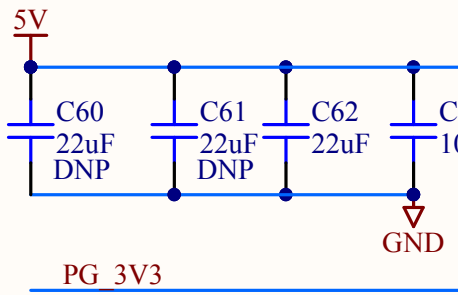
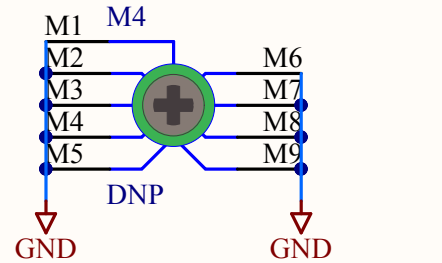
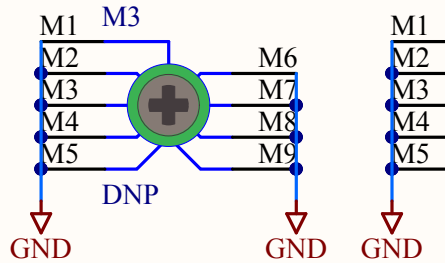
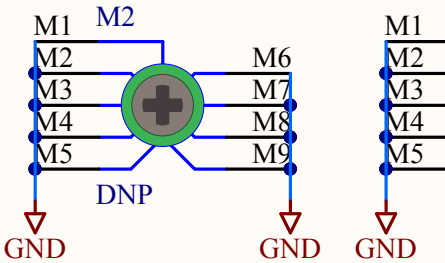
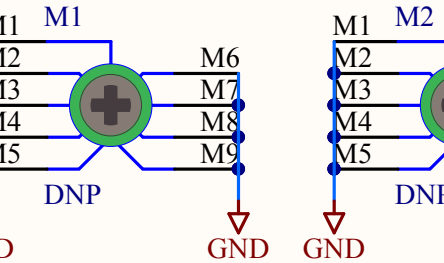
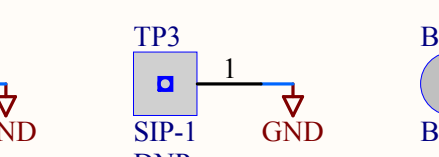
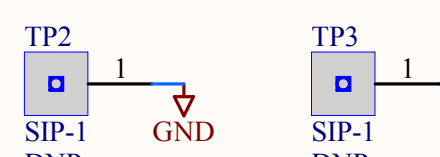
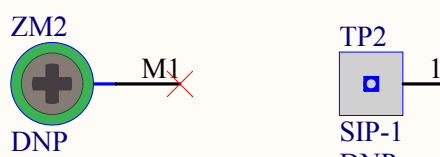
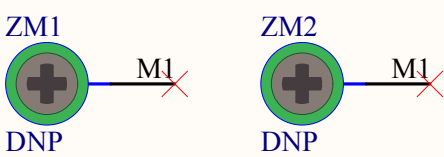
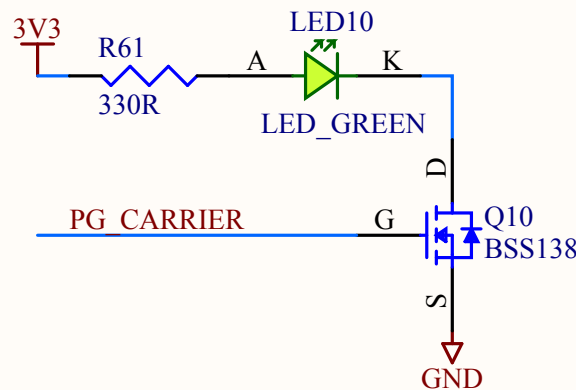
FAN



POR# Button



System Power Good



	1	2	3	4	5	6
A	<div>Revision / History</div> <div><div>V A</div><div>Mated with PCB V A</div></div> <div><div>V B</div><div>Correct PMOD Part# Correct Reg1 Sch Lib. Correct Reg2 Input Voltage. Change Mounting Hole size. Mated with PCB V B</div></div> <div><div>V C</div><div>Change Part# on REG1 & SW5 Set R53, R55, C89 to DNP Correct net name on PMOD JE,JF,JY Pin9-10 Change PROG_B button function to SRST# Revise circuitry on CARRIER_SRST#, PG_CARRIER & VCCIO_EN Revise Power Up Sequence Add MAX16025 Circuitry VCCIO_34/35 DC-DC feedback divider adopt 0.1% tolerance resistors Add pull low in PWR_EN Add jumper J6 for PG_CARRIER pull up Add test point for power (TP6~TP10) Revise value on R11 & R61 Revise Part# on XADC & Bumper Add Jumper Shunt for BOM Update J4 connections Mated with PCB Rev C</div></div> <div><div>V D</div><div>New circuitry added to replace MAX16025 * Added Q11, Q12, BAT54LT1G, and support components (Page 6) to replace MAX16025 part. Adds power switch/jack control. Mated with PCB Rev D Updated TP1-6 to be DNP graphically</div></div>					A
B						B
C						C
D						D
	1	2	3	4	5	6