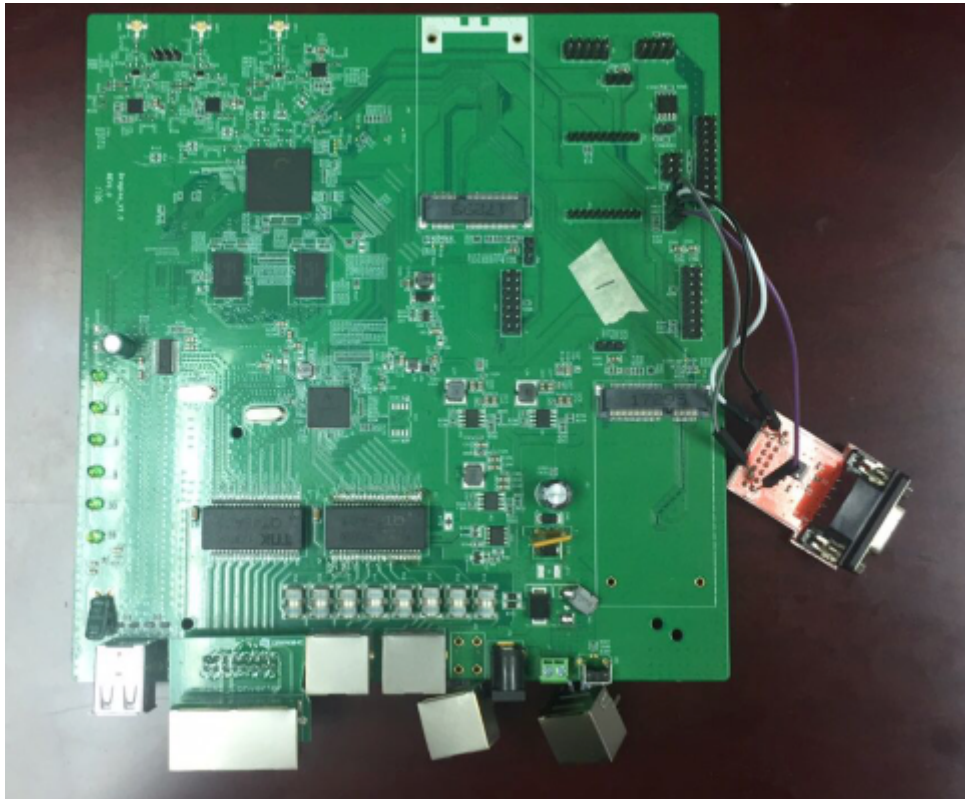Below is the photo for the first batch of QCA9558 Dev board.

Remark 1 – RJ45 Interface:

There are two onboard RJ45 ports, the hardware design has wrong wiring, we have fixed the wiring via two external RJ45 connector which are connect to the on board RJ45 via wire. Those two external RJ45 will work.

The pin header between USB and RJ45 ports are pins for another 2 RJ45 Interface. We have provided a RJ45 converter so these two ports works as well.

Remark 2 – UART Interface:

J4 pin header are UART interface, we found the QCA9558 interface only works with some type of UART IC, we are still checking what is wrong here. A verified RS232 to UART converter is provided to use this UART to debug. The pin connection in between J4 and the converter are(from bottom to top):

 J4 Pin 1 < -- > Converter 3.3v
 J4 Pin 2 < -- > Converter RX
 J4 Pin 3 < -- > Converter TX
 J4 Pin 4 < -- > Converter GND
Baud Rate: 115200

Remark 3 – U-boot:

The board are loaded with the u-boot which compiled from QSDK. The source code of QSDK can downloaded from this link:
https://www.dropbox.com/sh/kpwbv8k9rswhn8r/AACmNMwJnaMiw6ymYxbdKO35a?dl=0

Bin file (openwrt-ar71xx-ap135-qca-legacy-uboot.bin) can be found at:
http://chef.libremesh.org/QCA9558_AP135_QSDK_20161205/qsdk/bin/ar71xx/

Remark 4 – Firmware:

Command to Update to QSDK in U-Boot:

```
tftp 0x80060000 openwrt-ar71xx-generic-ap135-rootfs-squashfs.bin
erase 0x9f050000 +0xE30000
cp.b 0x80060000 0x9f050000 $filesize

tftp 0x80060000 openwrt-ar71xx-generic-ap135-kernel.bin
erase 0x9fE80000 +0x170000
cp.b 0x80060000 0x9fE80000 $filesize

setenv bootcmd bootm 0x9fe80000
saveenv
```

Command to update to LEDE:

```
tftp 0x80060000 rootfs
erase 0x9f050000 +0xE30000
cp.b 0x80060000 0x9f050000 $filesize

tftp 0x80060000 kernel
erase 0x9fE80000 +0x170000
cp.b 0x80060000 0x9fE80000 $filesize

setenv bootcmd bootm 0x9fe80000
saveenv
```

Remark 5 – 2.4G WiFi:

2.4G WiFi works but not calibrated yet (till 2017-10-19). The radio parameter of the 2.4G WiFi is from a production QCA9558 board.

We will update this remark after calibrated.

Remark 6 – 5.8G WiFi:

The dual 5.8G PCI-E interface has not been test yet. We will update this remark after calibrated.