2022 Digital IC Design

Final Project

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| NAME | | 魏本憲 | | | | |
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| **Functional Simulation Result of LZ77 Encoder** | | | | | | |
| Testing Pattern 0 | Pass | | Testing Pattern 1 | Pass | Testing Pattern 2 | Pass |
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| **Functional Simulation Result of LZ77 Decoder** | | | | | | |
| Testing Pattern 0 | Pass | | Testing Pattern 1 | Pass | Testing Pattern 2 | Pass |
|  | | | | | | |
| **Quality of Interpolated Results** | | | | | | |
| Testing Pattern 0 | 23.7970028 | | Testing Pattern 1 | 24.5099628 | Testing Pattern 2 | 27.8758807 |
| **Description of your design** | | | | | | |
| **LZ77encoder 電路是針對同一個字串去做壓縮為目標**  **LZ77 decoder 電路是testbench給定開頭和相同的字串去做設計**  **ELA 電路則是根據此行的上下兩排去做插值去做設計** | | | | | | |

*Scoring = Pattern 0 PSNR + Pattern 1 PSNR + Pattern 2 PSNR*

*The higher, the better.*