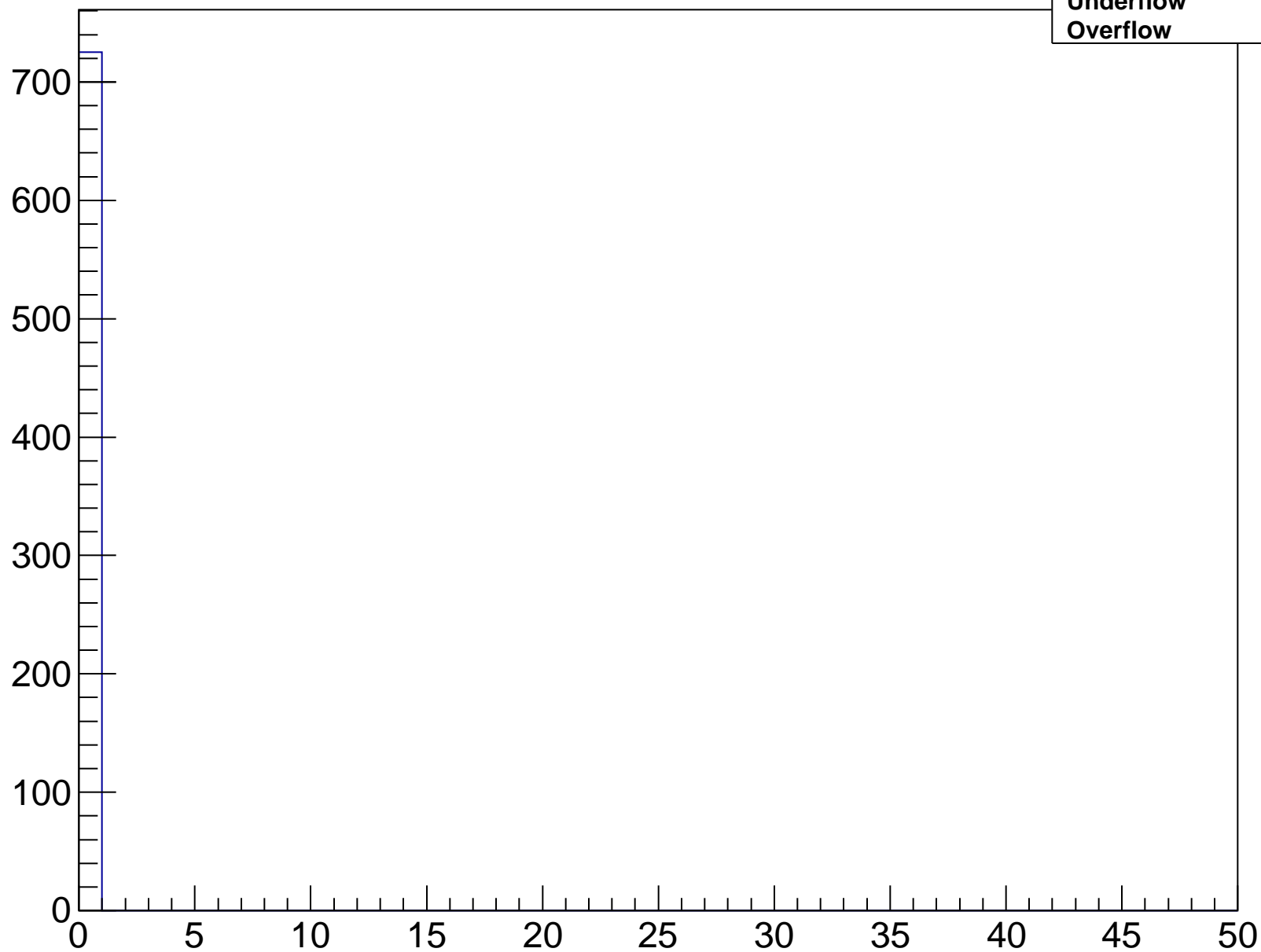
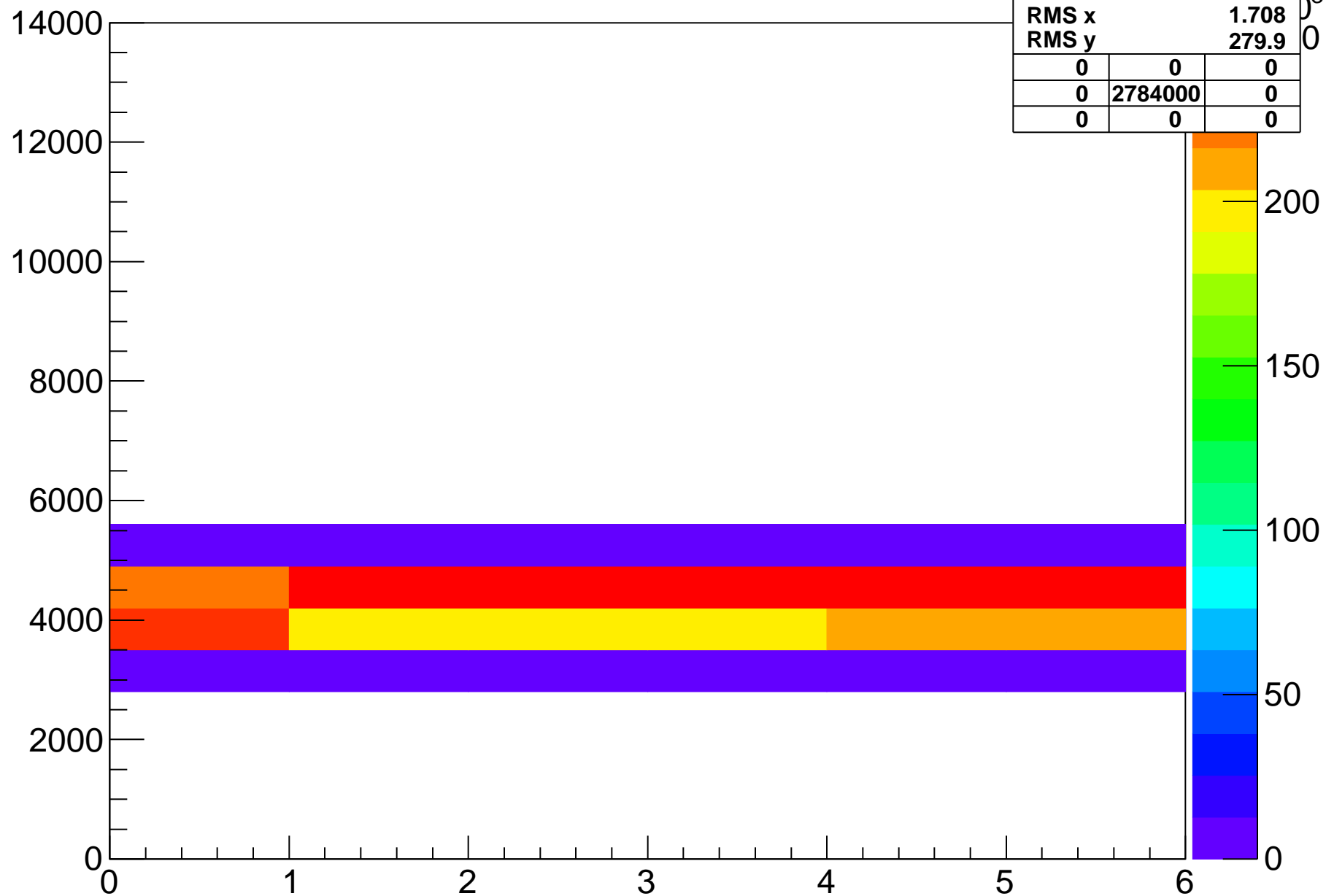


sampleCountHist

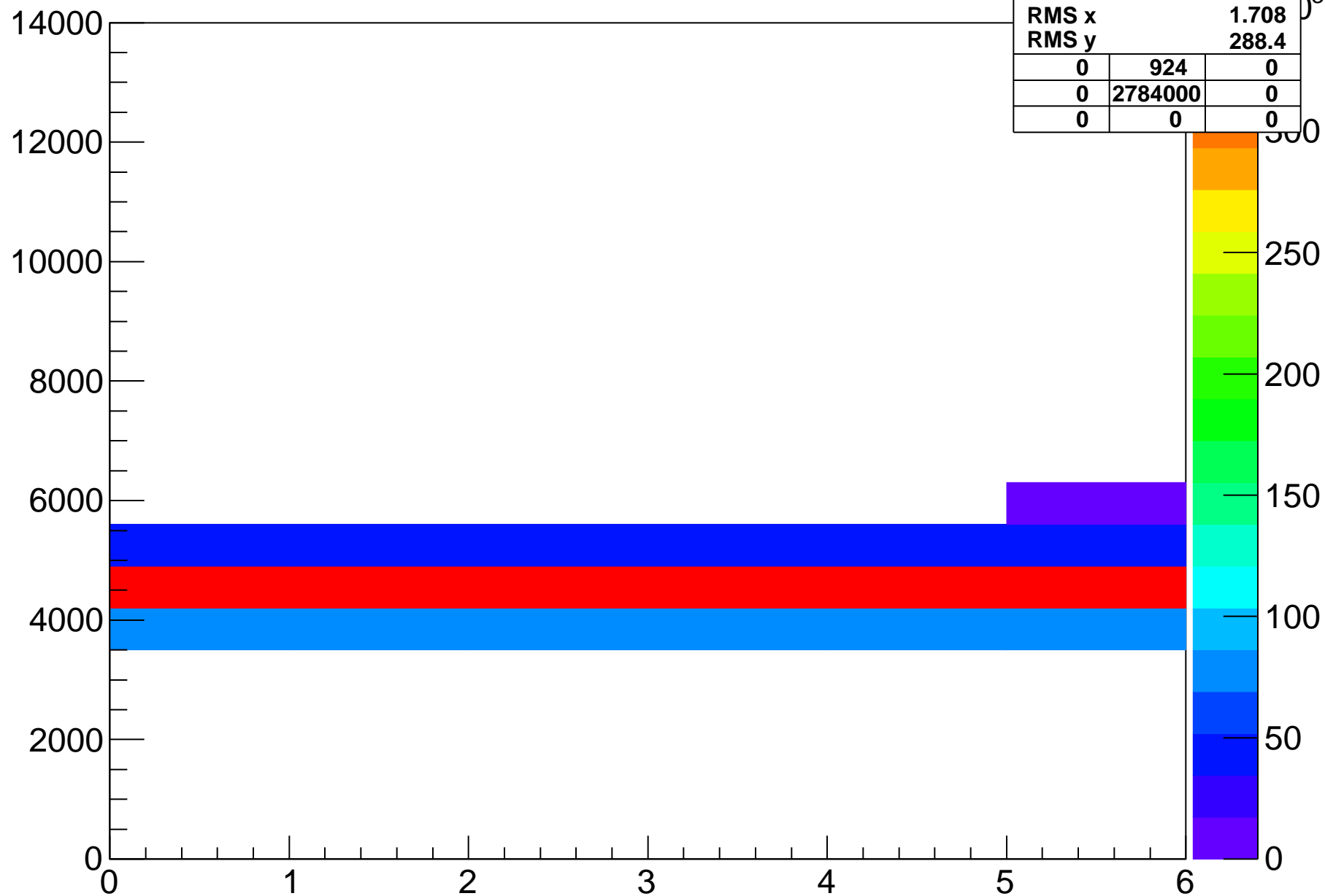
Entries	10150
Mean	0
RMS	0
Underflow	0
Overflow	9425



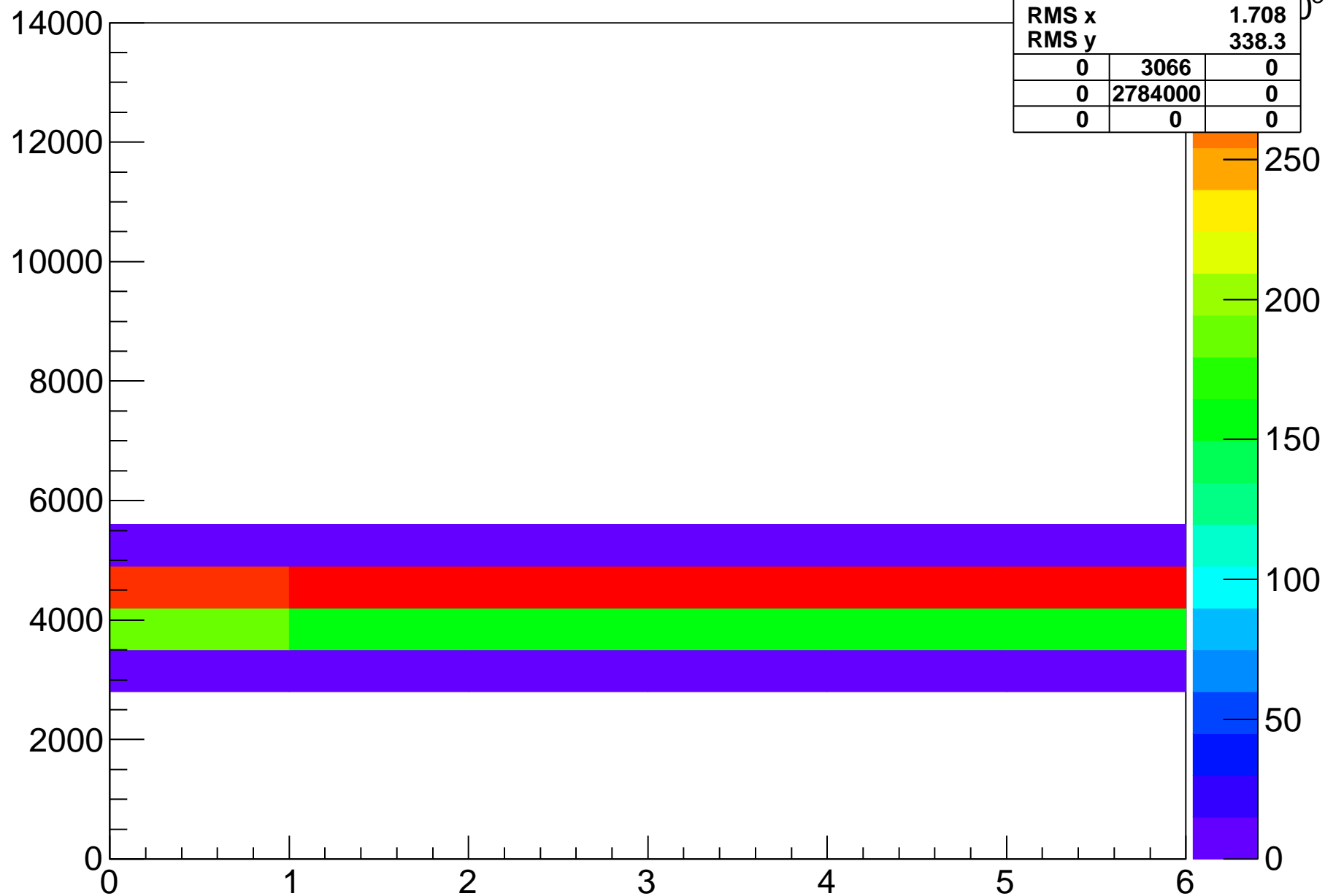
samples-fpga-0-hyb-0



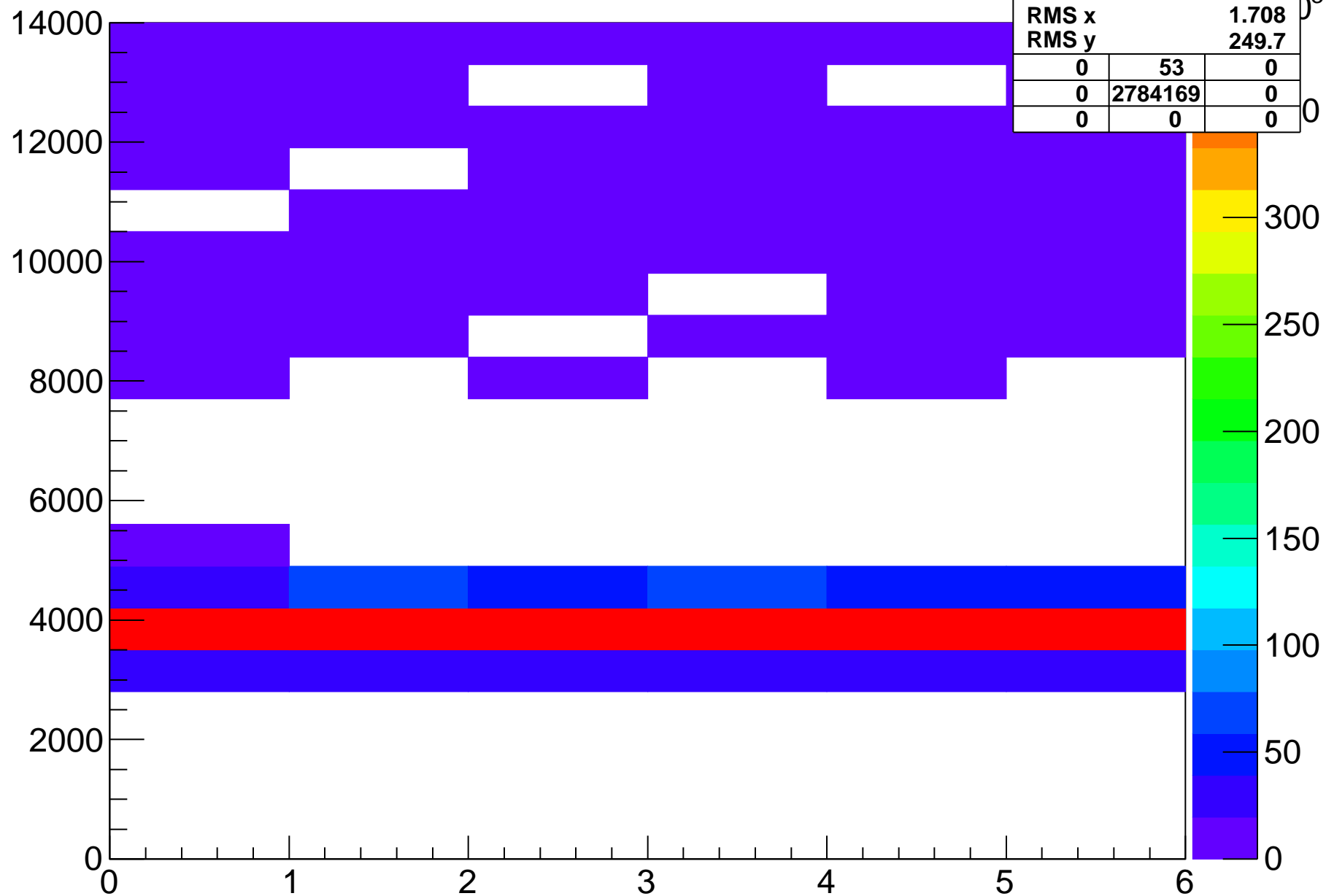
samples-fpga-0-hyb-1



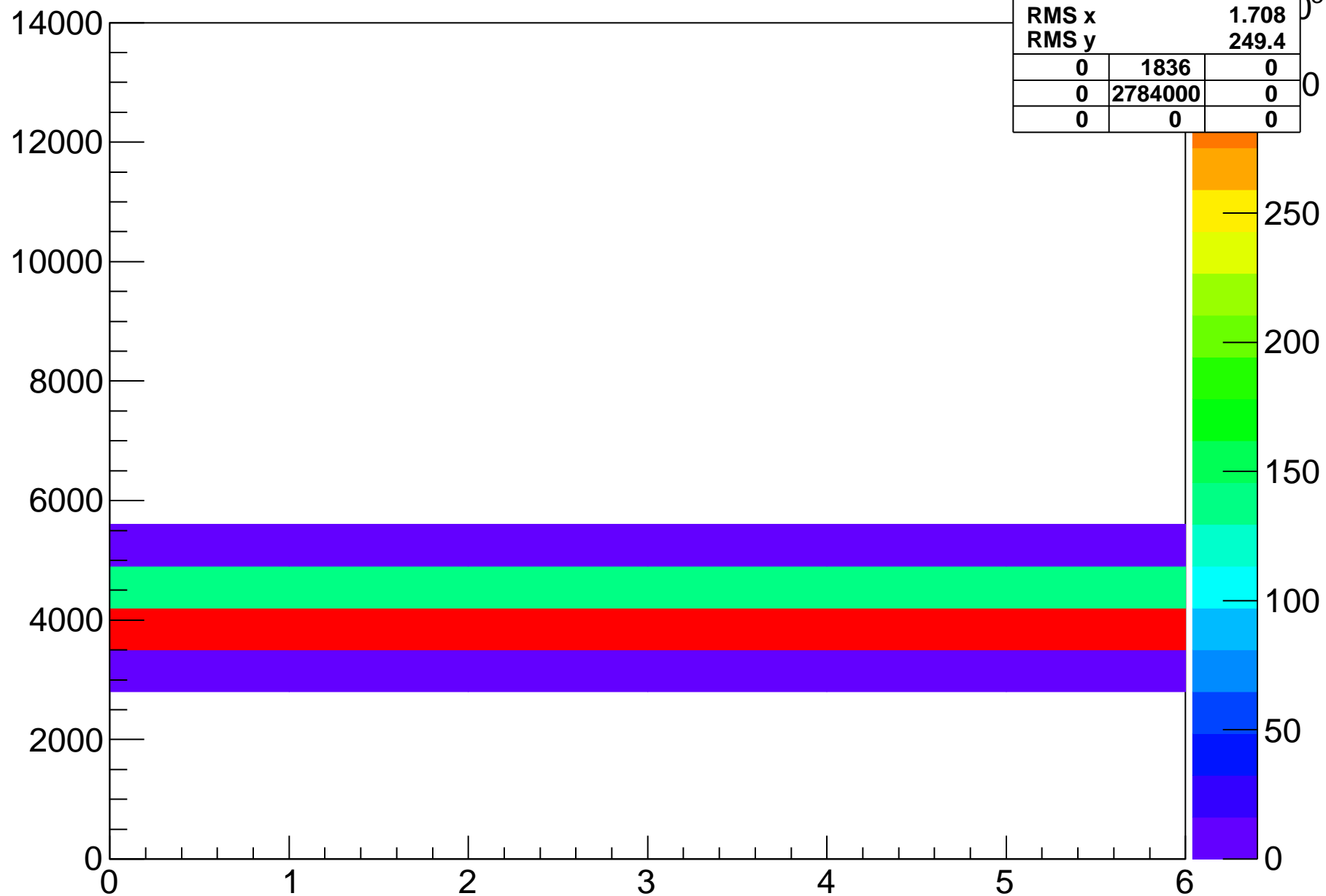
samples-fpga-0-hyb-2



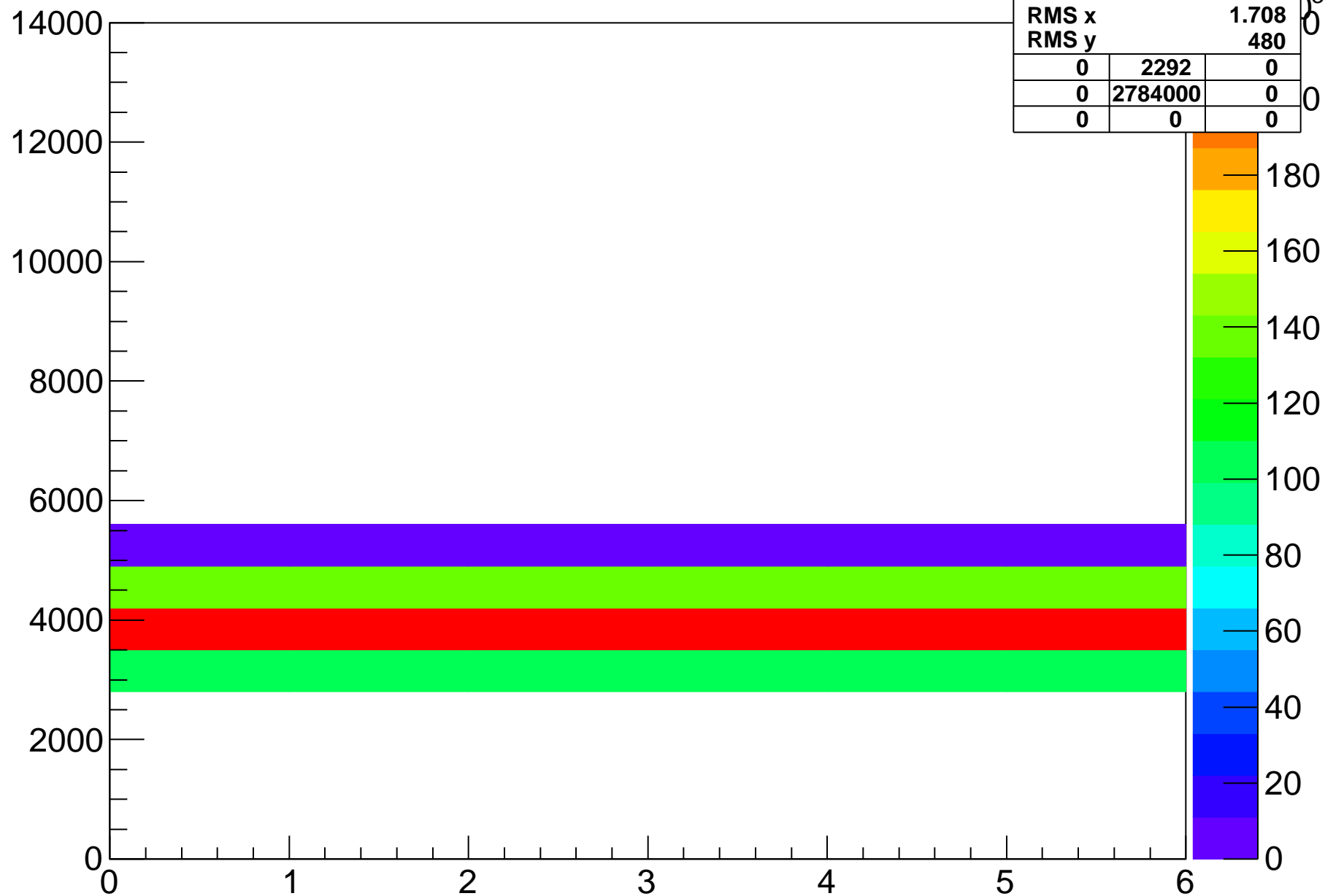
samples-fpga-0-hyb-3



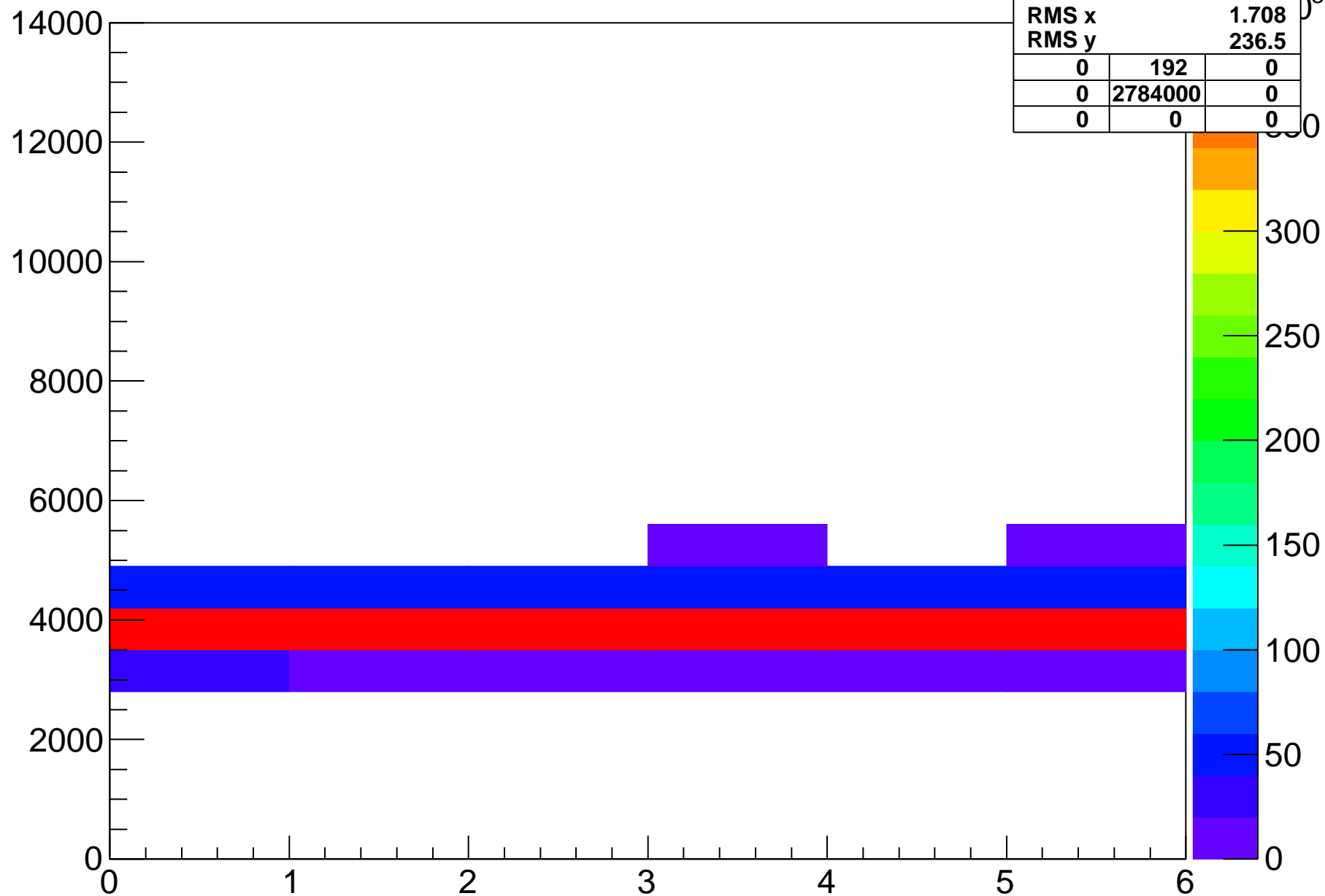
samples-fpga-1-hyb-0



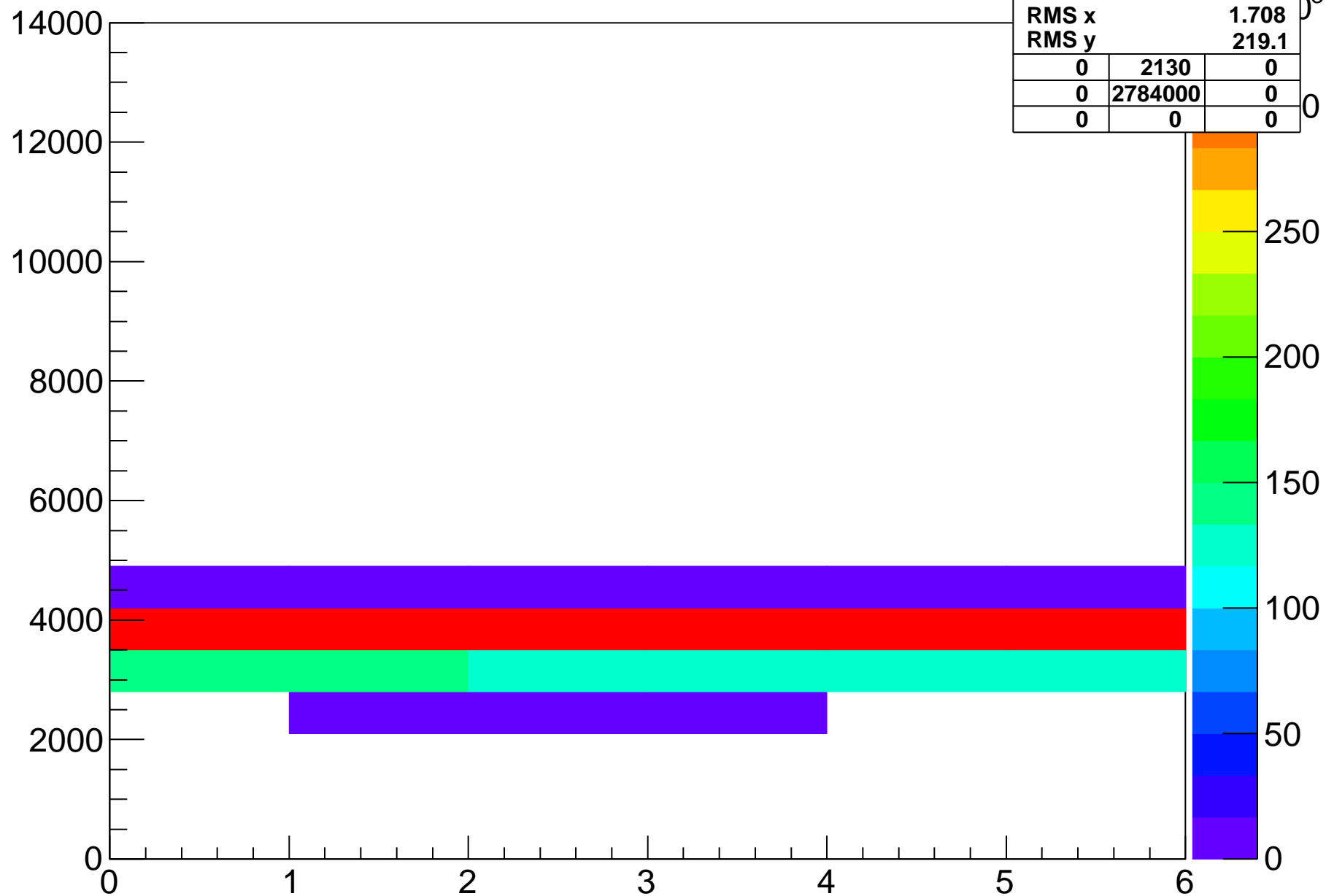
samples-fpga-1-hyb-1



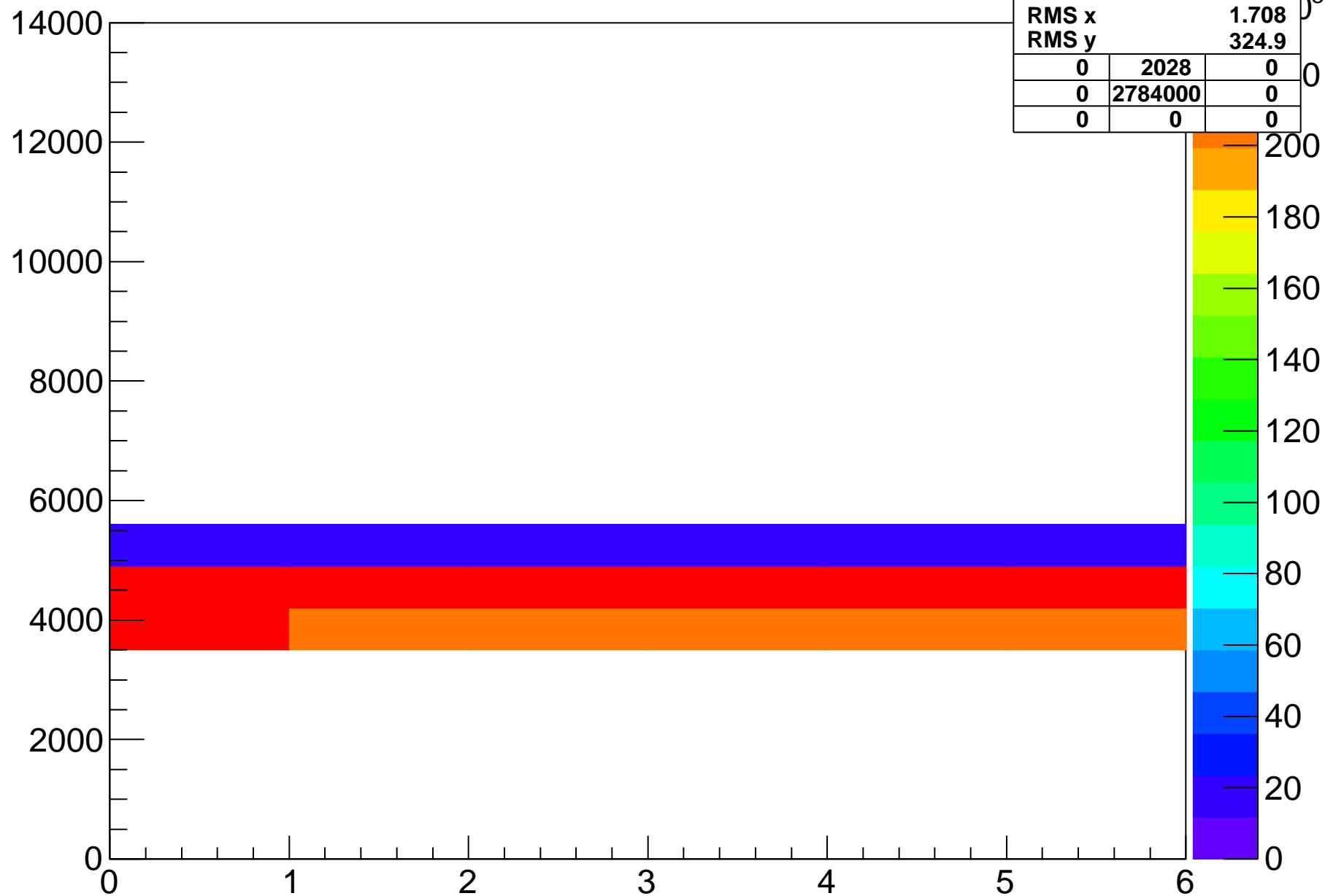
samples-fpga-1-hyb-2



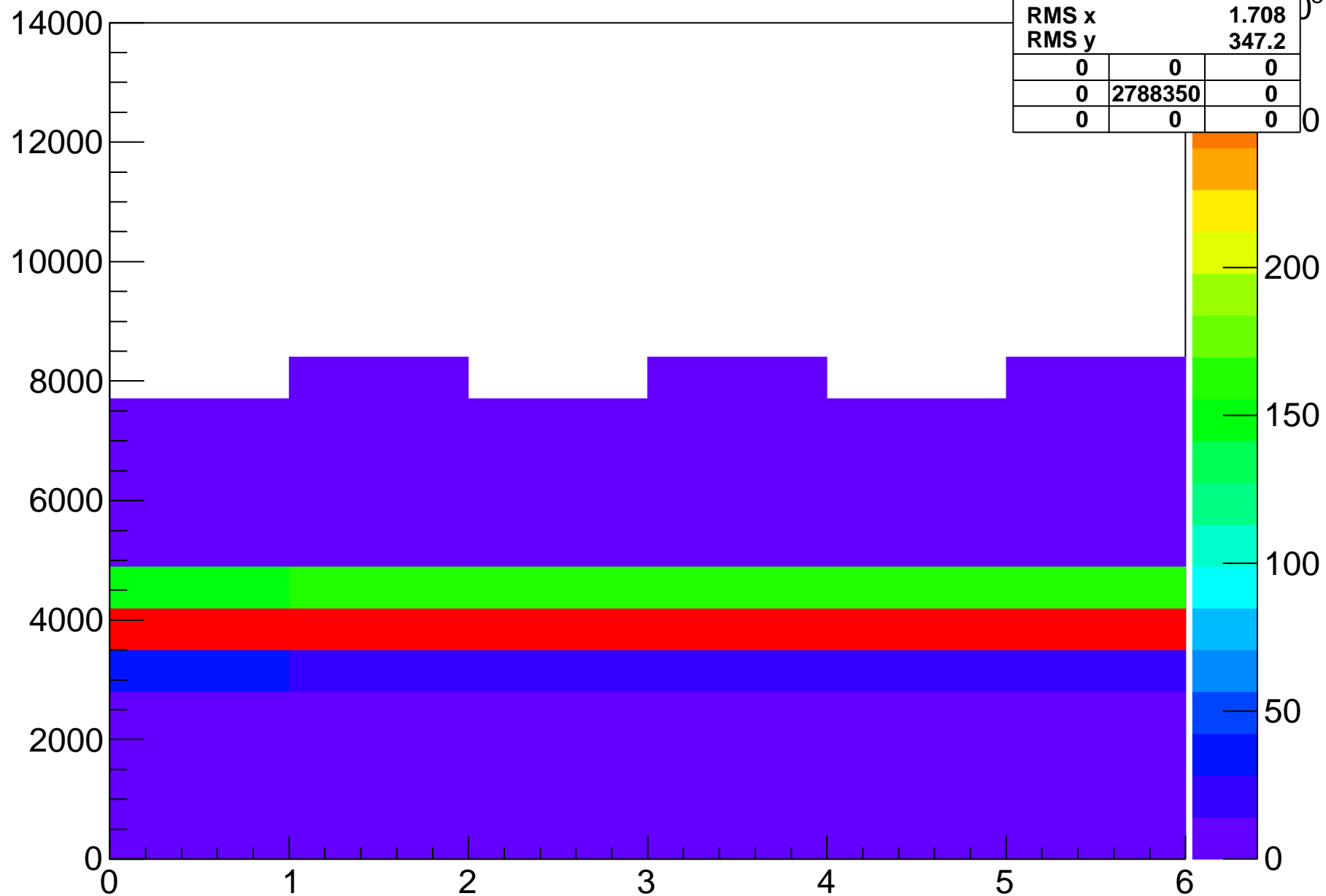
samples-fpga-1-hyb-3



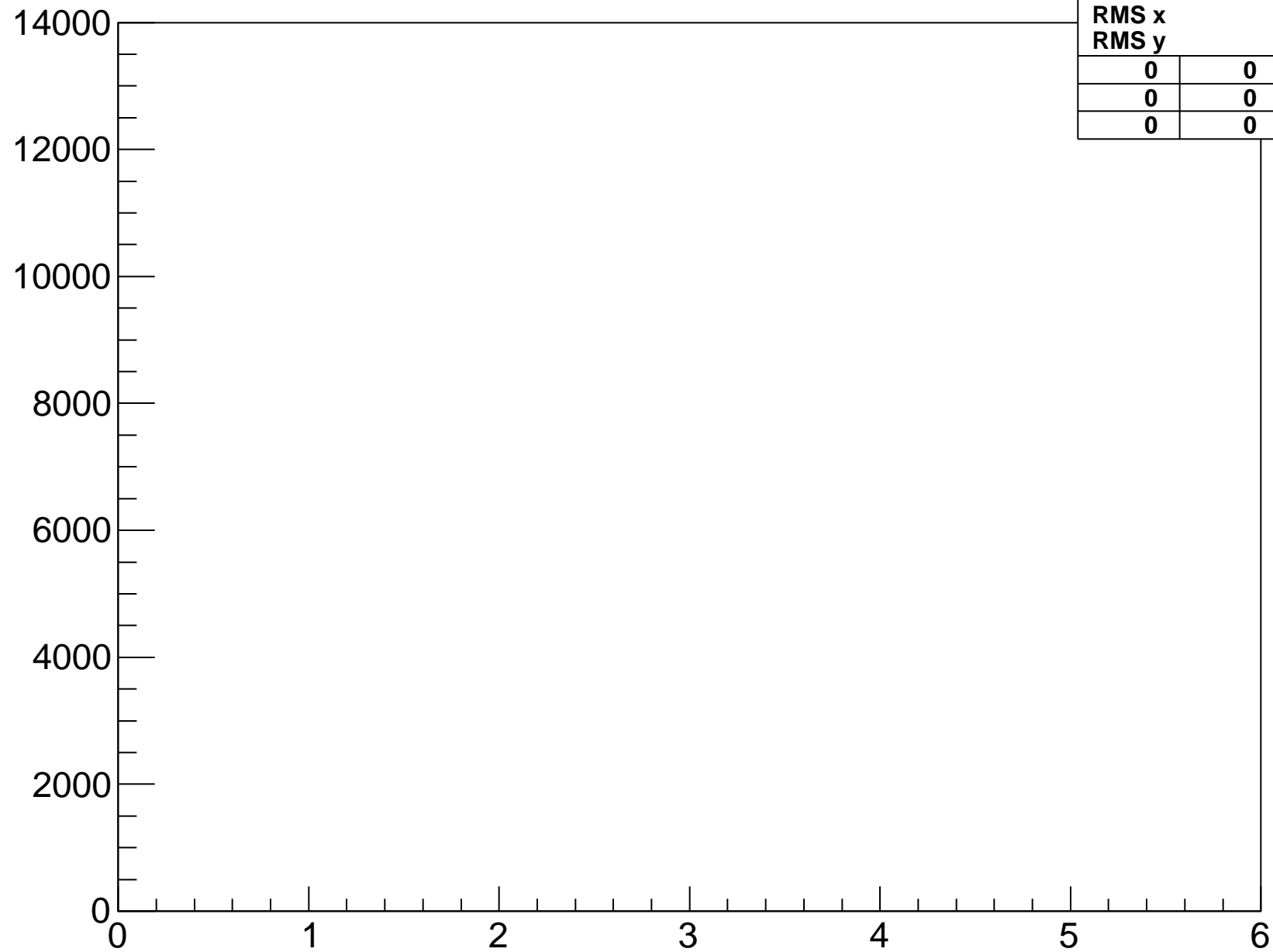
samples-fpga-2-hyb-0



samples-fpga-2-hyb-1

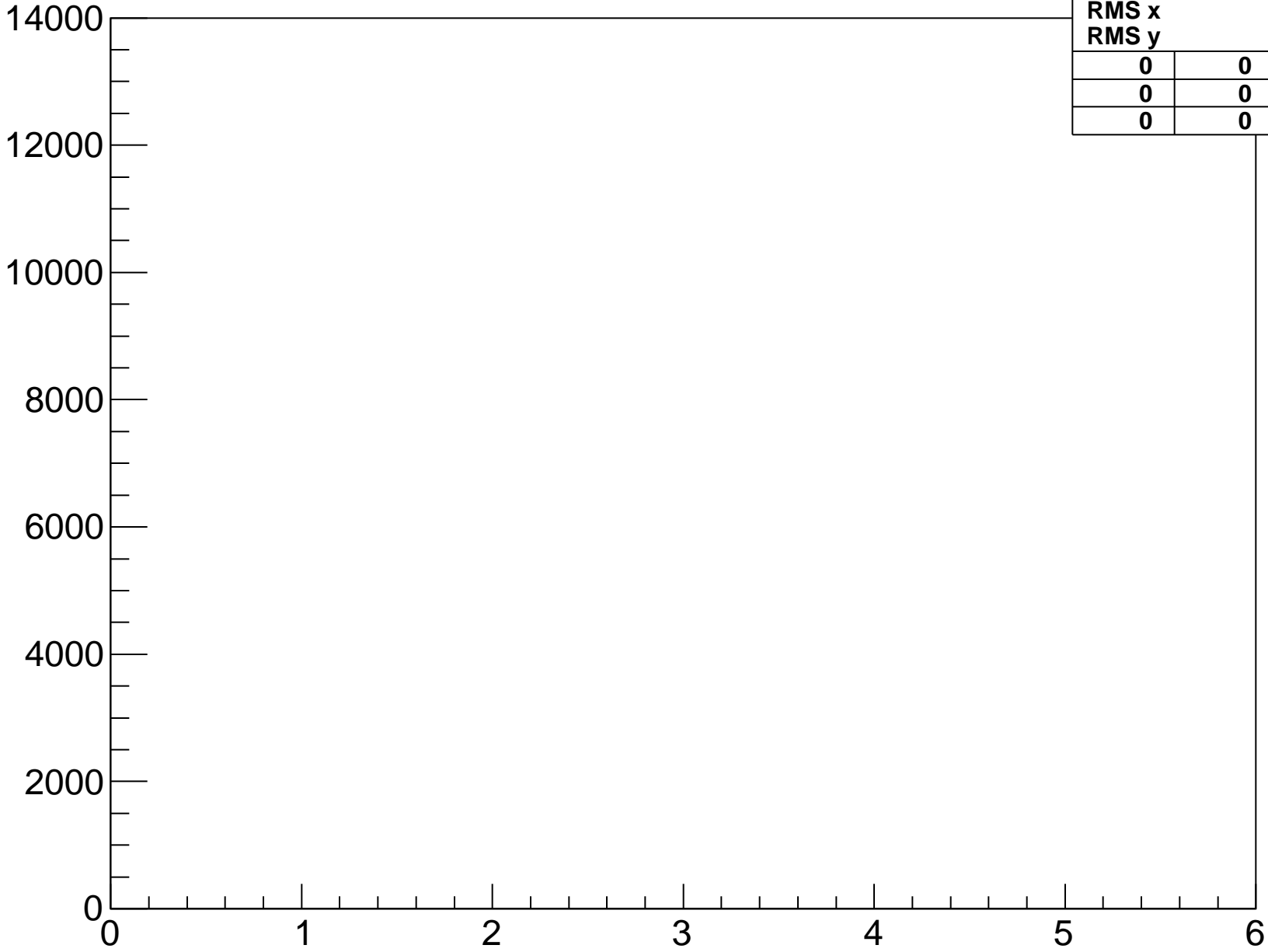


samples-fpga-2-hyb-2



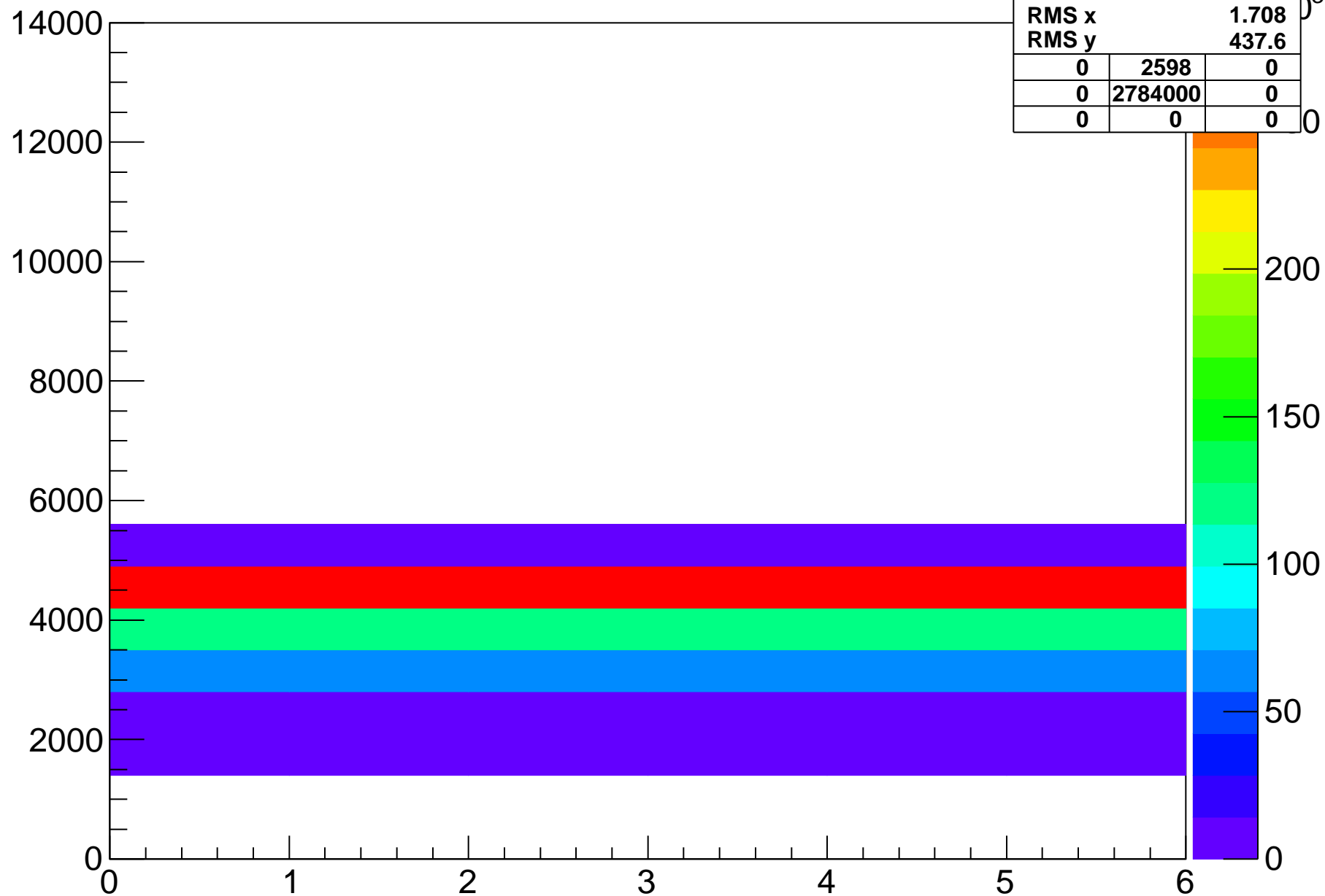
Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

samples-fpga-2-hyb-3

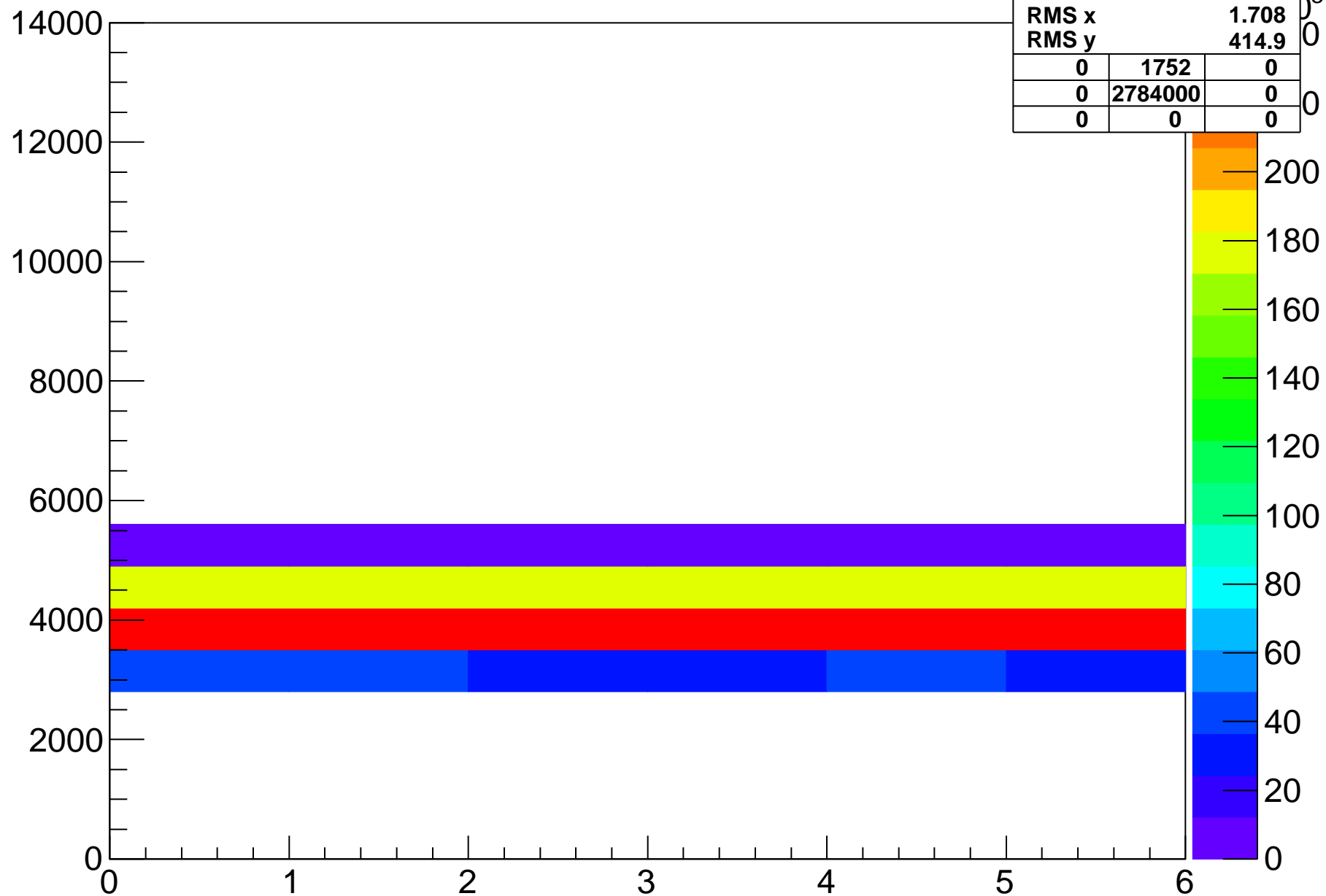


Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

samples-fpga-3-hyb-0

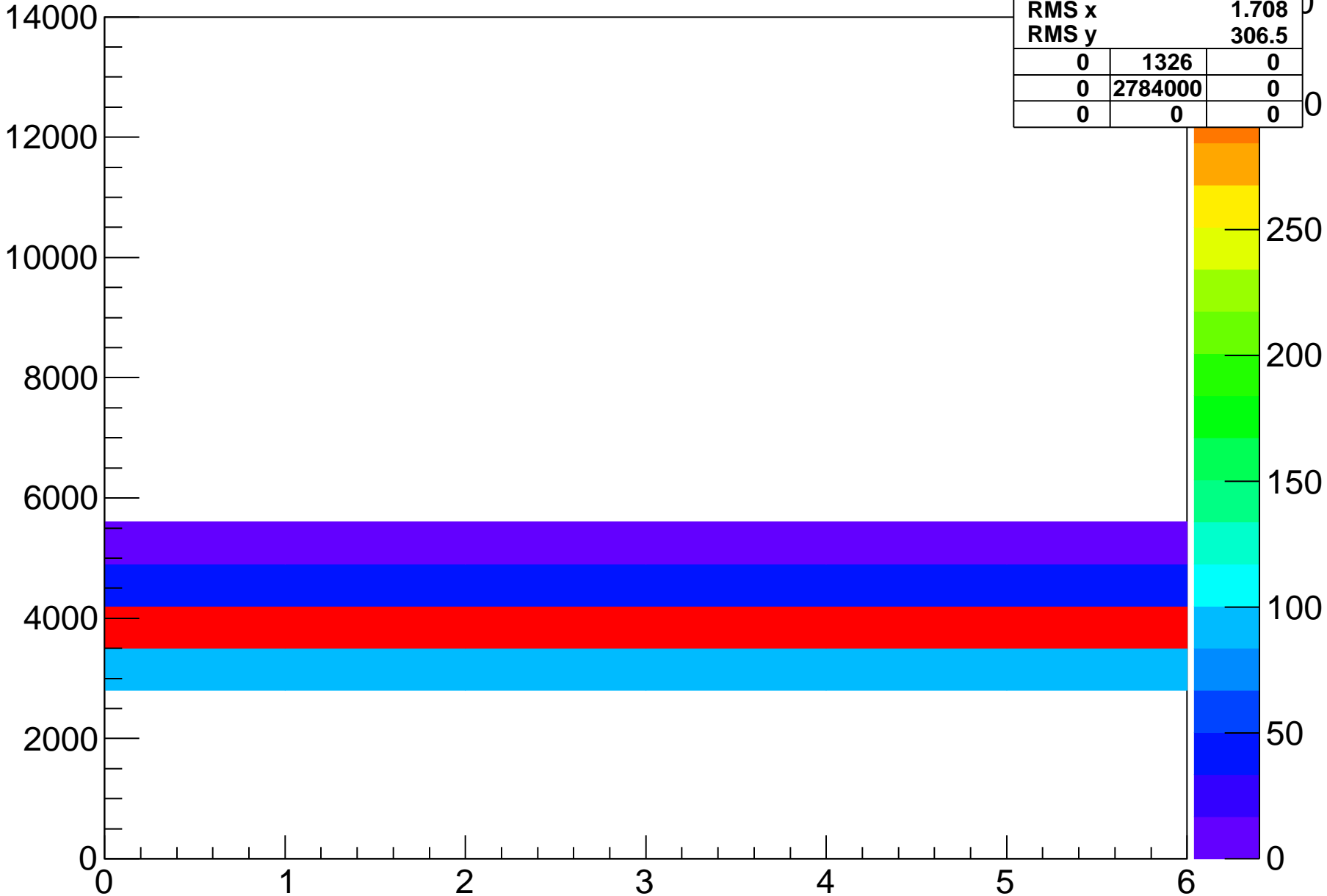


samples-fpga-3-hyb-1

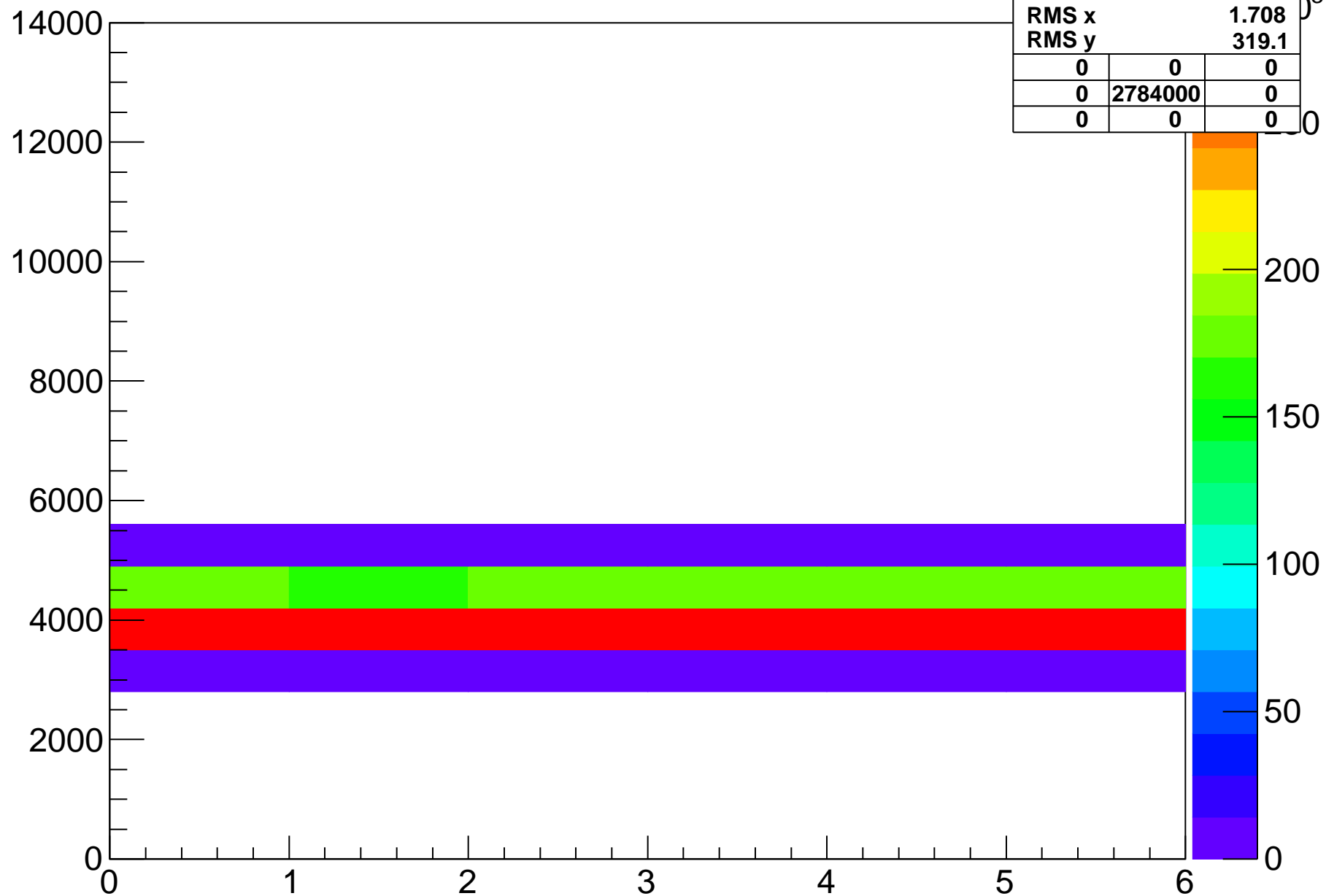


samples-fpga-3-hyb-2

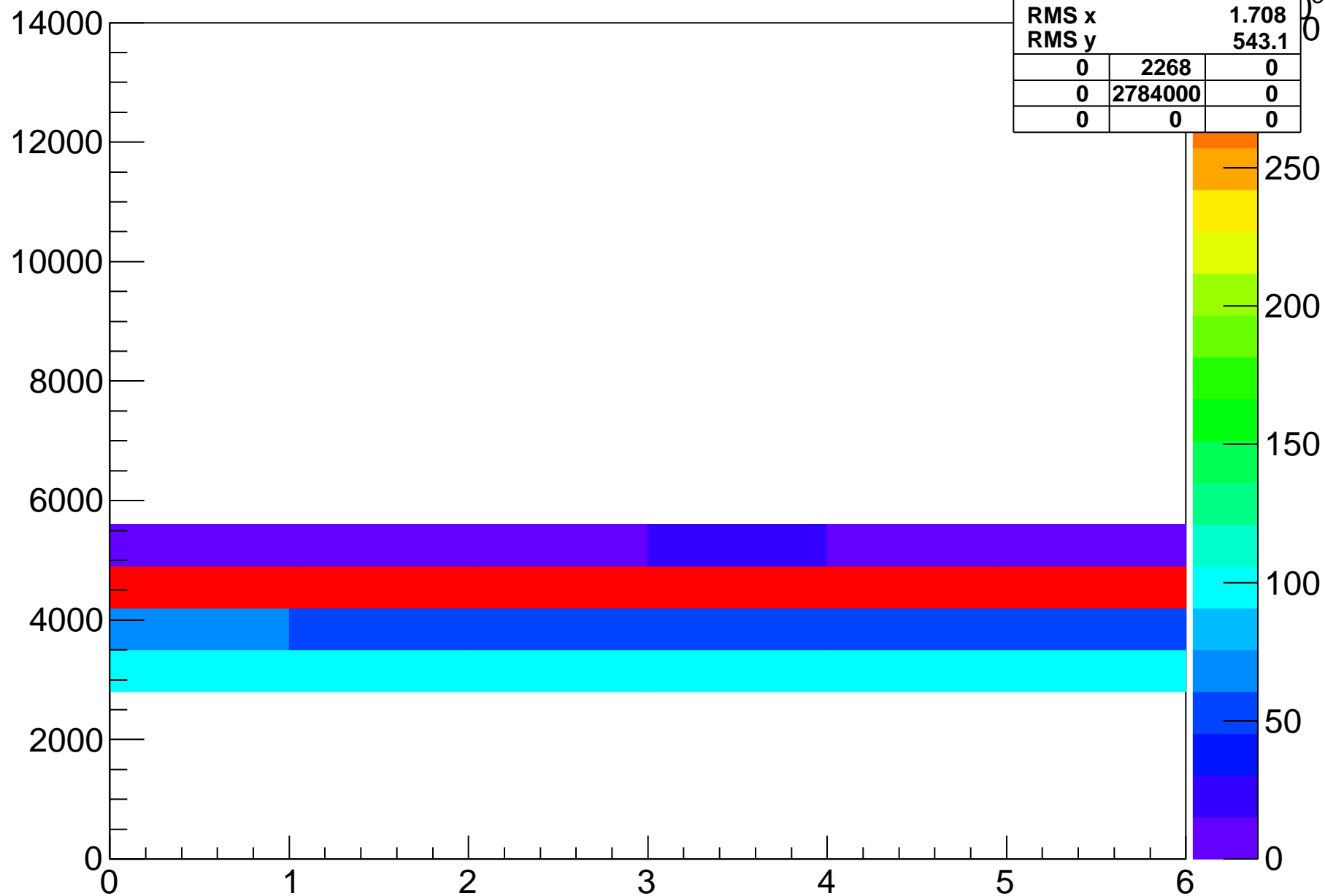
Entries	2785326	
Mean x	2.5	
Mean y	3793	
RMS x	1.708	
RMS y	306.5	
0	1326	0
0	2784000	0
0	0	0



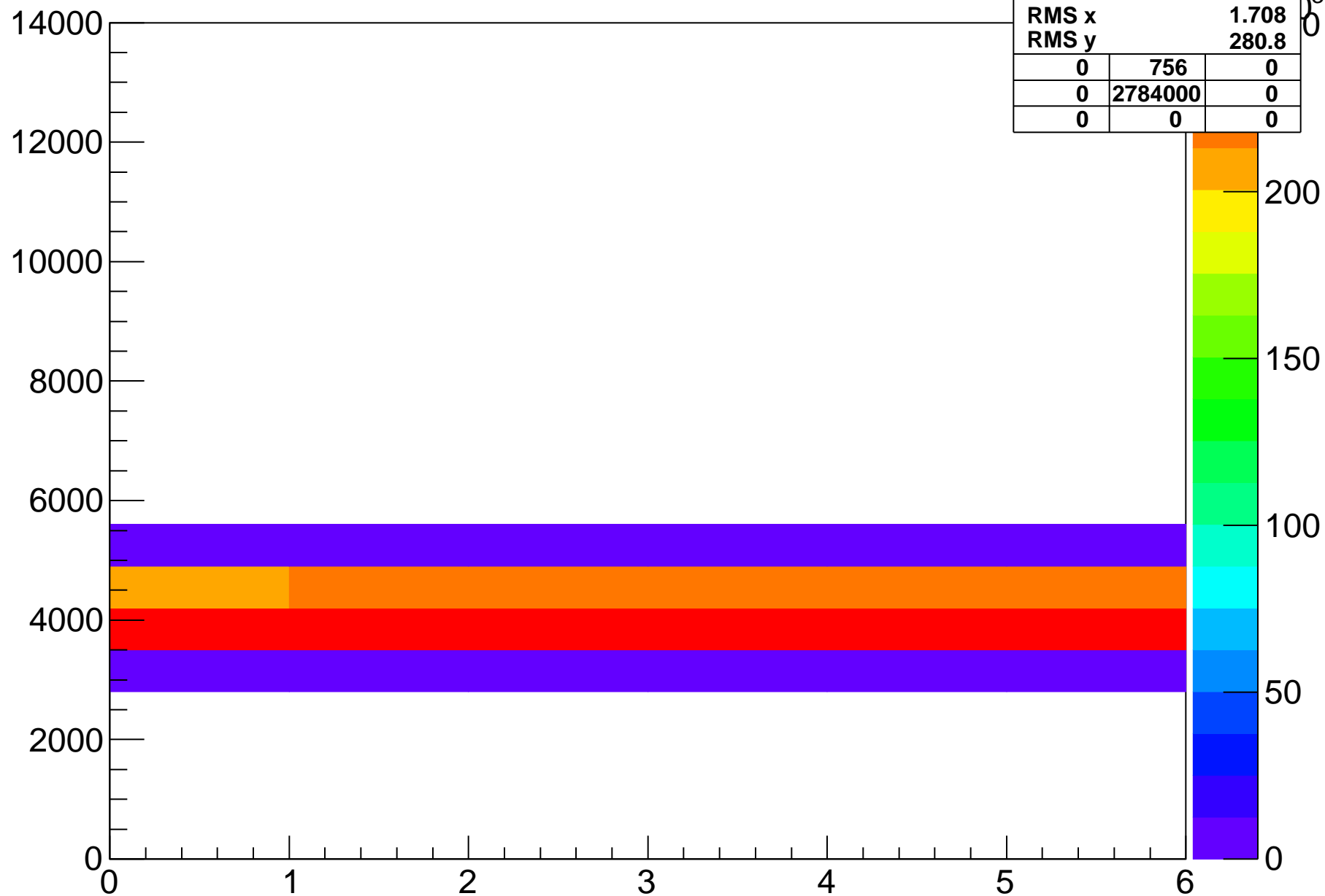
samples-fpga-3-hyb-3



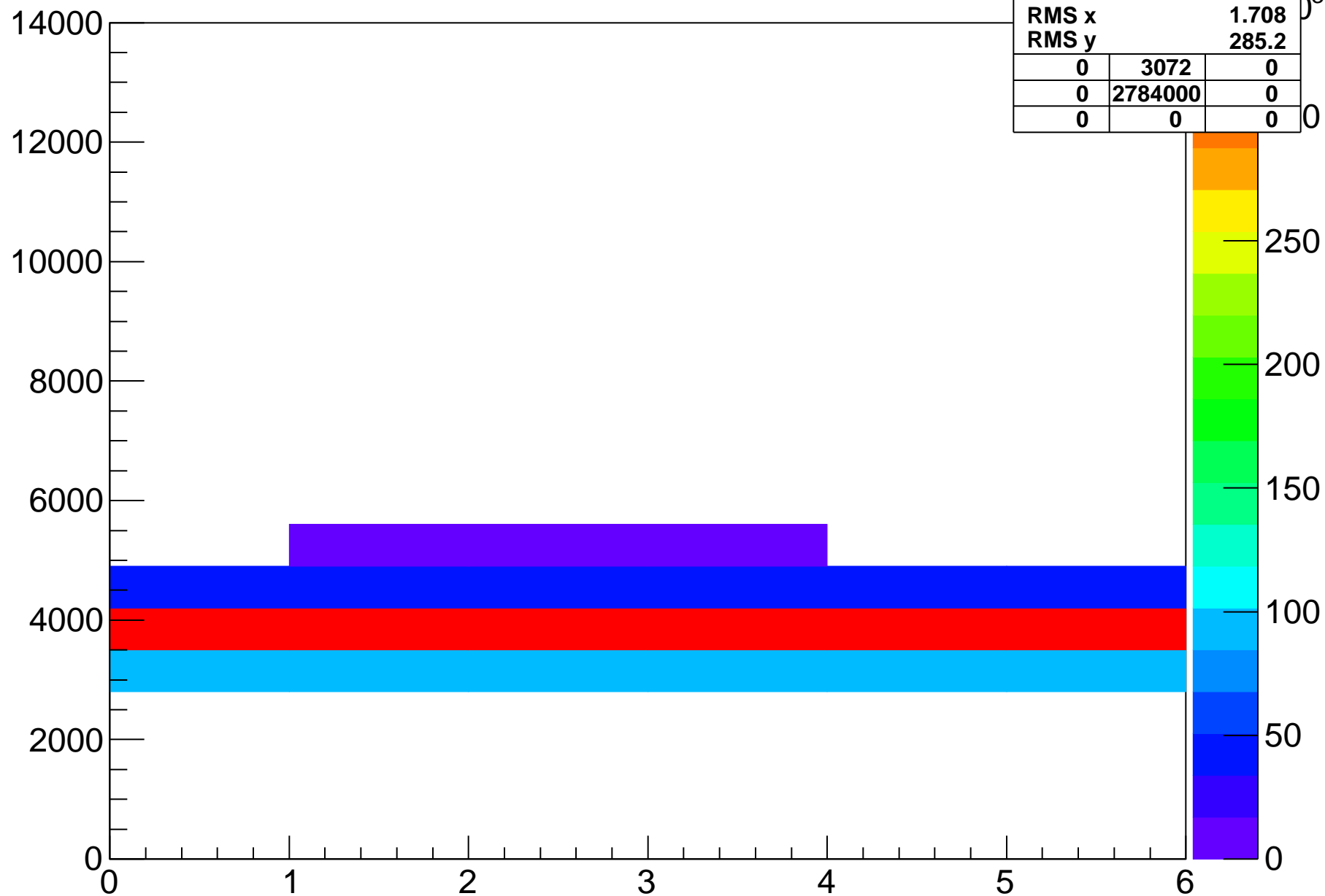
samples-fpga-4-hyb-0



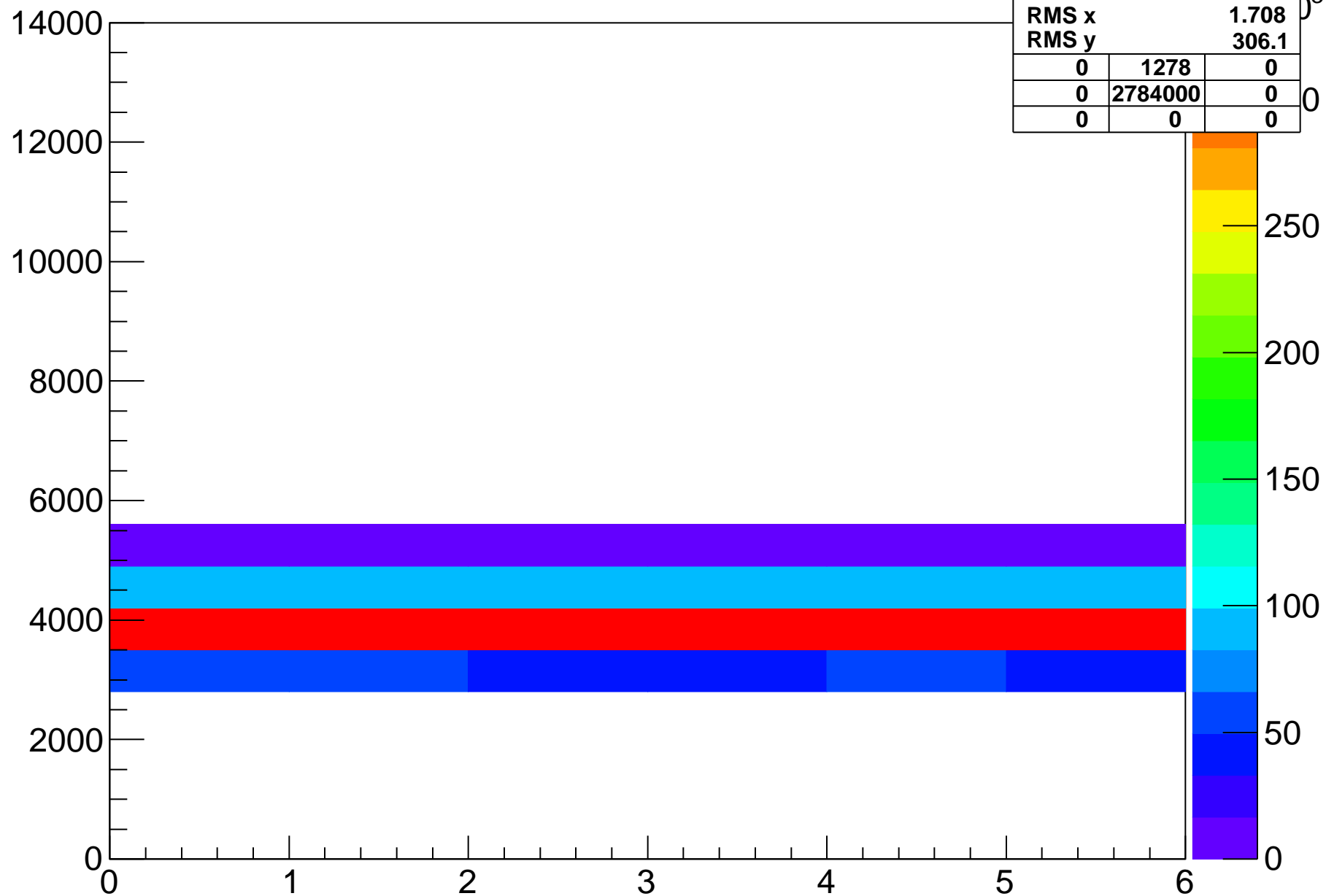
samples-fpga-4-hyb-1



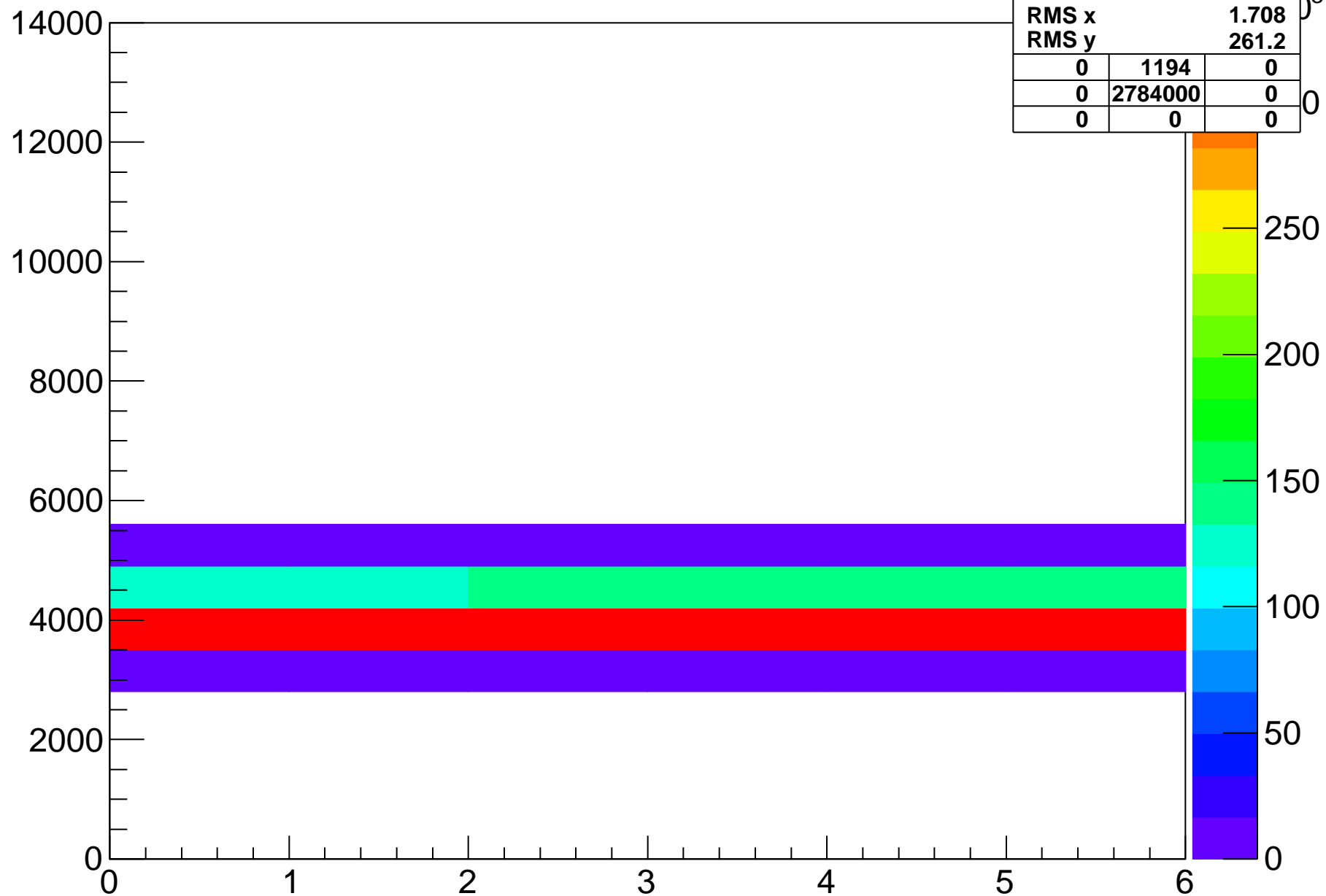
samples-fpga-4-hyb-2



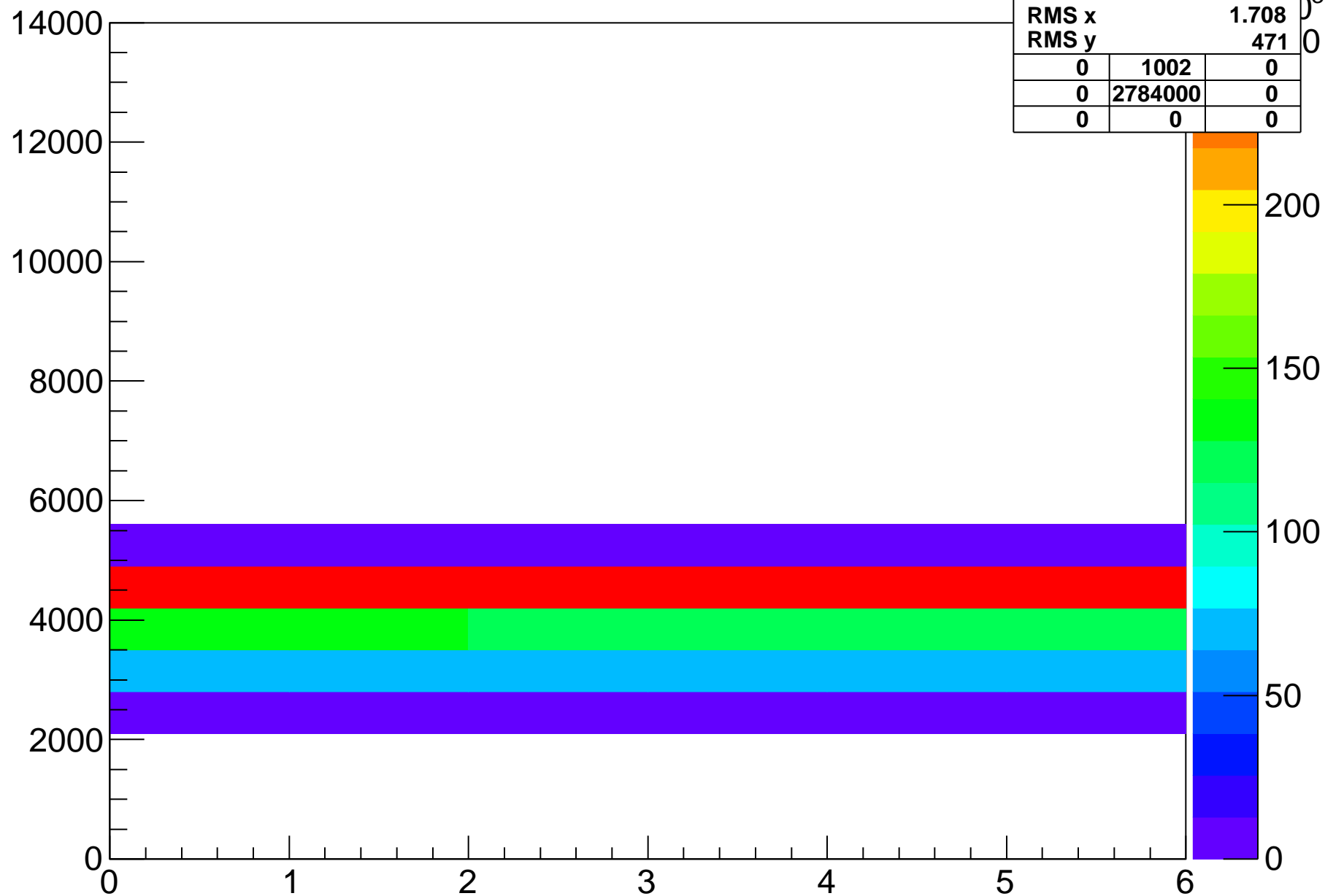
samples-fpga-4-hyb-3



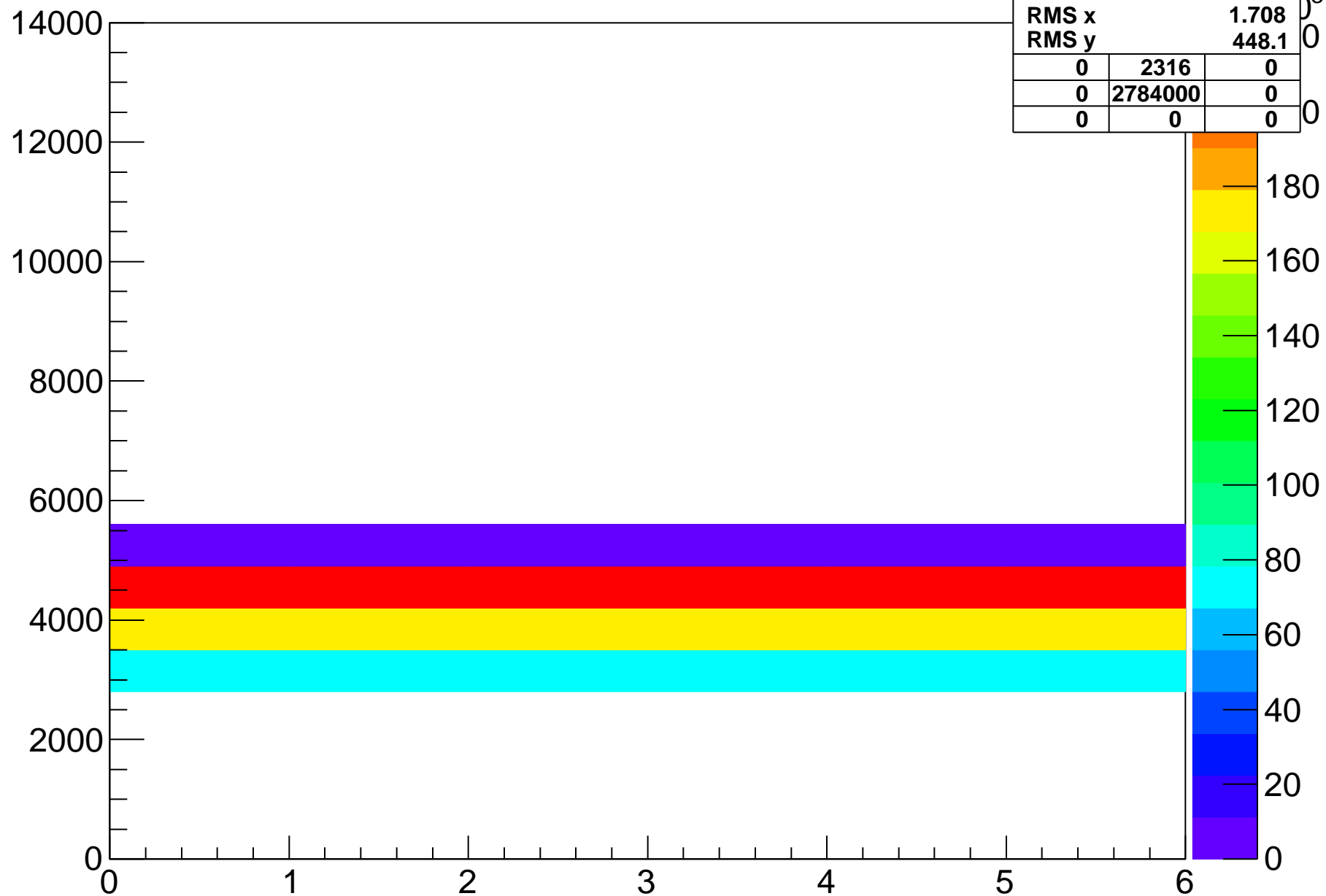
samples-fpga-5-hyb-0



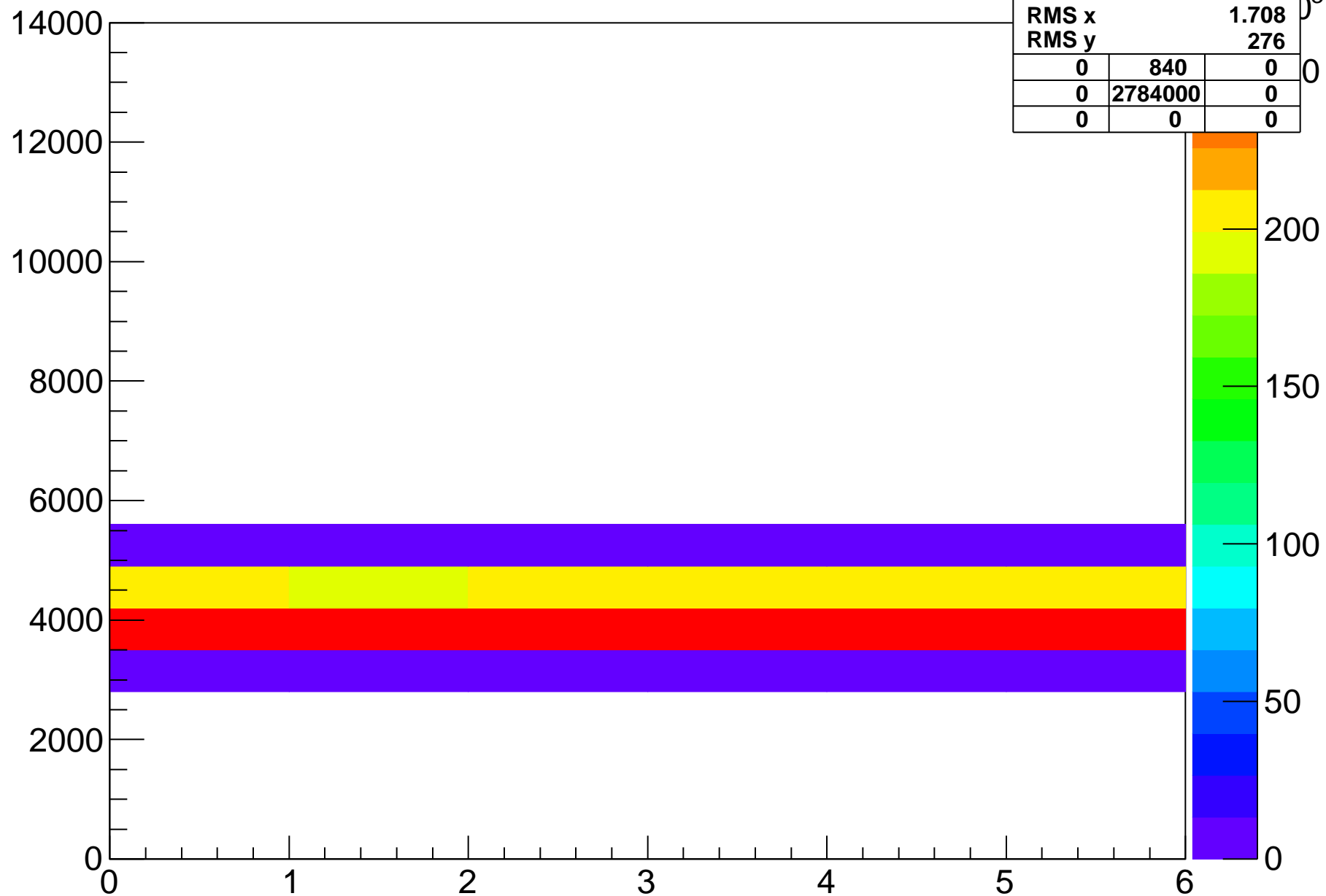
samples-fpga-5-hyb-1



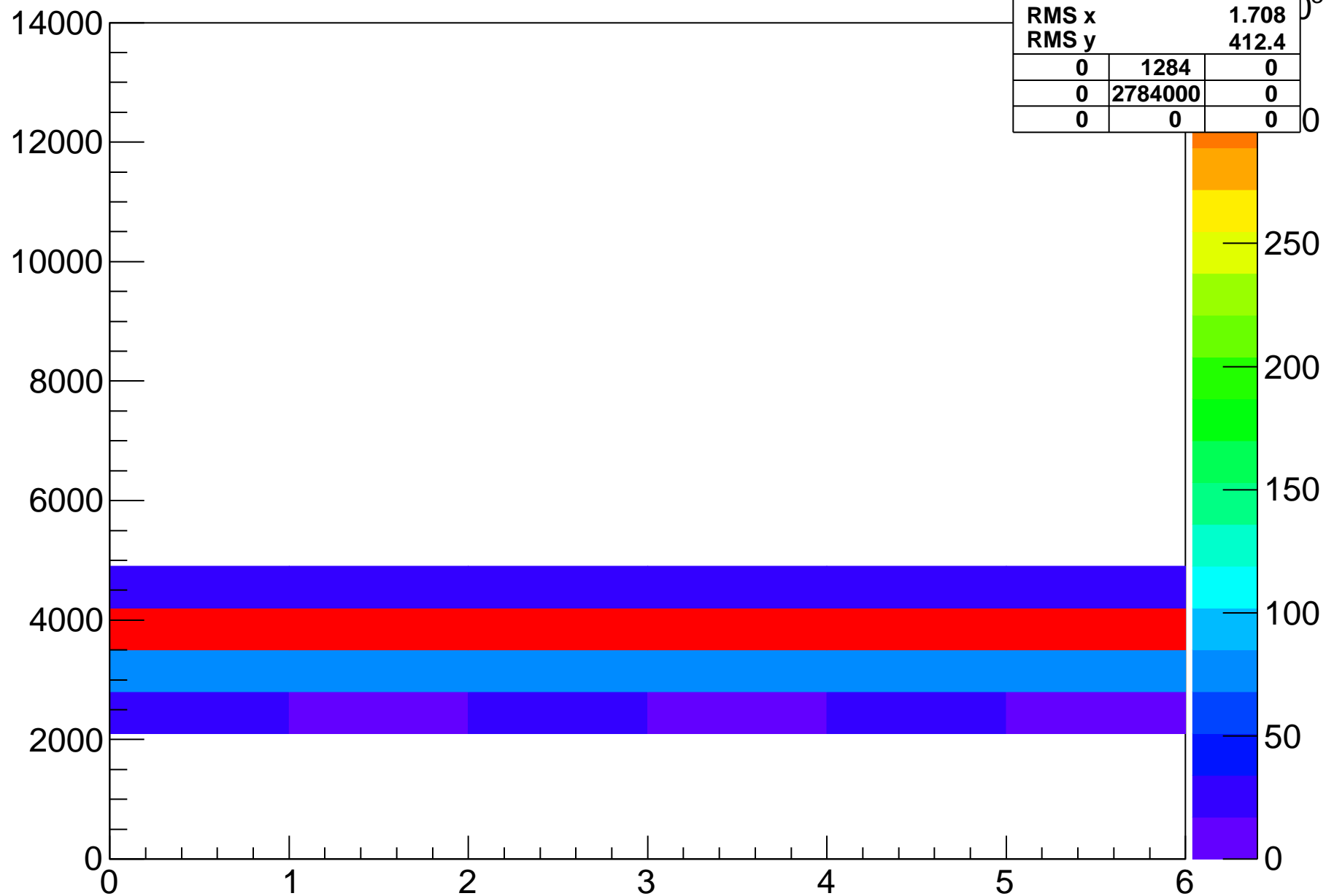
samples-fpga-5-hyb-2



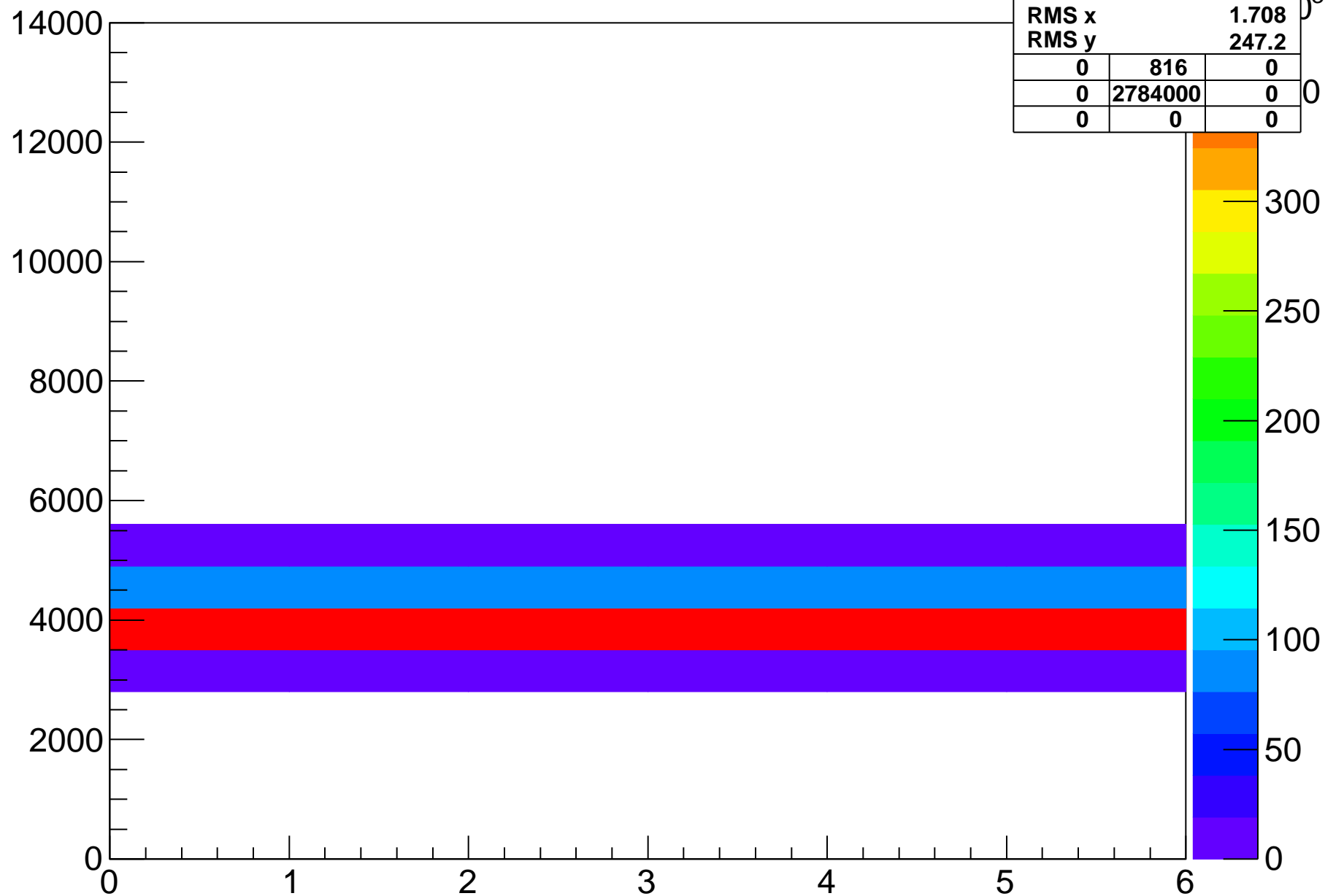
samples-fpga-5-hyb-3



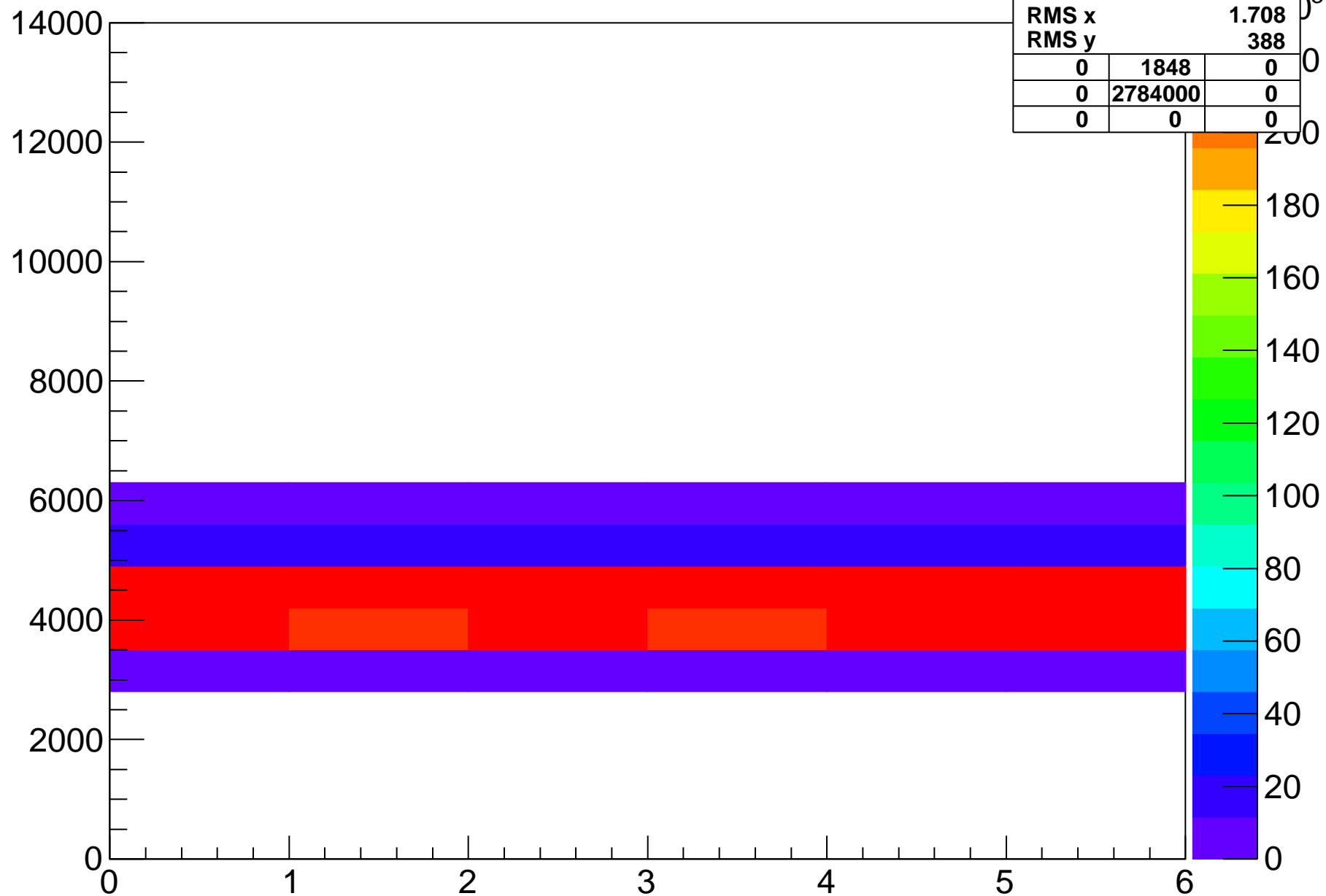
samples-fpga-6-hyb-0



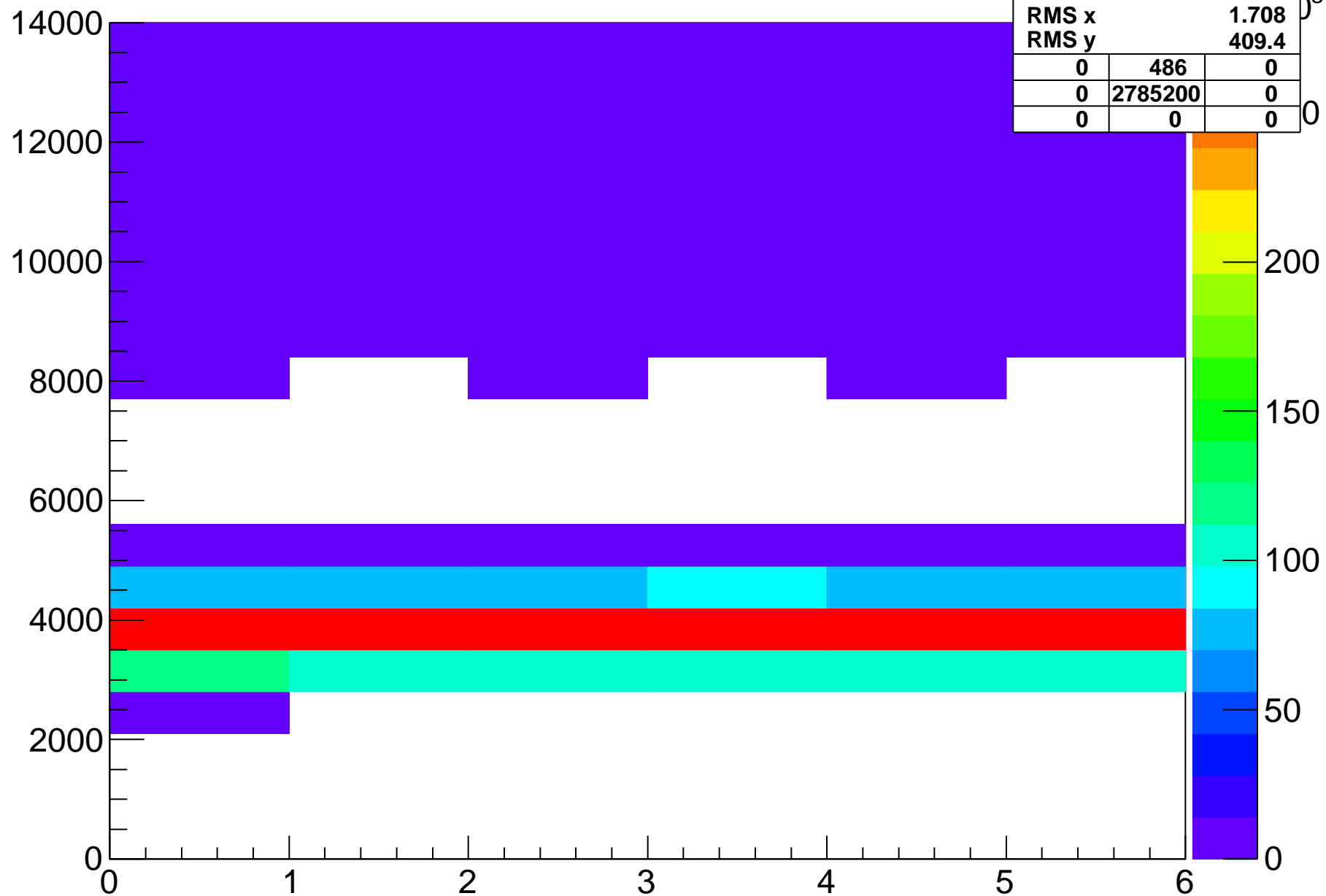
samples-fpga-6-hyb-1



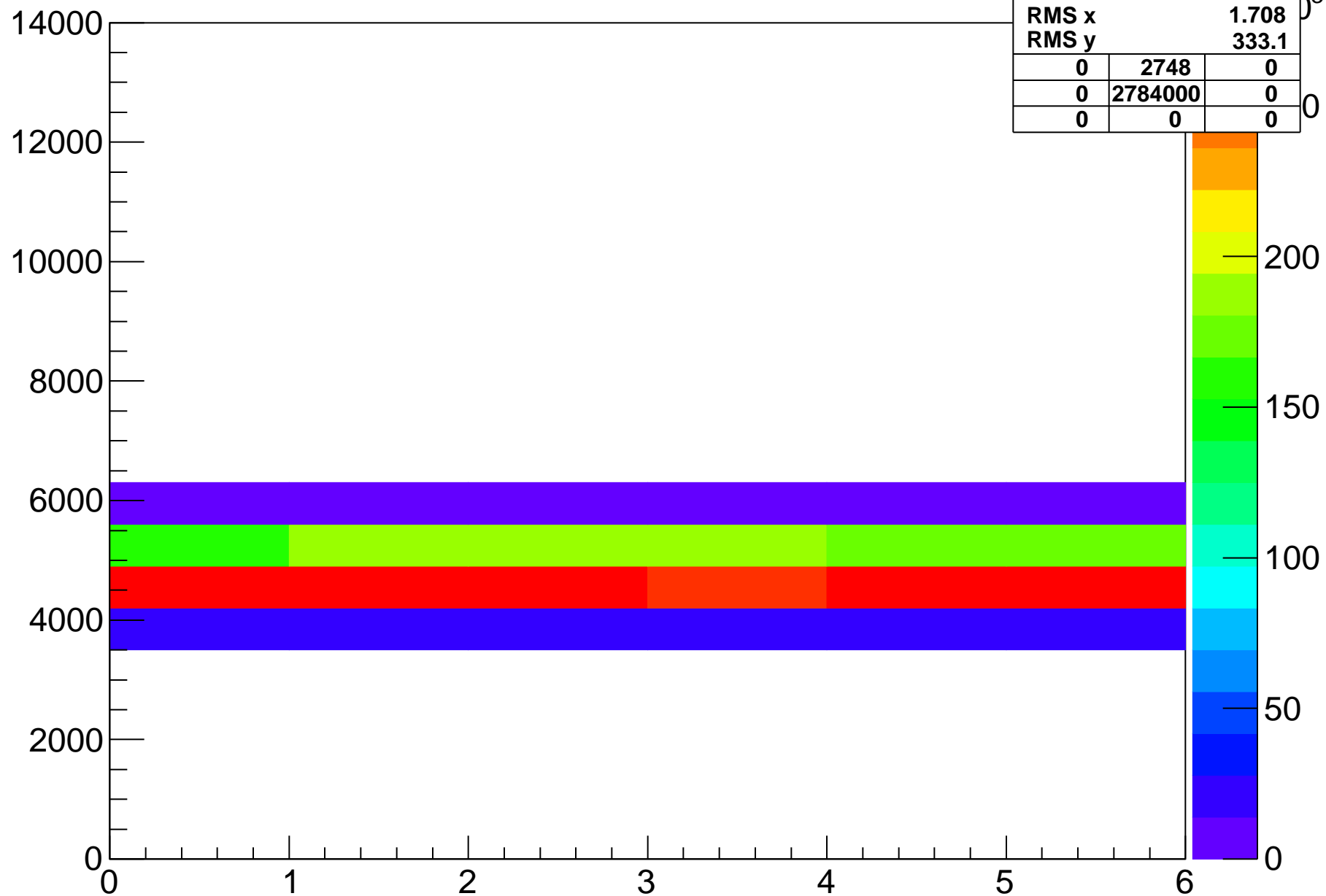
samples-fpga-6-hyb-2



samples-fpga-6-hyb-3

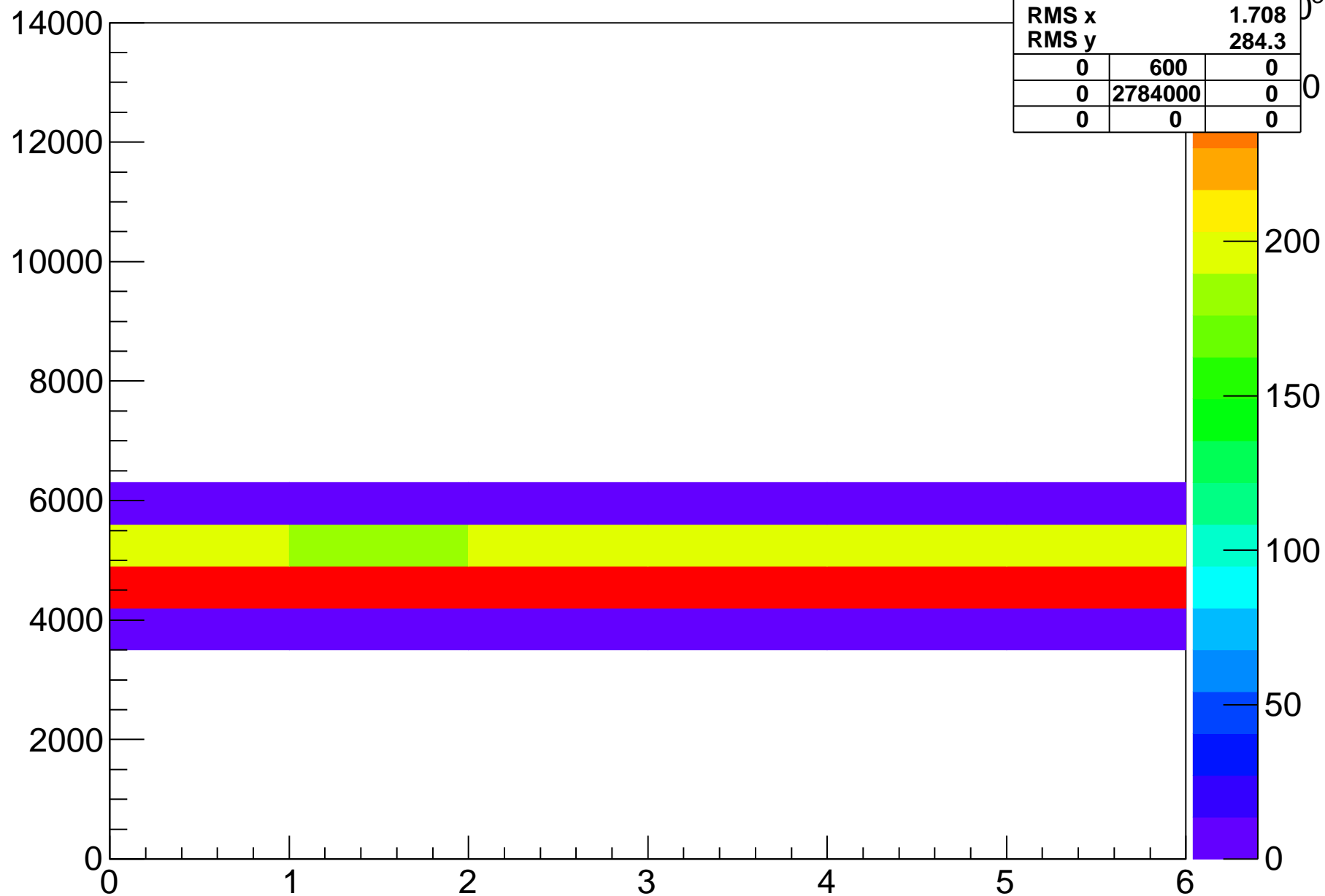


samples-fpga-7-hyb-0

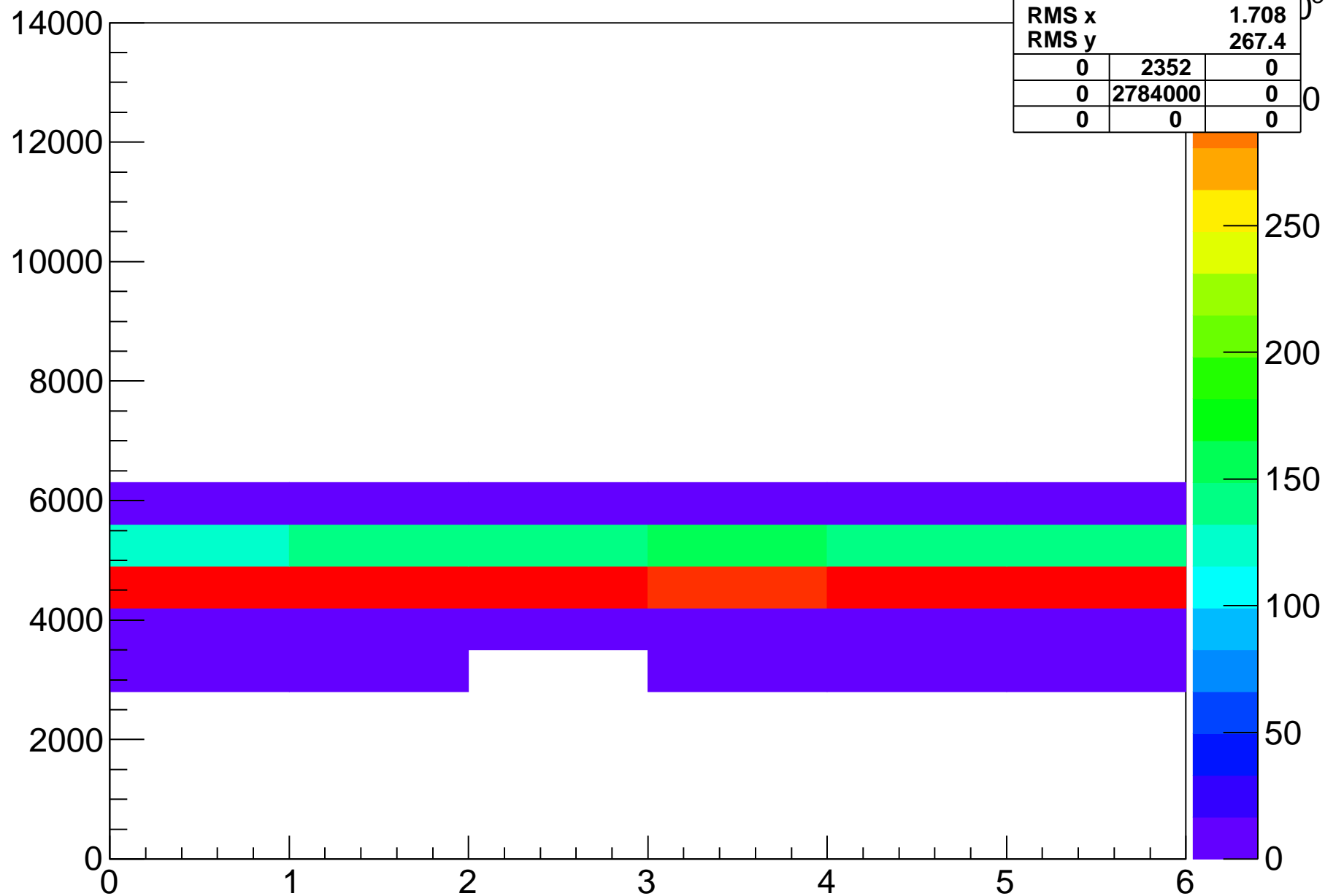


Entries	2786748	
Mean x	2.5	
Mean y	4775	
RMS x	1.708	
RMS y	333.1	
0	2748	0
0	2784000	0
0	0	0

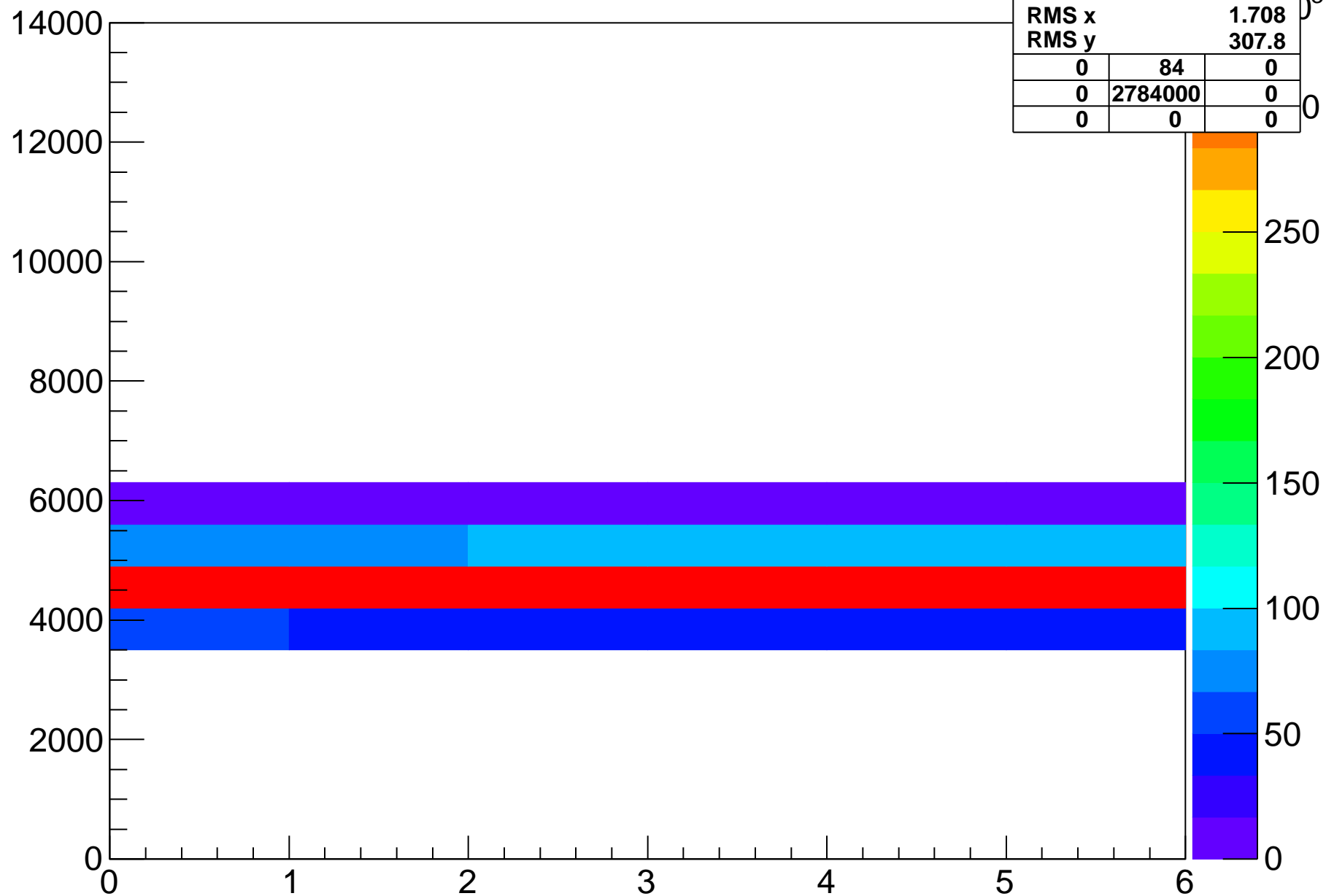
samples-fpga-7-hyb-1



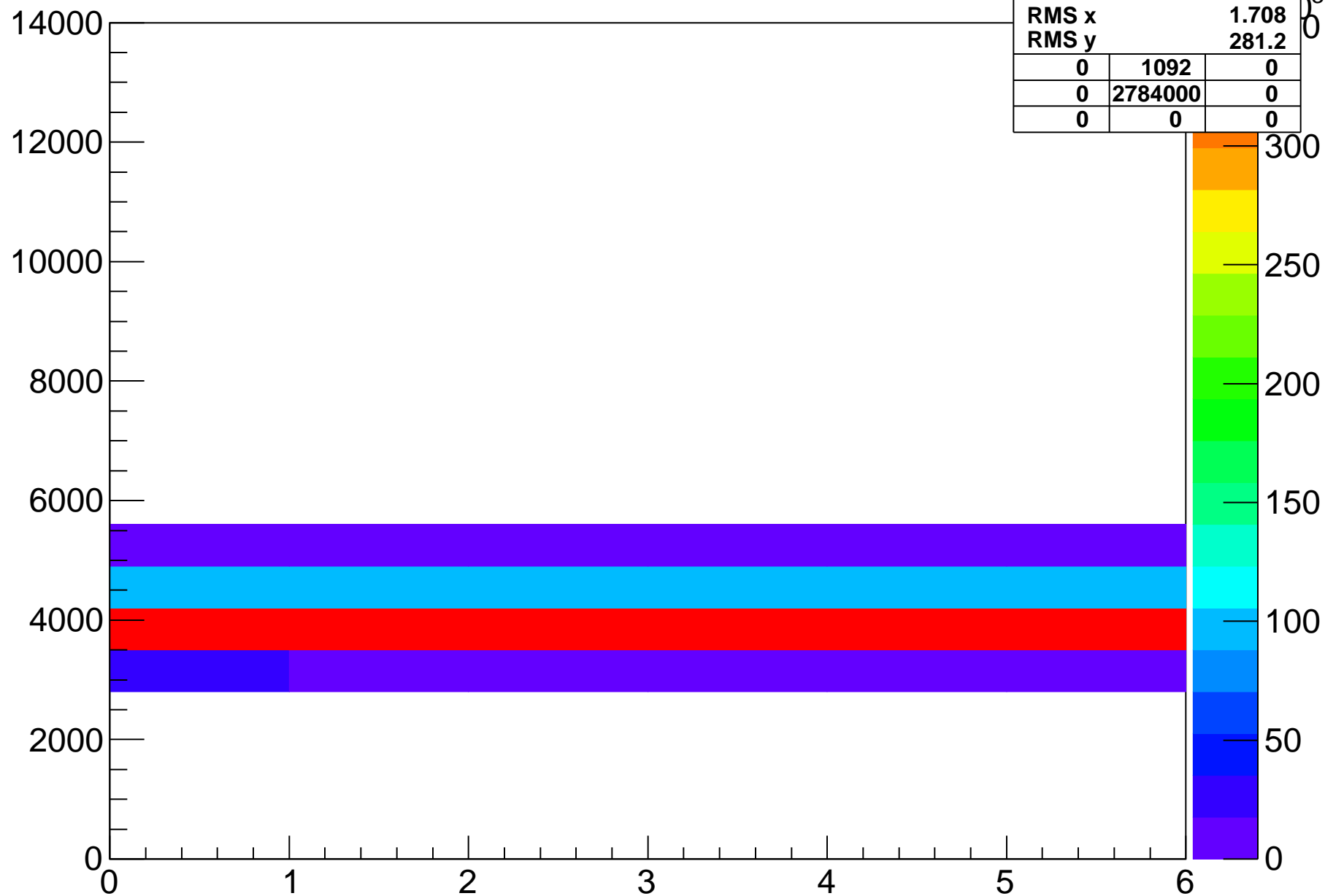
samples-fpga-7-hyb-2



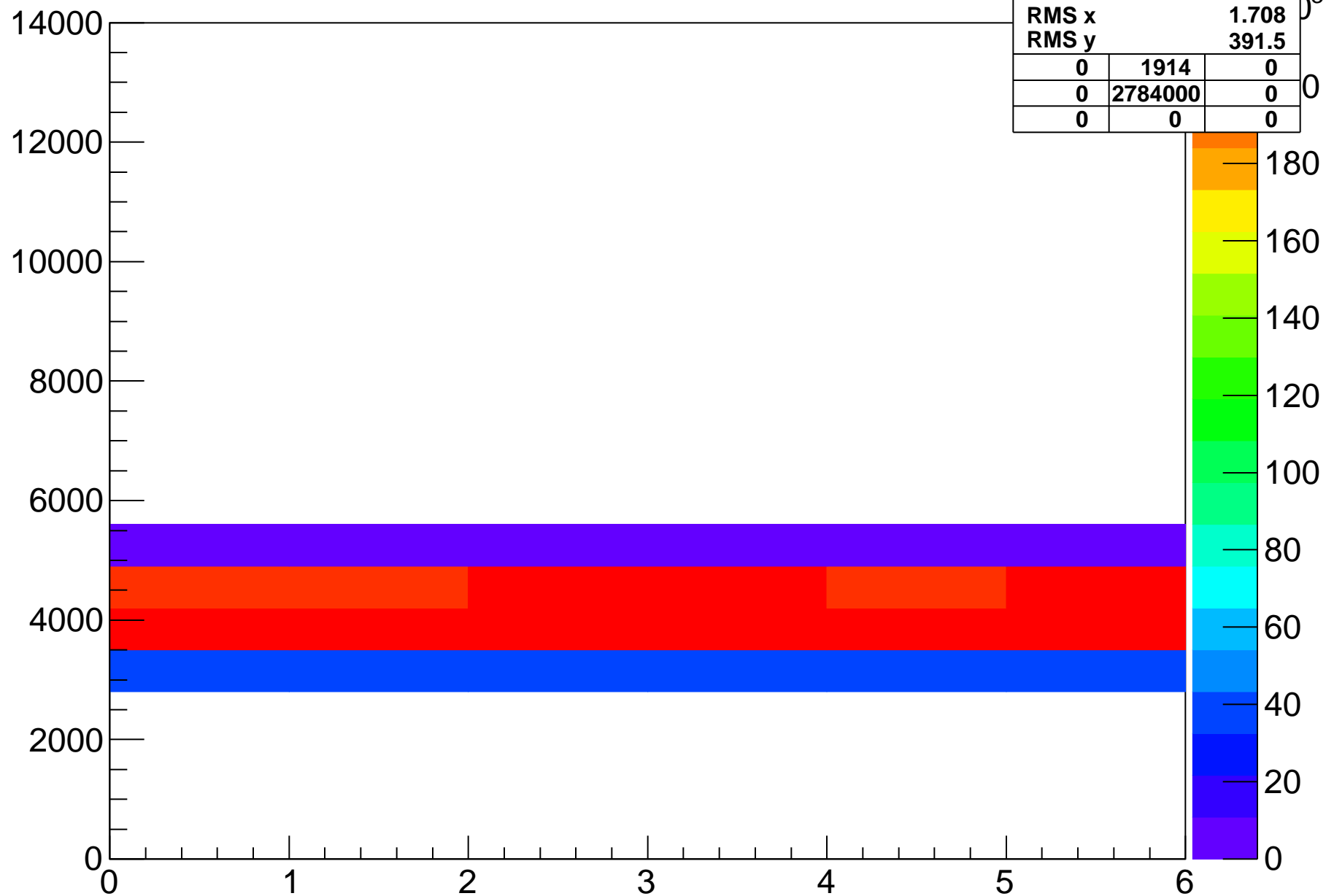
samples-fpga-7-hyb-3



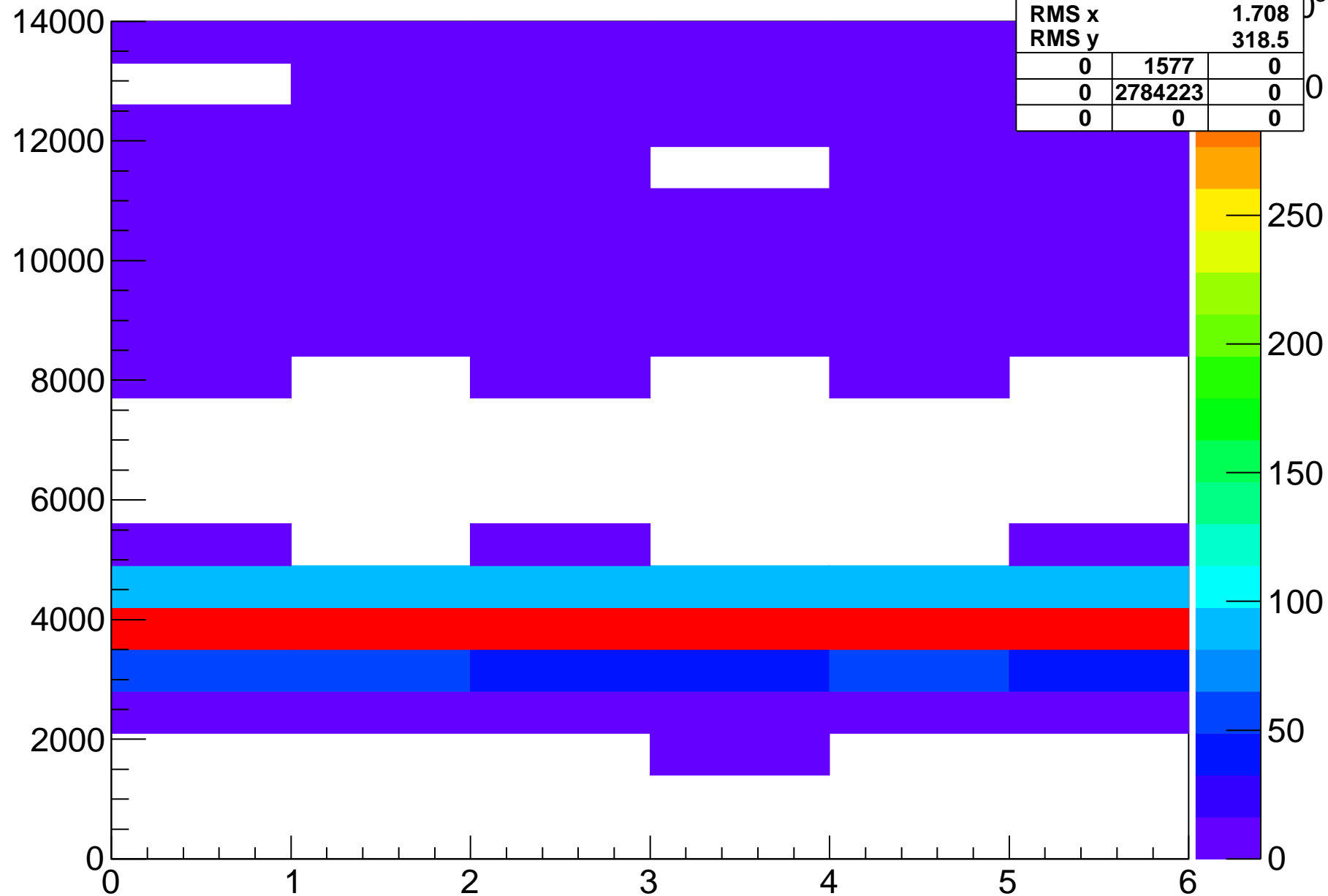
samples-fpga-8-hyb-0



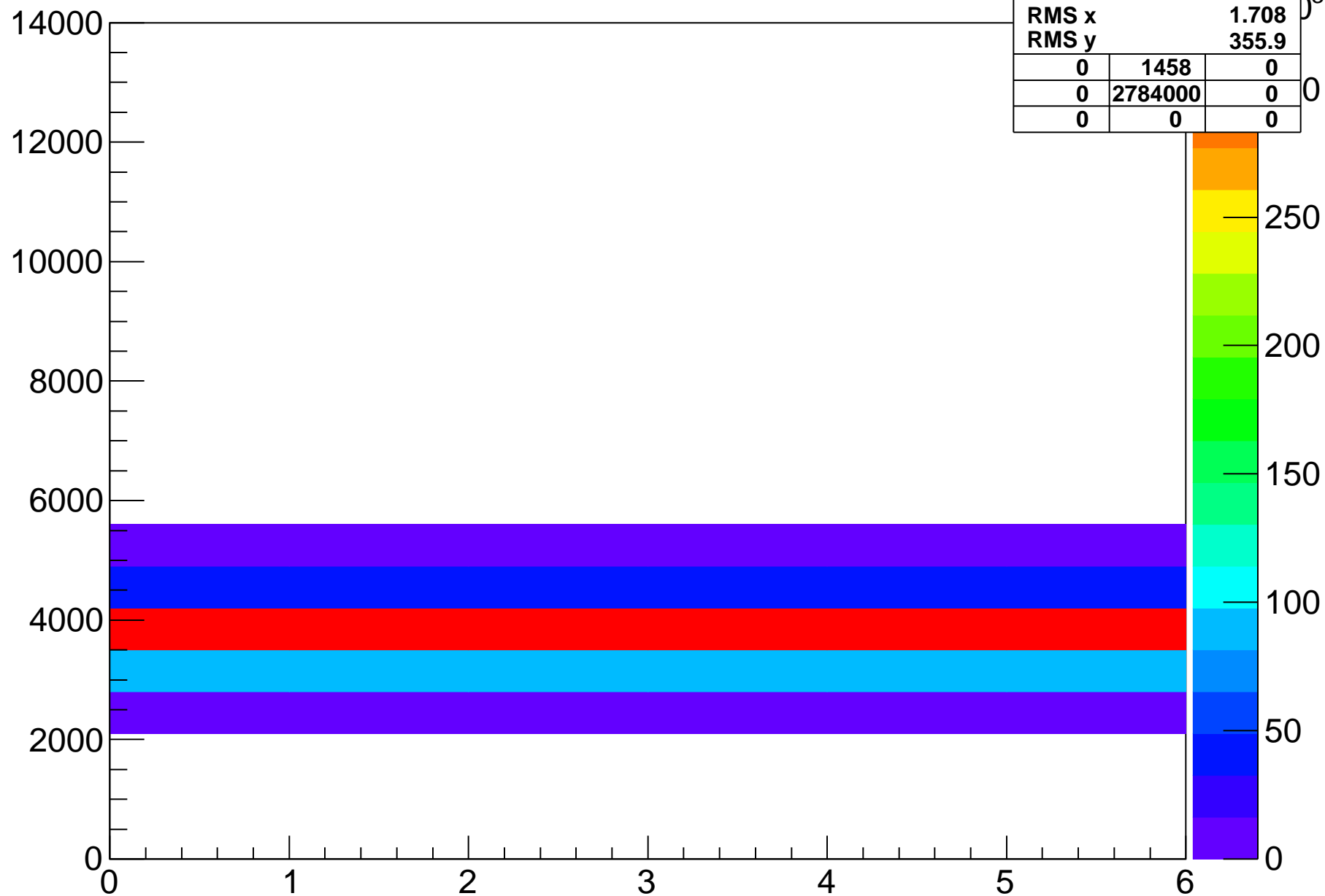
samples-fpga-8-hyb-1



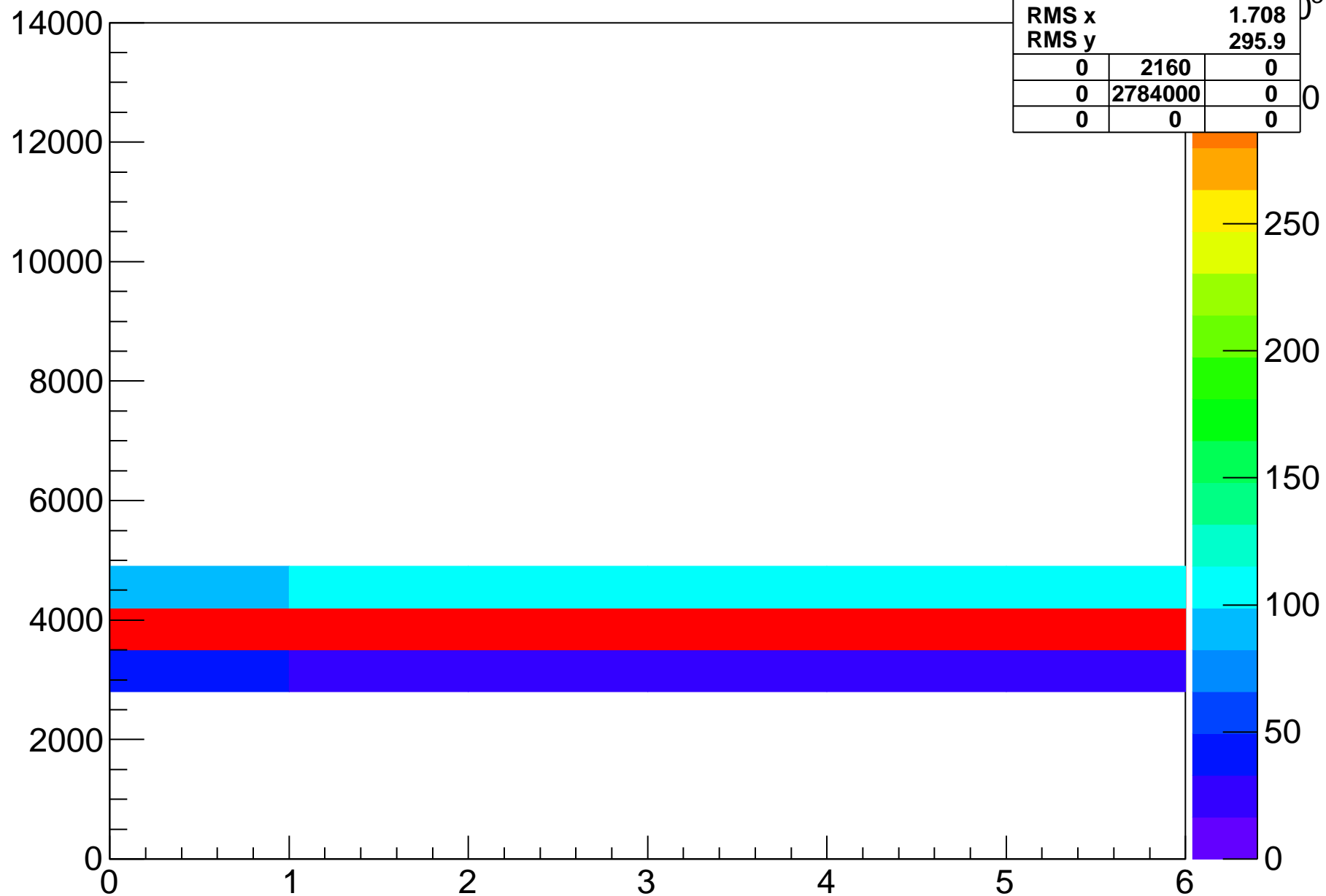
samples-fpga-8-hyb-2



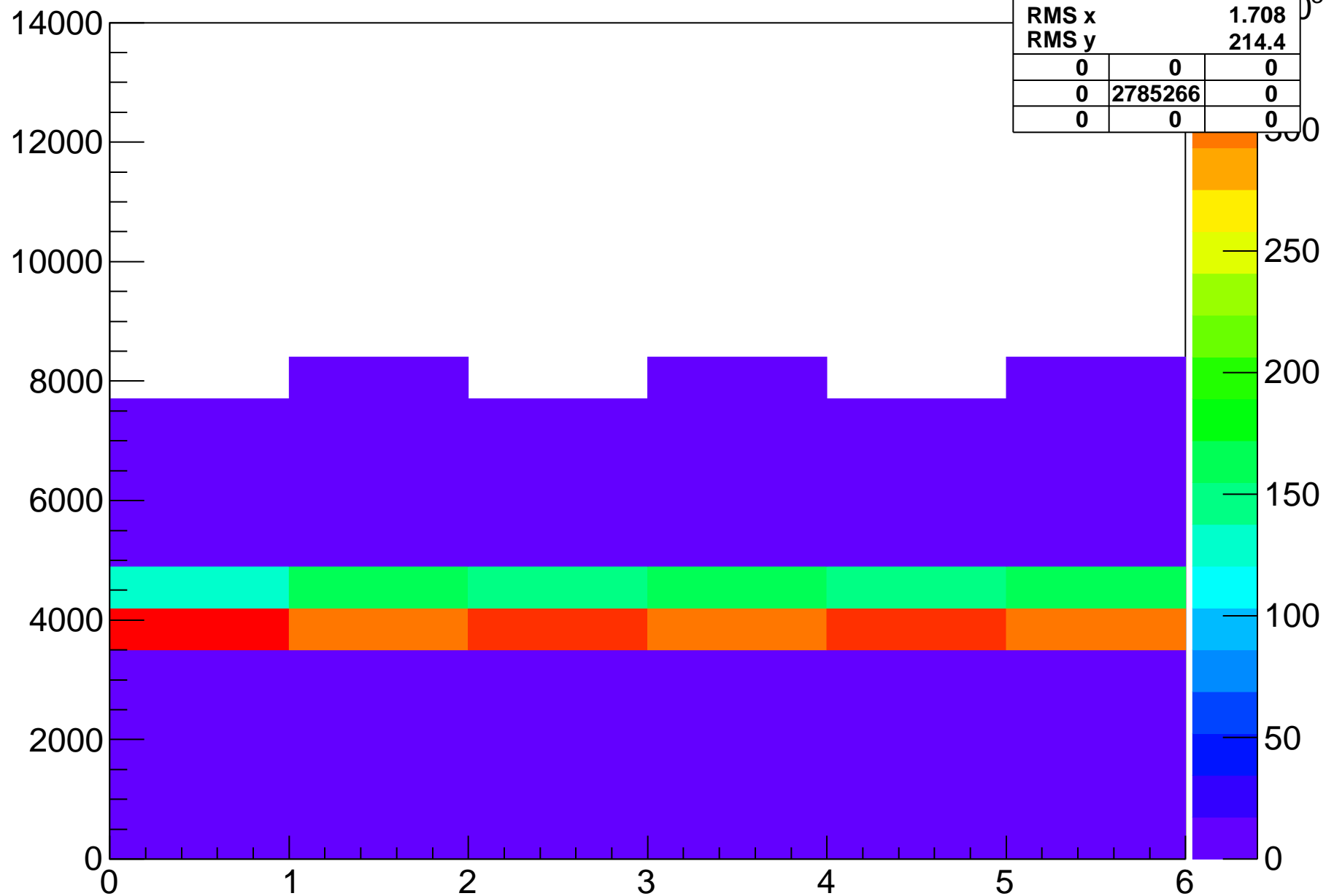
samples-fpga-8-hyb-3



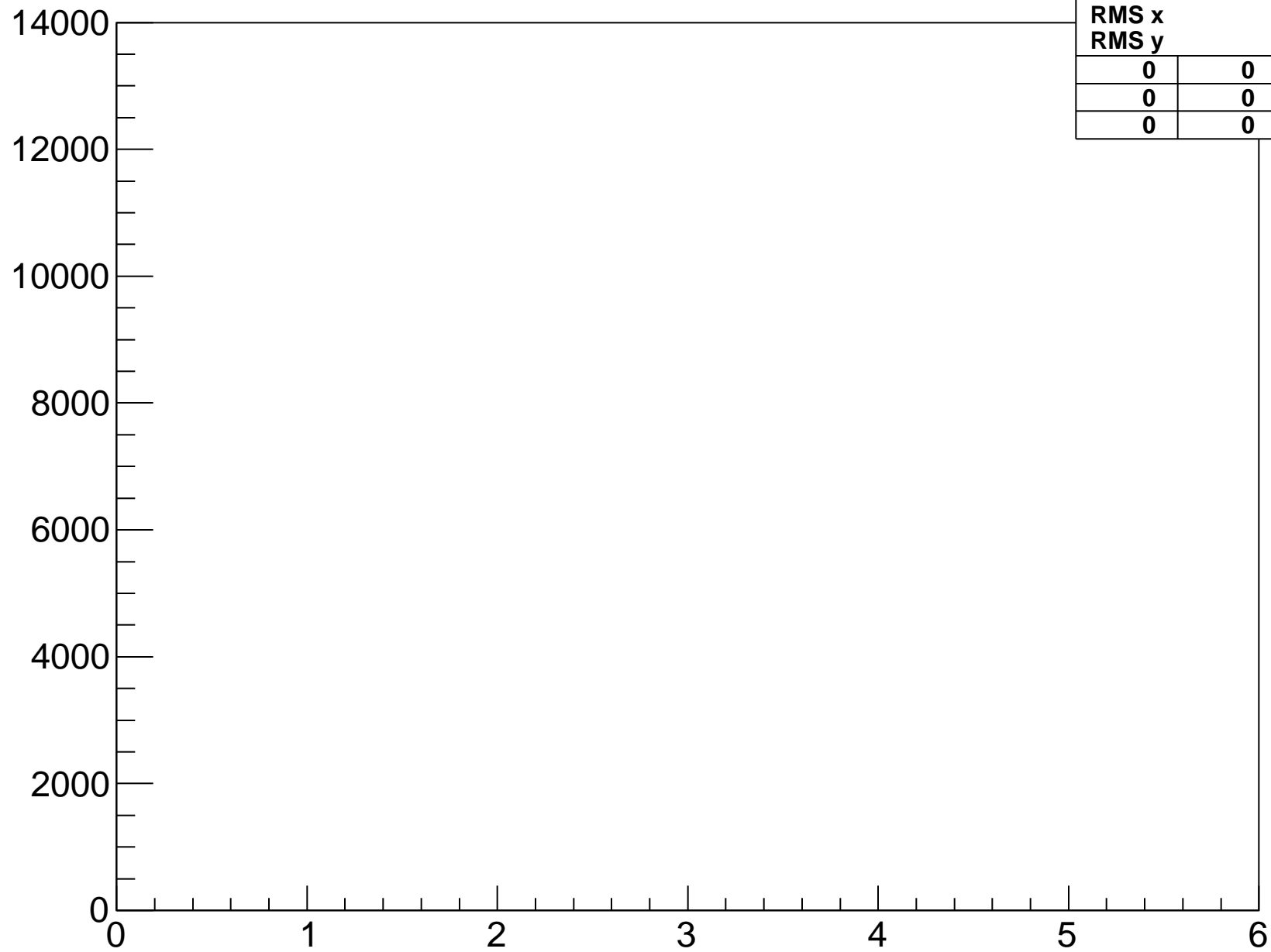
samples-fpga-9-hyb-0



samples-fpga-9-hyb-1

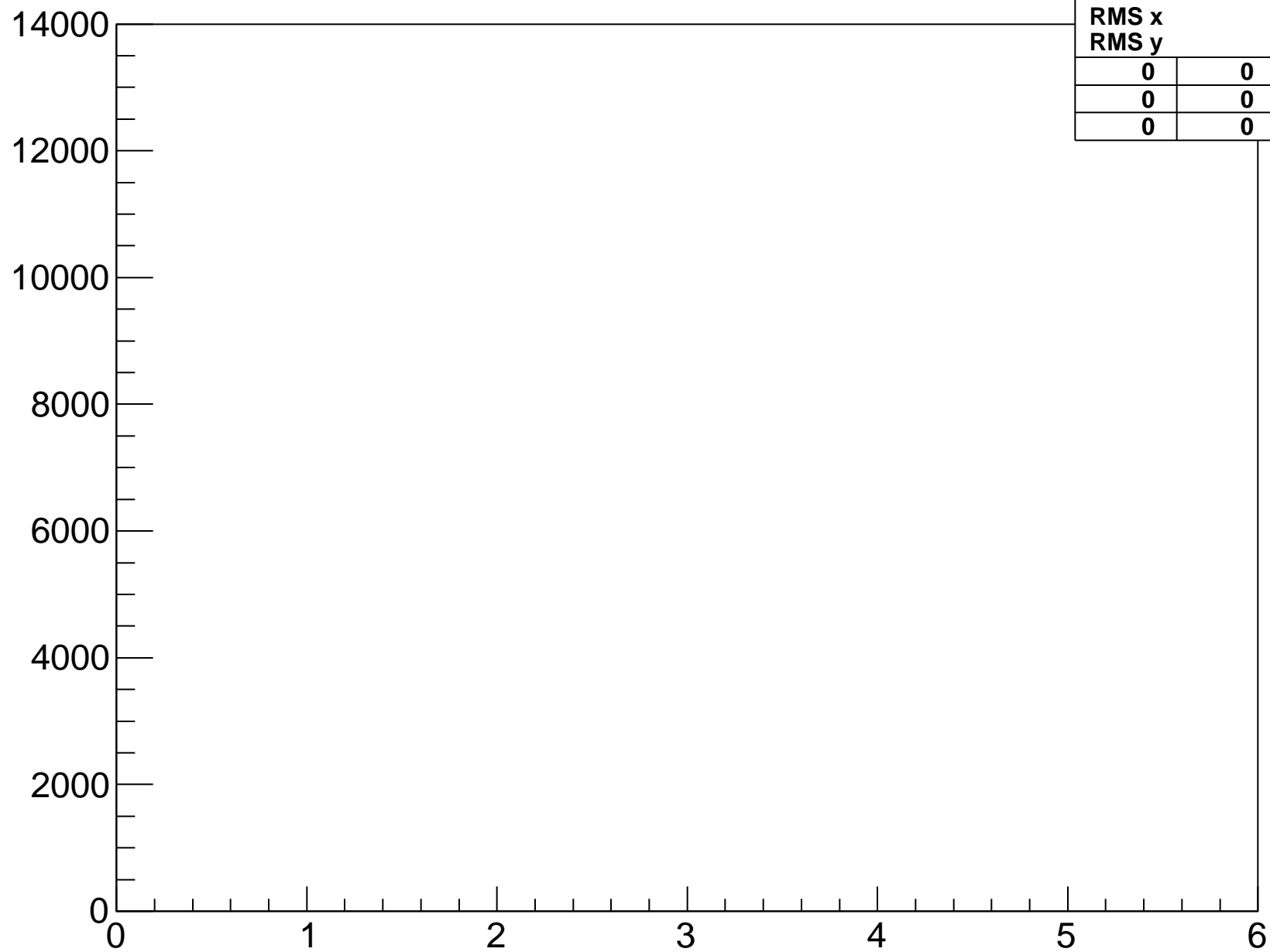


samples-fpga-9-hyb-2



Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

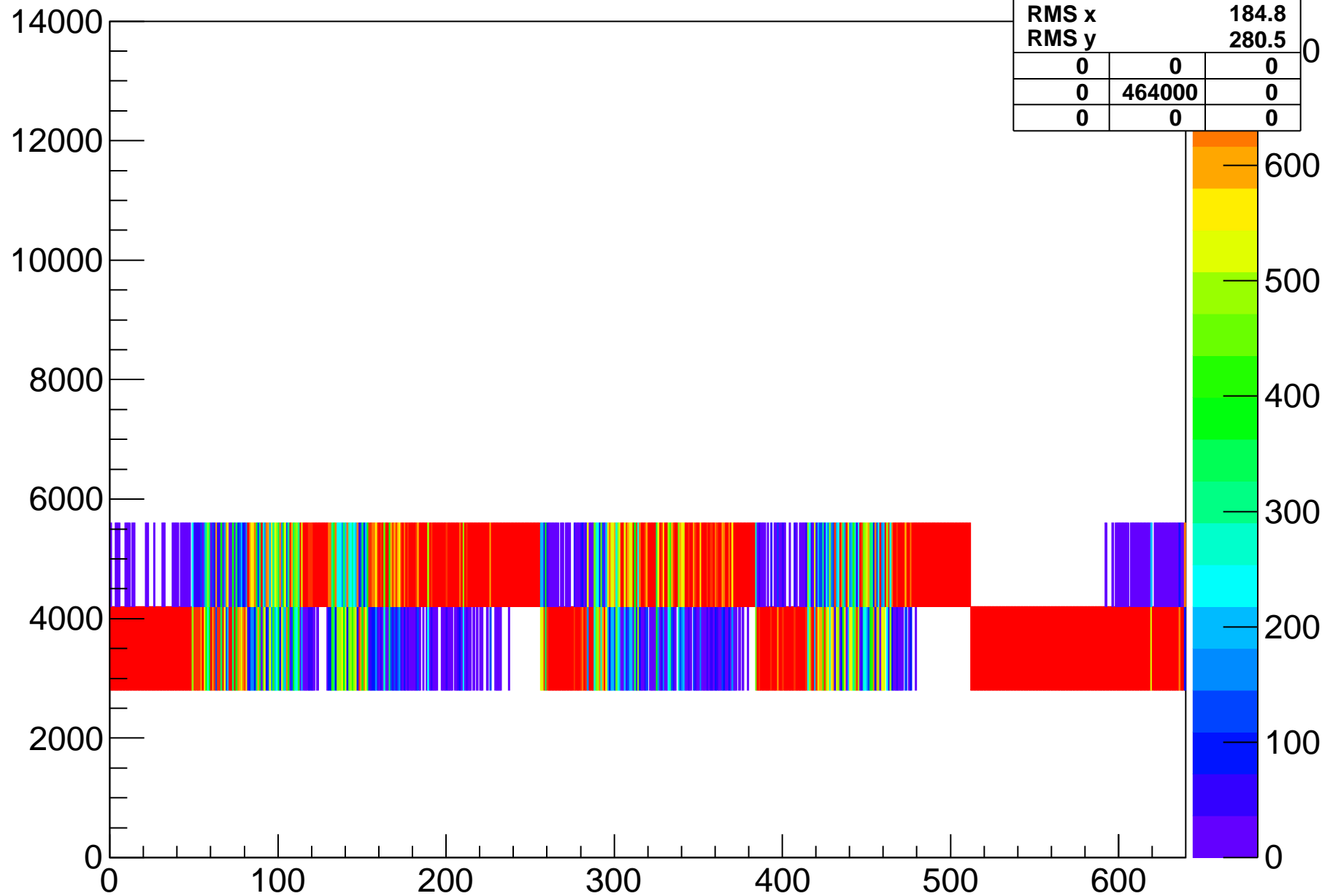
samples-fpga-9-hyb-3



Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	
0	0	0	
0	0	0	

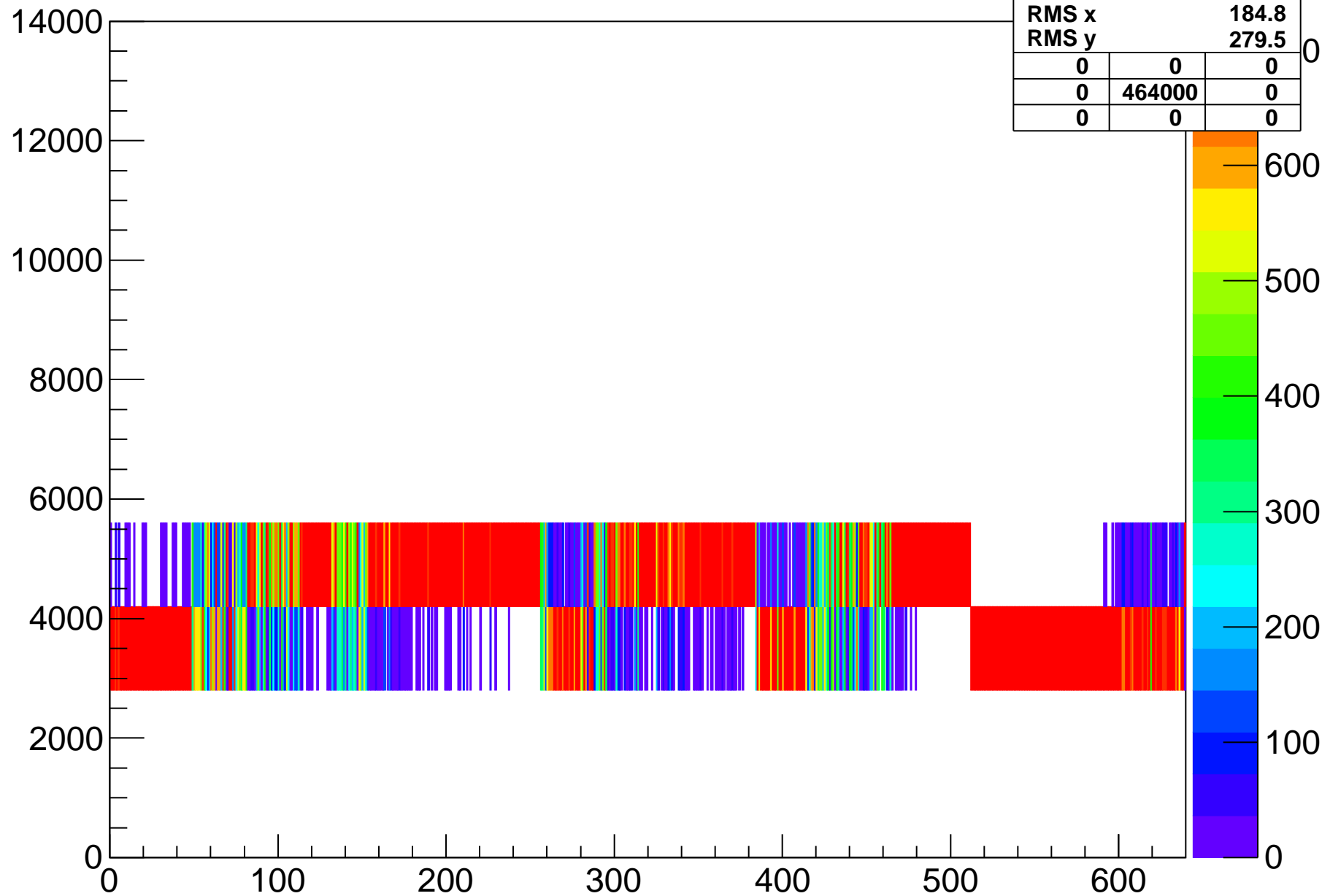
baselinesamples-fpga-0-hyb-0-sample-0

Entries		464000
Mean x		319.5
Mean y		4156
RMS x		184.8
RMS y		280.5
0	0	0
0	464000	0
0	0	0



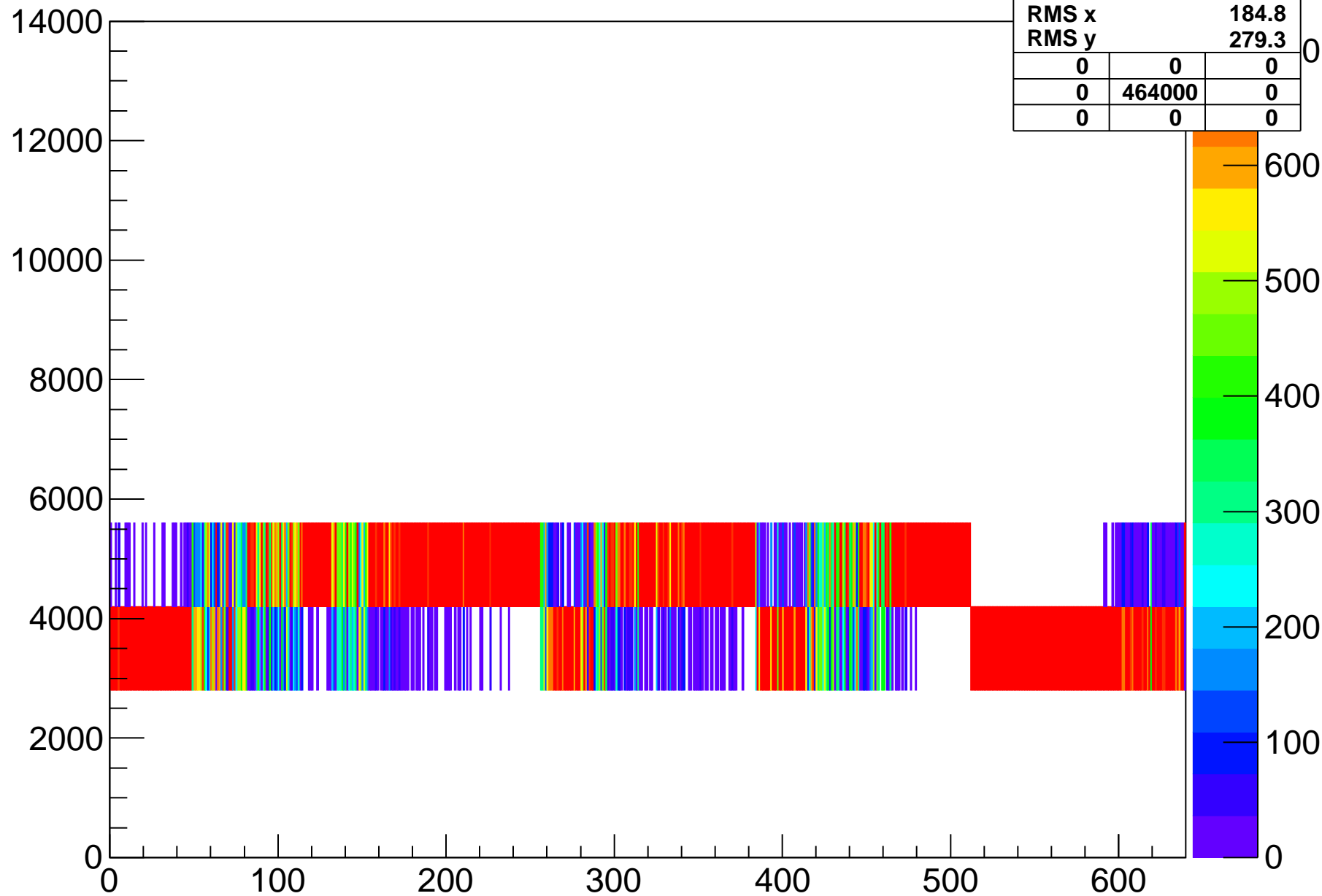
baselinesamples-fpga-0-hyb-0-sample-1

Entries		464000
Mean x		319.5
Mean y		4197
RMS x		184.8
RMS y		279.5
0	0	0
0	464000	0
0	0	0



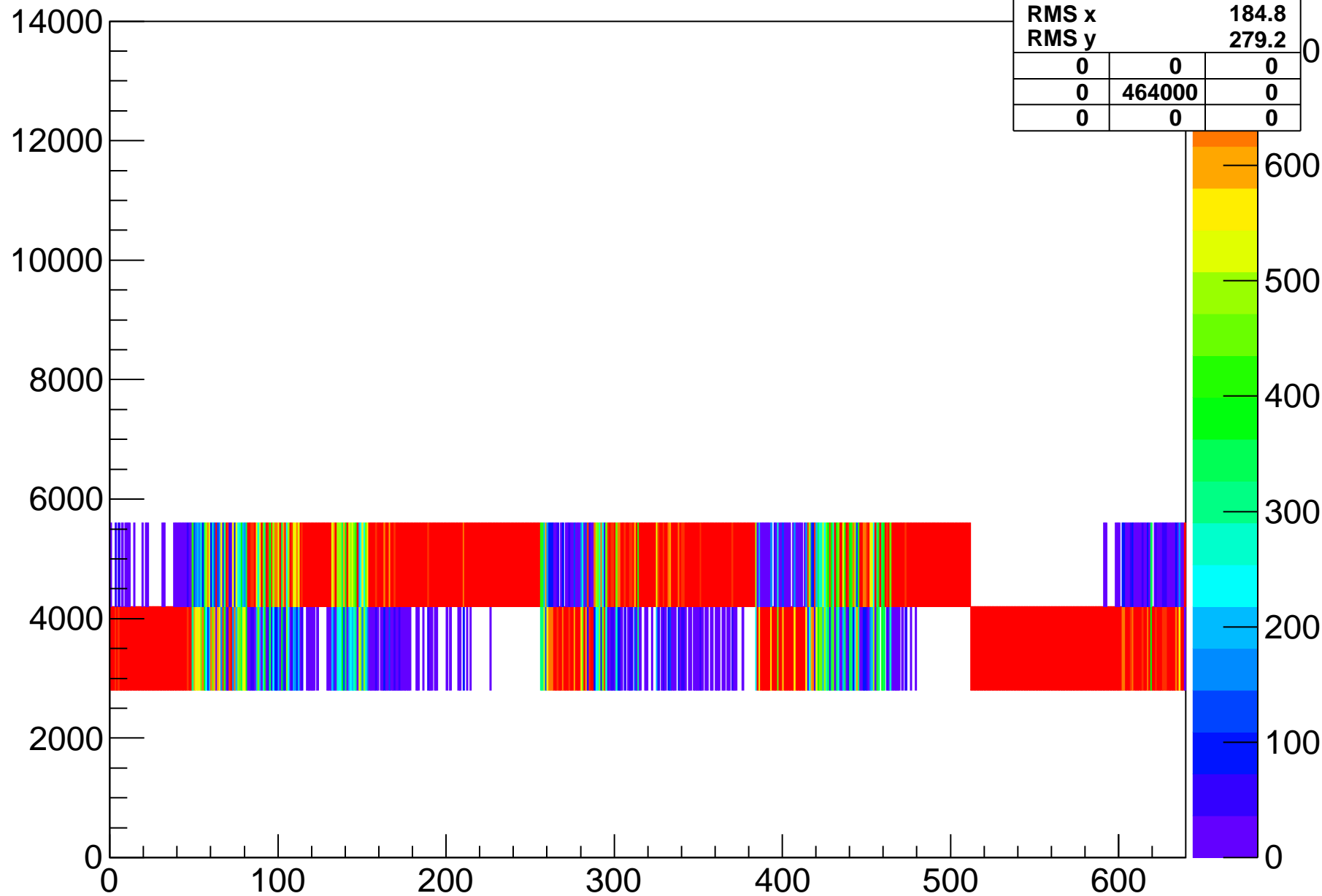
baselinesamples-fpga-0-hyb-0-sample-2

Entries	464000	
Mean x	319.5	
Mean y	4195	
RMS x	184.8	
RMS y	279.3	
0	0	0
0	464000	0
0	0	0



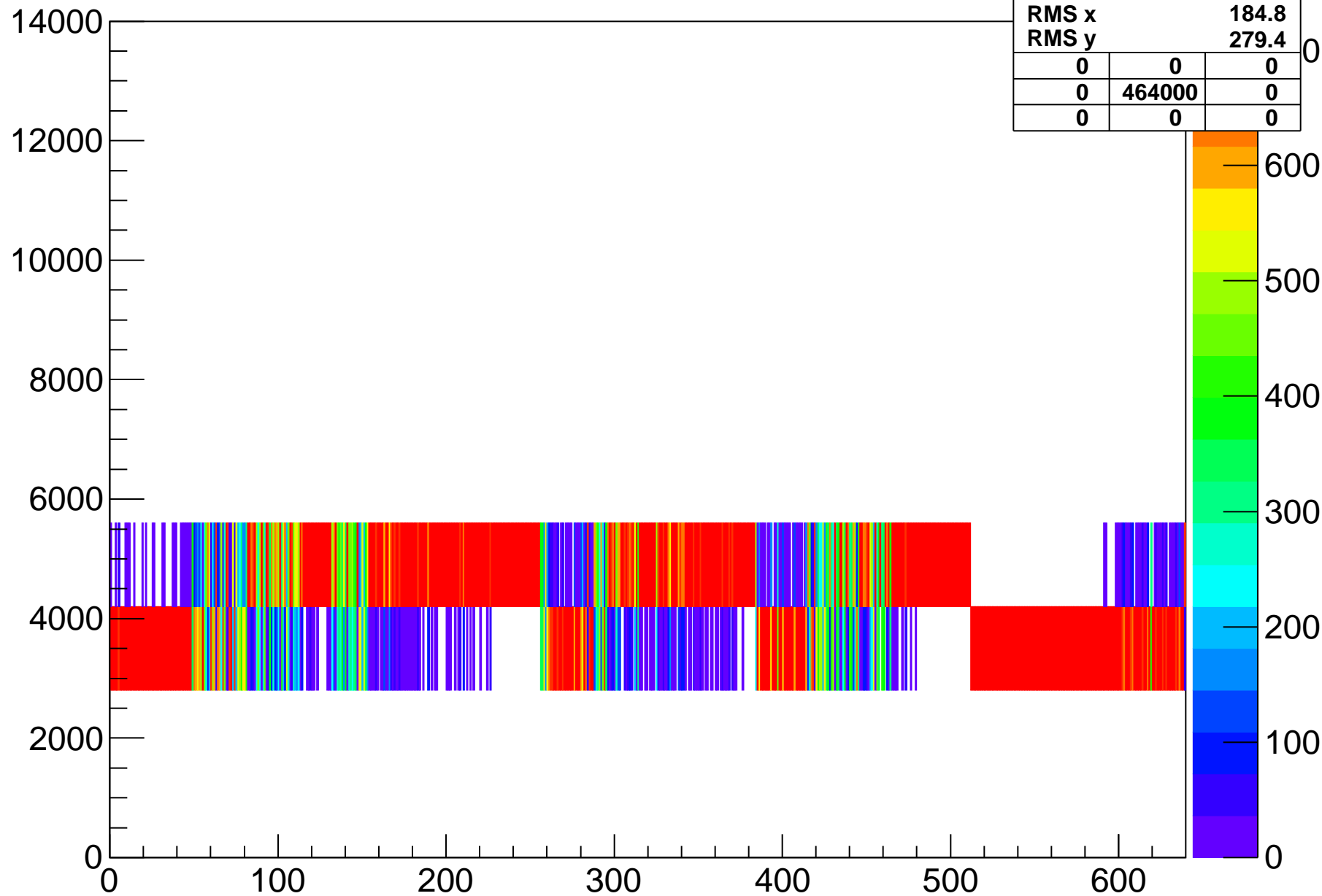
baselinesamples-fpga-0-hyb-0-sample-3

Entries	464000	
Mean x	319.5	
Mean y	4199	
RMS x	184.8	
RMS y	279.2	
0	0	0
0	464000	0
0	0	0



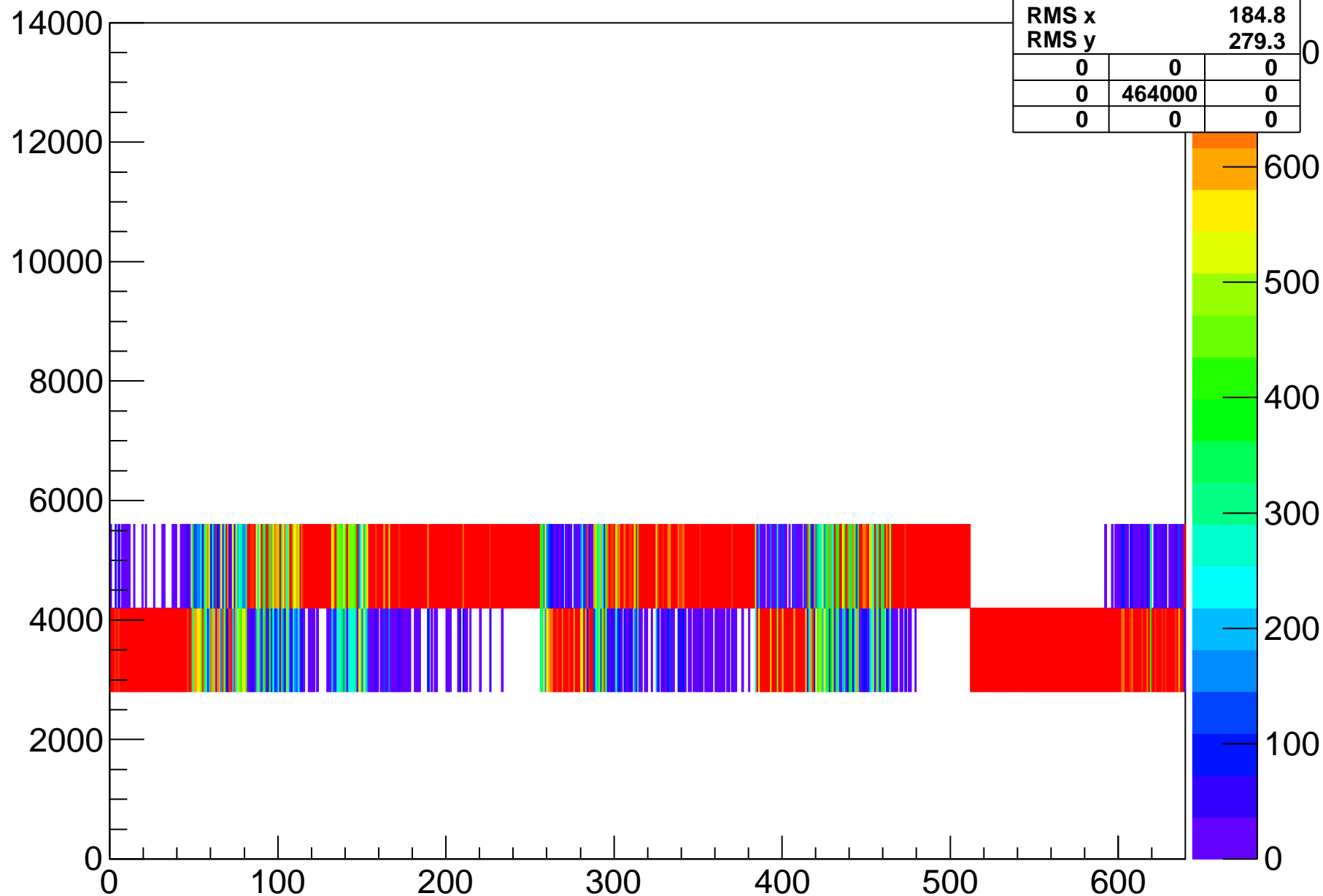
baselinesamples-fpga-0-hyb-0-sample-4

Entries			464000
Mean x			319.5
Mean y			4191
RMS x			184.8
RMS y			279.4
0	0	0	0
0	464000	0	0
0	0	0	0



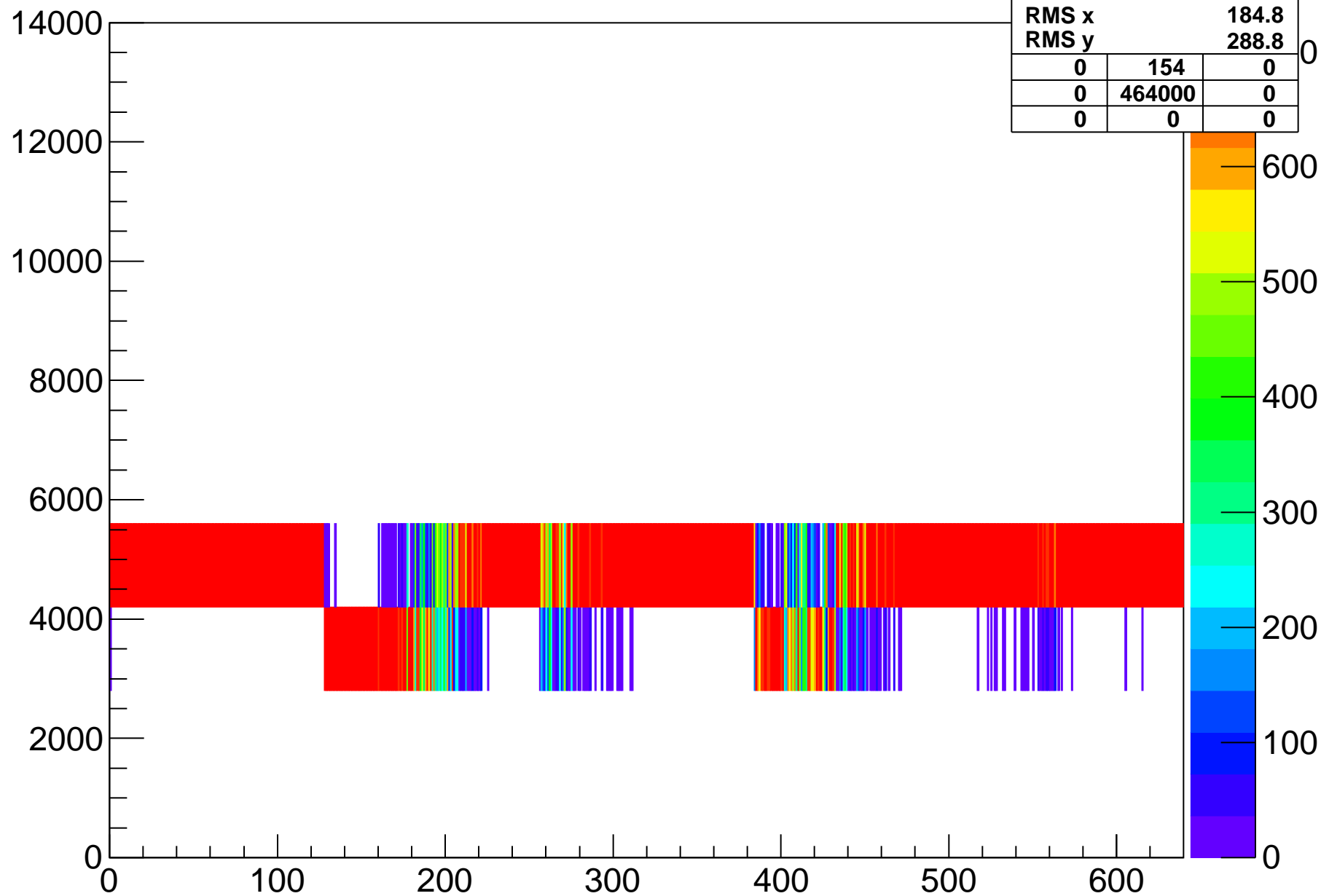
baselinesamples-fpga-0-hyb-0-sample-5

Entries	464000	
Mean x	319.5	
Mean y	4195	
RMS x	184.8	
RMS y	279.3	
0	0	0
0	464000	0
0	0	0



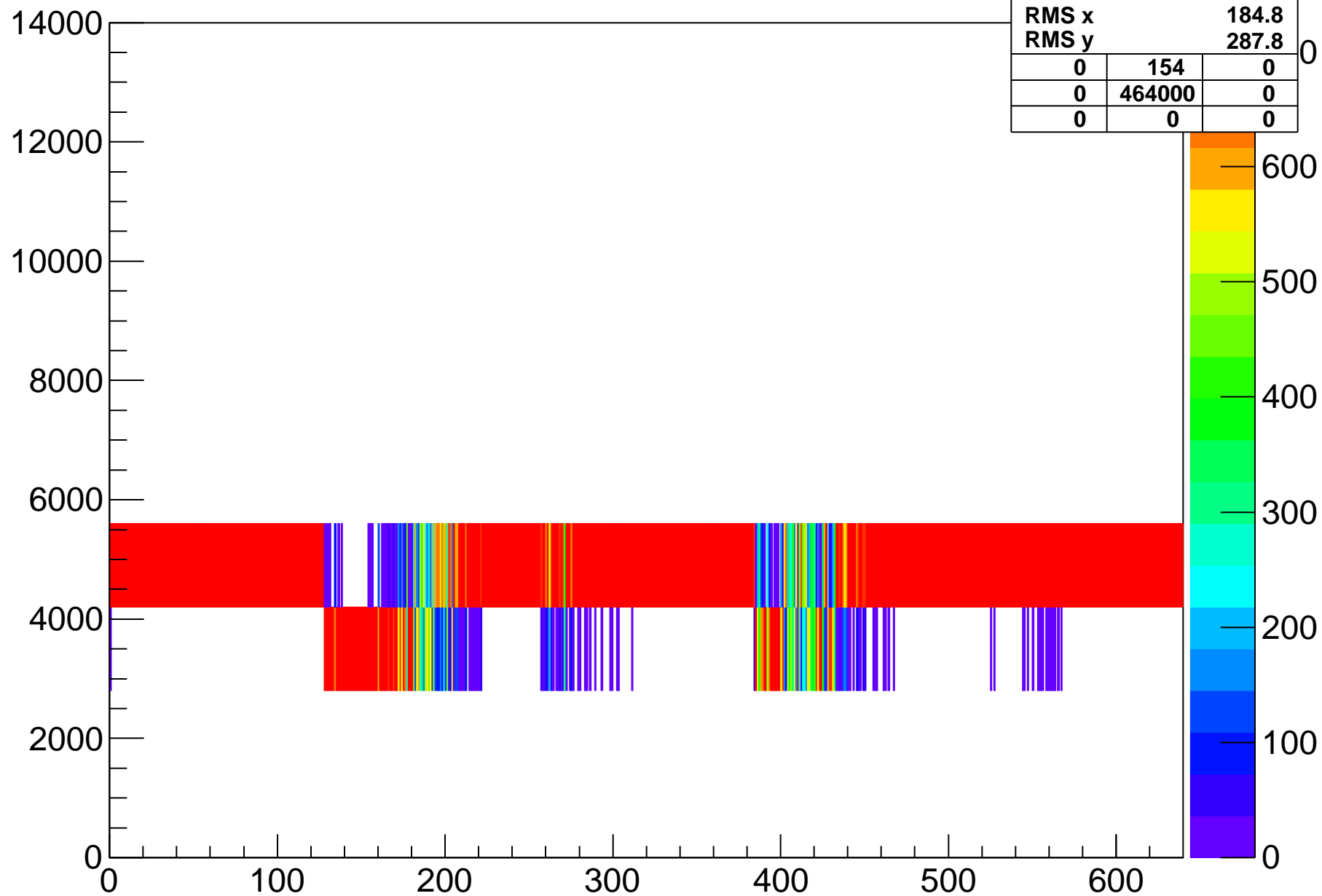
baselinesamples-fpga-0-hyb-1-sample-0

Entries	464154	
Mean x	319.5	
Mean y	4457	
RMS x	184.8	
RMS y	288.8	
0	154	0
0	464000	0
0	0	0



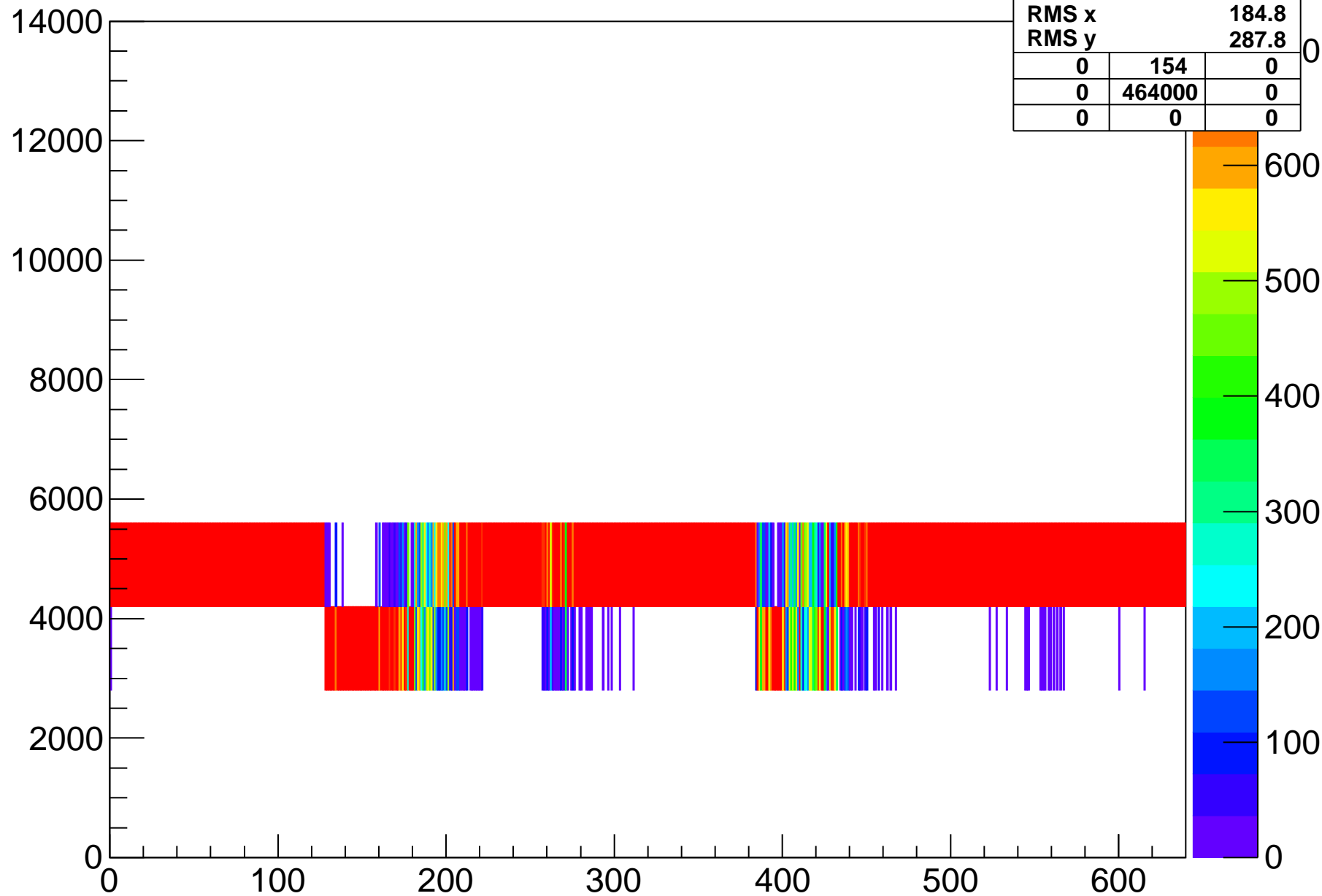
baselinesamples-fpga-0-hyb-1-sample-1

Entries	464154	
Mean x	319.5	
Mean y	4494	
RMS x	184.8	
RMS y	287.8	
0	154	0
0	464000	0
0	0	0



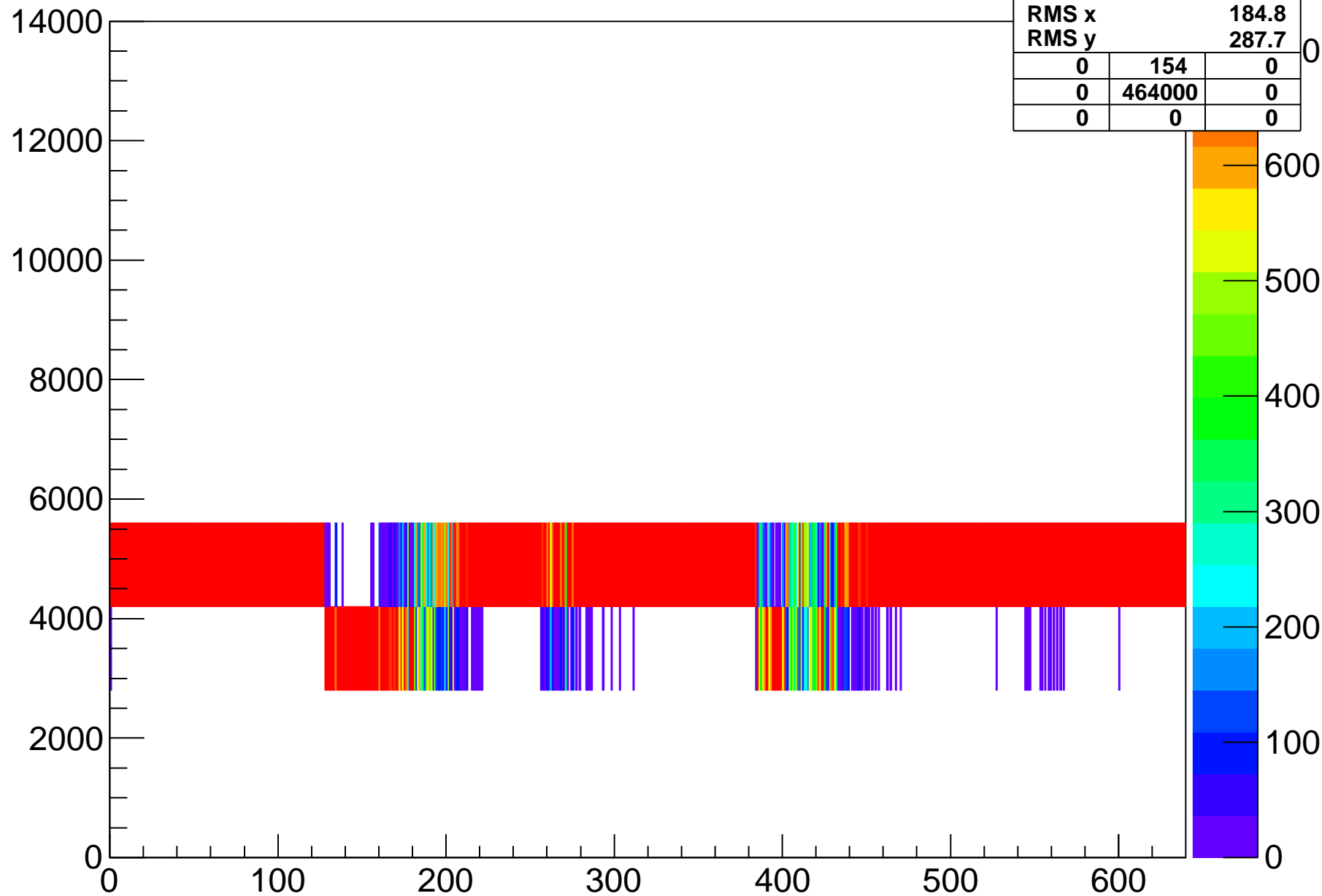
baselinesamples-fpga-0-hyb-1-sample-2

Entries	464154	
Mean x	319.5	
Mean y	4491	
RMS x	184.8	
RMS y	287.8	
0	154	0
0	464000	0
0	0	0



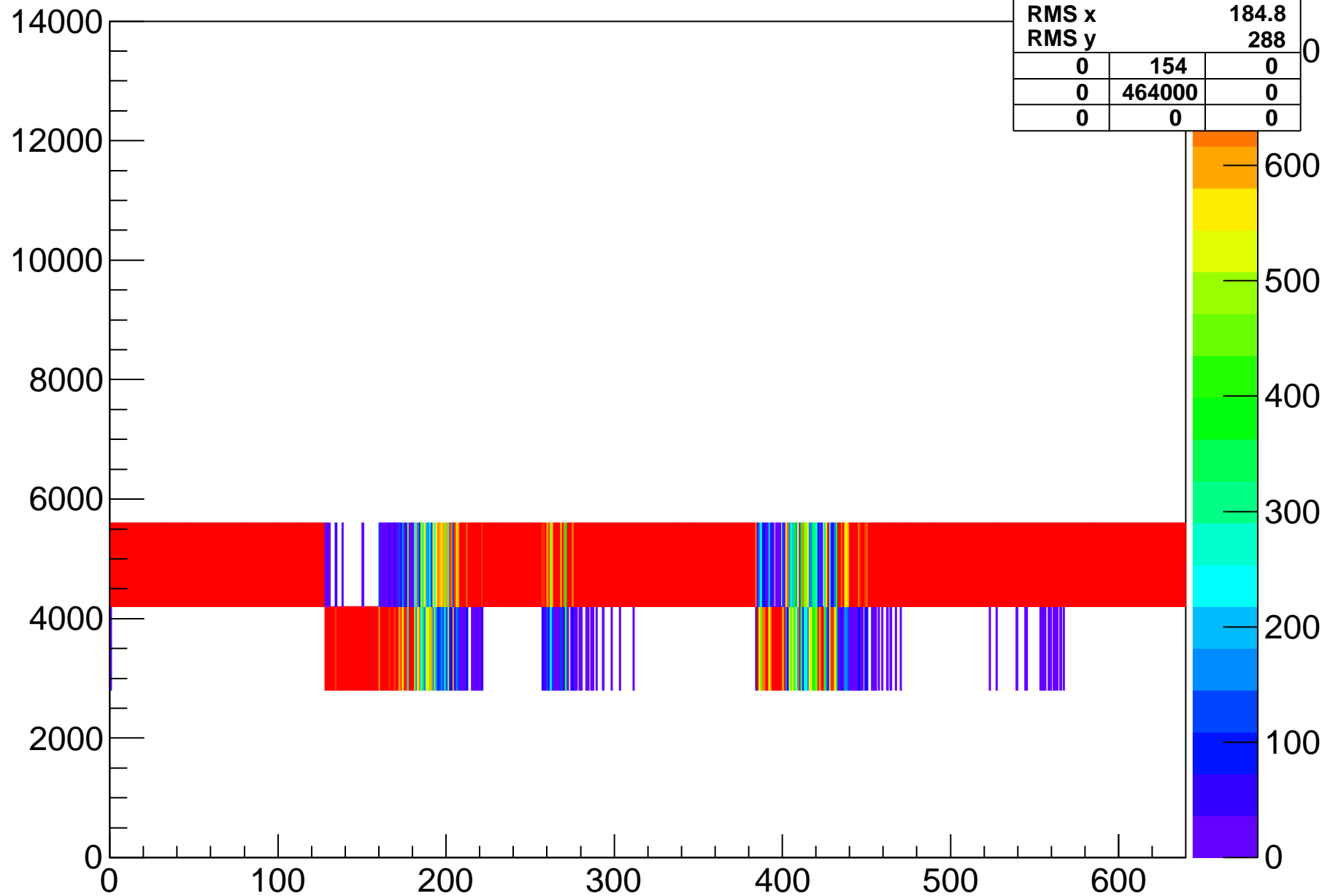
baselinesamples-fpga-0-hyb-1-sample-3

Entries	464154	
Mean x	319.5	
Mean y	4496	
RMS x	184.8	
RMS y	287.7	
0	154	0
0	464000	0
0	0	0



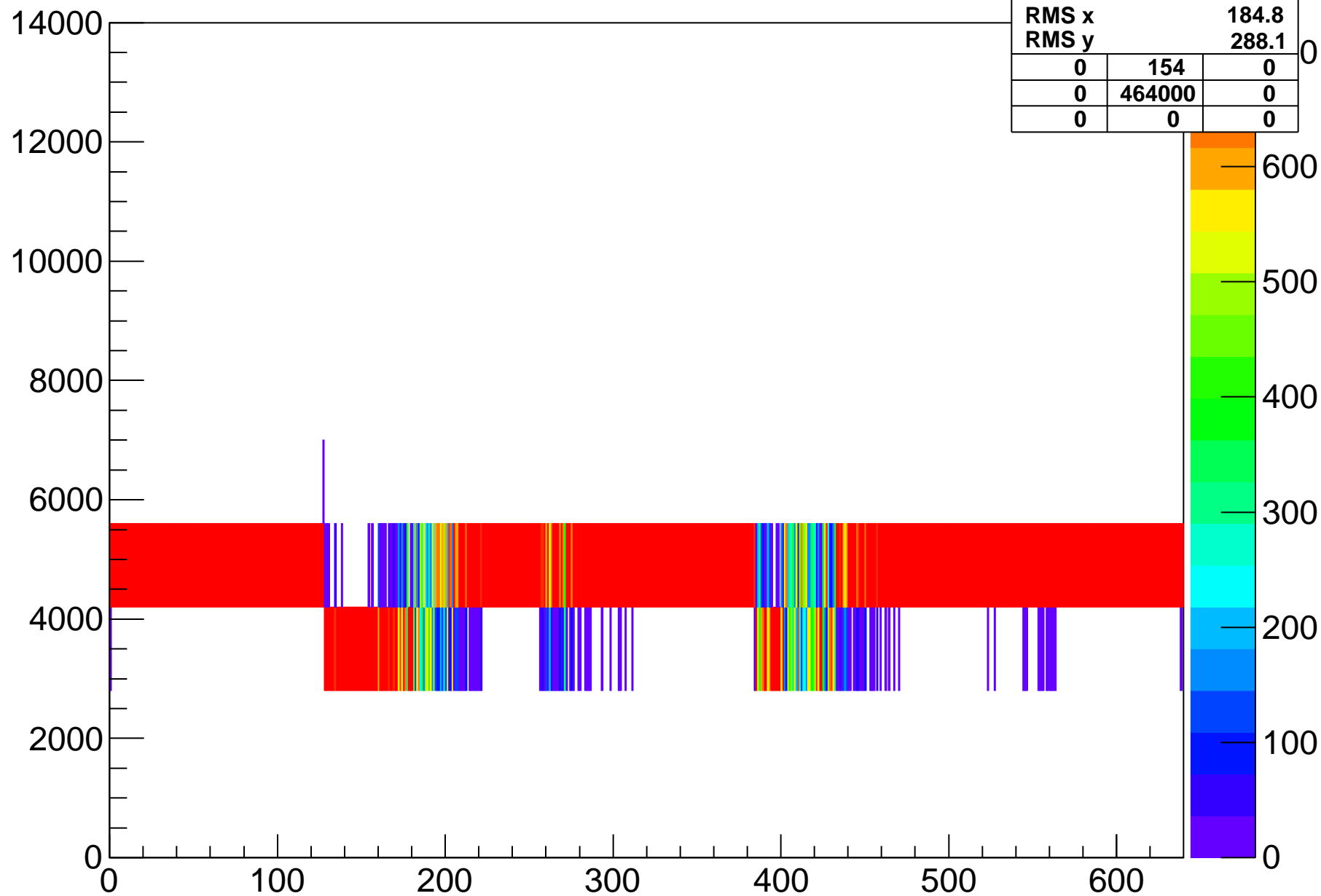
baselinesamples-fpga-0-hyb-1-sample-4

Entries	464154	
Mean x	319.5	
Mean y	4488	
RMS x	184.8	
RMS y	288	
0	154	0
0	464000	0
0	0	0



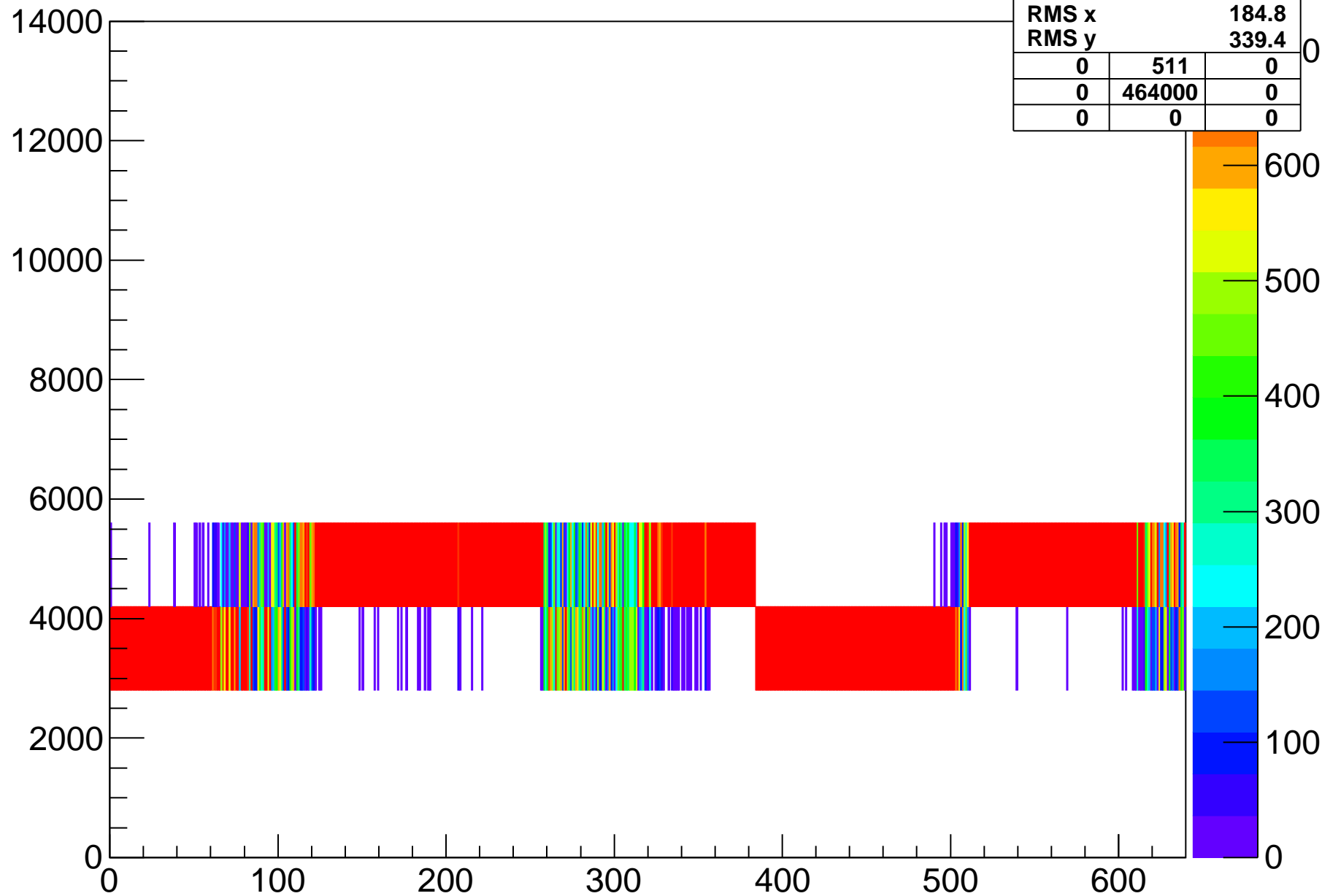
baselinesamples-fpga-0-hyb-1-sample-5

Entries	464154	
Mean x	319.5	
Mean y	4491	
RMS x	184.8	
RMS y	288.1	
0	154	0
0	464000	0
0	0	0



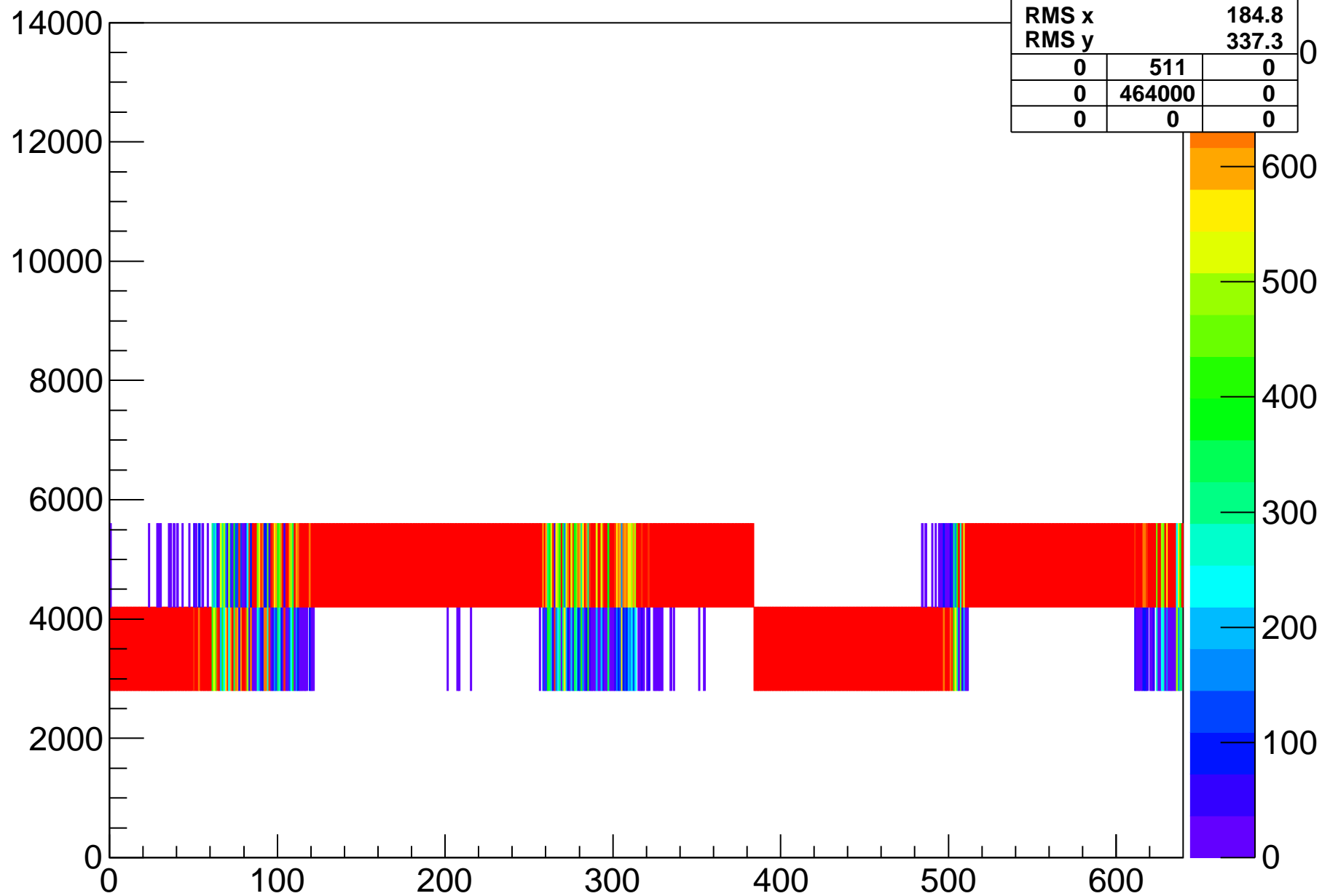
baselinesamples-fpga-0-hyb-2-sample-0

Entries	464511	
Mean x	319.5	
Mean y	4215	
RMS x	184.8	
RMS y	339.4	
0	511	0
0	464000	0
0	0	0



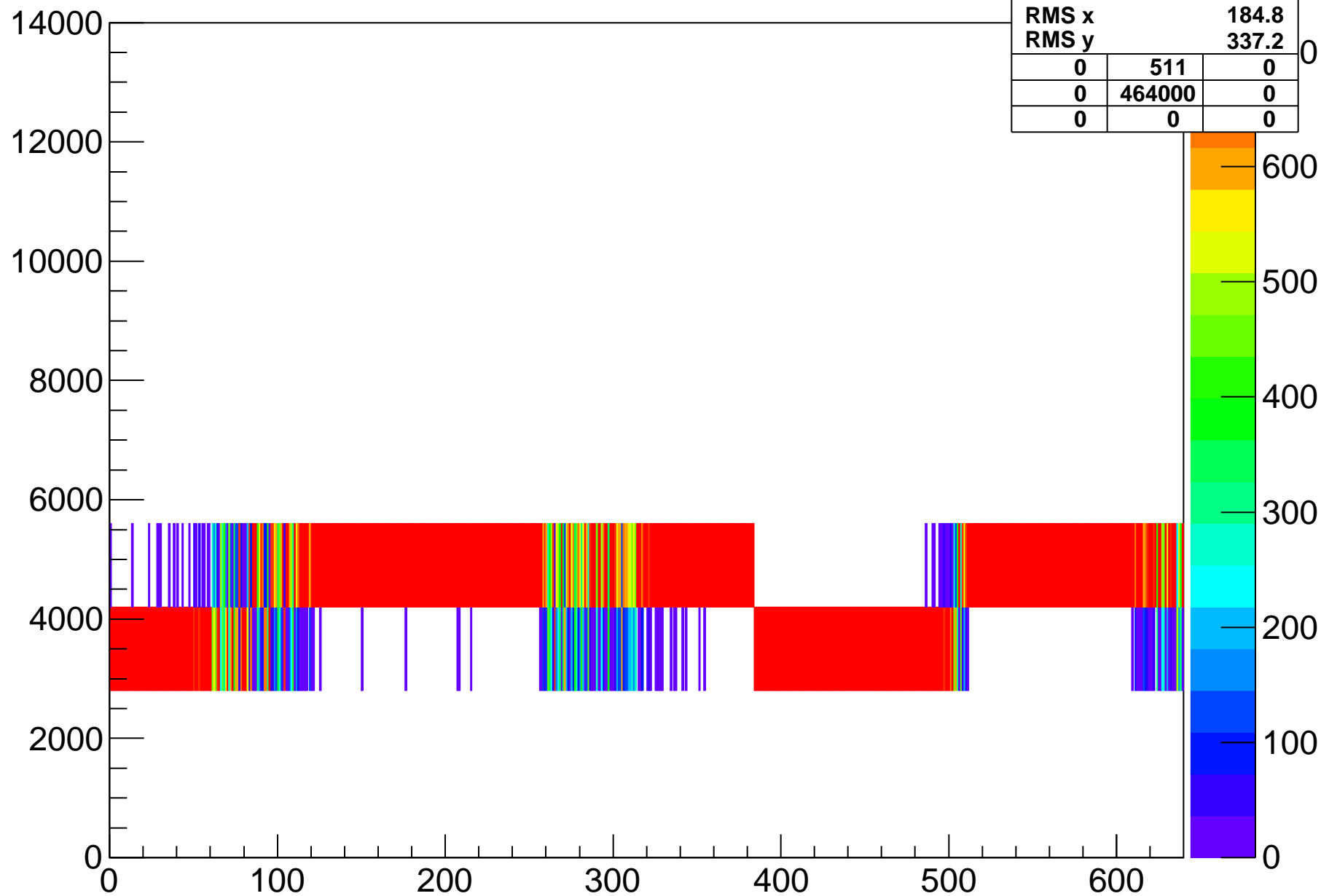
baselinesamples-fpga-0-hyb-2-sample-1

Entries	464511	
Mean x	319.5	
Mean y	4279	
RMS x	184.8	
RMS y	337.3	
0	511	0
0	464000	0
0	0	0



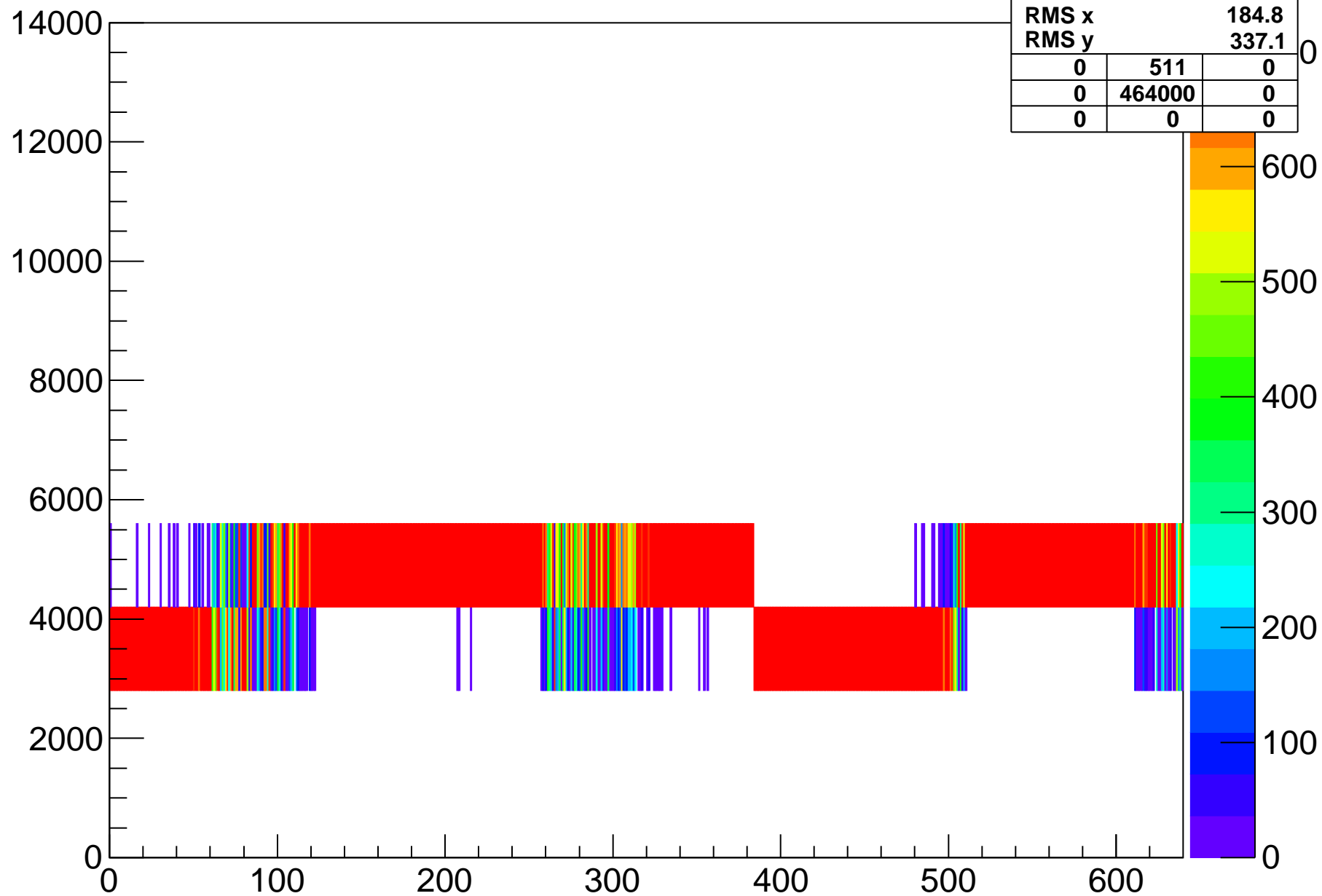
baselinesamples-fpga-0-hyb-2-sample-2

Entries	464511	
Mean x	319.5	
Mean y	4273	
RMS x	184.8	
RMS y	337.2	
0	511	0
0	464000	0
0	0	0



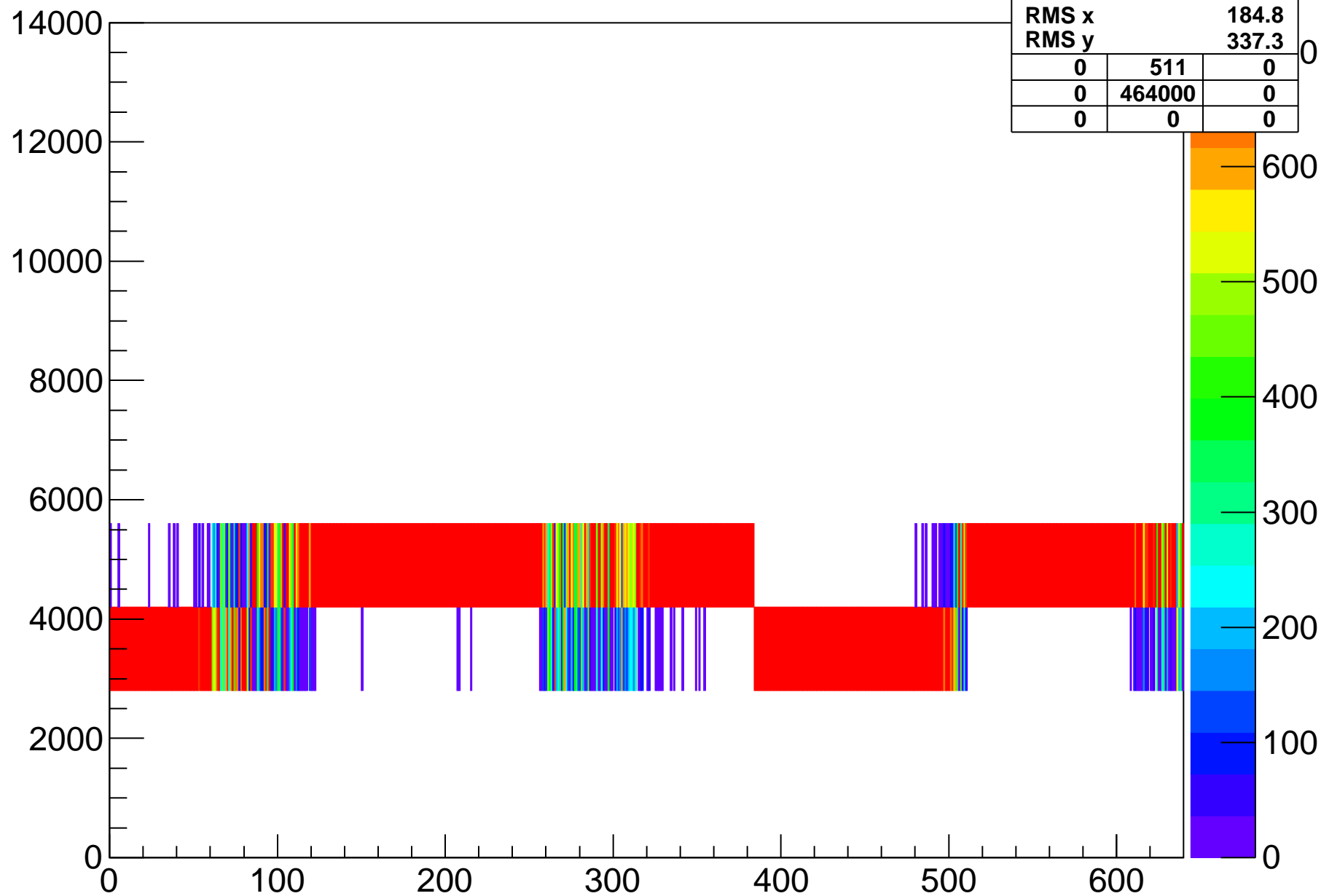
baselinesamples-fpga-0-hyb-2-sample-3

Entries	464511	
Mean x	319.5	
Mean y	4279	
RMS x	184.8	
RMS y	337.1	
0	511	0
0	464000	0
0	0	0



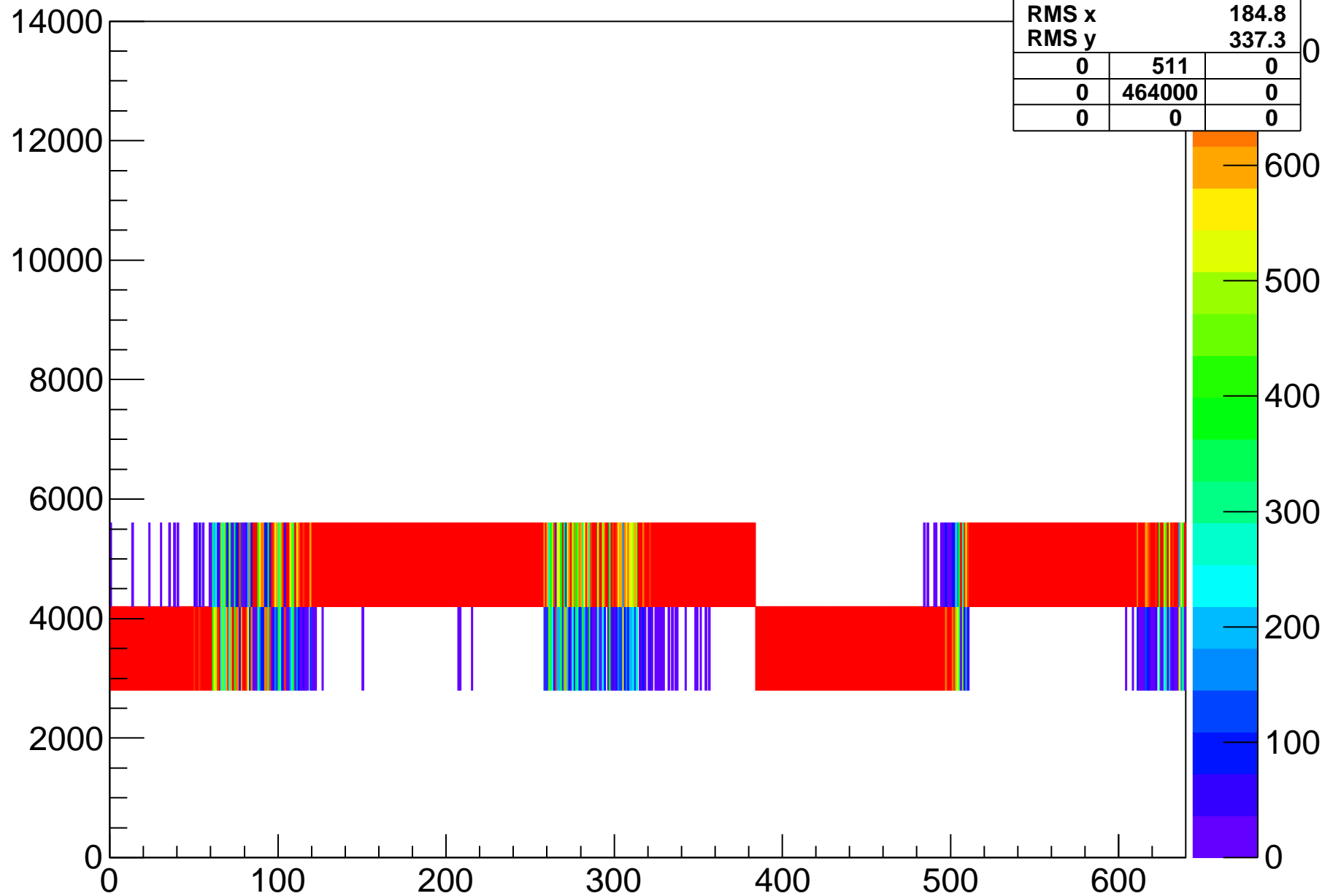
baselinesamples-fpga-0-hyb-2-sample-4

Entries	464511	
Mean x	319.5	
Mean y	4270	
RMS x	184.8	
RMS y	337.3	
0	511	0
0	464000	0
0	0	0



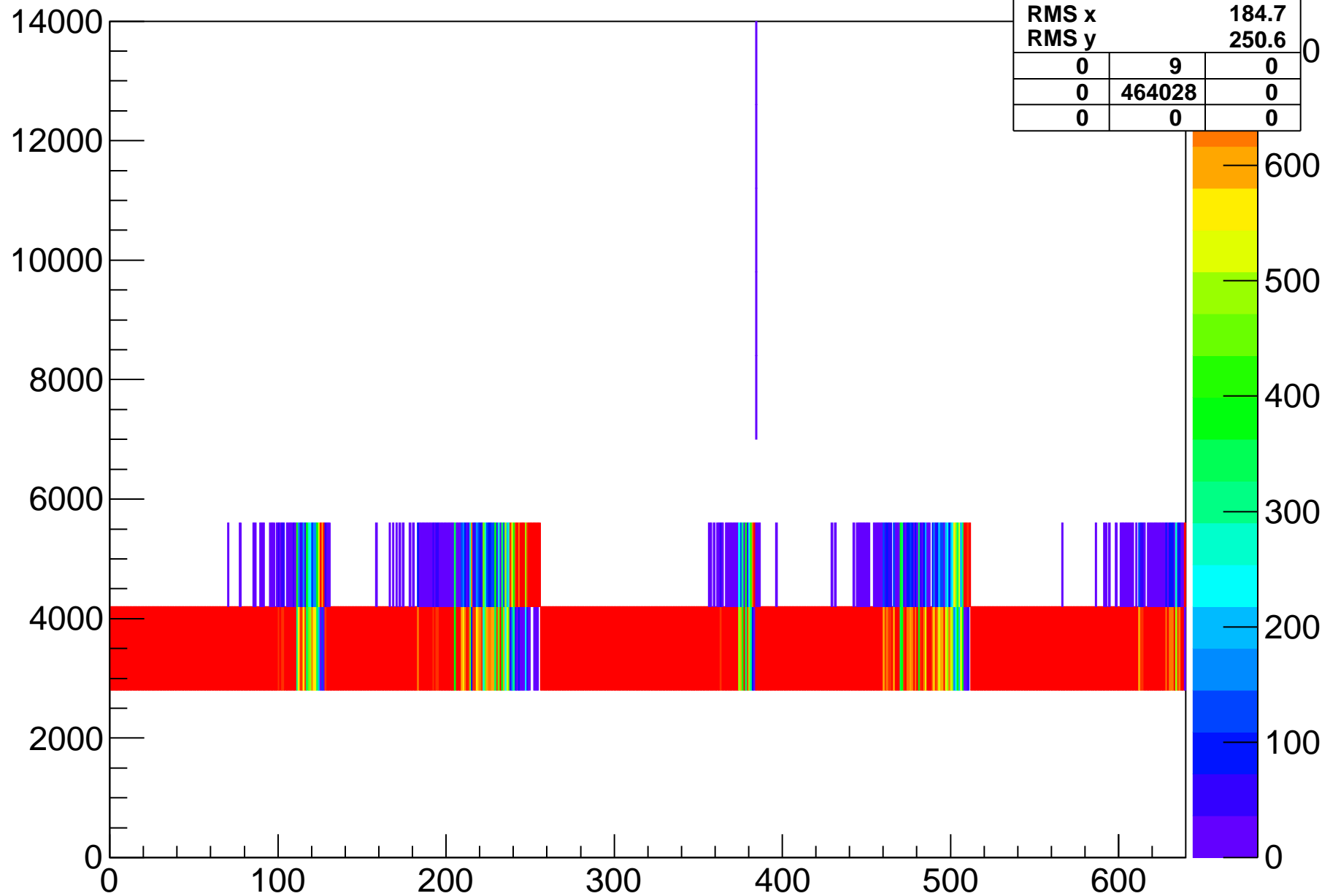
baselinesamples-fpga-0-hyb-2-sample-5

Entries	464511	
Mean x	319.5	
Mean y	4274	
RMS x	184.8	
RMS y	337.3	
0	511	0
0	464000	0
0	0	0



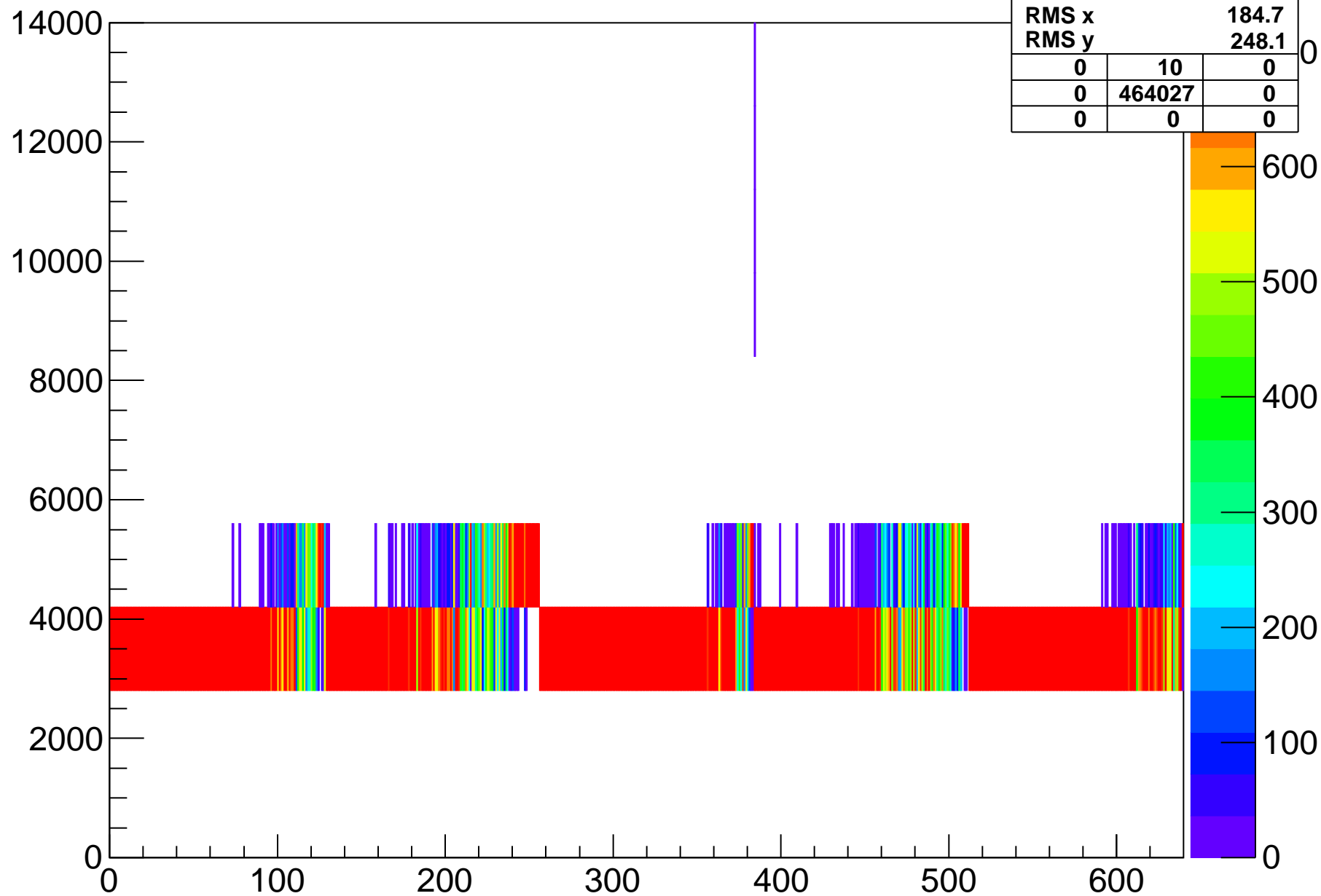
baselinesamples-fpga-0-hyb-3-sample-0

Entries	464037	
Mean x	319.5	
Mean y	3886	
RMS x	184.7	
RMS y	250.6	
0	9	0
0	464028	0
0	0	0



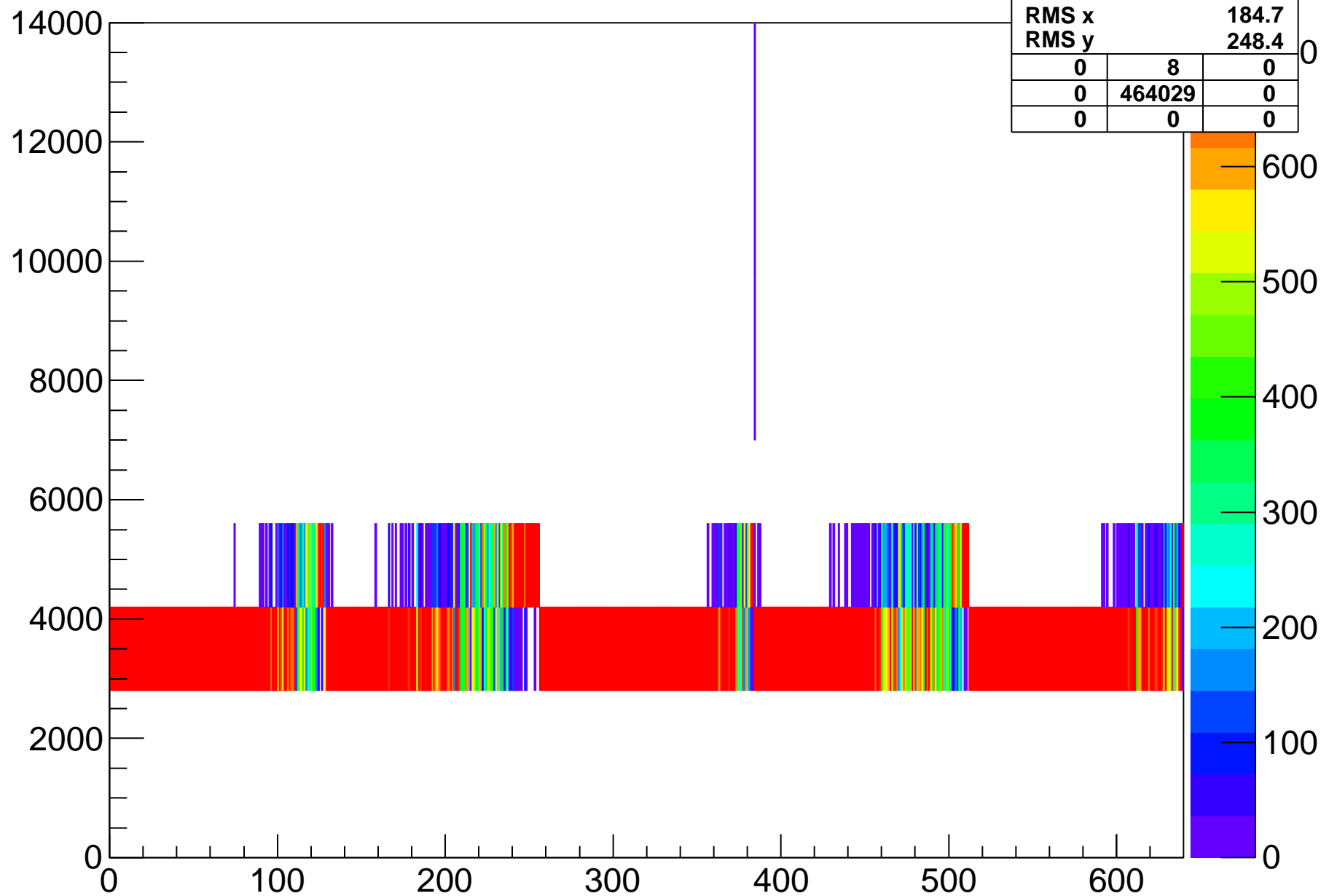
baselinesamples-fpga-0-hyb-3-sample-1

Entries	464037	
Mean x	319.5	
Mean y	3938	
RMS x	184.7	
RMS y	248.1	
0	10	0
0	464027	0
0	0	0



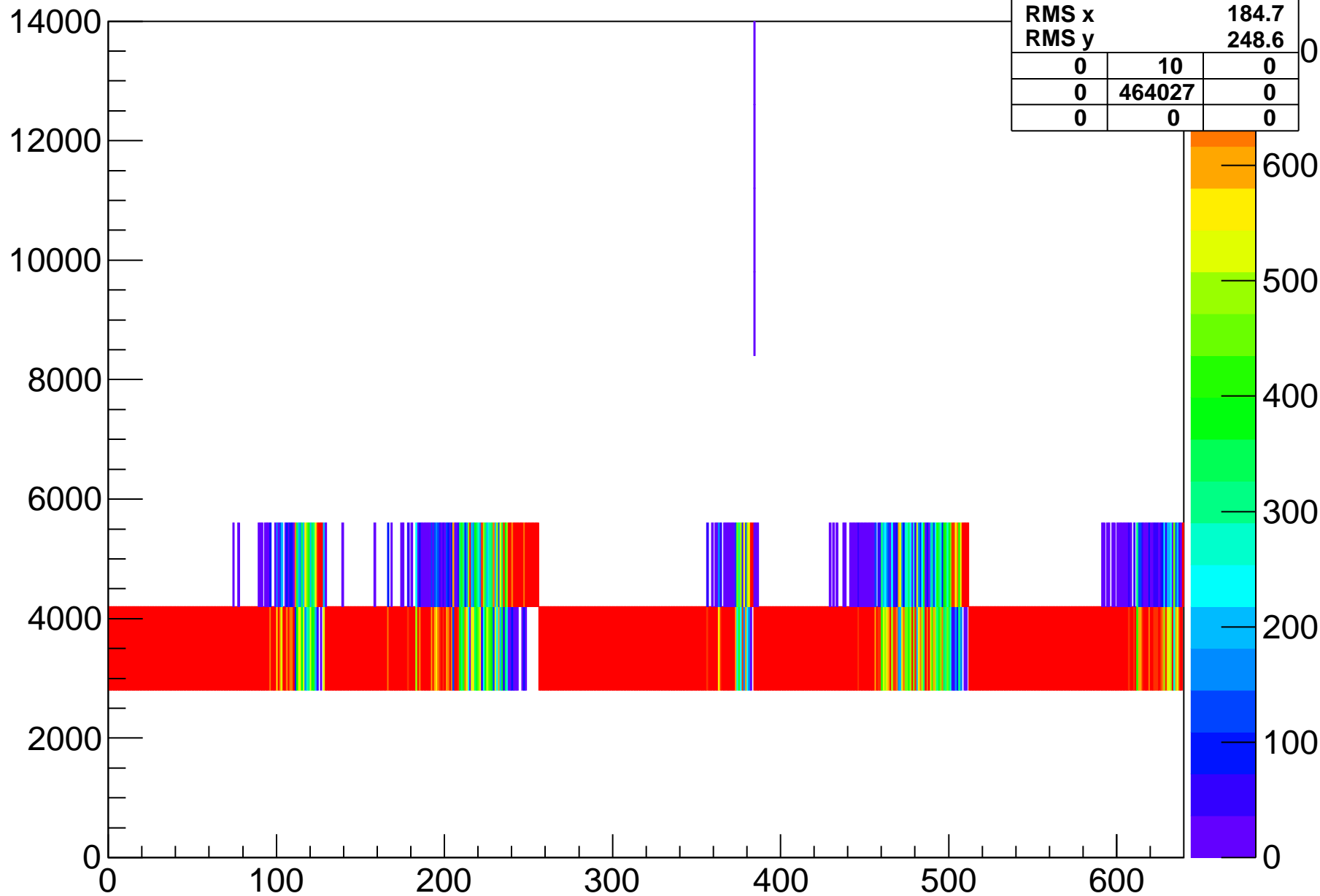
baselinesamples-fpga-0-hyb-3-sample-2

Entries	464037	
Mean x	319.5	
Mean y	3935	
RMS x	184.7	
RMS y	248.4	
0	8	0
0	464029	0
0	0	0



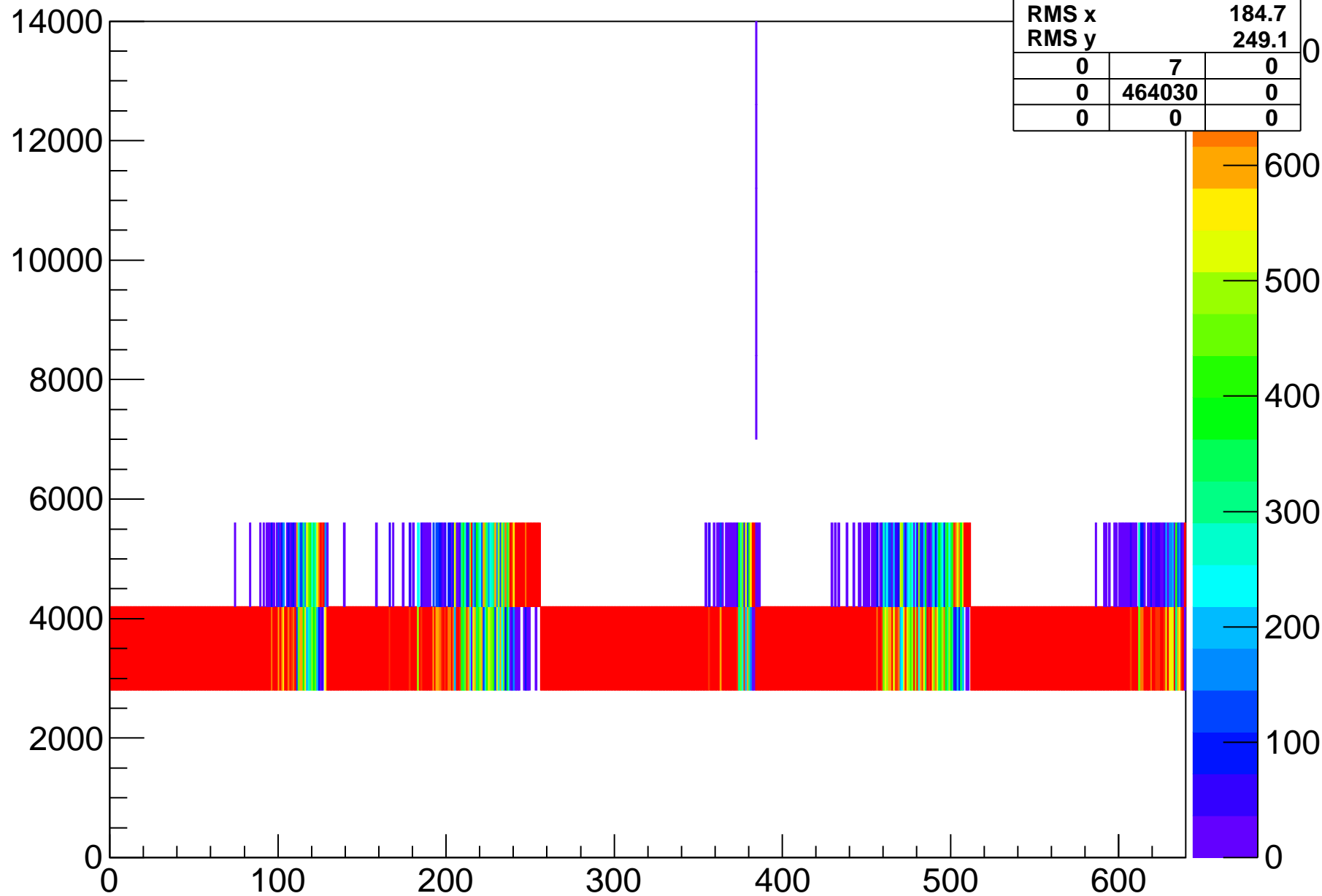
baselinesamples-fpga-0-hyb-3-sample-3

Entries	464037	
Mean x	319.5	
Mean y	3939	
RMS x	184.7	
RMS y	248.6	
0	10	0
0	464027	0
0	0	0



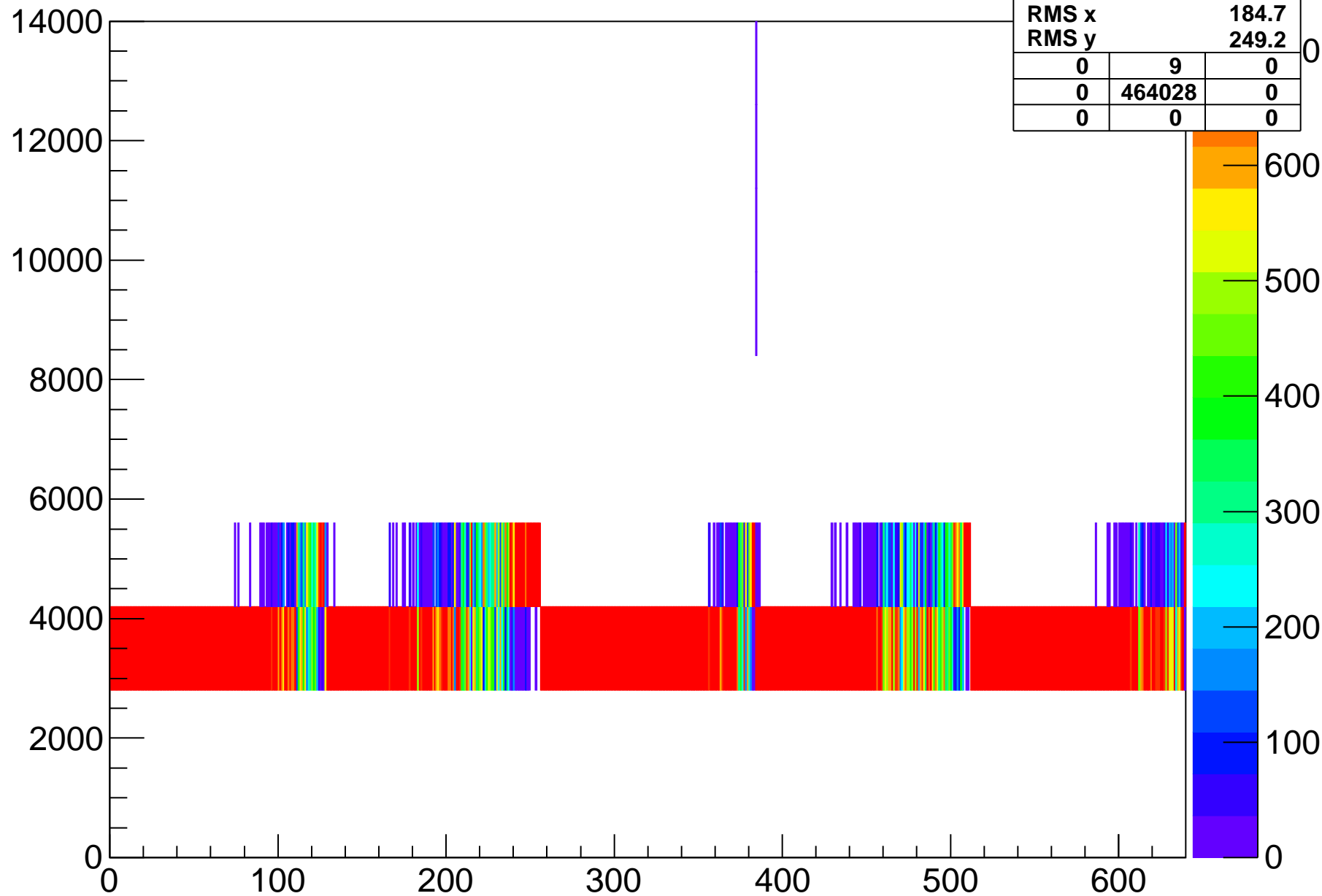
baselinesamples-fpga-0-hyb-3-sample-4

Entries	464037	
Mean x	319.5	
Mean y	3931	
RMS x	184.7	
RMS y	249.1	
0	7	0
0	464030	0
0	0	0



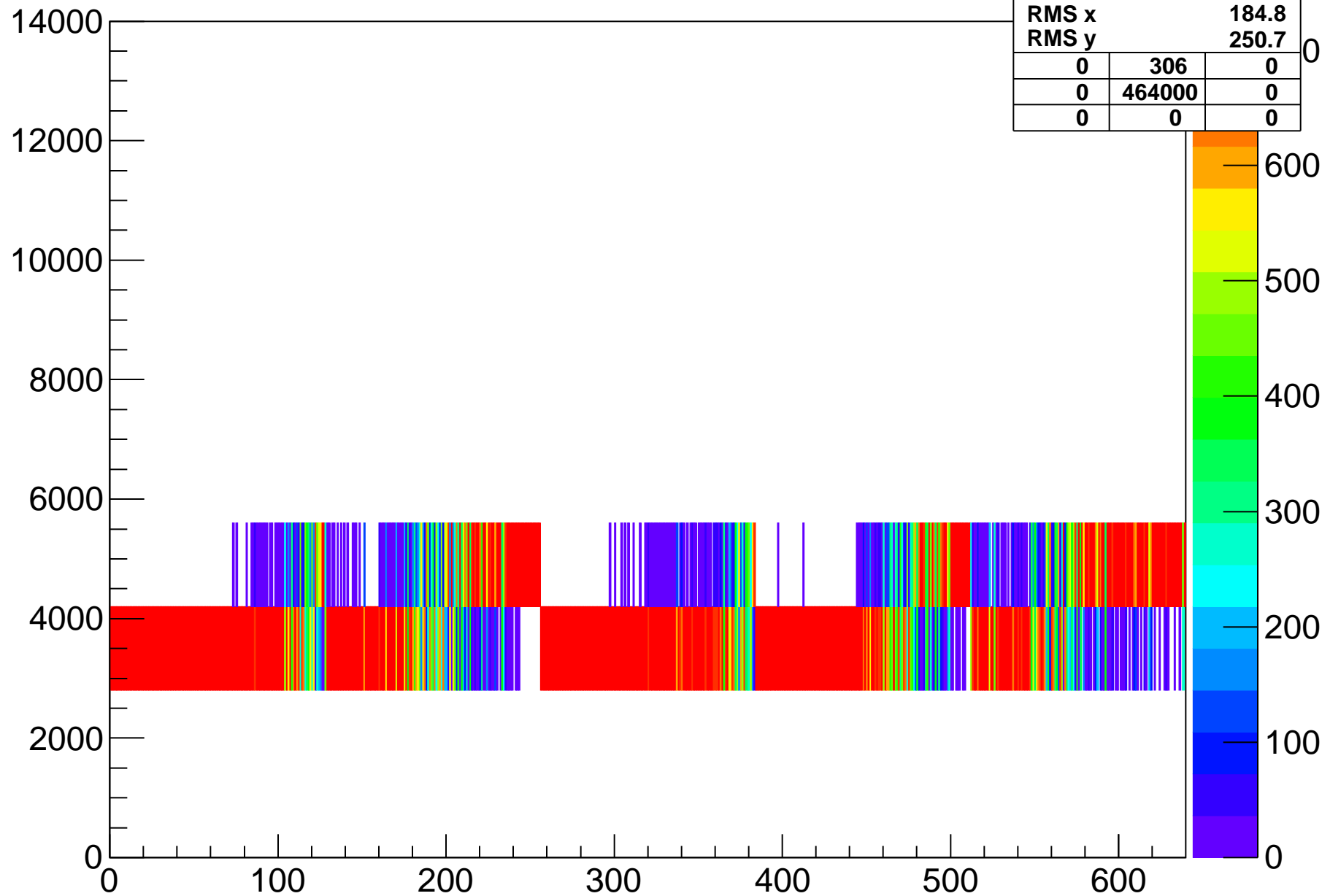
baselinesamples-fpga-0-hyb-3-sample-5

Entries	464037	
Mean x	319.5	
Mean y	3934	
RMS x	184.7	
RMS y	249.2	
0	9	0
0	464028	0
0	0	0



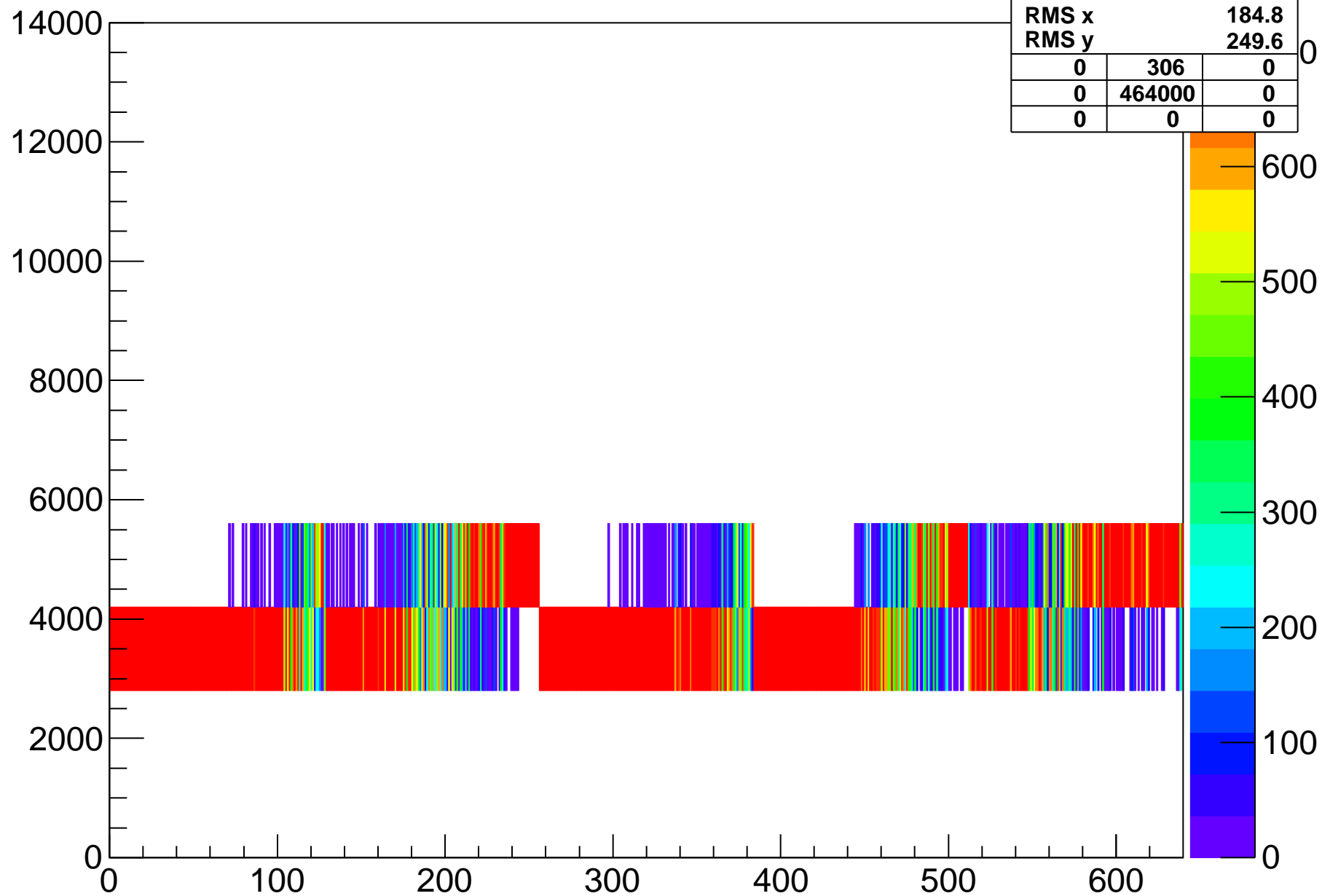
baselinesamples-fpga-1-hyb-0-sample-0

Entries	464306	
Mean x	319.5	
Mean y	4050	
RMS x	184.8	
RMS y	250.7	
0	306	0
0	464000	0
0	0	0



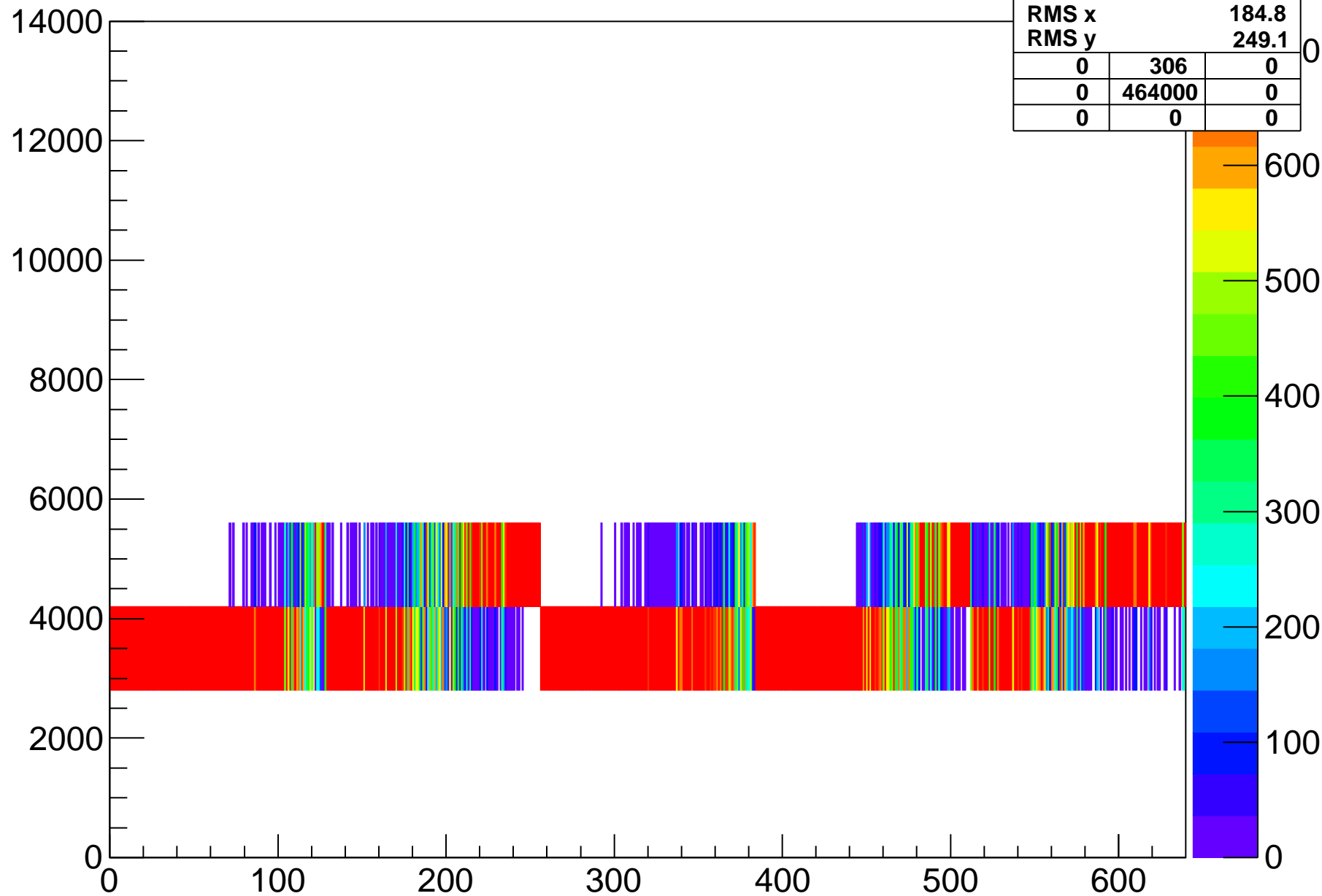
baselinesamples-fpga-1-hyb-0-sample-1

Entries	464306	
Mean x	319.5	
Mean y	4051	
RMS x	184.8	
RMS y	249.6	
0	306	0
0	464000	0
0	0	0



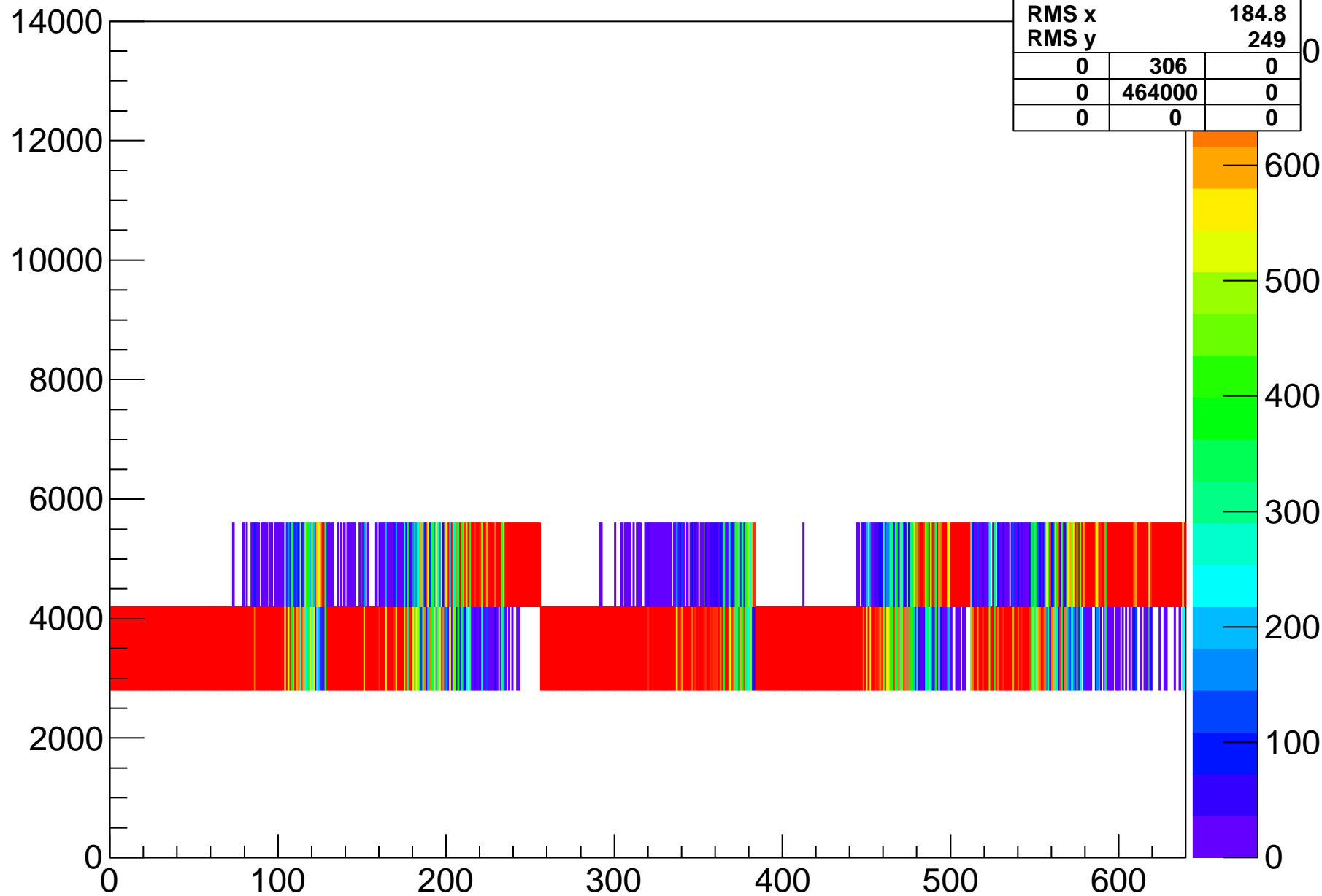
baselinesamples-fpga-1-hyb-0-sample-2

Entries	464306	
Mean x	319.5	
Mean y	4061	
RMS x	184.8	
RMS y	249.1	
0	306	0
0	464000	0
0	0	0



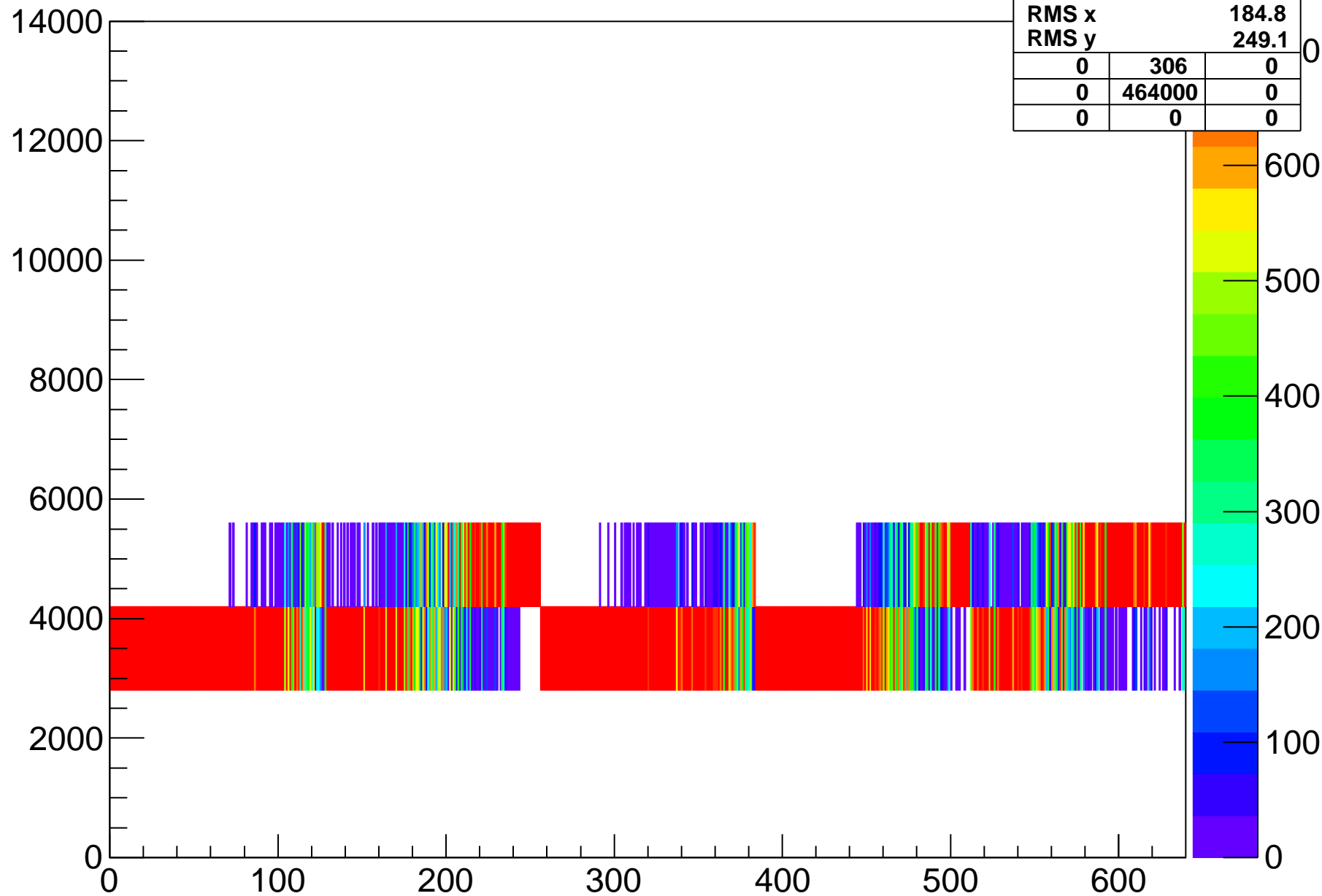
baselinesamples-fpga-1-hyb-0-sample-3

Entries	464306	
Mean x	319.5	
Mean y	4061	
RMS x	184.8	
RMS y	249	
0	306	0
0	464000	0
0	0	0



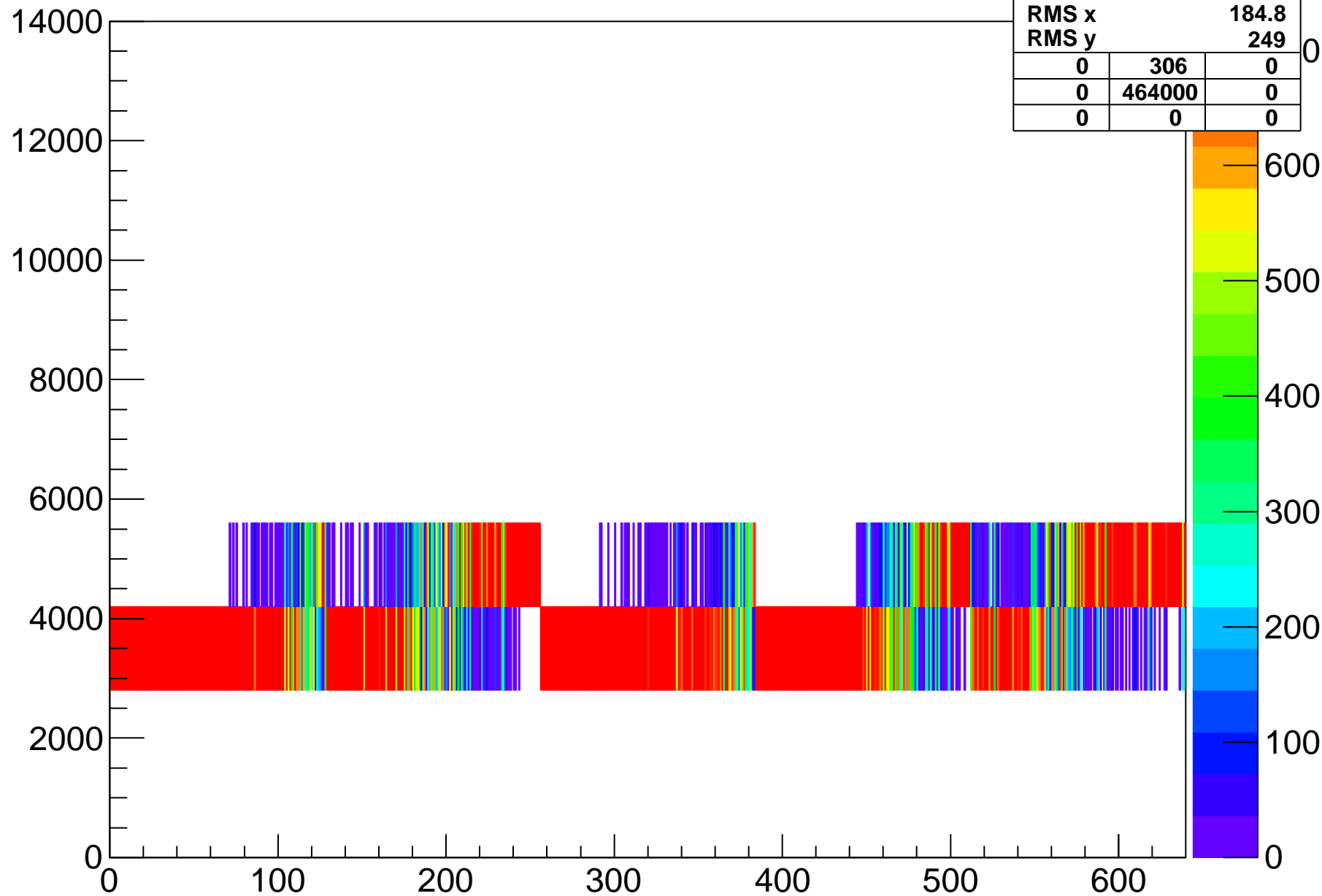
baselinesamples-fpga-1-hyb-0-sample-4

Entries	464306	
Mean x	319.5	
Mean y	4057	
RMS x	184.8	
RMS y	249.1	
0	306	0
0	464000	0
0	0	0



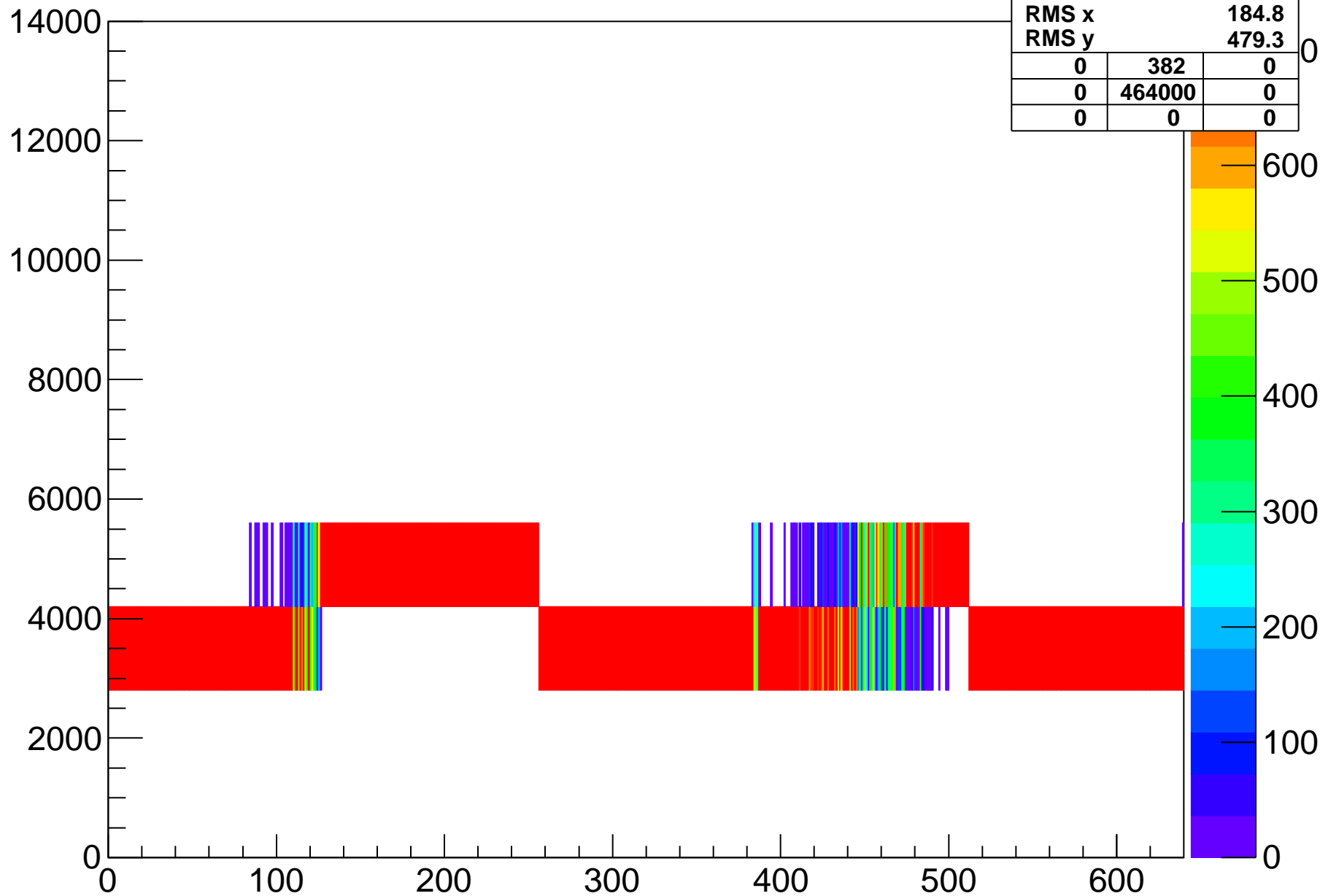
baselinesamples-fpga-1-hyb-0-sample-5

Entries	464306	
Mean x	319.5	
Mean y	4059	
RMS x	184.8	
RMS y	249	
0	306	0
0	464000	0
0	0	0



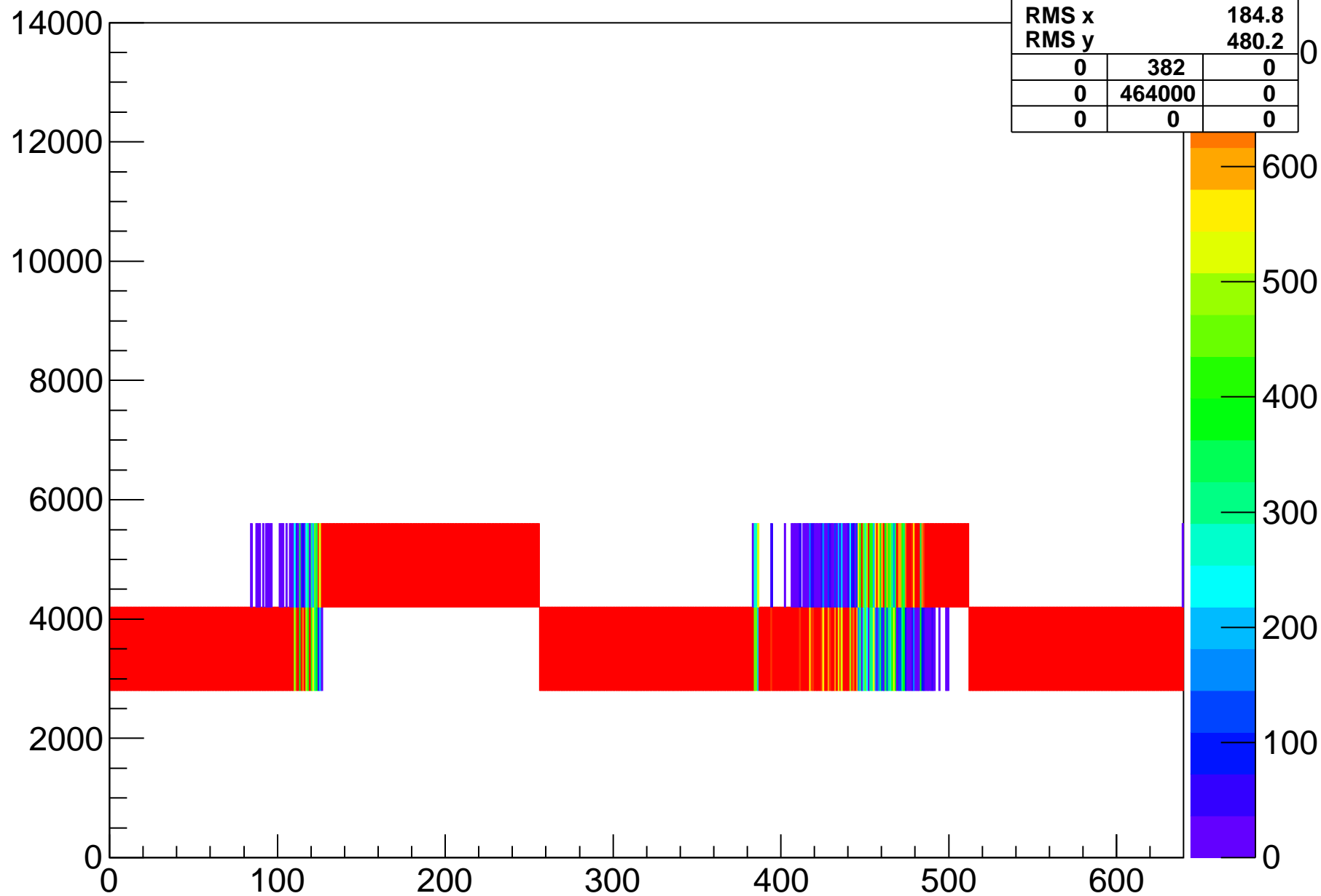
baselinesamples-fpga-1-hyb-1-sample-0

Entries	464382	
Mean x	319.5	
Mean y	3901	
RMS x	184.8	
RMS y	479.3	
0	382	0
0	464000	0
0	0	0



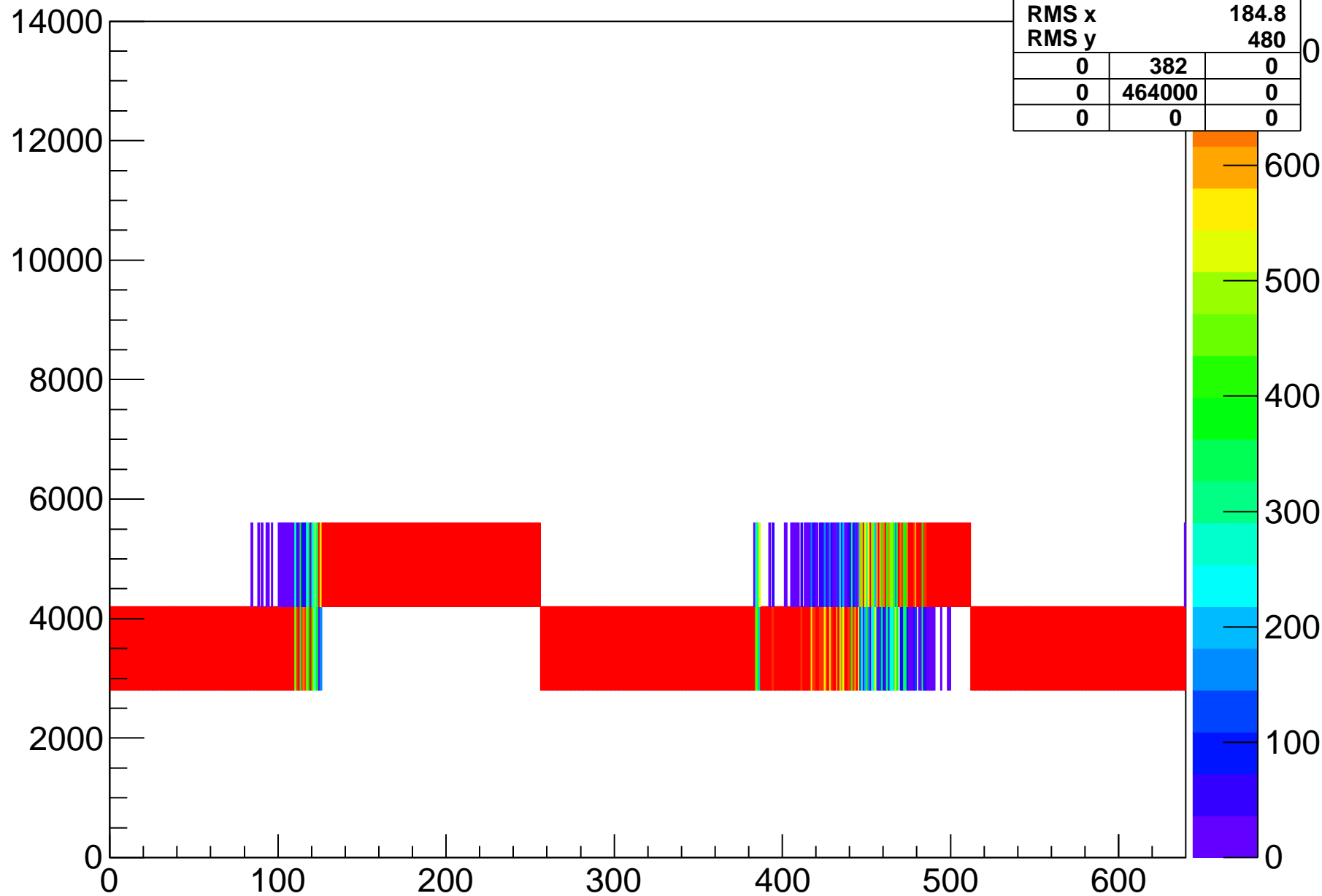
baselinesamples-fpga-1-hyb-1-sample-1

Entries	464382	
Mean x	319.5	
Mean y	3905	
RMS x	184.8	
RMS y	480.2	
0	382	0
0	464000	0
0	0	0



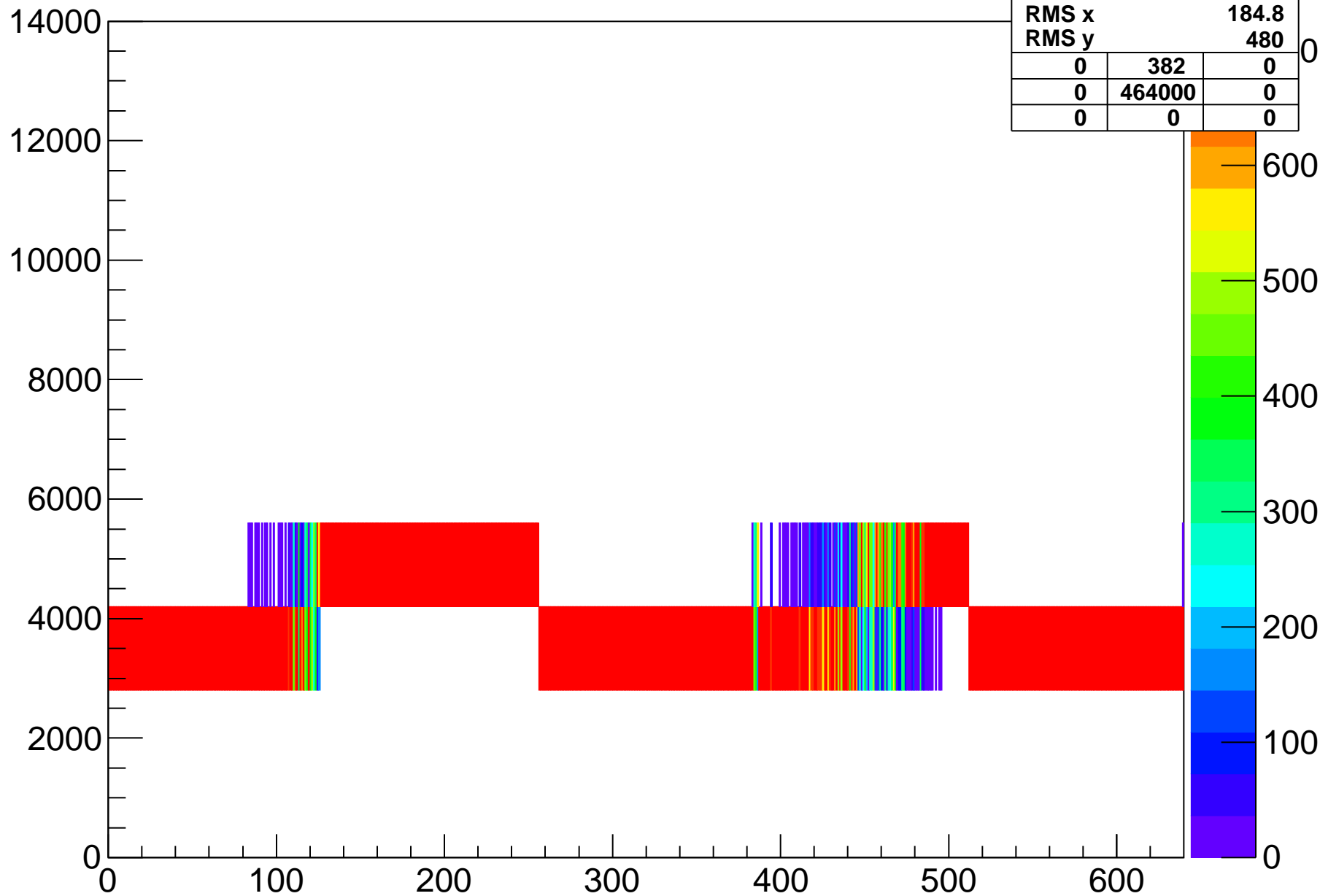
baselinesamples-fpga-1-hyb-1-sample-2

Entries	464382	
Mean x	319.5	
Mean y	3913	
RMS x	184.8	
RMS y	480	
0	382	0
0	464000	0
0	0	0



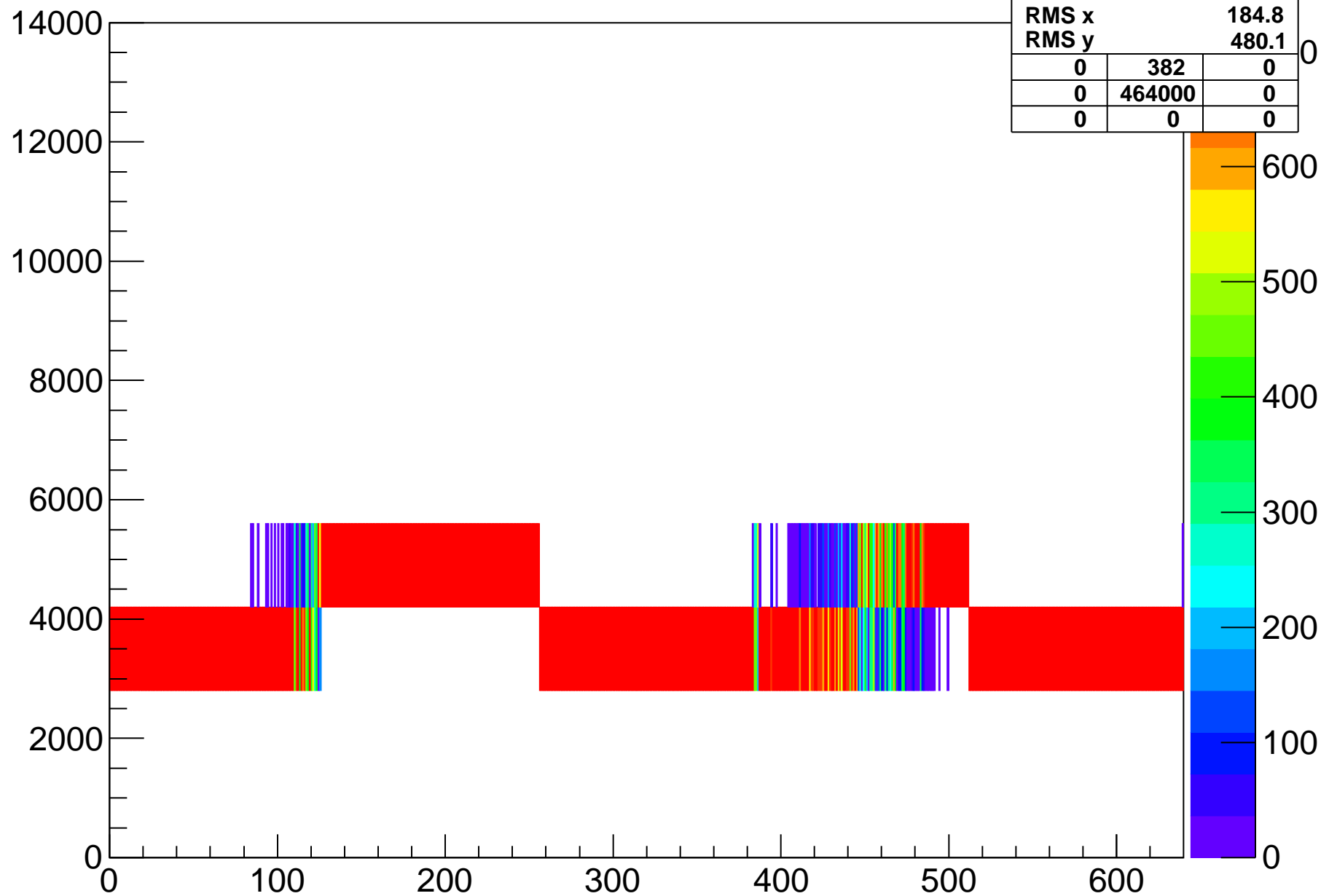
baselinesamples-fpga-1-hyb-1-sample-3

Entries	464382	
Mean x	319.5	
Mean y	3914	
RMS x	184.8	
RMS y	480	
0	382	0
0	464000	0
0	0	0



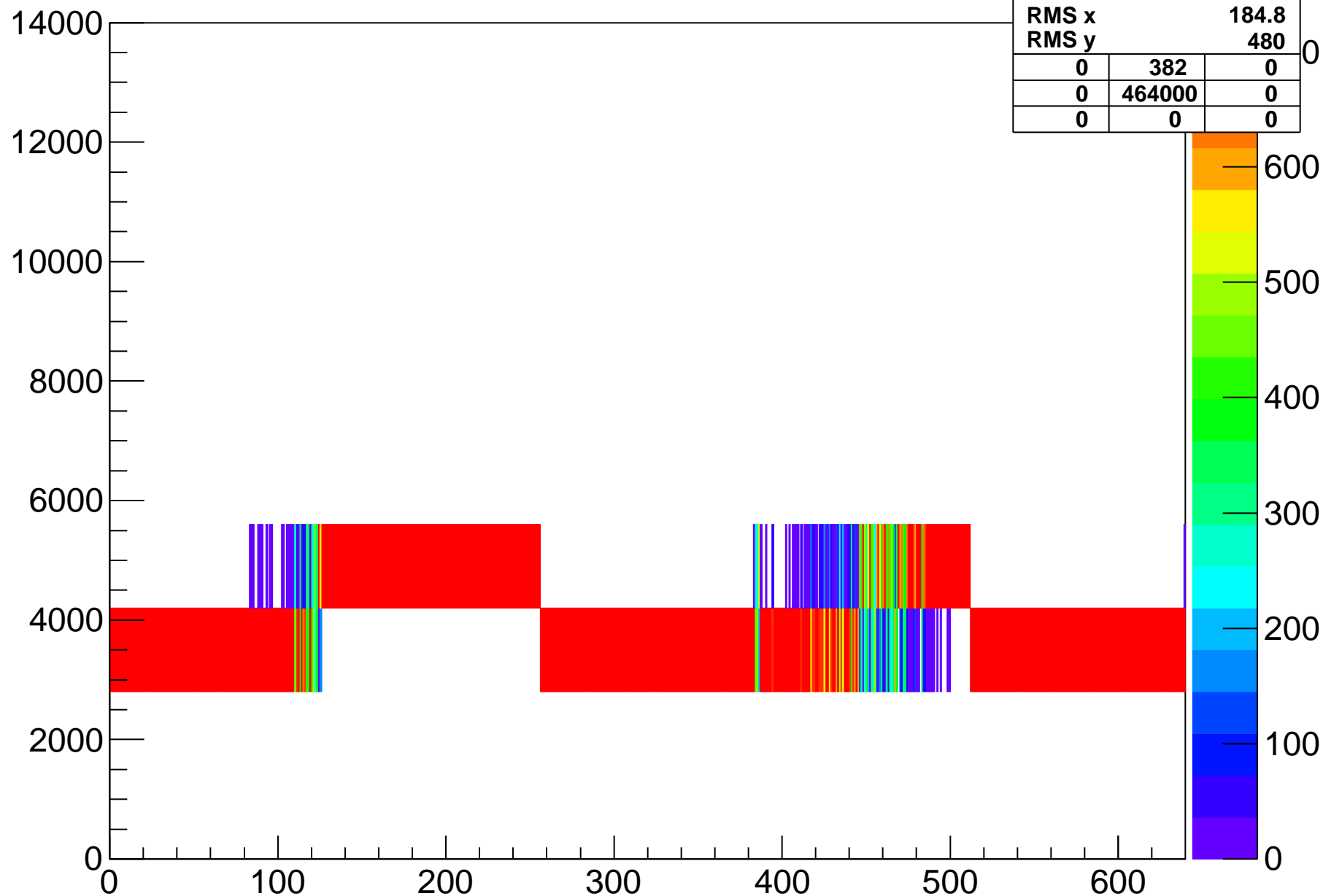
baselinesamples-fpga-1-hyb-1-sample-4

Entries	464382	
Mean x	319.5	
Mean y	3909	
RMS x	184.8	
RMS y	480.1	
0	382	0
0	464000	0
0	0	0



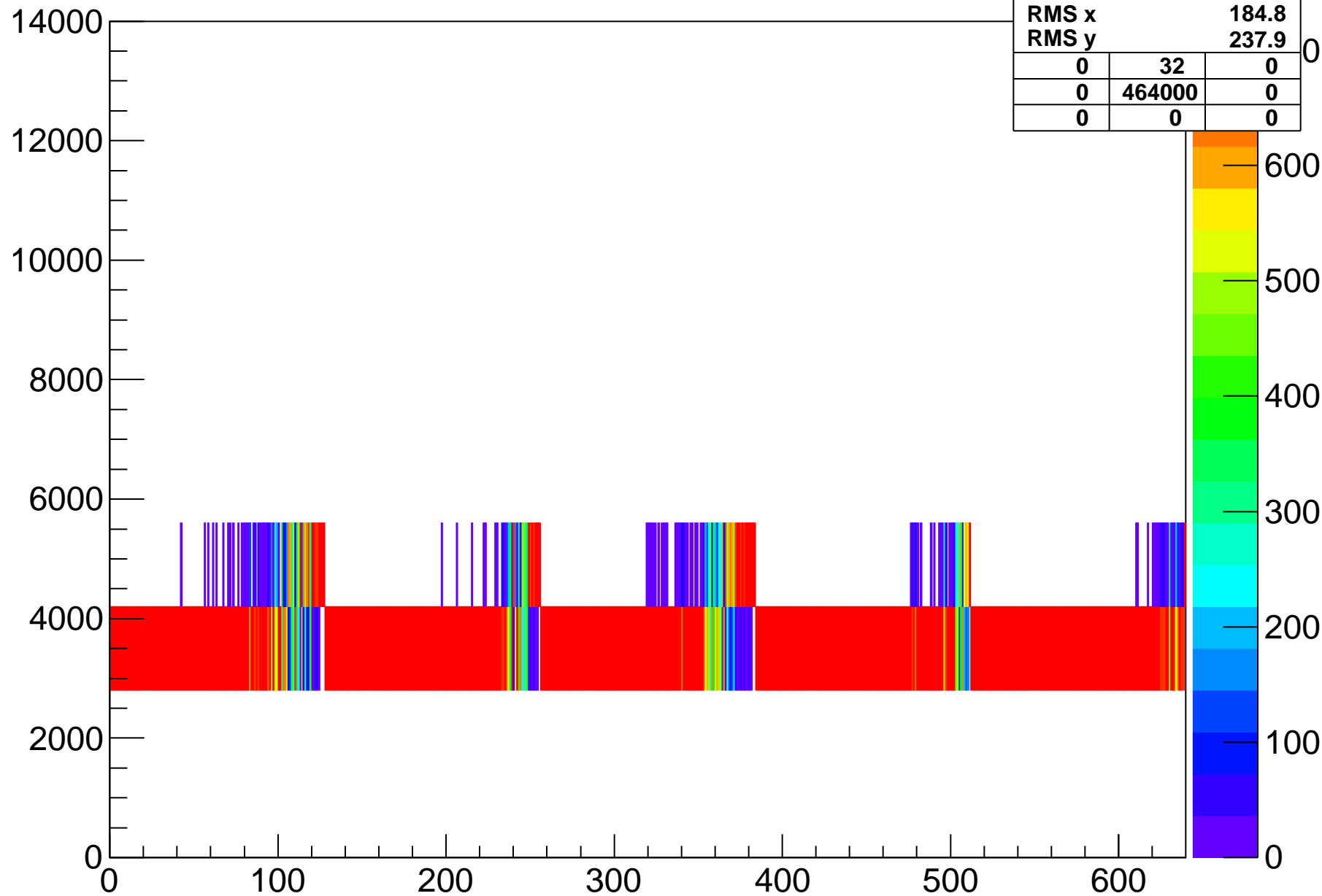
baselinesamples-fpga-1-hyb-1-sample-5

Entries	464382	
Mean x	319.5	
Mean y	3911	
RMS x	184.8	
RMS y	480	
0	382	0
0	464000	0
0	0	0



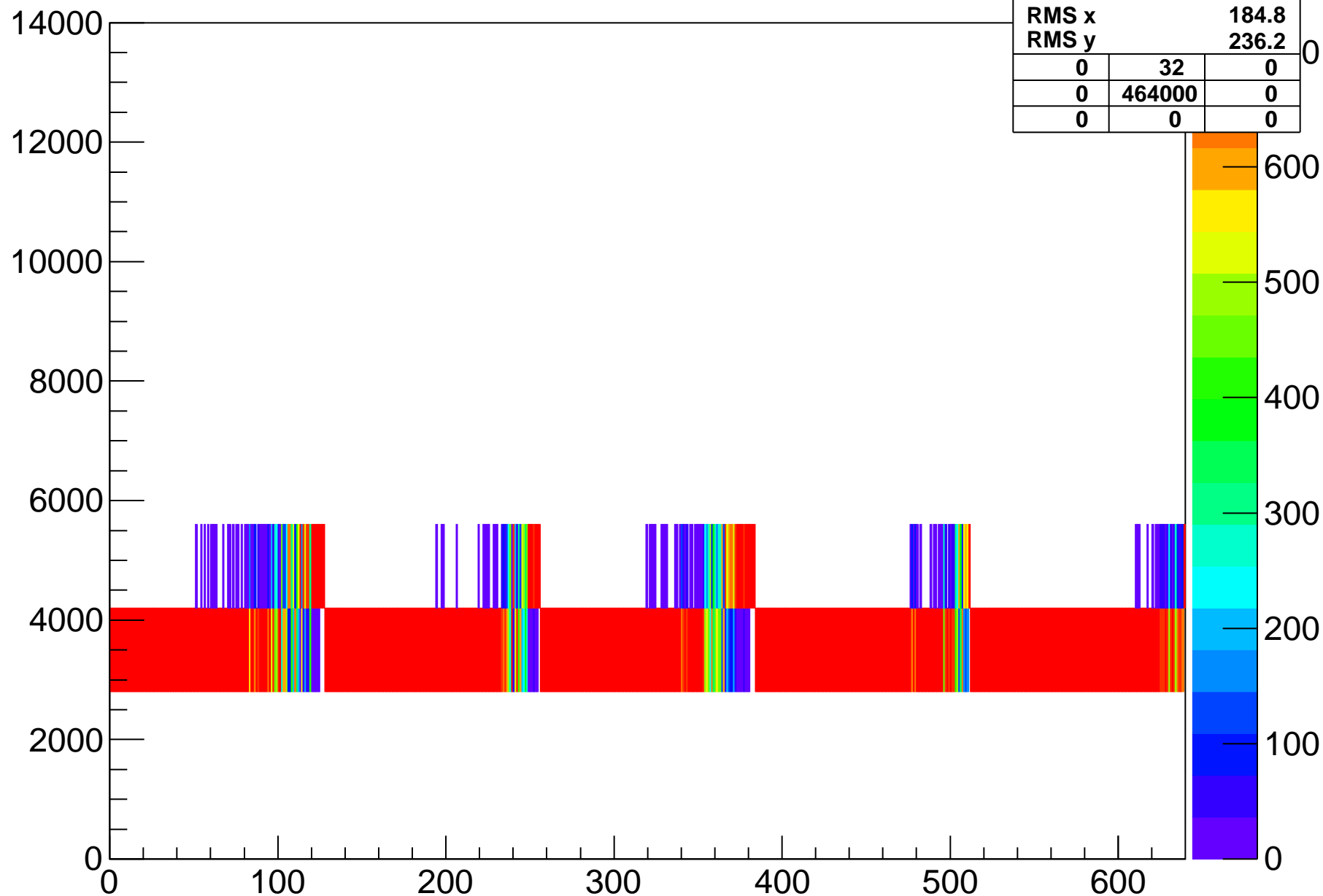
baselinesamples-fpga-1-hyb-2-sample-0

Entries	464032	
Mean x	319.5	
Mean y	3876	
RMS x	184.8	
RMS y	237.9	
0	32	0
0	464000	0
0	0	0



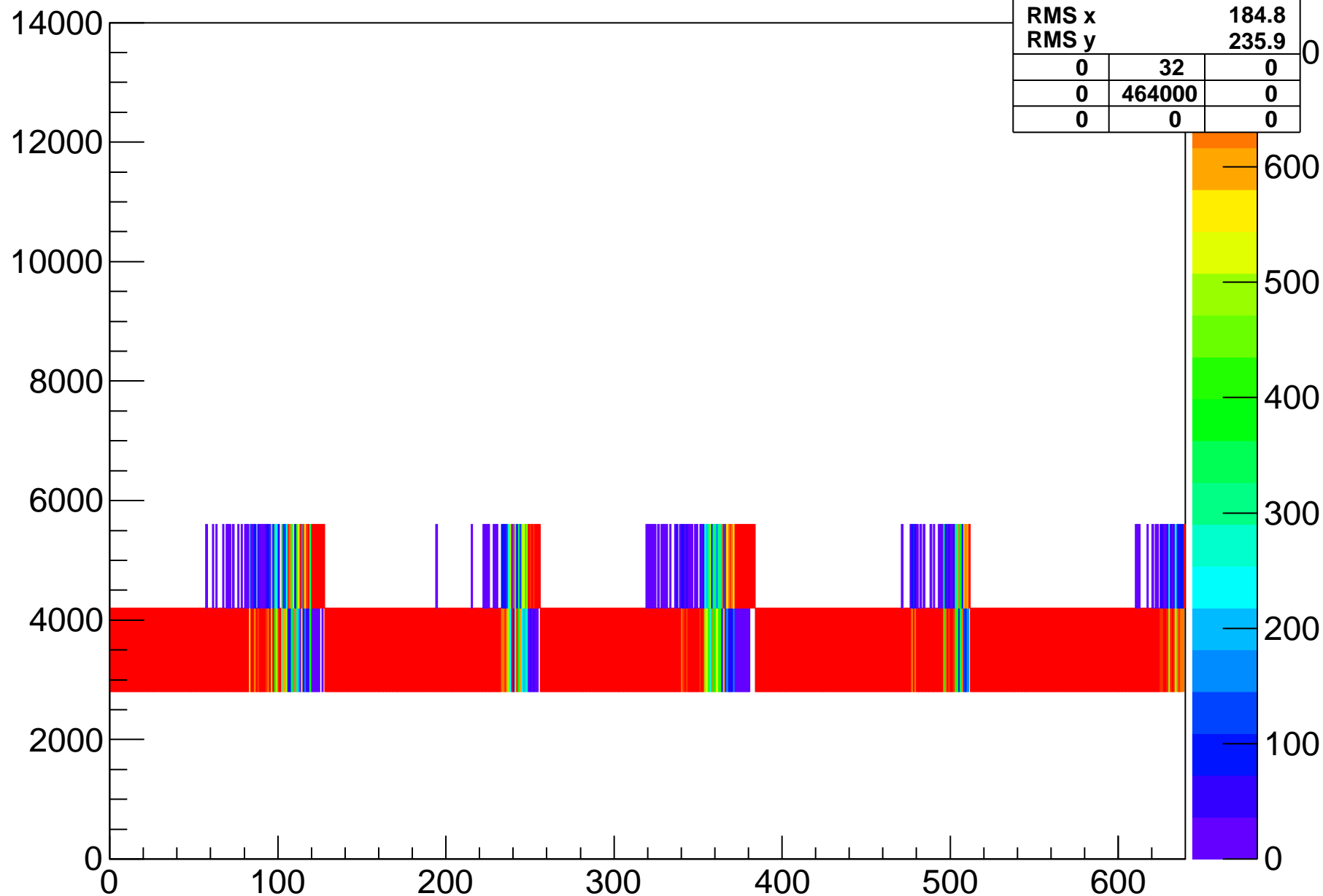
baselinesamples-fpga-1-hyb-2-sample-1

Entries	464032	
Mean x	319.5	
Mean y	3886	
RMS x	184.8	
RMS y	236.2	
0	32	0
0	464000	0
0	0	0



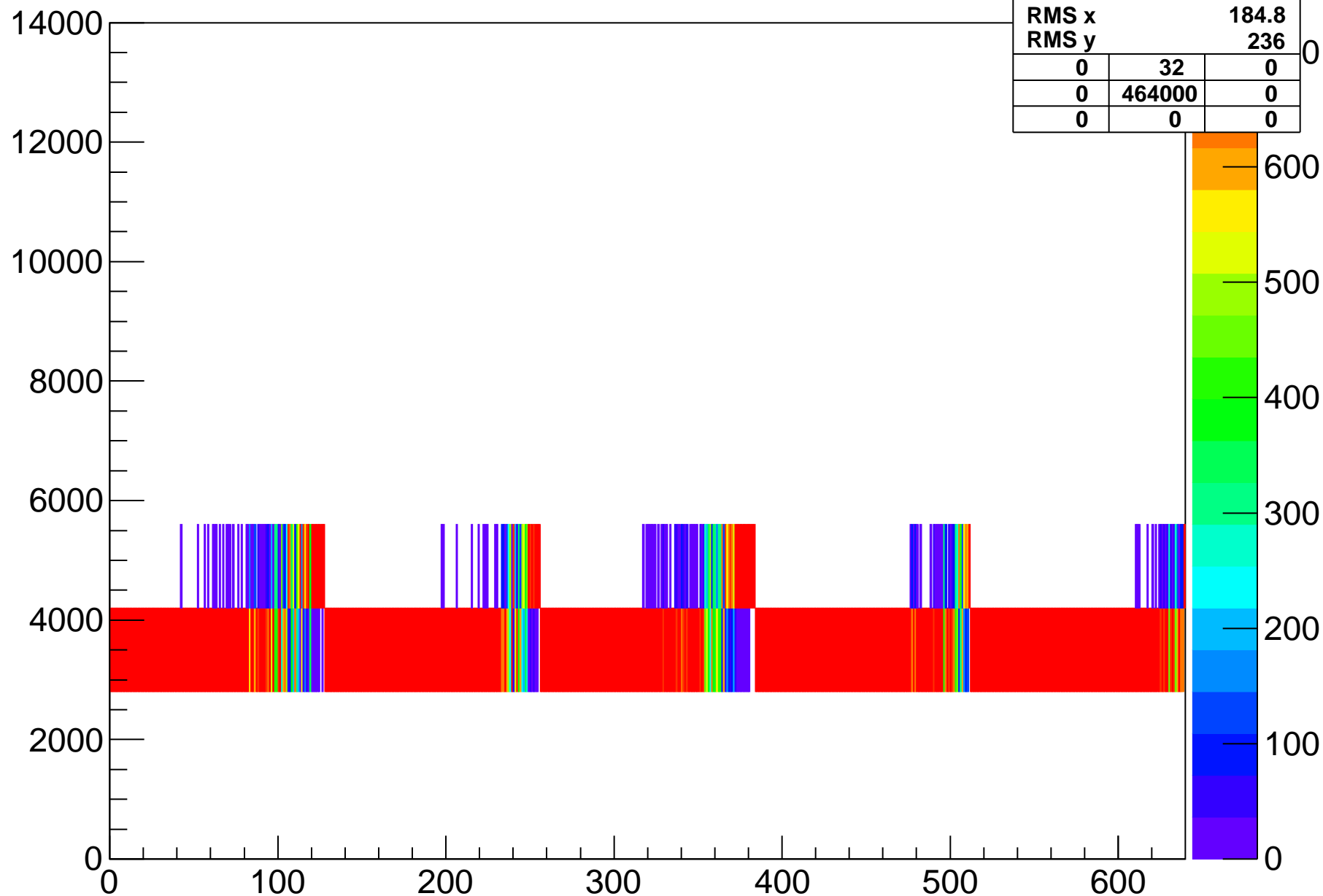
baselinesamples-fpga-1-hyb-2-sample-2

Entries	464032	
Mean x	319.5	
Mean y	3890	
RMS x	184.8	
RMS y	235.9	
0	32	0
0	464000	0
0	0	0



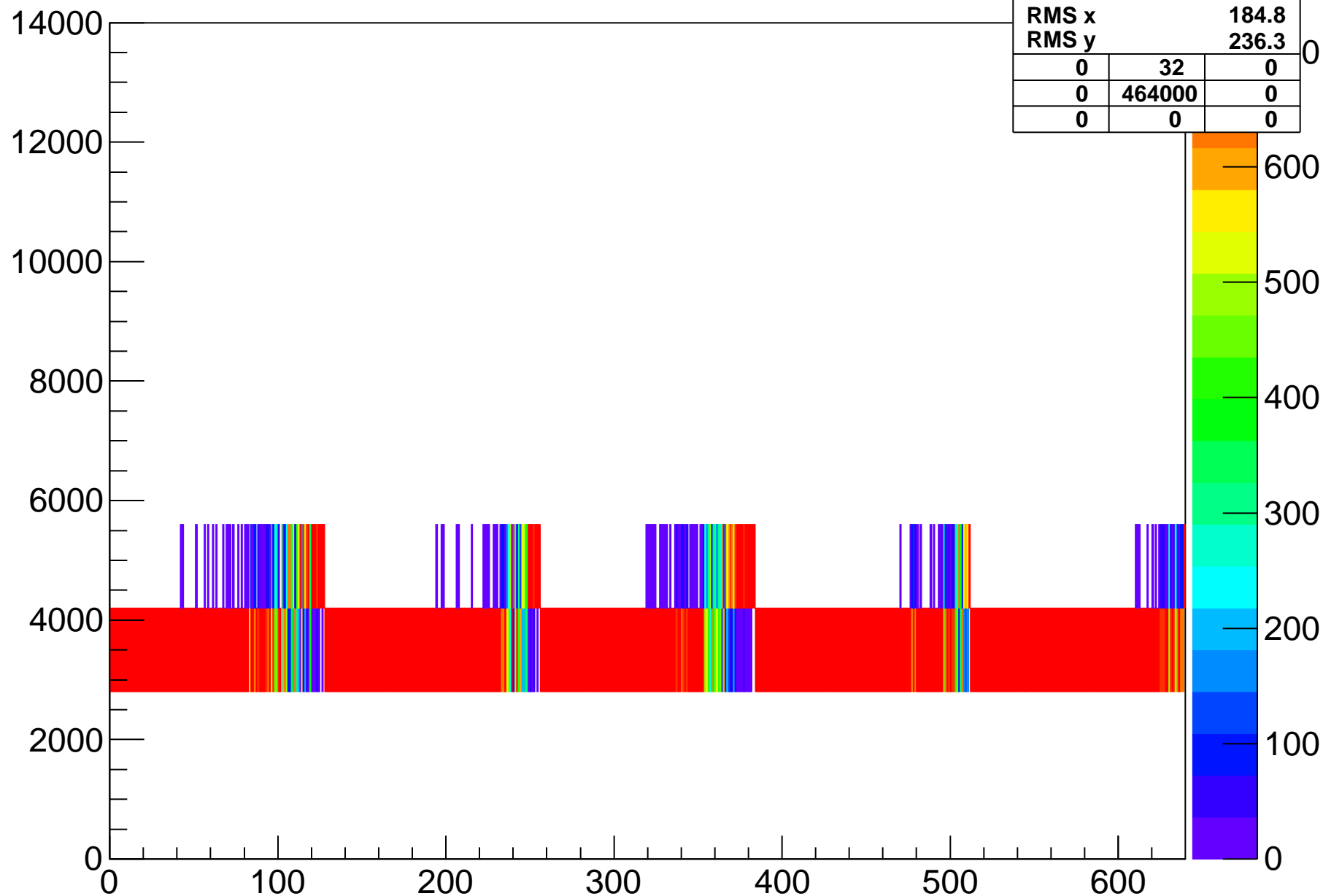
baselinesamples-fpga-1-hyb-2-sample-3

Entries	464032	
Mean x	319.5	
Mean y	3891	
RMS x	184.8	
RMS y	236	
0	32	0
0	464000	0
0	0	0



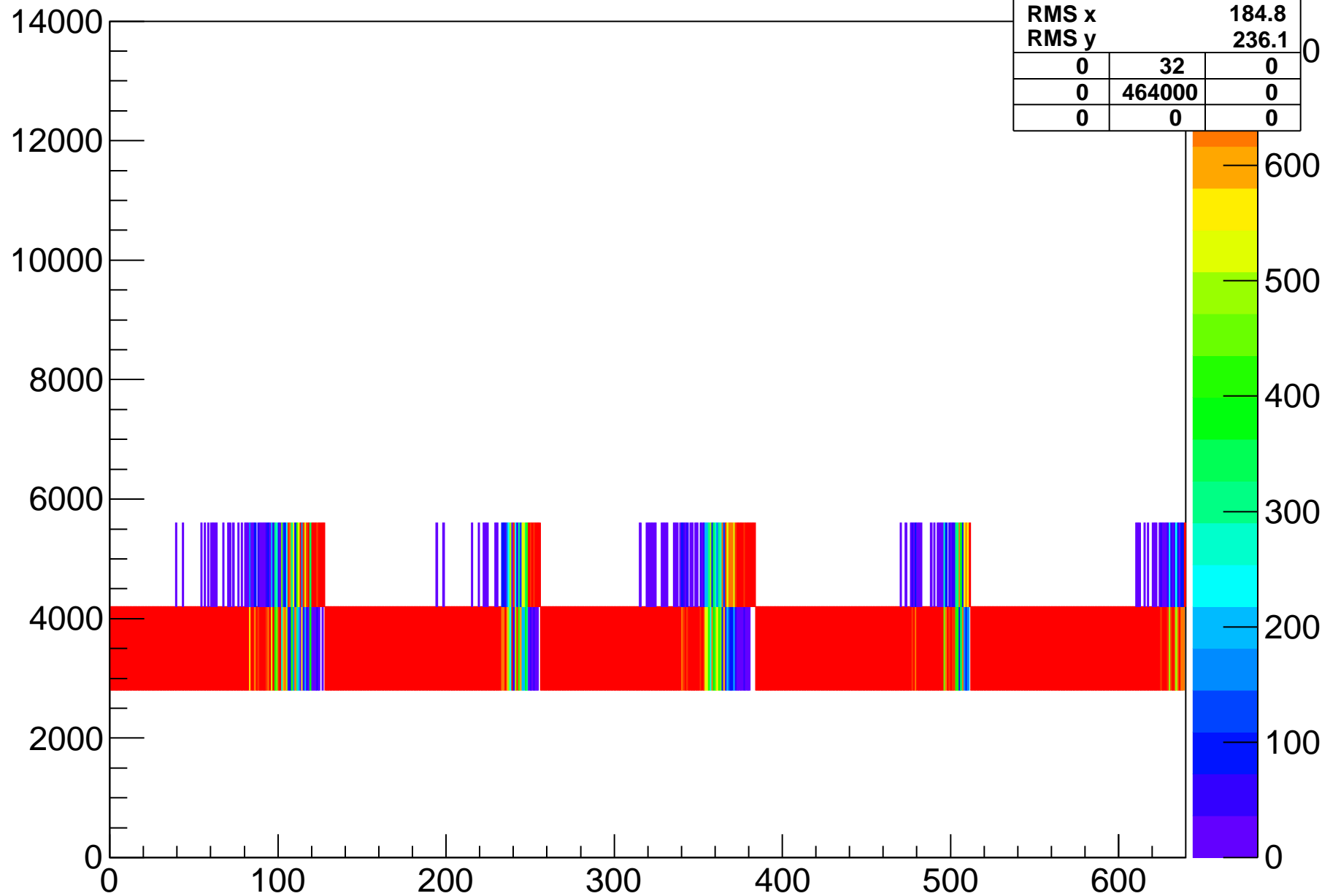
baselinesamples-fpga-1-hyb-2-sample-4

Entries	464032	
Mean x	319.5	
Mean y	3888	
RMS x	184.8	
RMS y	236.3	
0	32	0
0	464000	0
0	0	0



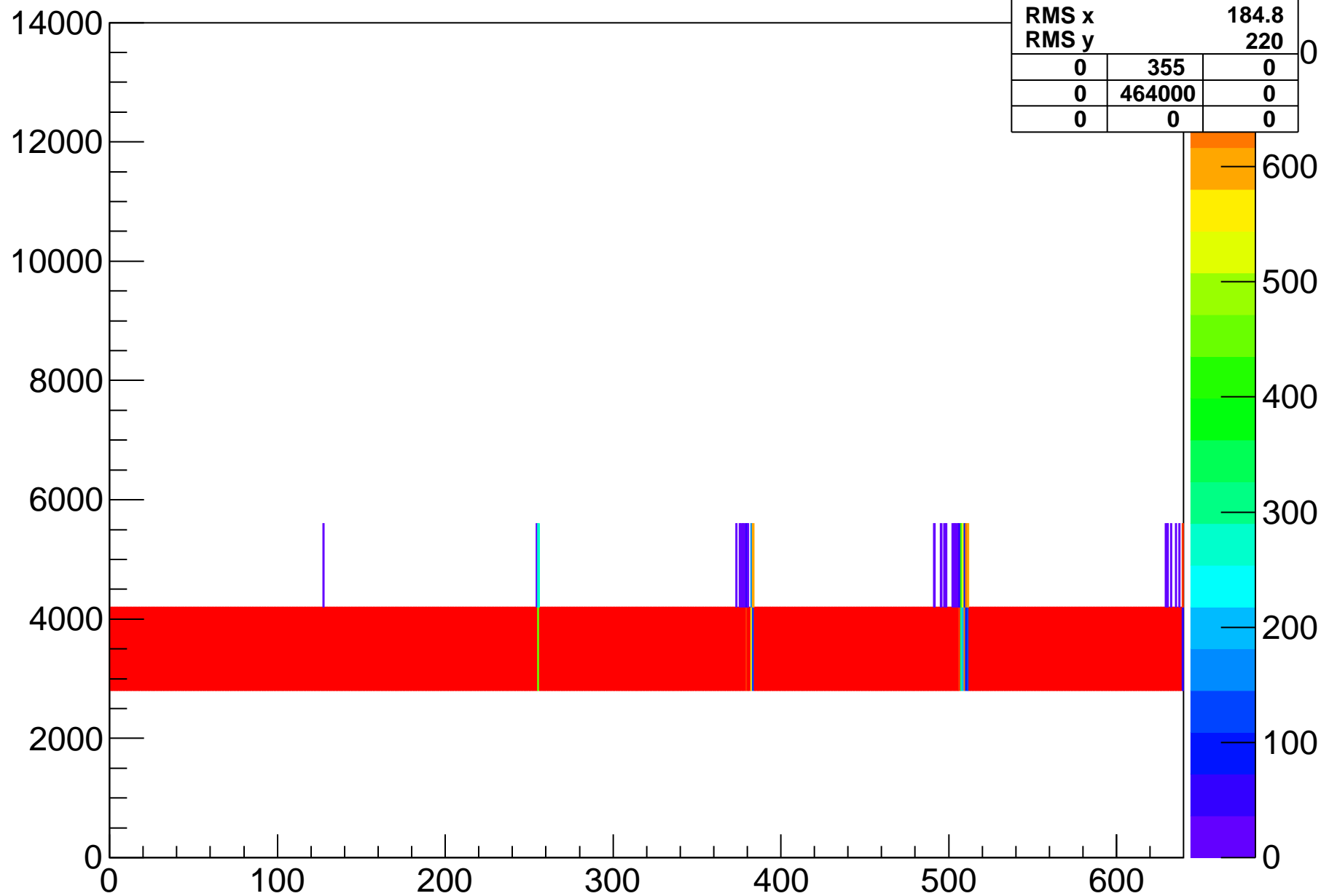
baselinesamples-fpga-1-hyb-2-sample-5

Entries	464032	
Mean x	319.5	
Mean y	3889	
RMS x	184.8	
RMS y	236.1	
0	32	0
0	464000	0
0	0	0



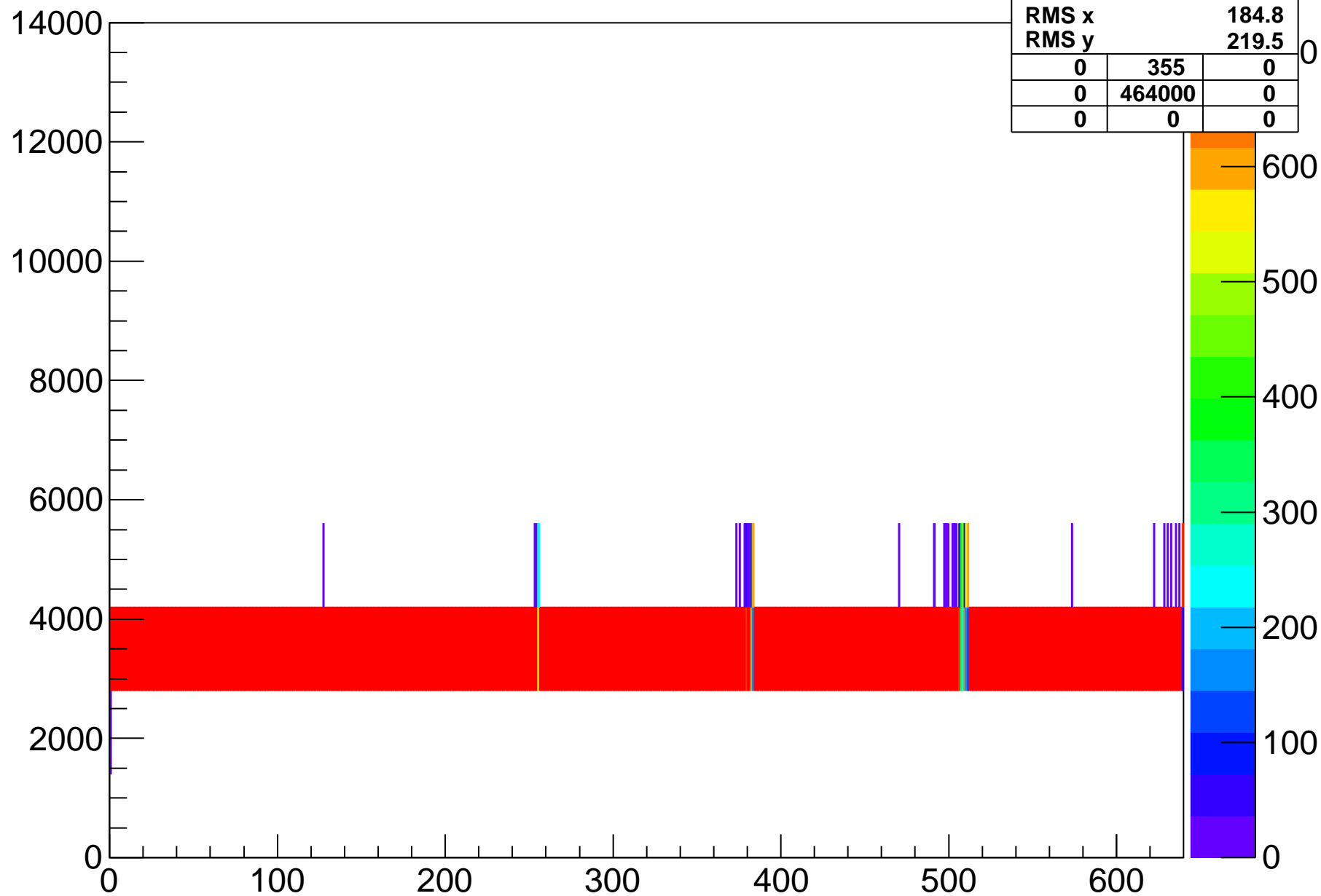
baselinesamples-fpga-1-hyb-3-sample-0

Entries	464355	
Mean x	319.5	
Mean y	3620	
RMS x	184.8	
RMS y	220	
0	355	0
0	464000	0
0	0	0



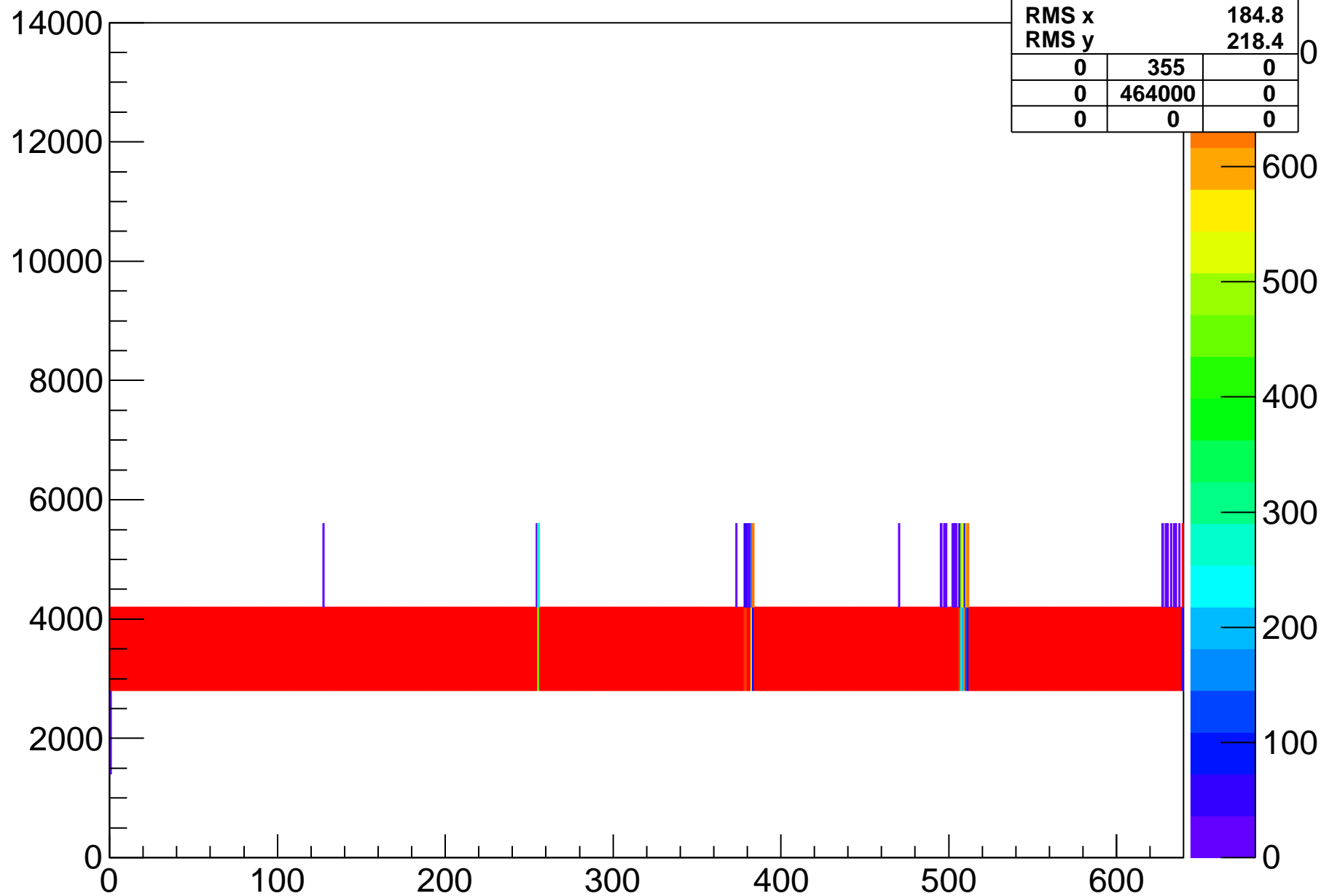
baselinesamples-fpga-1-hyb-3-sample-1

Entries	464355	
Mean x	319.5	
Mean y	3608	
RMS x	184.8	
RMS y	219.5	
0	355	0
0	464000	0
0	0	0



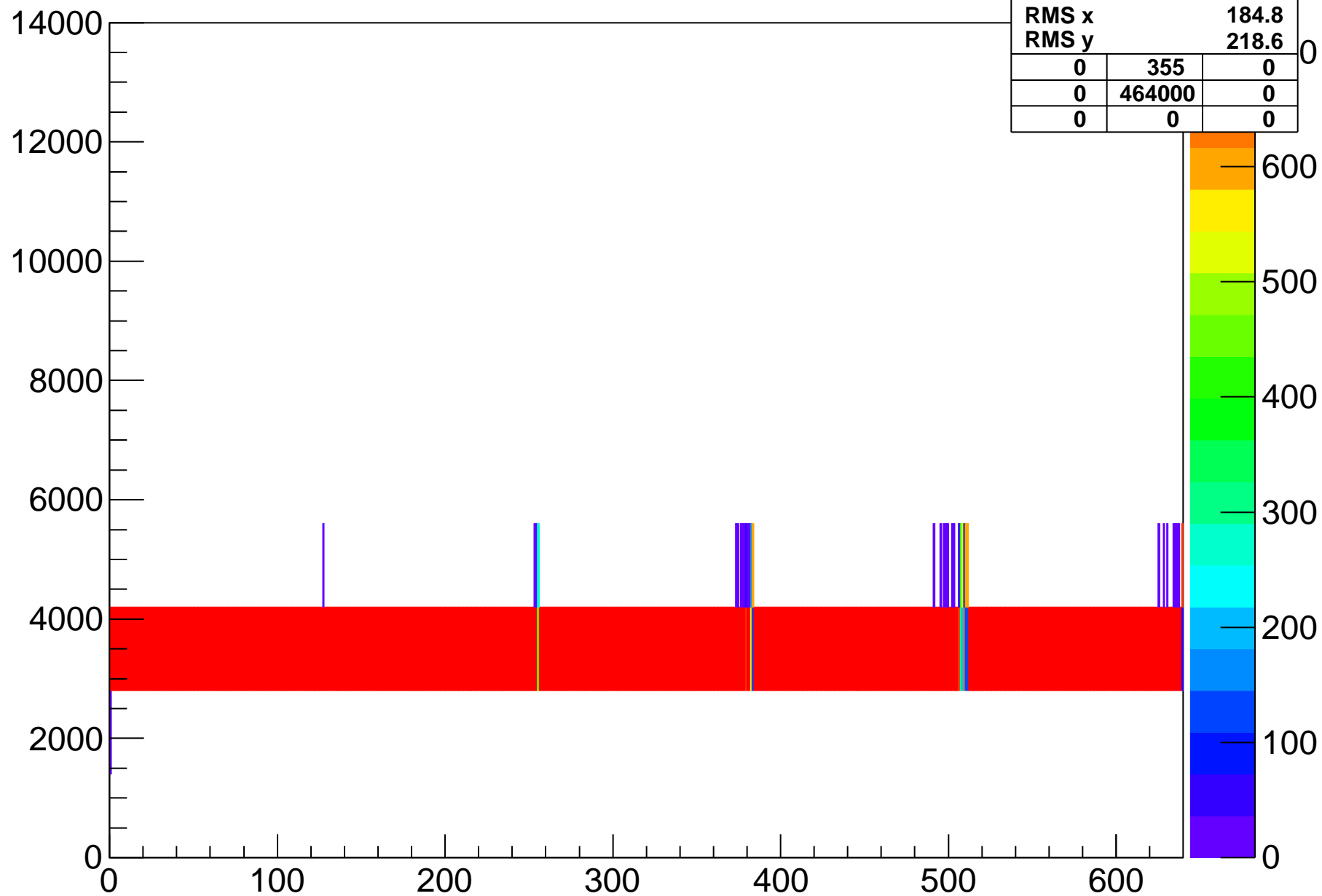
baselinesamples-fpga-1-hyb-3-sample-2

Entries	464355	
Mean x	319.5	
Mean y	3627	
RMS x	184.8	
RMS y	218.4	
0	355	0
0	464000	0
0	0	0



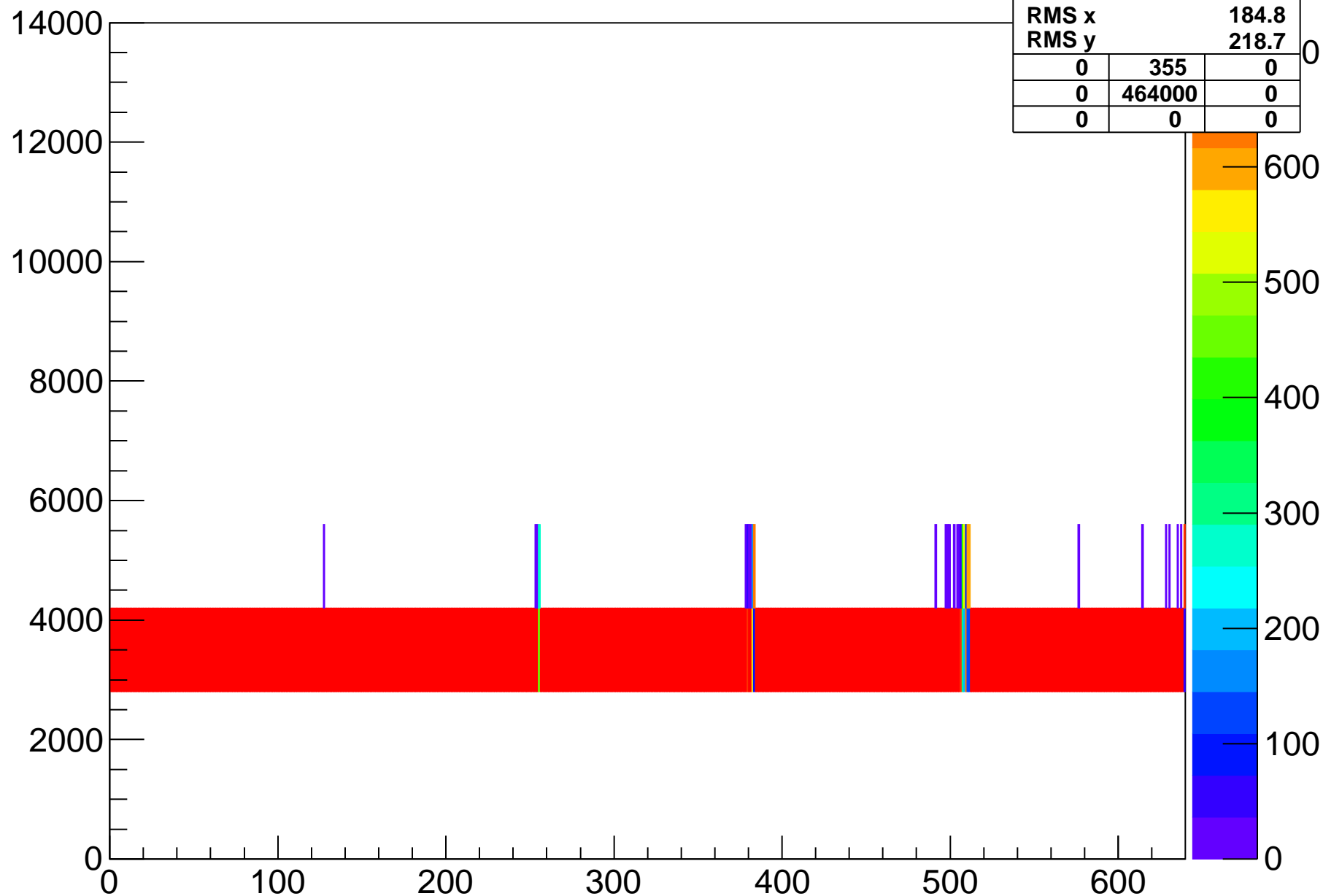
baselinesamples-fpga-1-hyb-3-sample-3

Entries	464355	
Mean x	319.5	
Mean y	3622	
RMS x	184.8	
RMS y	218.6	
0	355	0
0	464000	0
0	0	0



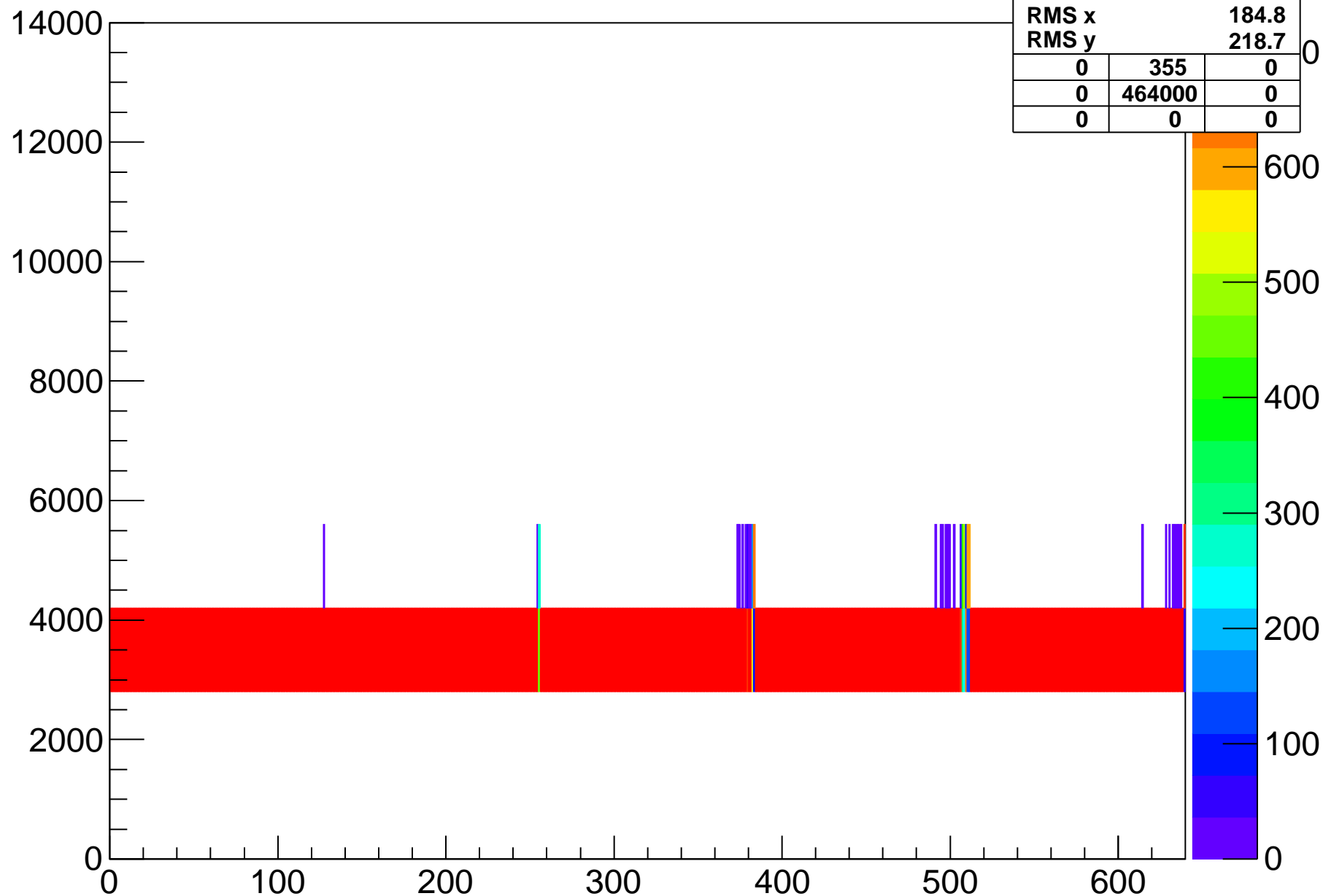
baselinesamples-fpga-1-hyb-3-sample-4

Entries	464355	
Mean x	319.5	
Mean y	3620	
RMS x	184.8	
RMS y	218.7	
0	355	0
0	464000	0
0	0	0



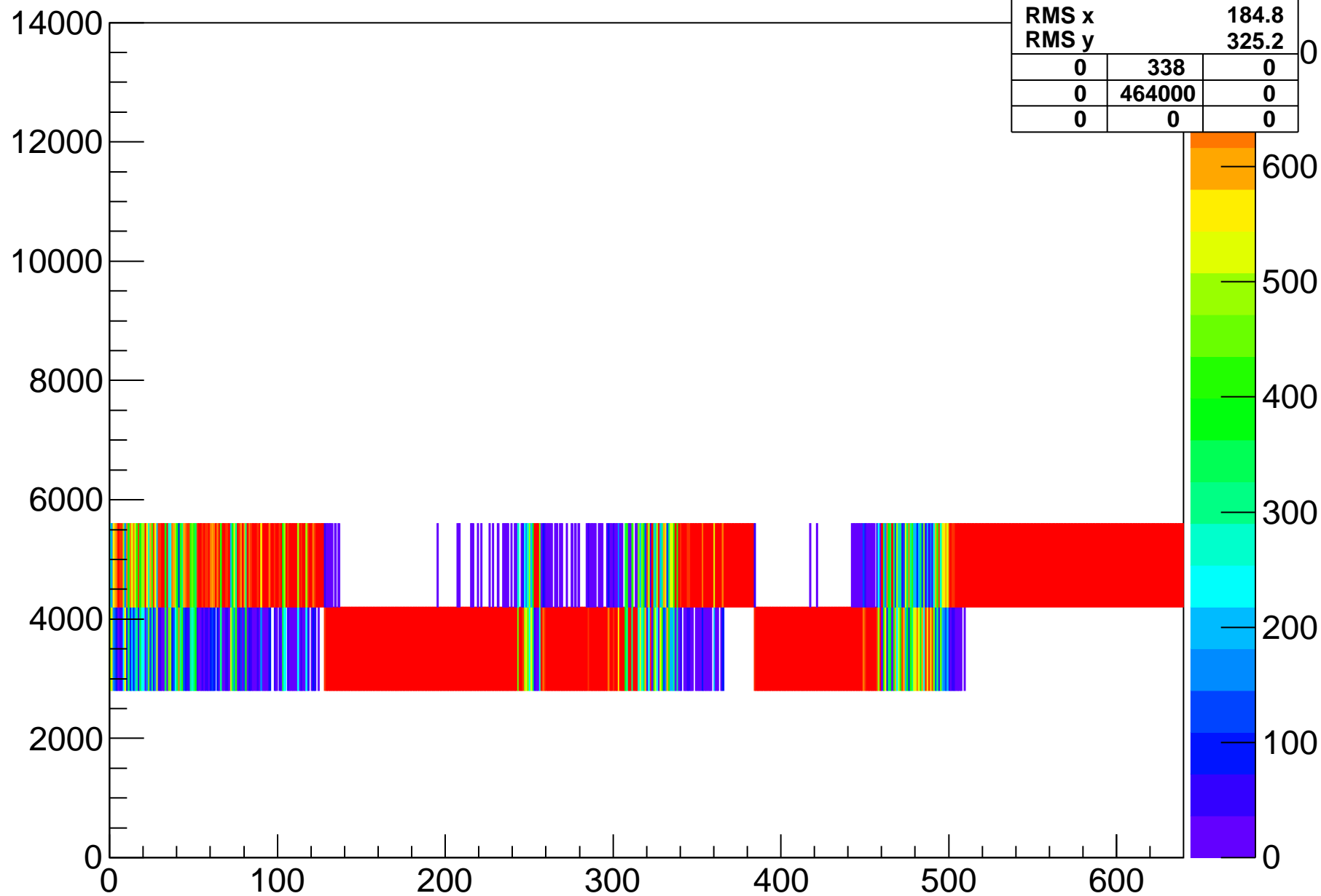
baselinesamples-fpga-1-hyb-3-sample-5

Entries	464355	
Mean x	319.5	
Mean y	3622	
RMS x	184.8	
RMS y	218.7	
0	355	0
0	464000	0
0	0	0



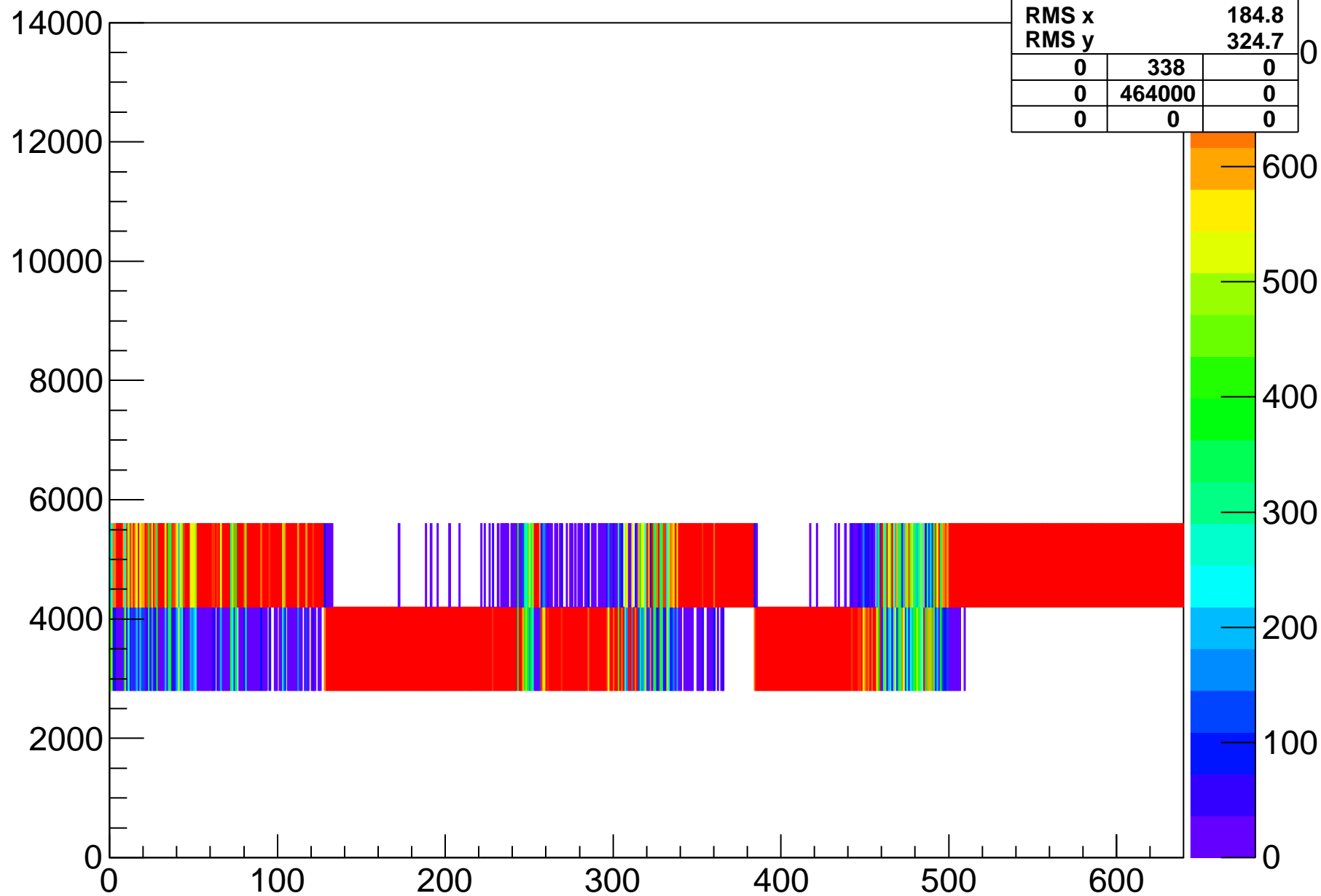
baselinesamples-fpga-2-hyb-0-sample-0

Entries	464338	
Mean x	319.5	
Mean y	4253	
RMS x	184.8	
RMS y	325.2	
0	338	0
0	464000	0
0	0	0



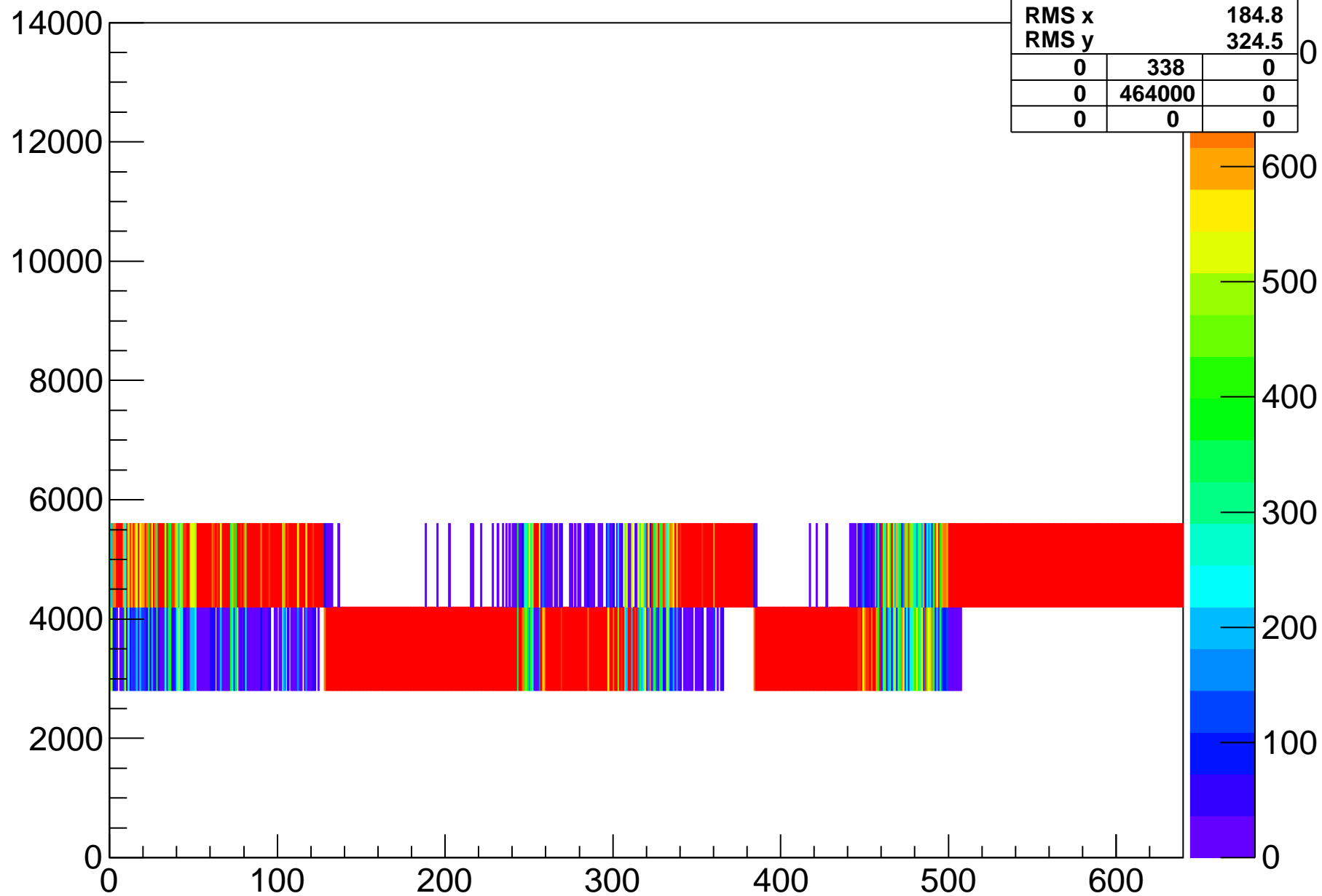
baselinesamples-fpga-2-hyb-0-sample-1

Entries	464338	
Mean x	319.5	
Mean y	4281	
RMS x	184.8	
RMS y	324.7	
0	338	0
0	464000	0
0	0	0



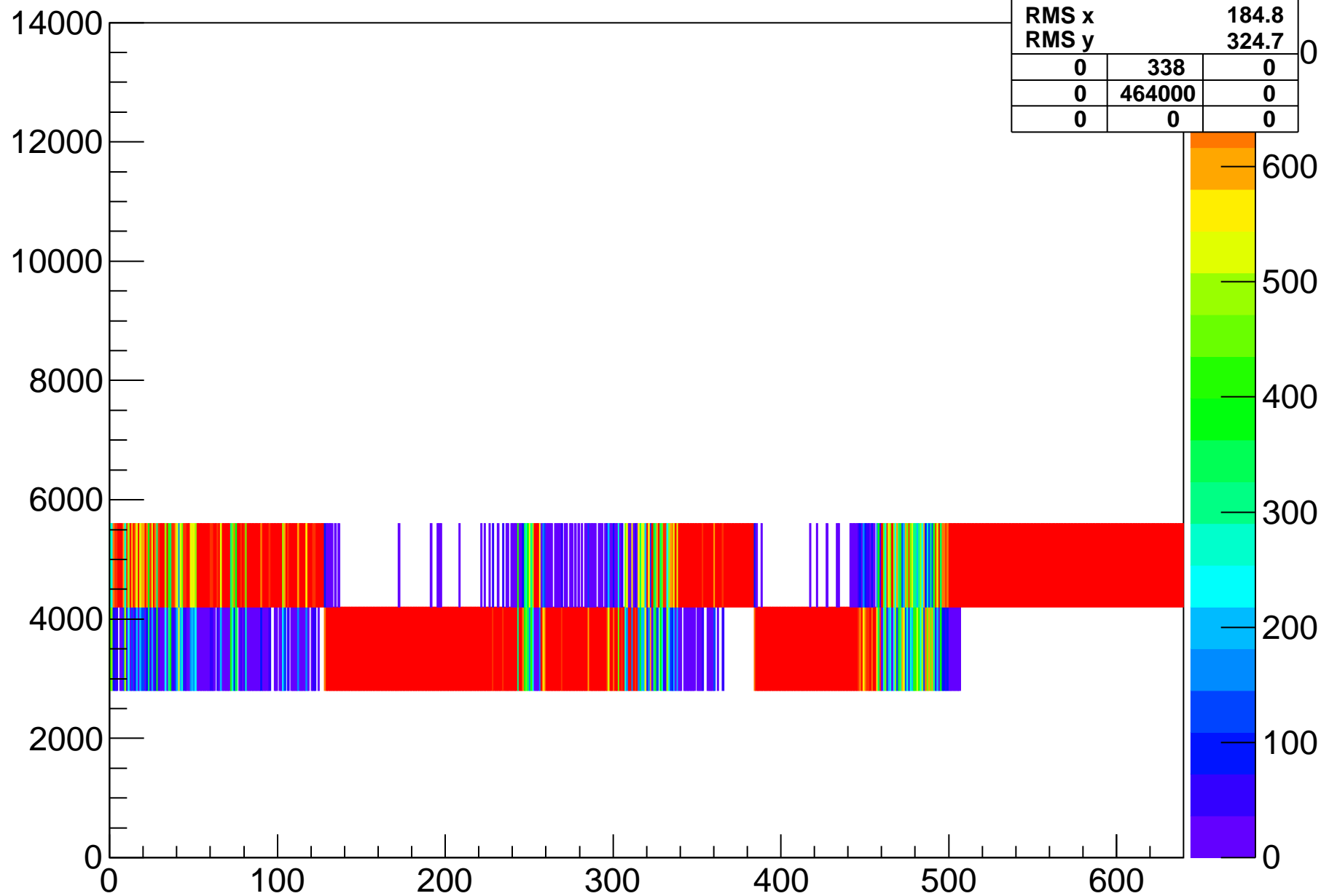
baselinesamples-fpga-2-hyb-0-sample-2

Entries	464338	
Mean x	319.5	
Mean y	4277	
RMS x	184.8	
RMS y	324.5	
0	338	0
0	464000	0
0	0	0



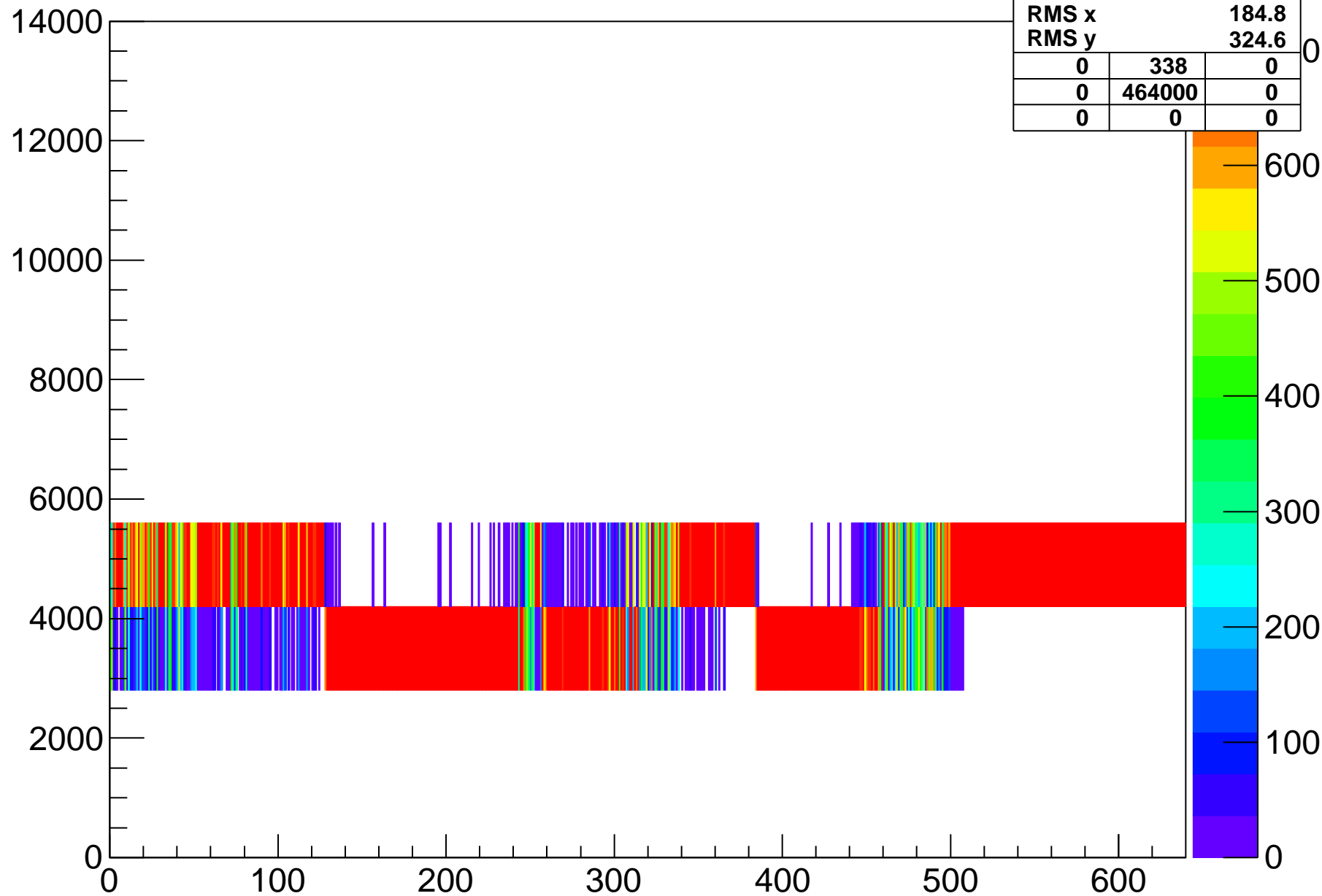
baselinesamples-fpga-2-hyb-0-sample-3

Entries	464338	
Mean x	319.5	
Mean y	4281	
RMS x	184.8	
RMS y	324.7	
0	338	0
0	464000	0
0	0	0



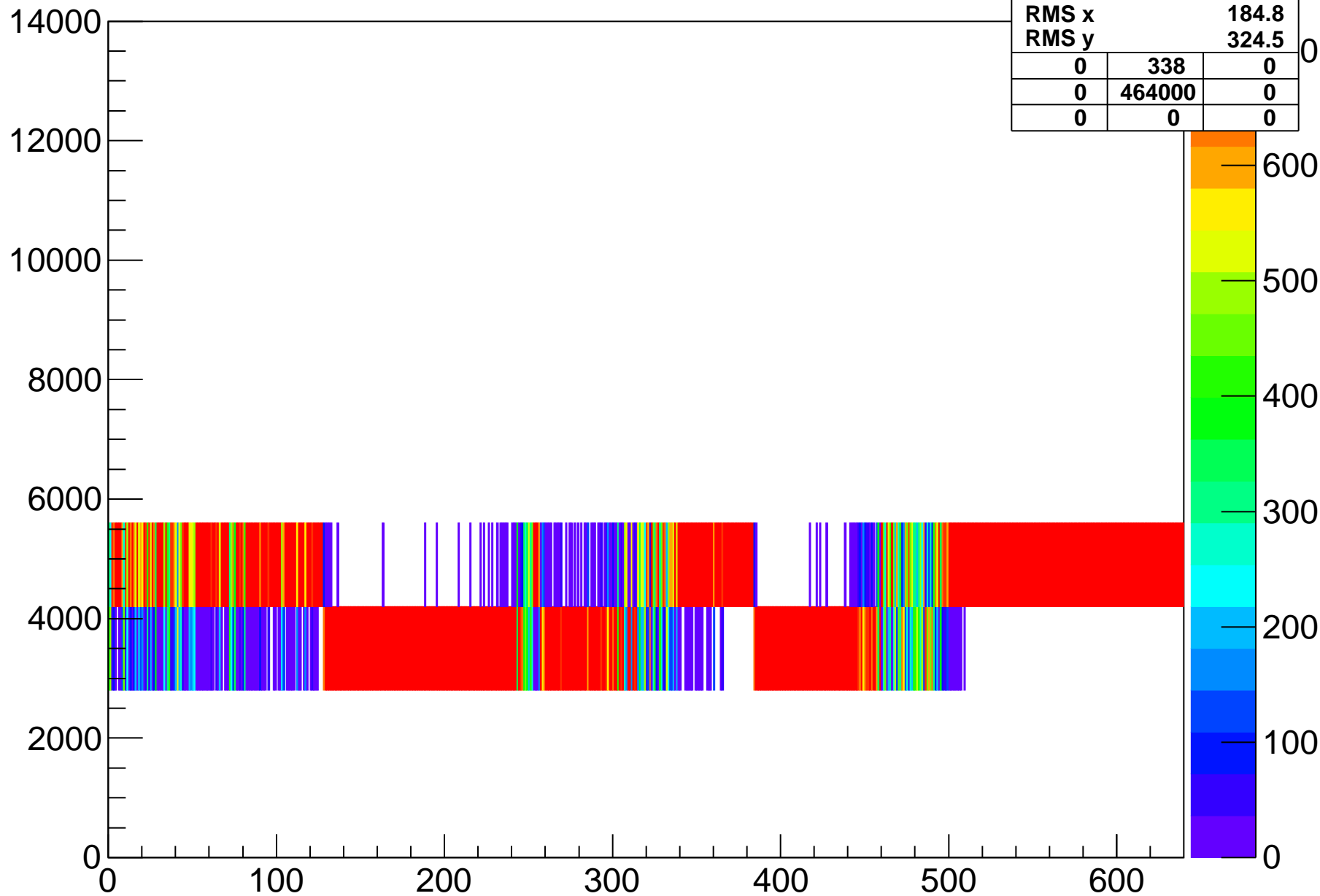
baselinesamples-fpga-2-hyb-0-sample-4

Entries	464338	
Mean x	319.5	
Mean y	4280	
RMS x	184.8	
RMS y	324.6	
0	338	0
0	464000	0
0	0	0



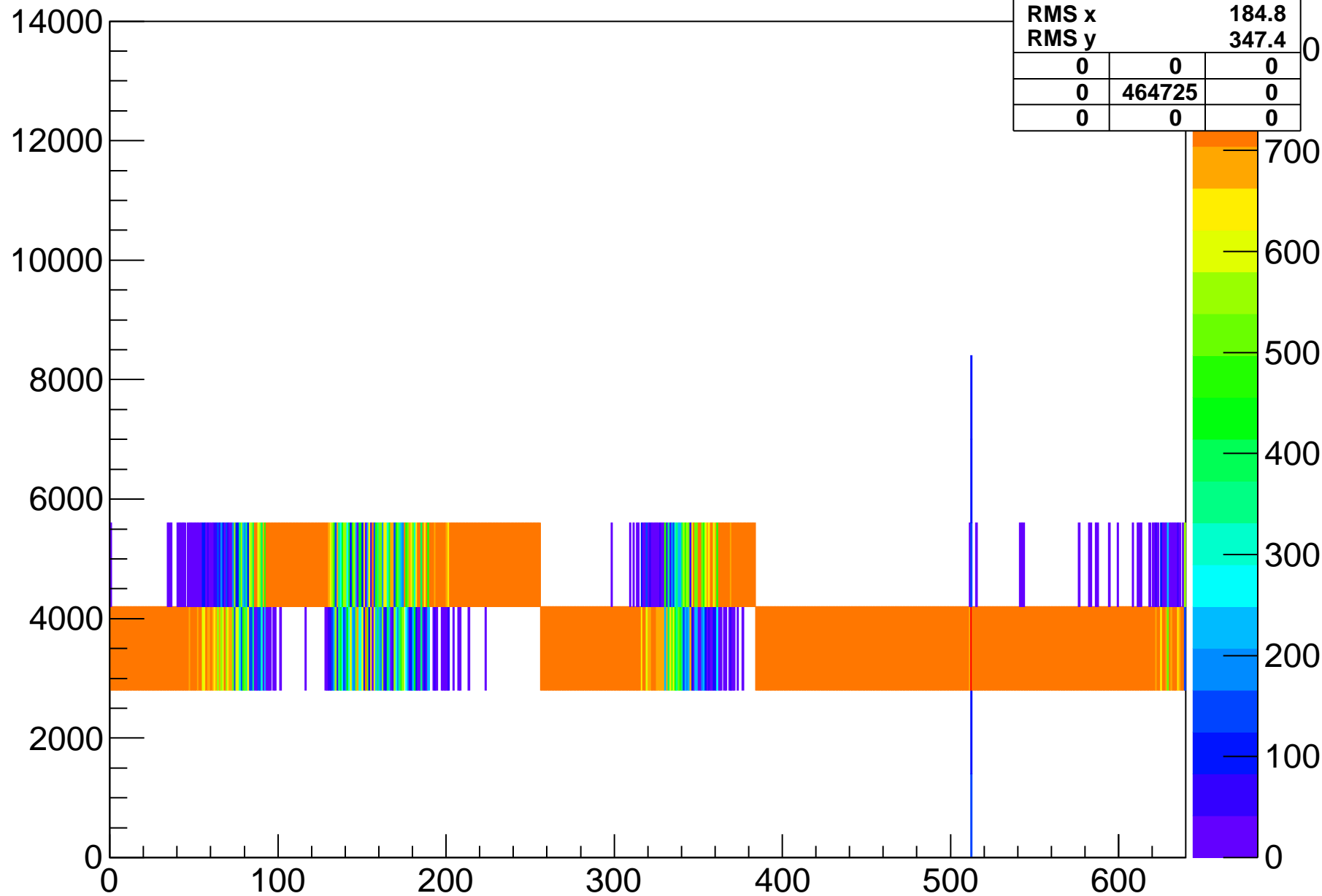
baselinesamples-fpga-2-hyb-0-sample-5

Entries	464338	
Mean x	319.5	
Mean y	4280	
RMS x	184.8	
RMS y	324.5	
0	338	0
0	464000	0
0	0	0



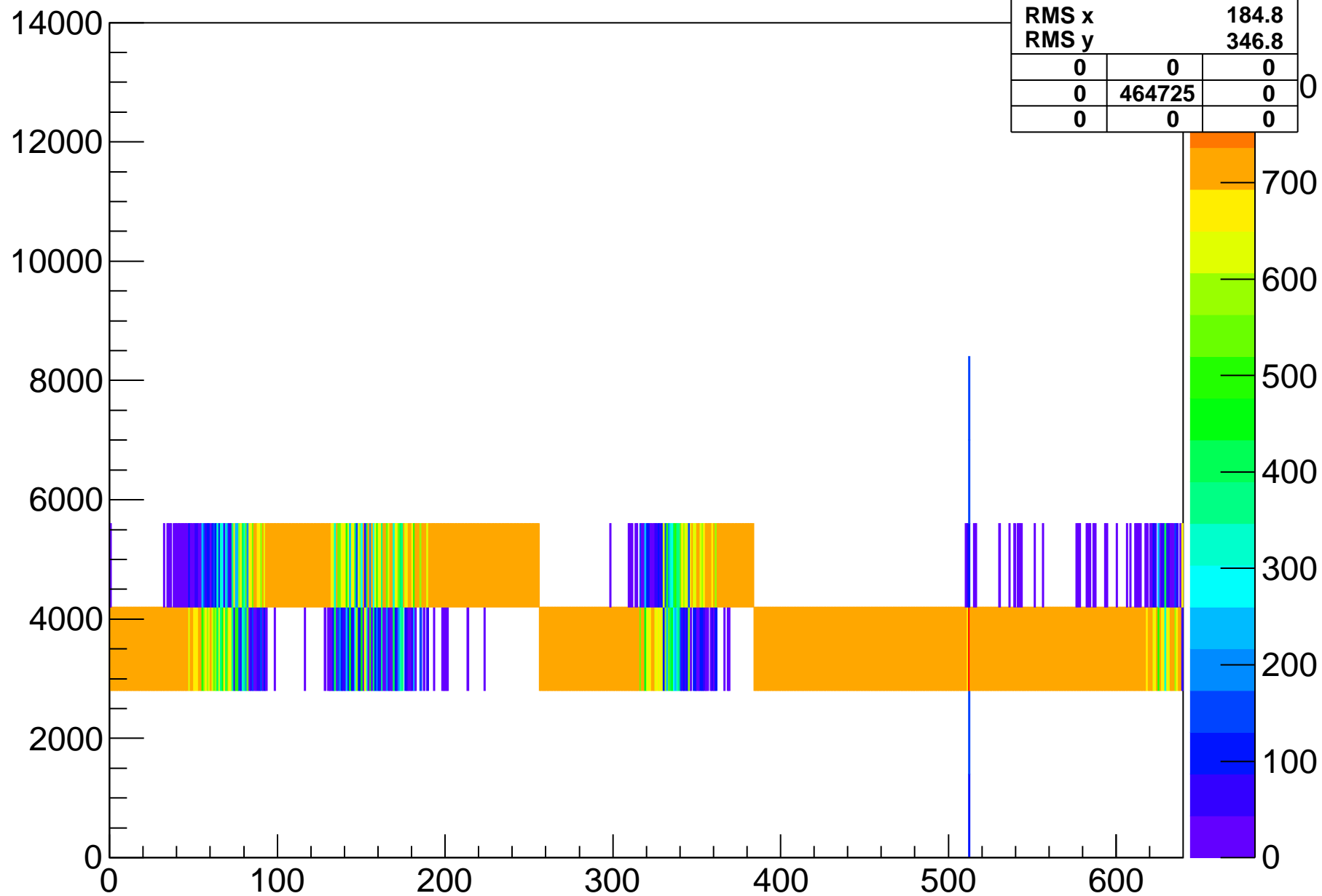
baselinesamples-fpga-2-hyb-1-sample-0

Entries	464725	
Mean x	319.8	
Mean y	4008	
RMS x	184.8	
RMS y	347.4	
0	0	0
0	464725	0
0	0	0



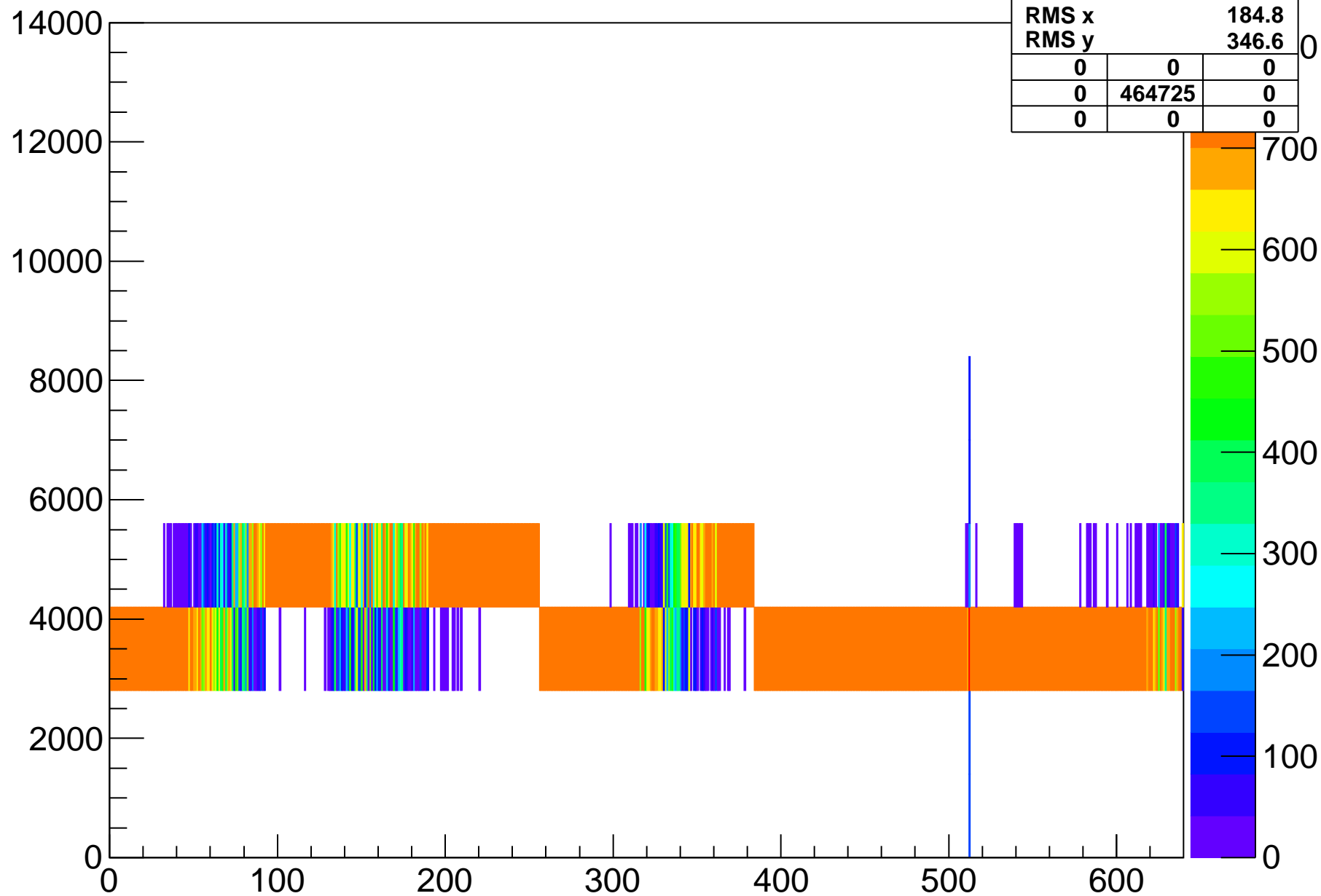
baselinesamples-fpga-2-hyb-1-sample-1

Entries	464725	
Mean x	319.8	
Mean y	4046	
RMS x	184.8	
RMS y	346.8	
0	0	0
0	464725	0
0	0	0



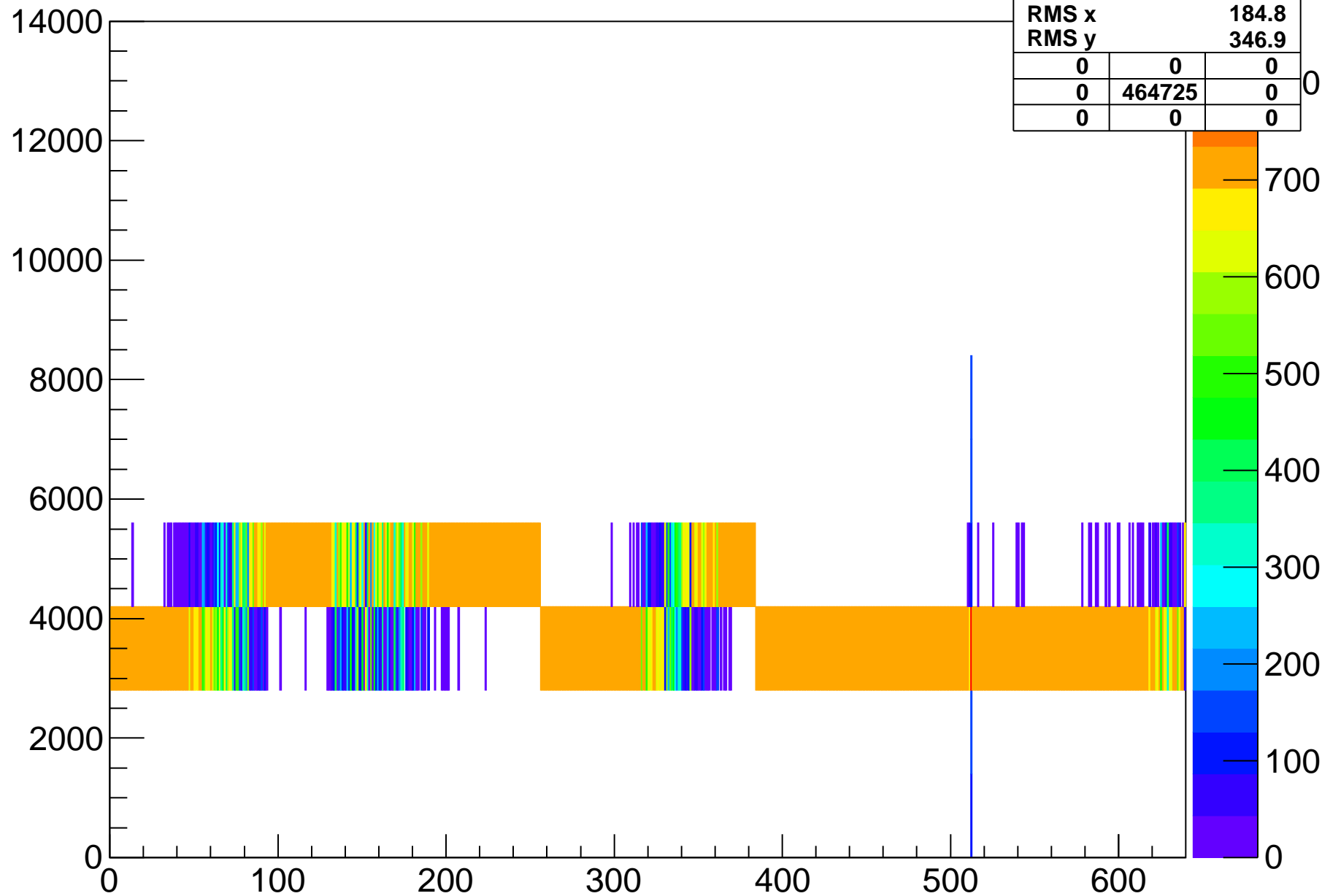
baselinesamples-fpga-2-hyb-1-sample-2

Entries	464725	
Mean x	319.8	
Mean y	4042	
RMS x	184.8	
RMS y	346.6	
0	0	0
0	464725	0
0	0	0



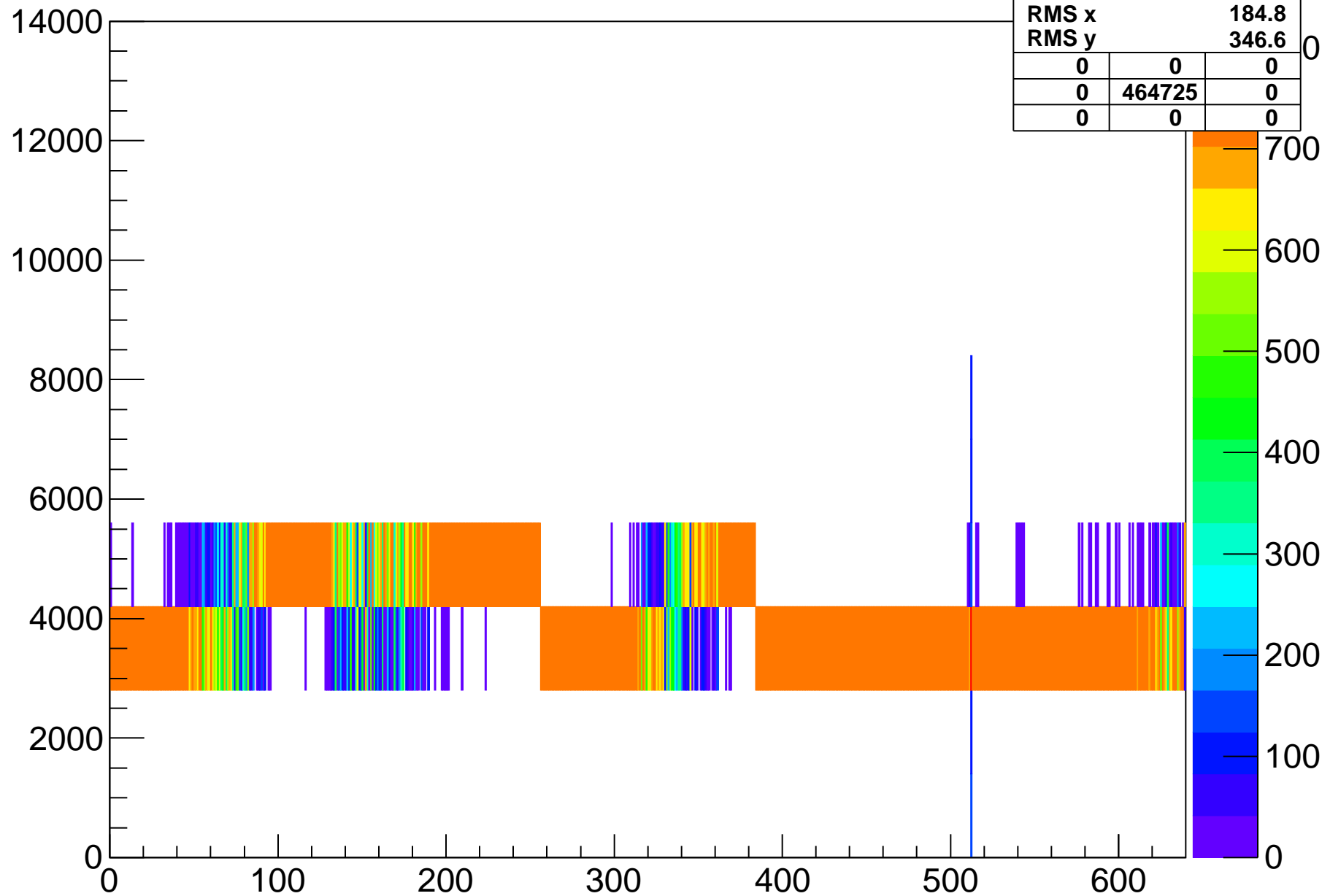
baselinesamples-fpga-2-hyb-1-sample-3

Entries	464725	
Mean x	319.8	
Mean y	4045	
RMS x	184.8	
RMS y	346.9	
0	0	0
0	464725	0
0	0	0



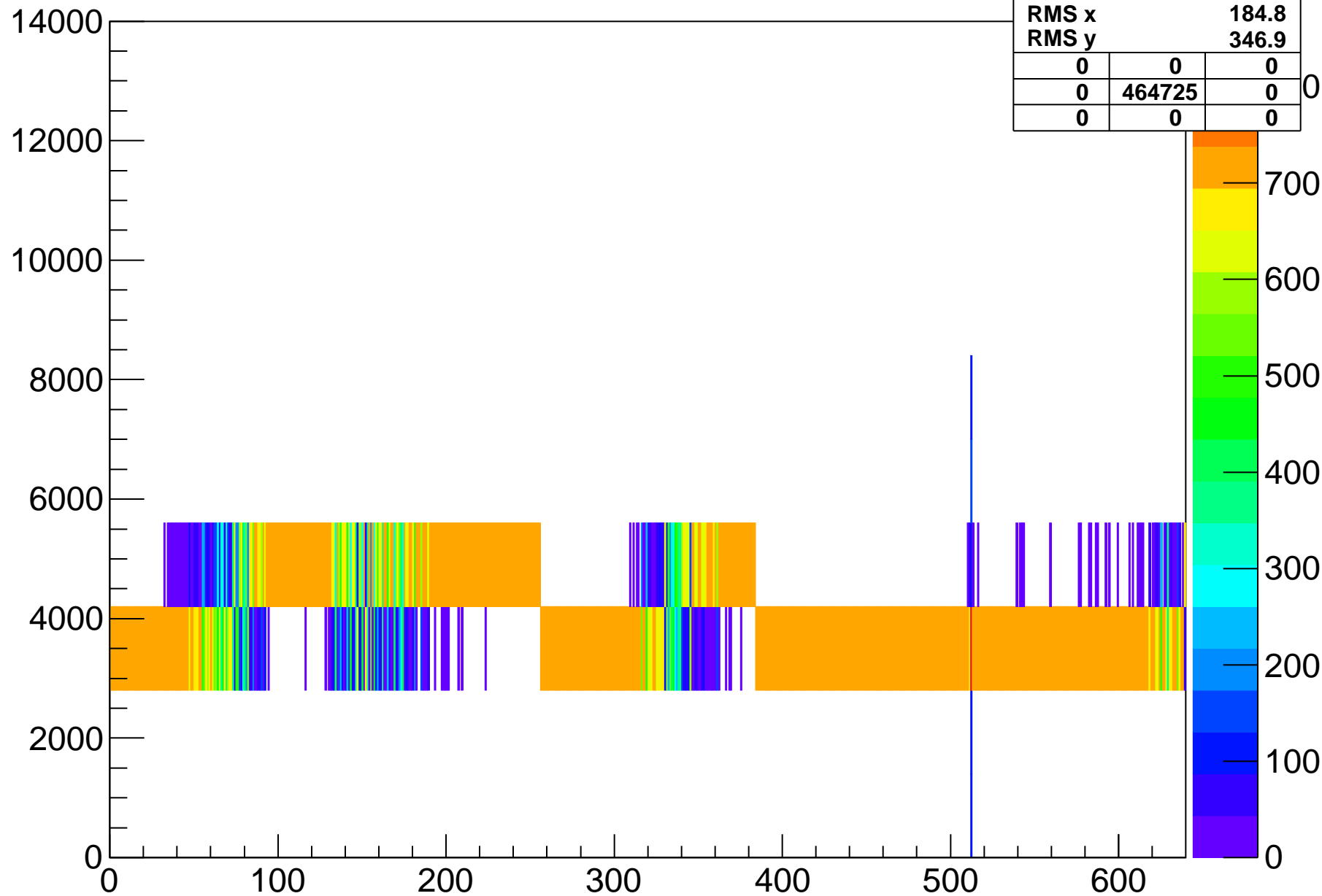
baselinesamples-fpga-2-hyb-1-sample-4

Entries	464725	
Mean x	319.8	
Mean y	4045	
RMS x	184.8	
RMS y	346.6	
0	0	0
0	464725	0
0	0	0

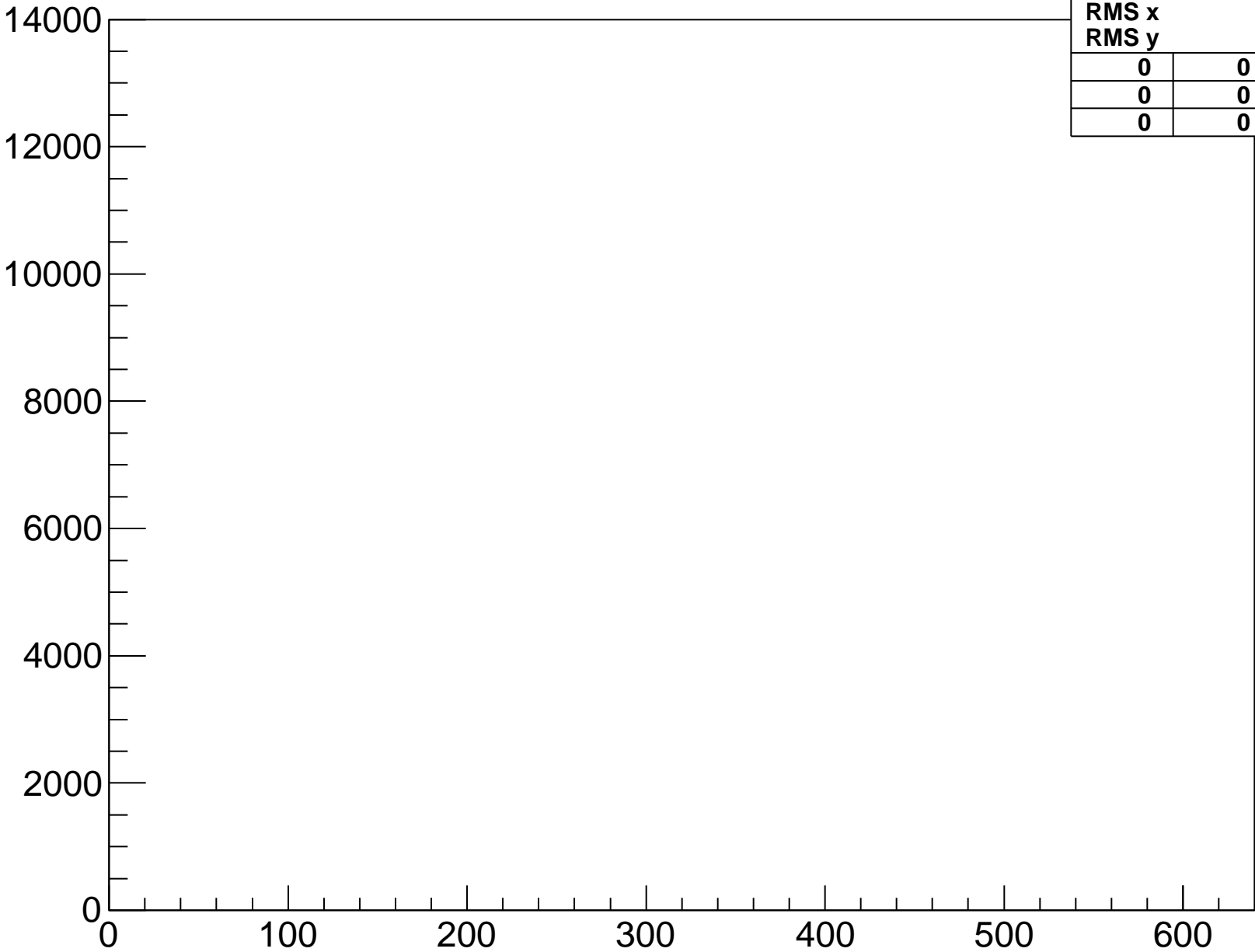


baselinesamples-fpga-2-hyb-1-sample-5

Entries	464725	
Mean x	319.8	
Mean y	4045	
RMS x	184.8	
RMS y	346.9	
0	0	0
0	464725	0
0	0	0



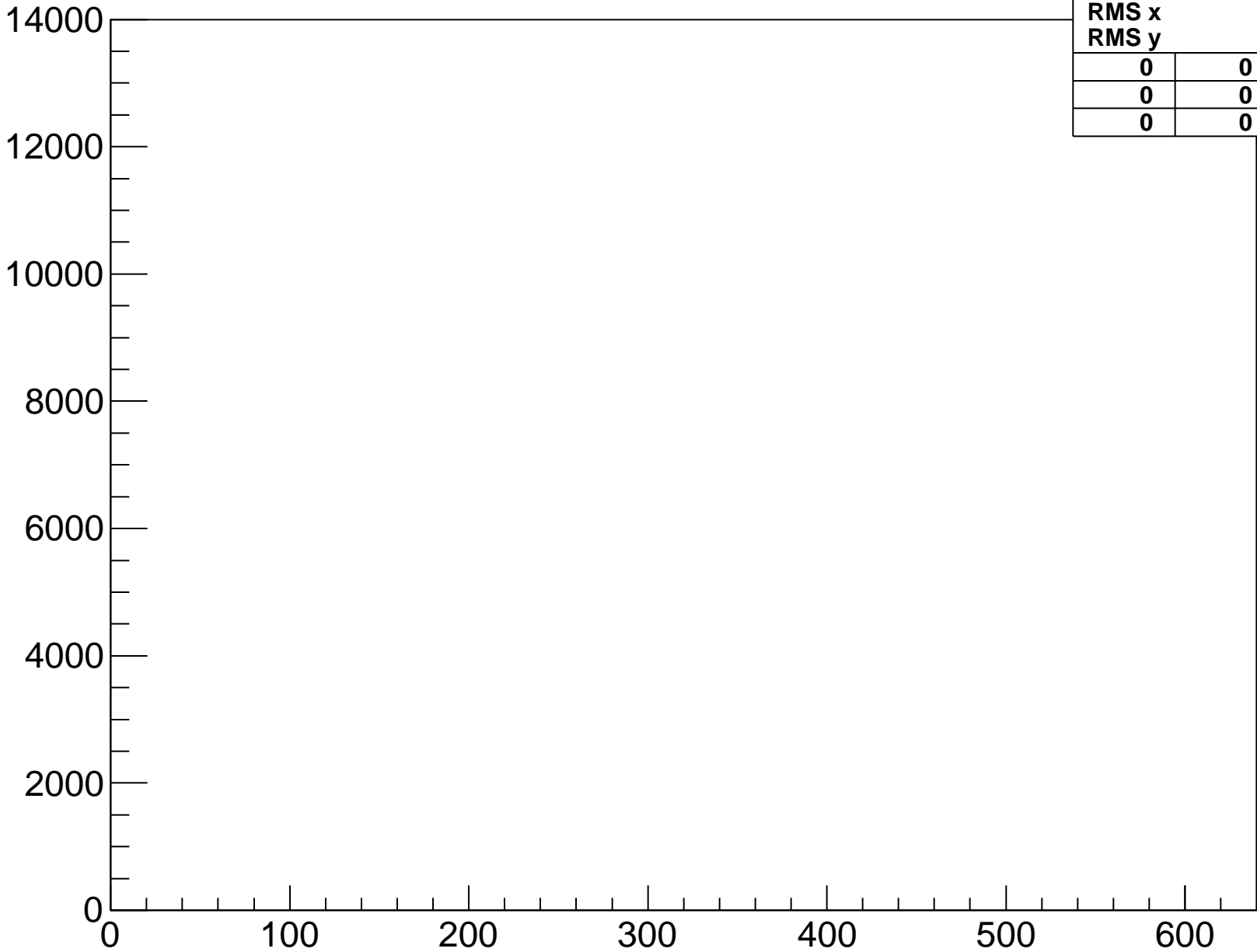
baselinesamples-fpga-2-hyb-2-sample-0



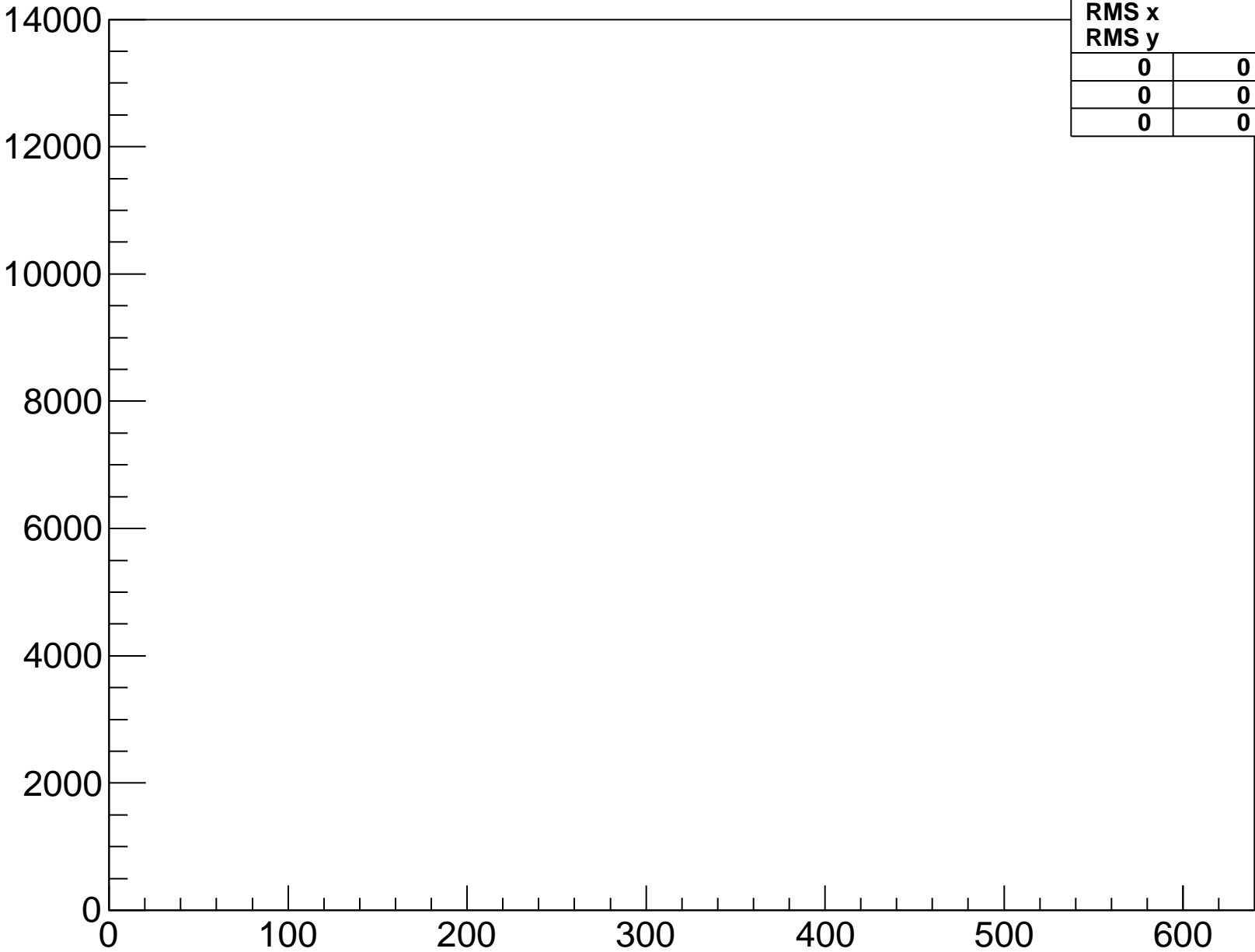
Entries	0		
Mean x	0		
Mean y	0		
RMS x	0		
RMS y	0		
0	0	0	
0	0	0	
0	0	0	

baselinesamples-fpga-2-hyb-2-sample-1

Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0

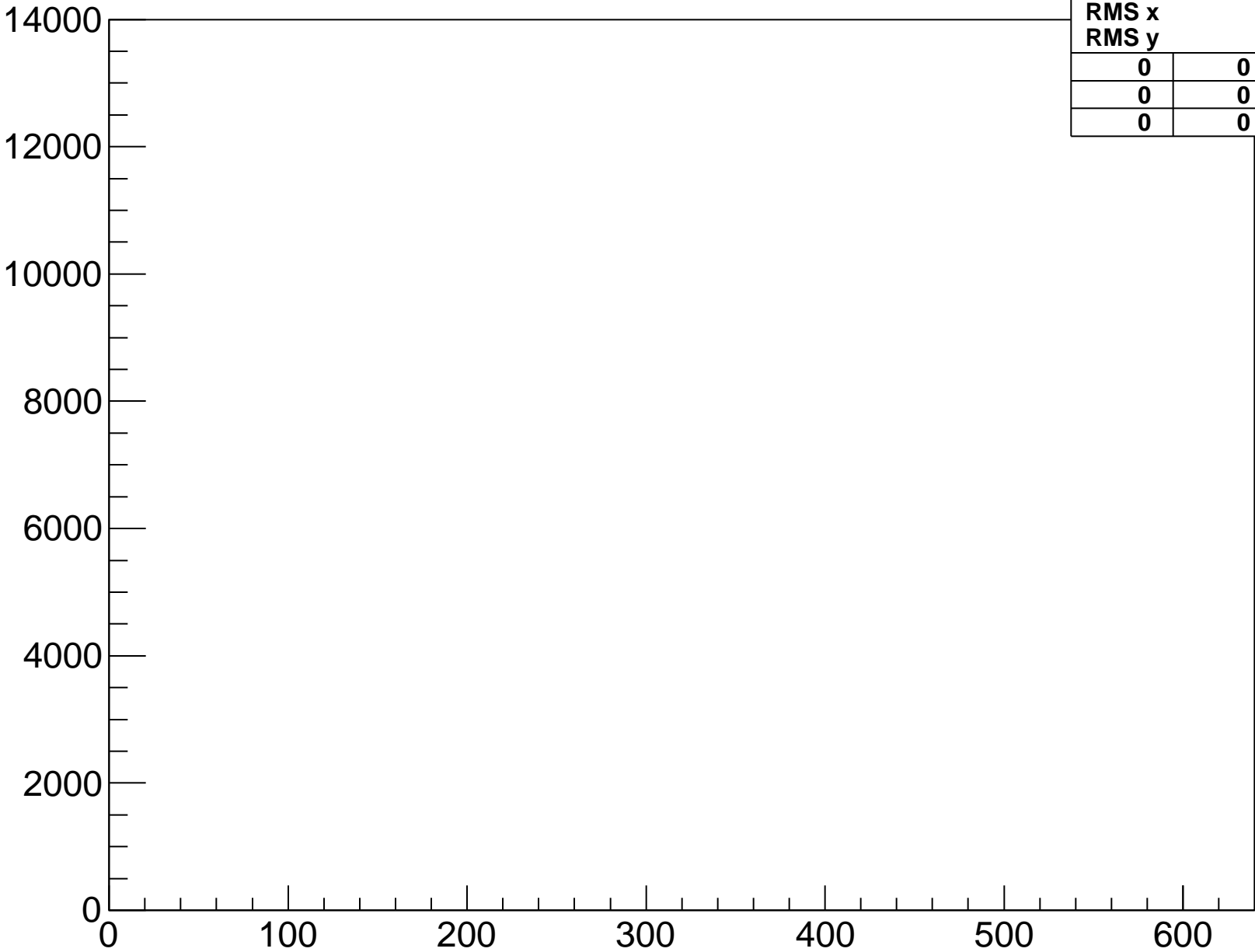


baselinesamples-fpga-2-hyb-2-sample-2



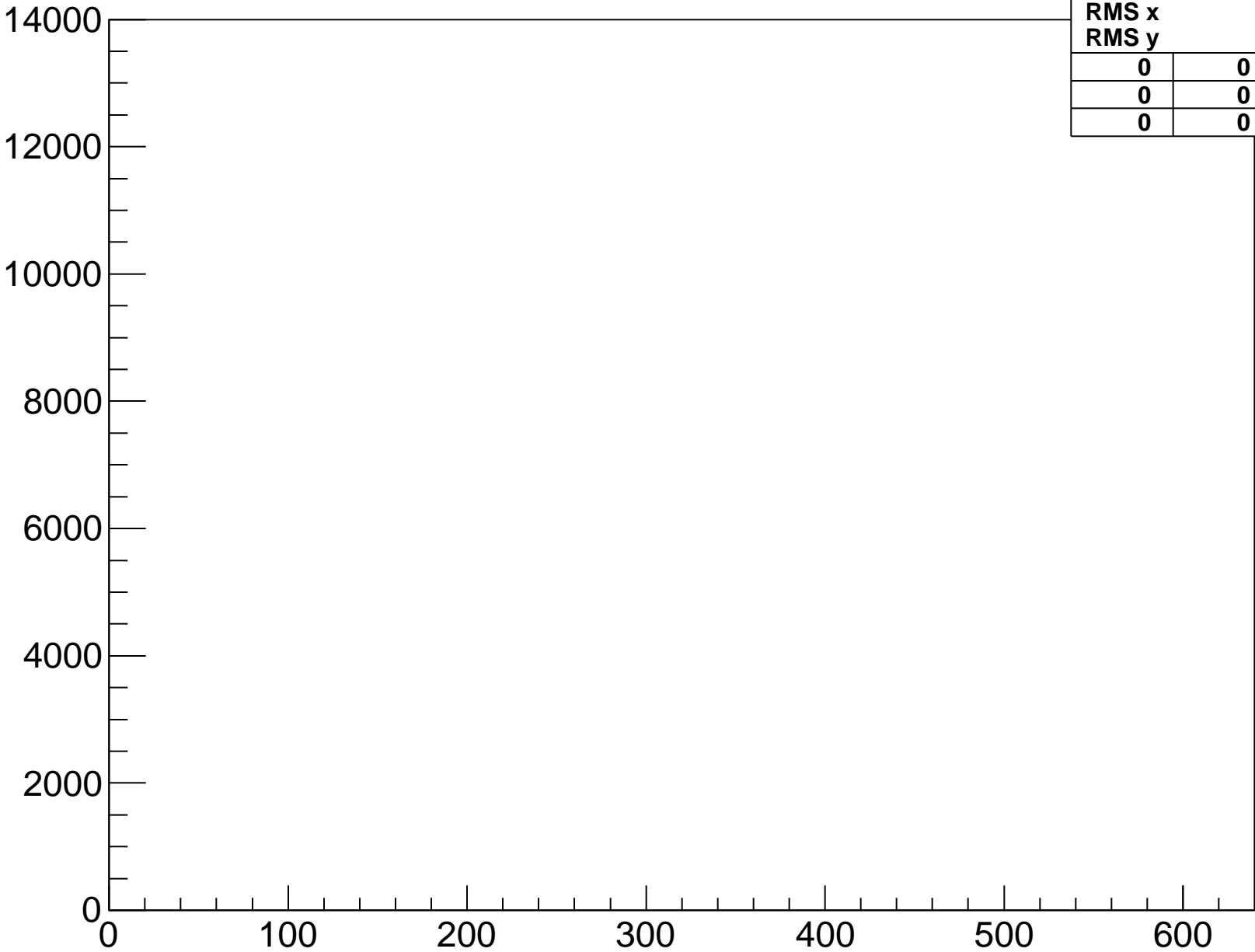
Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0

baselinesamples-fpga-2-hyb-2-sample-3



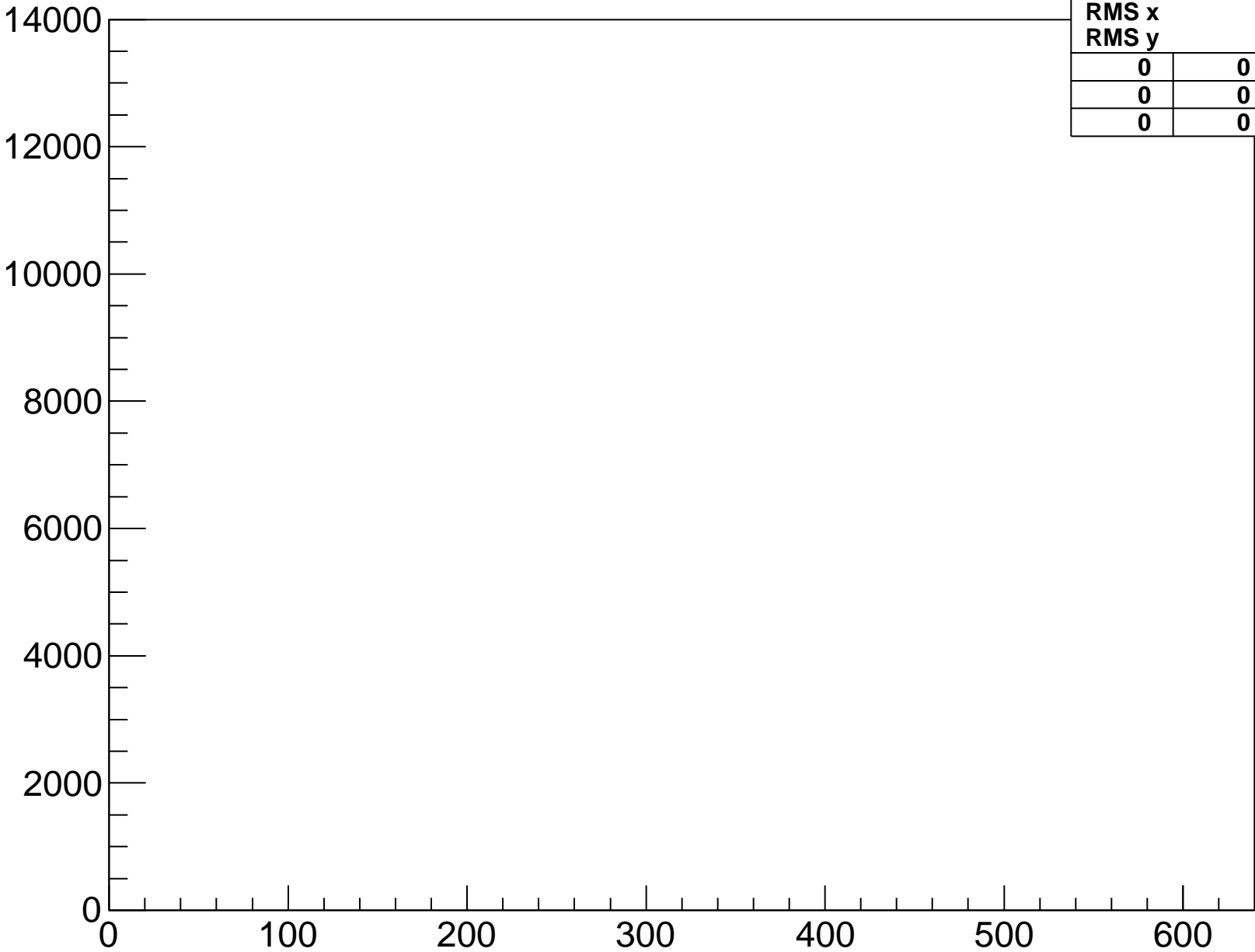
Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

baselinesamples-fpga-2-hyb-2-sample-4



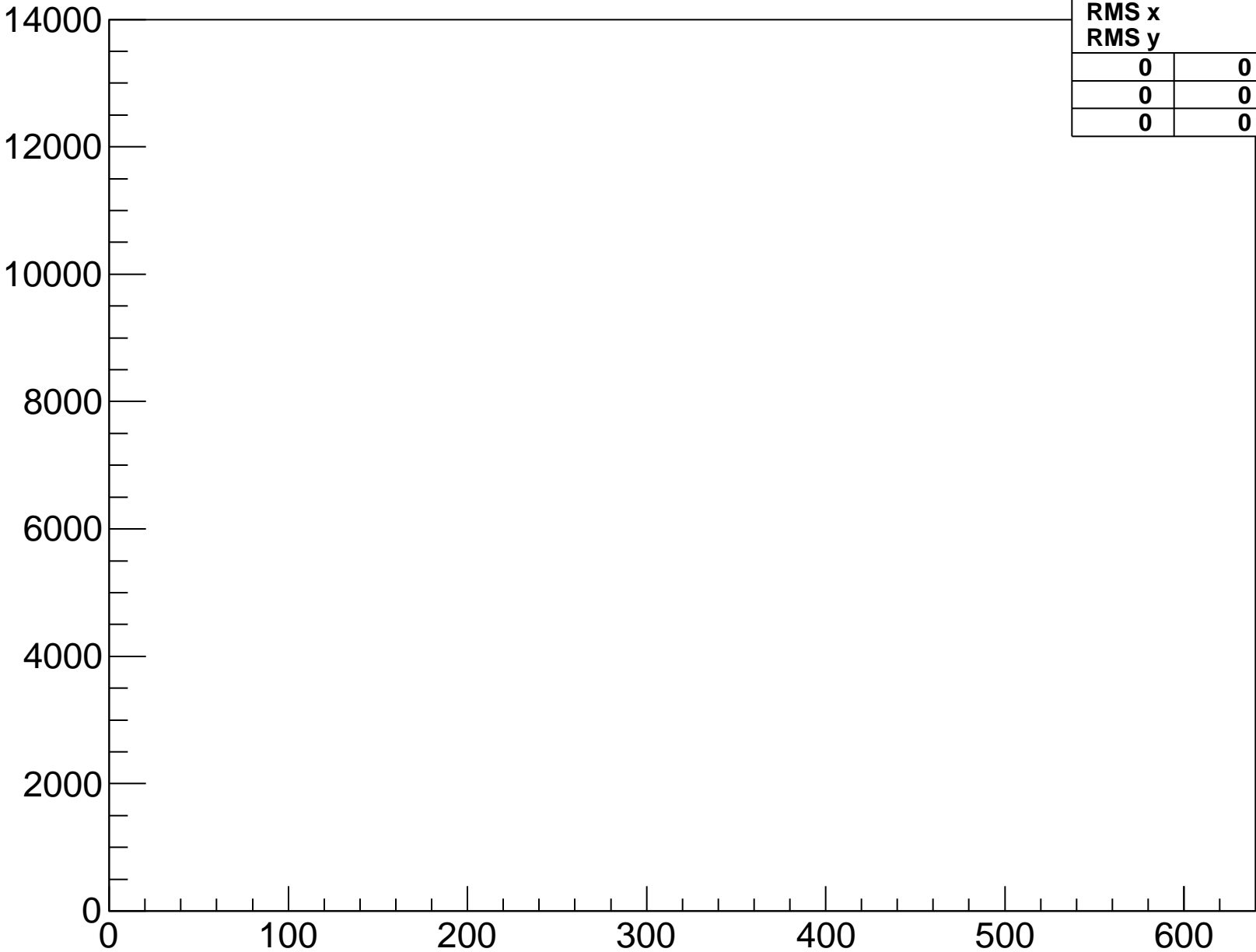
Entries	0		
Mean x	0		
Mean y	0		
RMS x	0		
RMS y	0		
0	0	0	
0	0	0	
0	0	0	

baselinesamples-fpga-2-hyb-2-sample-5



Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0

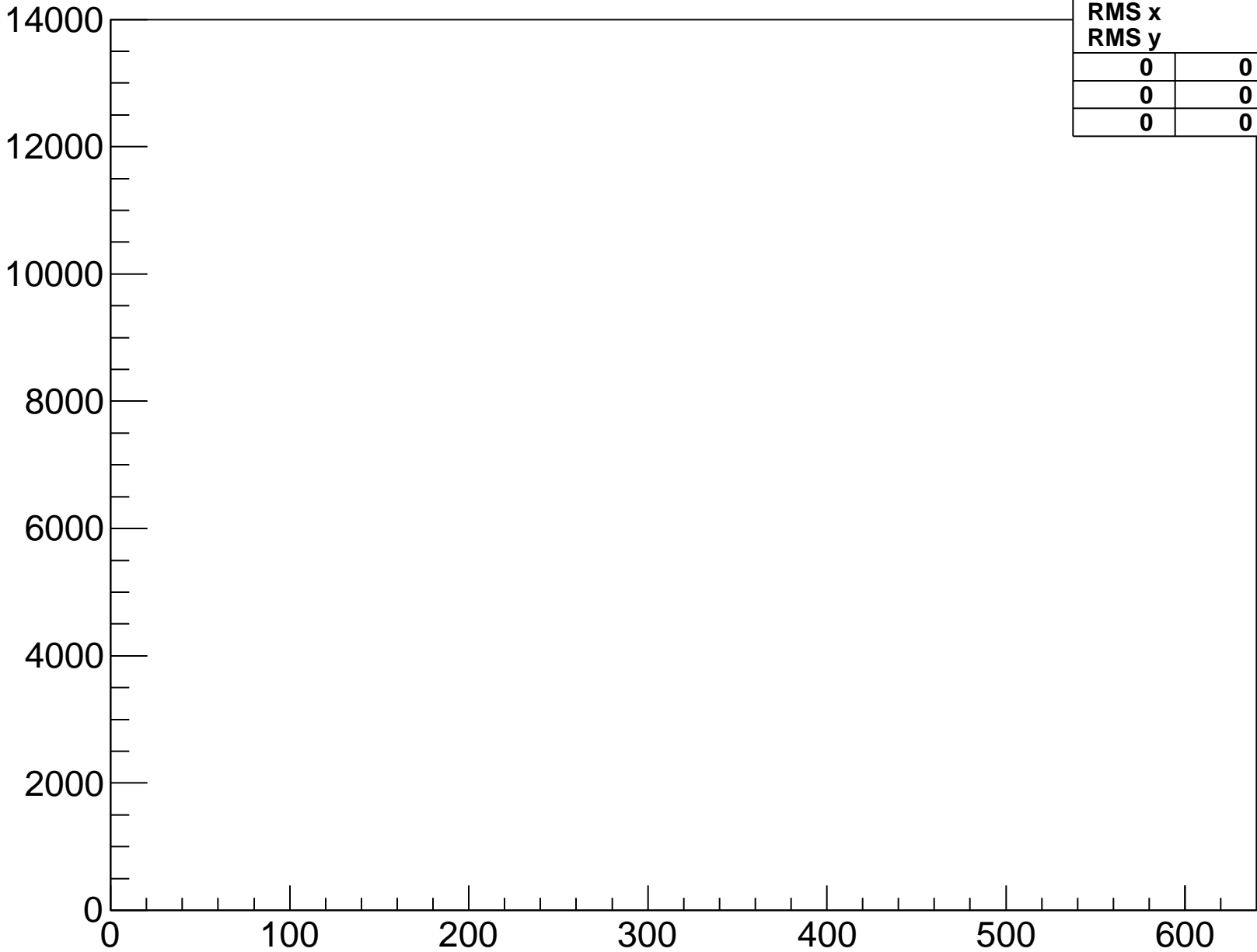
baselinesamples-fpga-2-hyb-3-sample-0



Entries	0		
Mean x	0		
Mean y	0		
RMS x	0		
RMS y	0		
0	0	0	
0	0	0	
0	0	0	

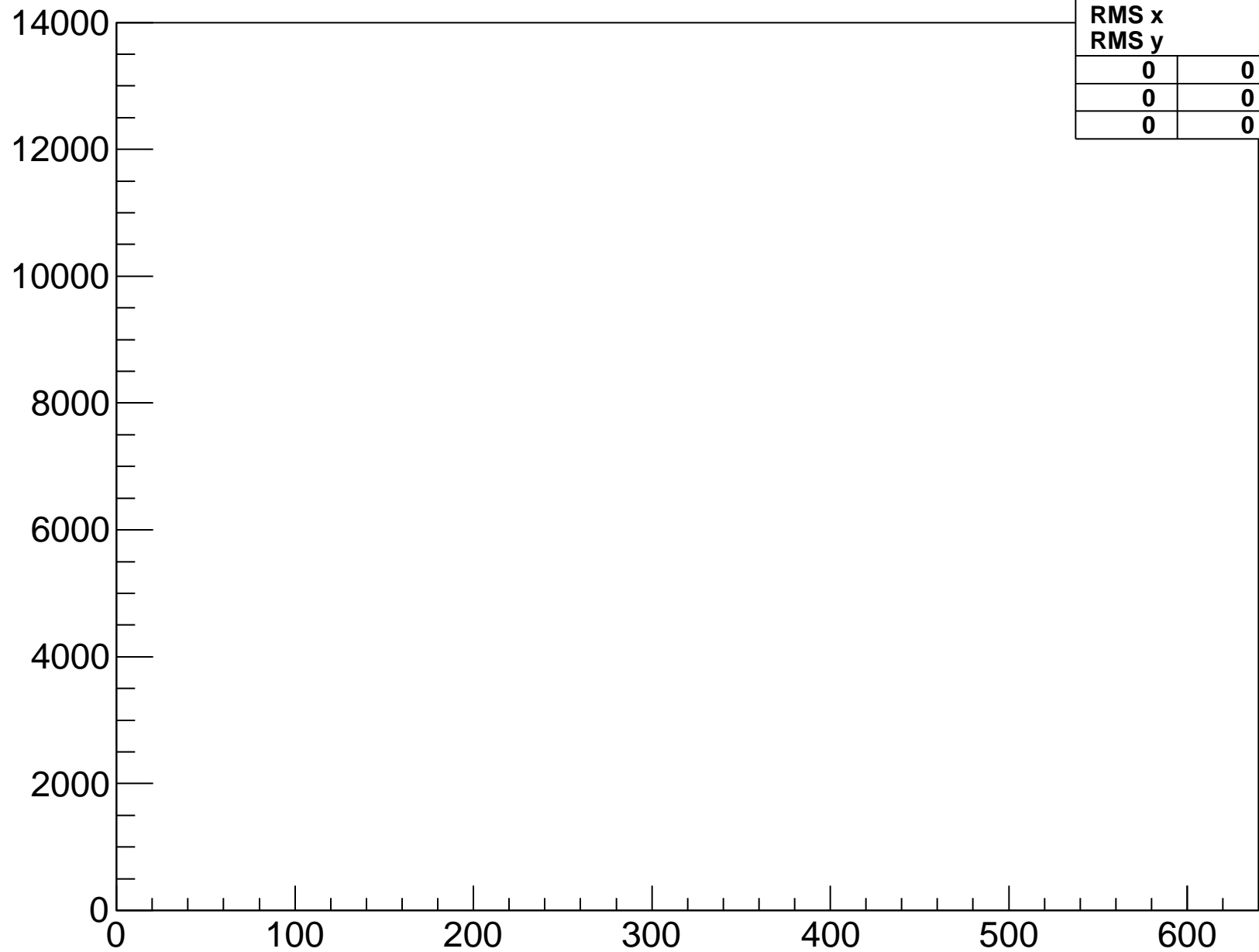
baselinesamples-fpga-2-hyb-3-sample-1

Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

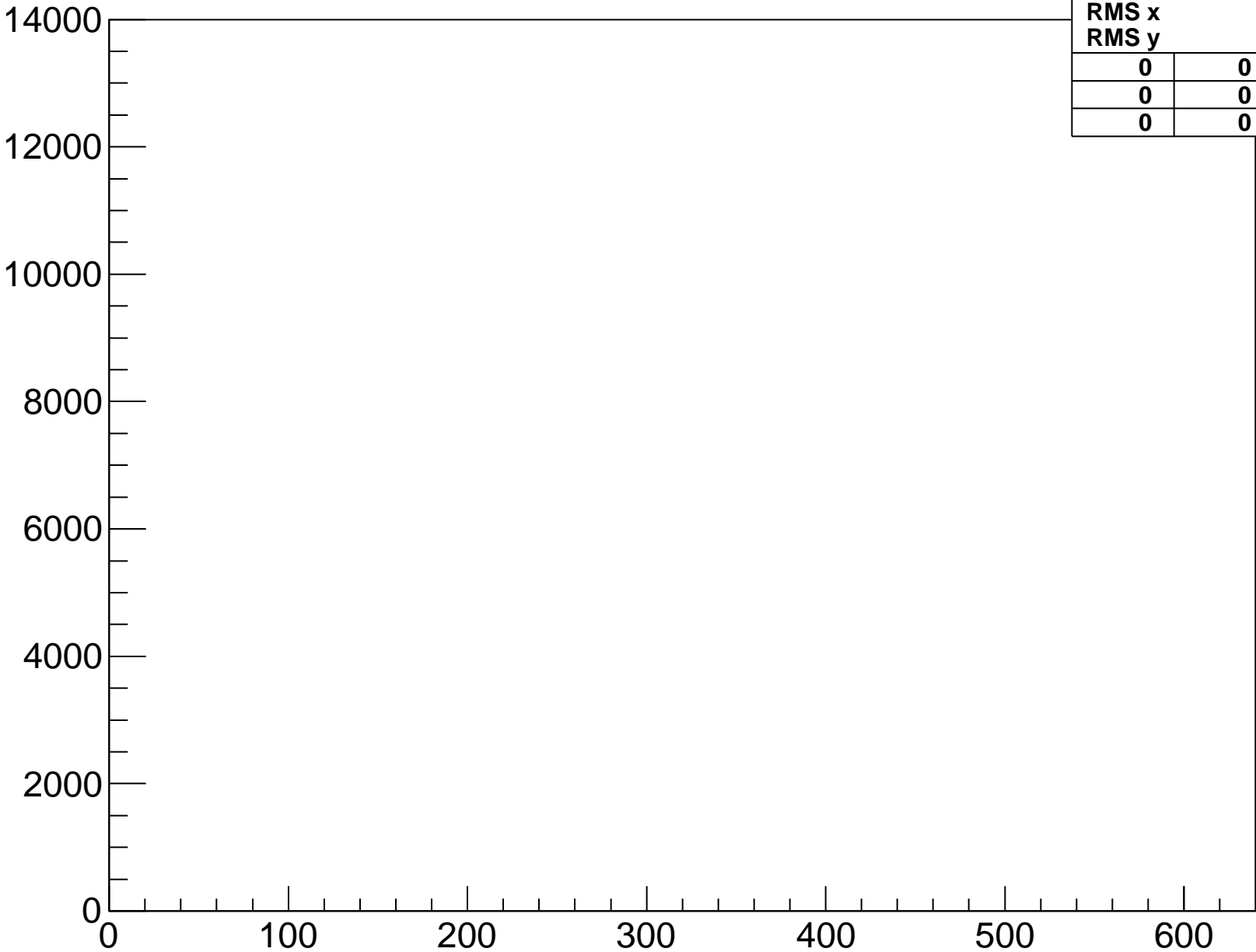


baselinesamples-fpga-2-hyb-3-sample-2

Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0



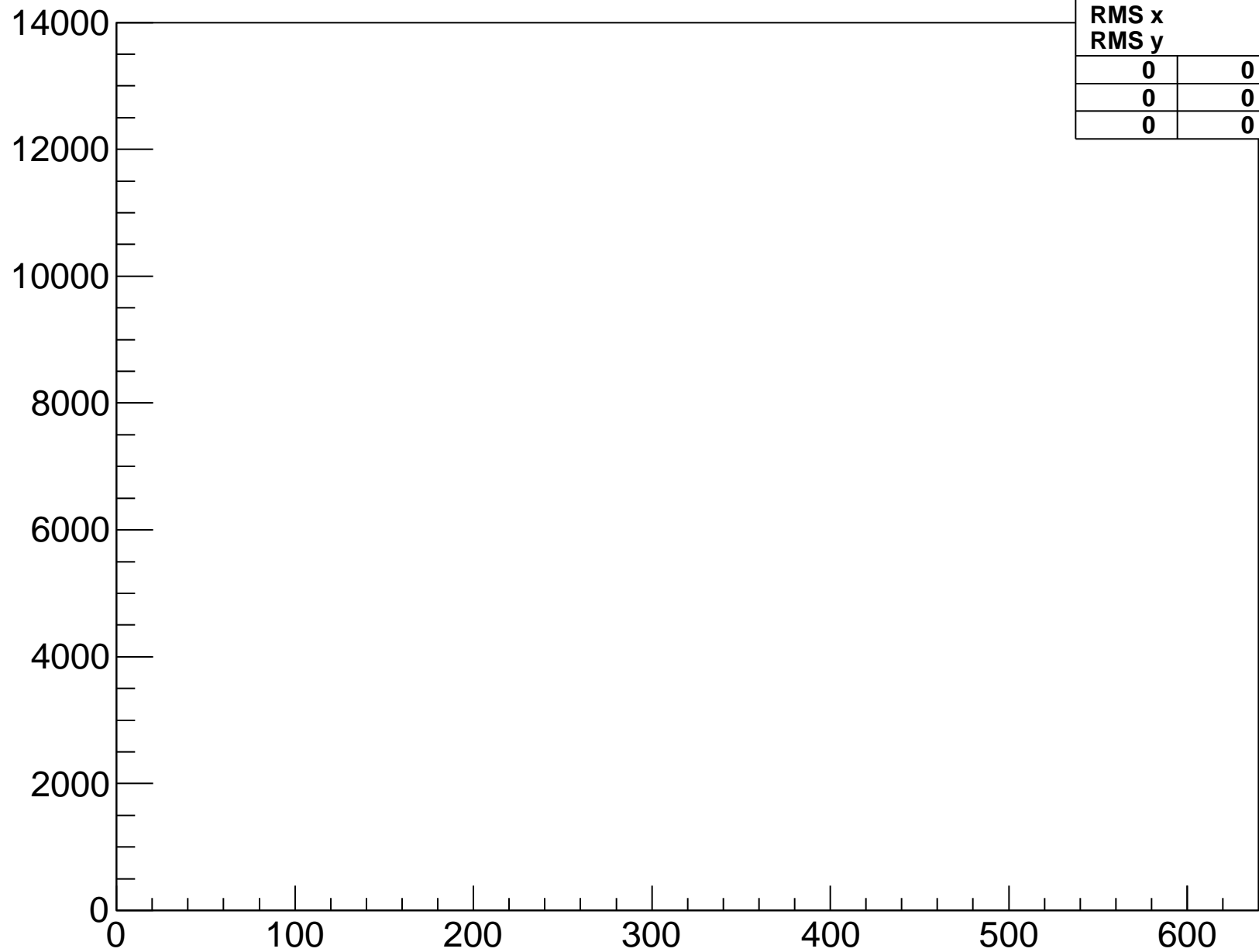
baselinesamples-fpga-2-hyb-3-sample-3



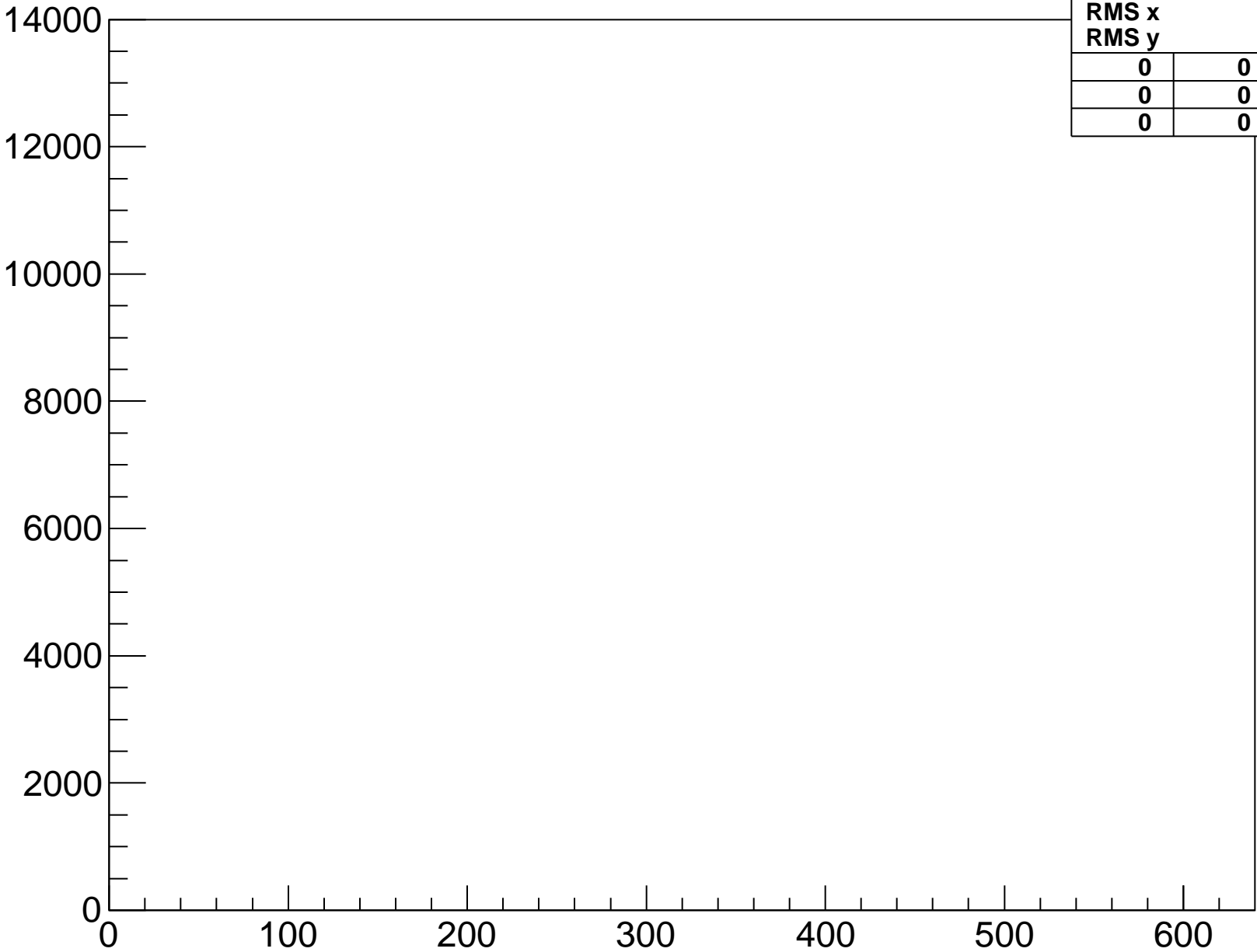
Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0

baselinesamples-fpga-2-hyb-3-sample-4

Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0



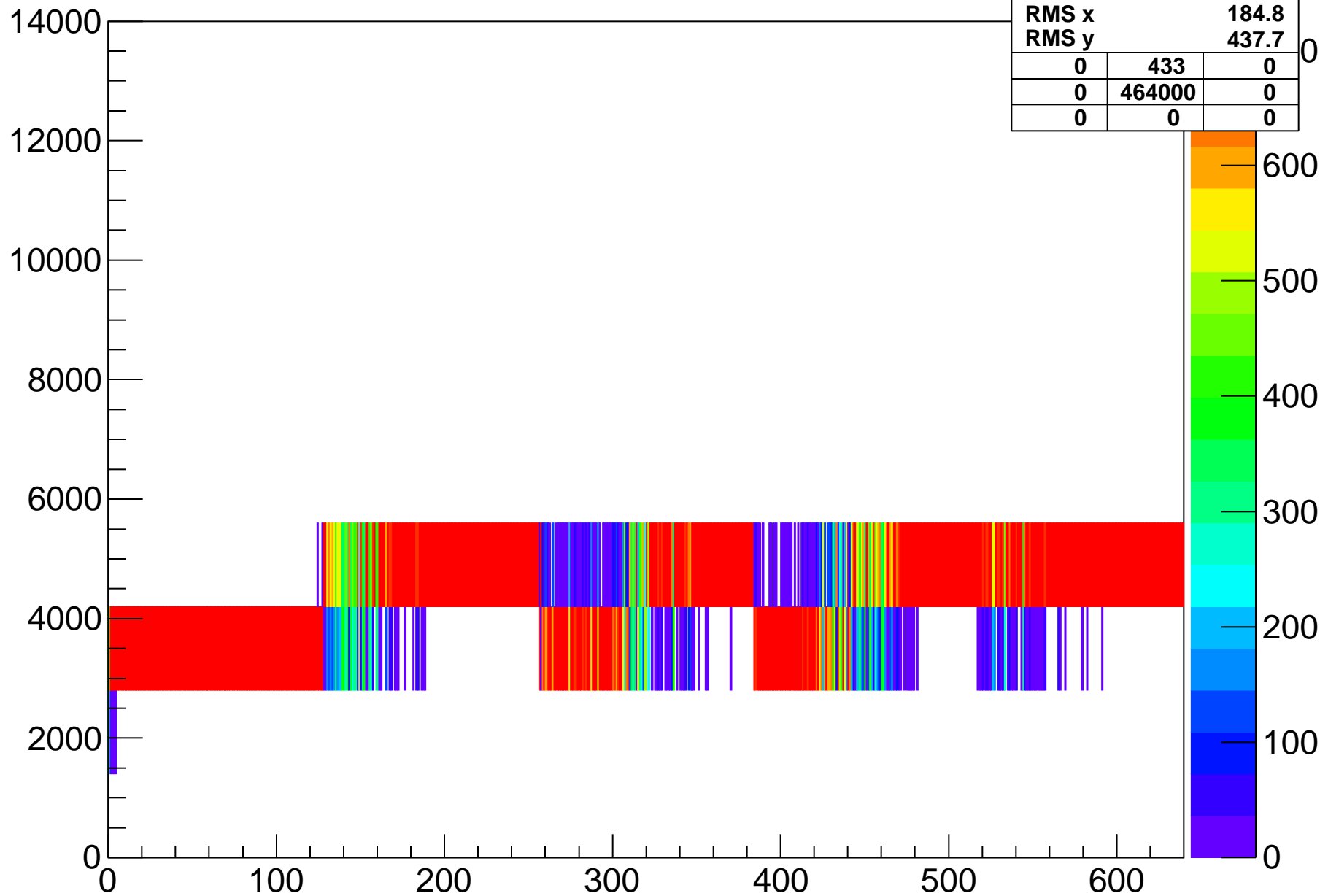
baselinesamples-fpga-2-hyb-3-sample-5



Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

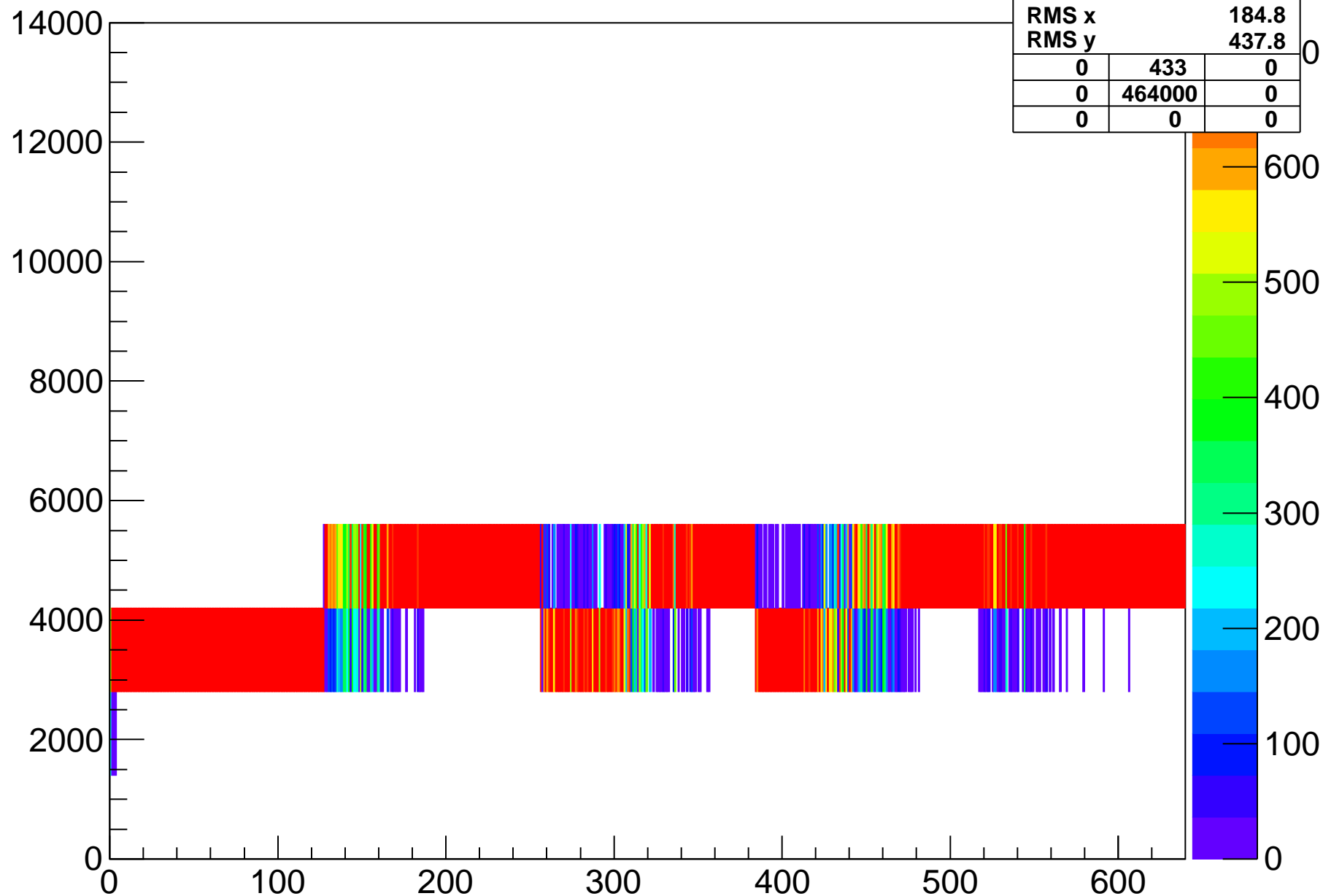
baselinesamples-fpga-3-hyb-0-sample-0

Entries	464433	
Mean x	319.5	
Mean y	4175	
RMS x	184.8	
RMS y	437.7	
0	433	0
0	464000	0
0	0	0



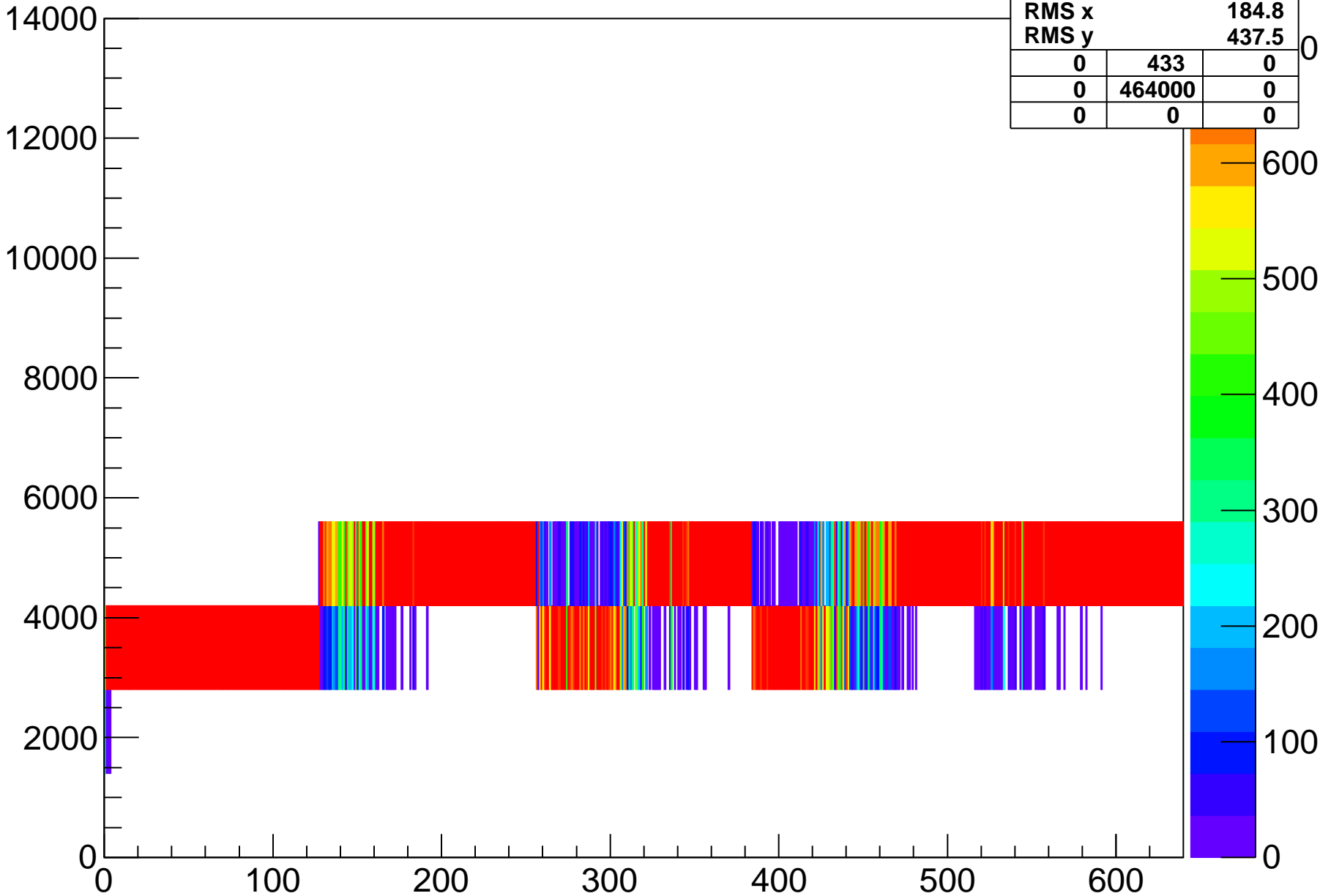
baselinesamples-fpga-3-hyb-0-sample-1

Entries	464433	
Mean x	319.5	
Mean y	4179	
RMS x	184.8	
RMS y	437.8	
0	433	0
0	464000	0
0	0	0



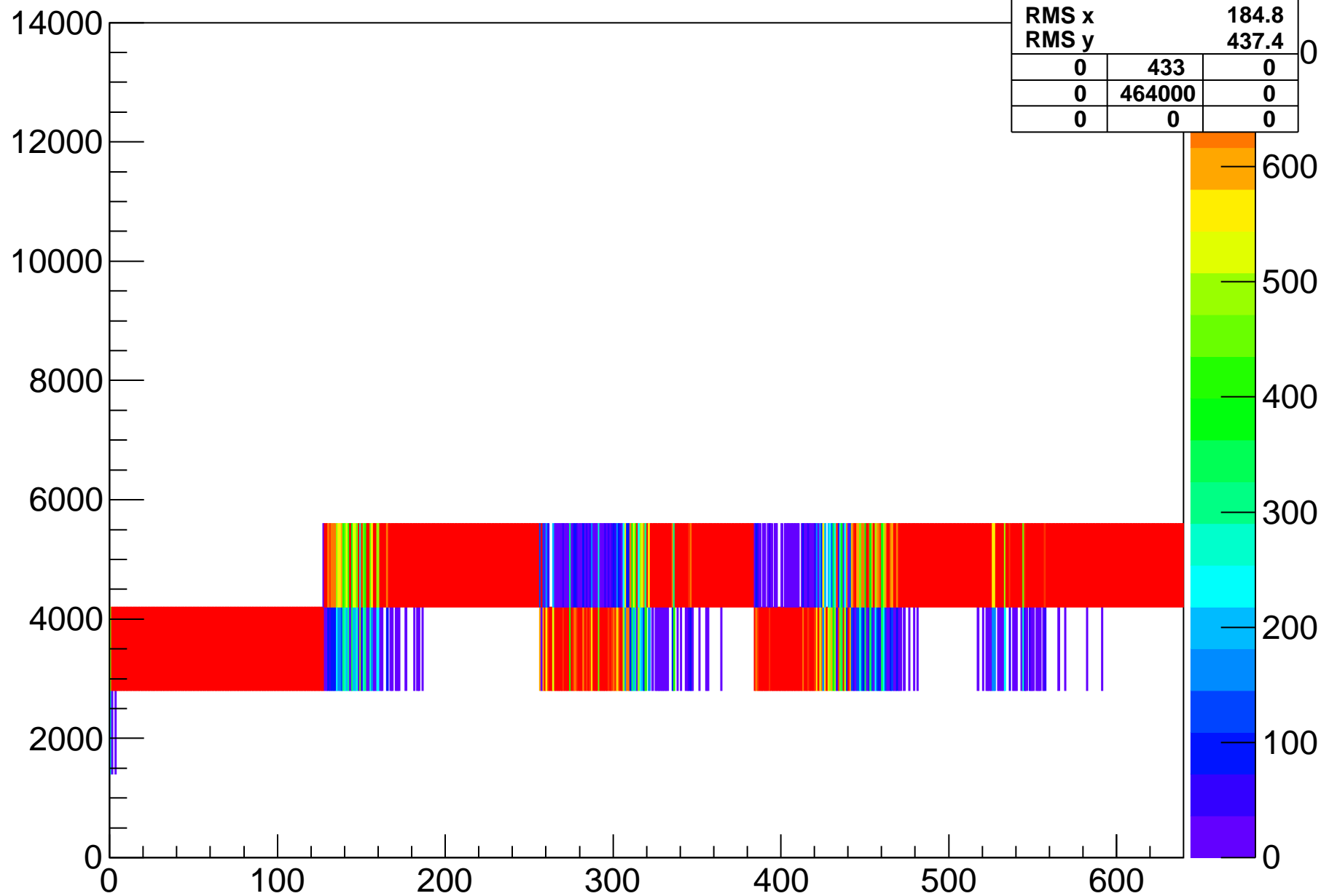
baselinesamples-fpga-3-hyb-0-sample-2

Entries	464433	
Mean x	319.5	
Mean y	4189	
RMS x	184.8	
RMS y	437.5	
0	433	0
0	464000	0
0	0	0



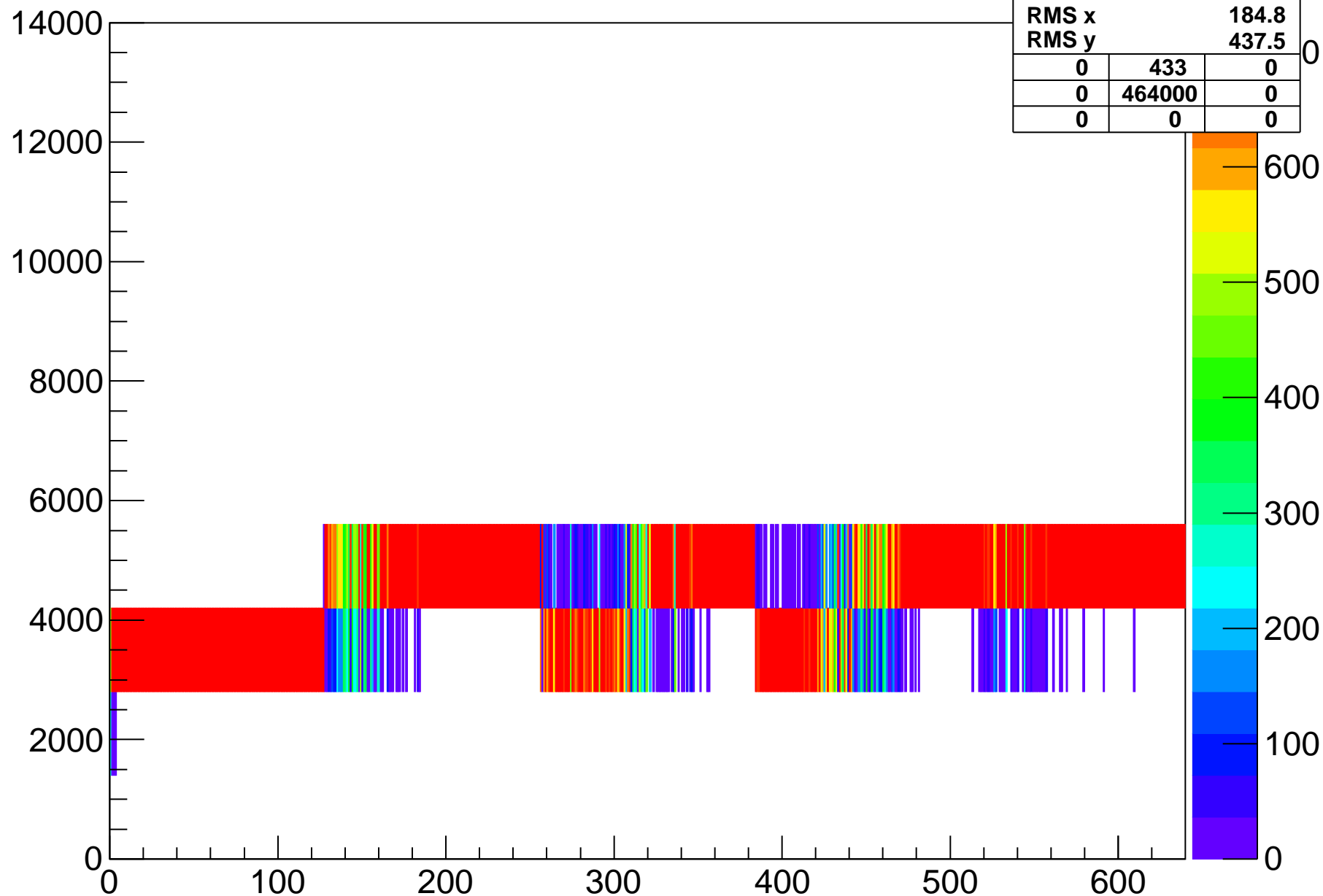
baselinesamples-fpga-3-hyb-0-sample-3

Entries	464433	
Mean x	319.5	
Mean y	4188	
RMS x	184.8	
RMS y	437.4	
0	433	0
0	464000	0
0	0	0



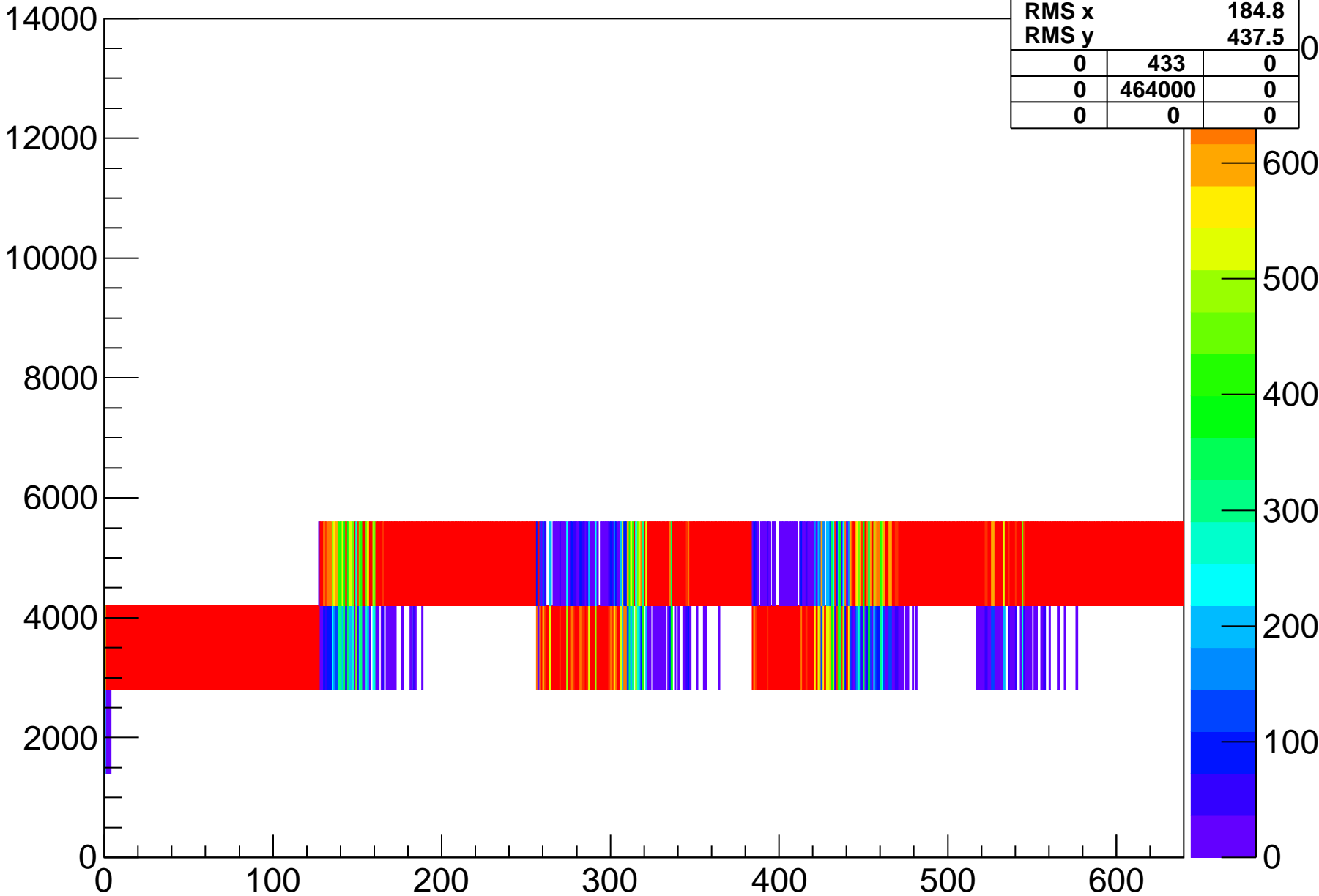
baselinesamples-fpga-3-hyb-0-sample-4

Entries	464433	
Mean x	319.5	
Mean y	4182	
RMS x	184.8	
RMS y	437.5	
0	433	0
0	464000	0
0	0	0



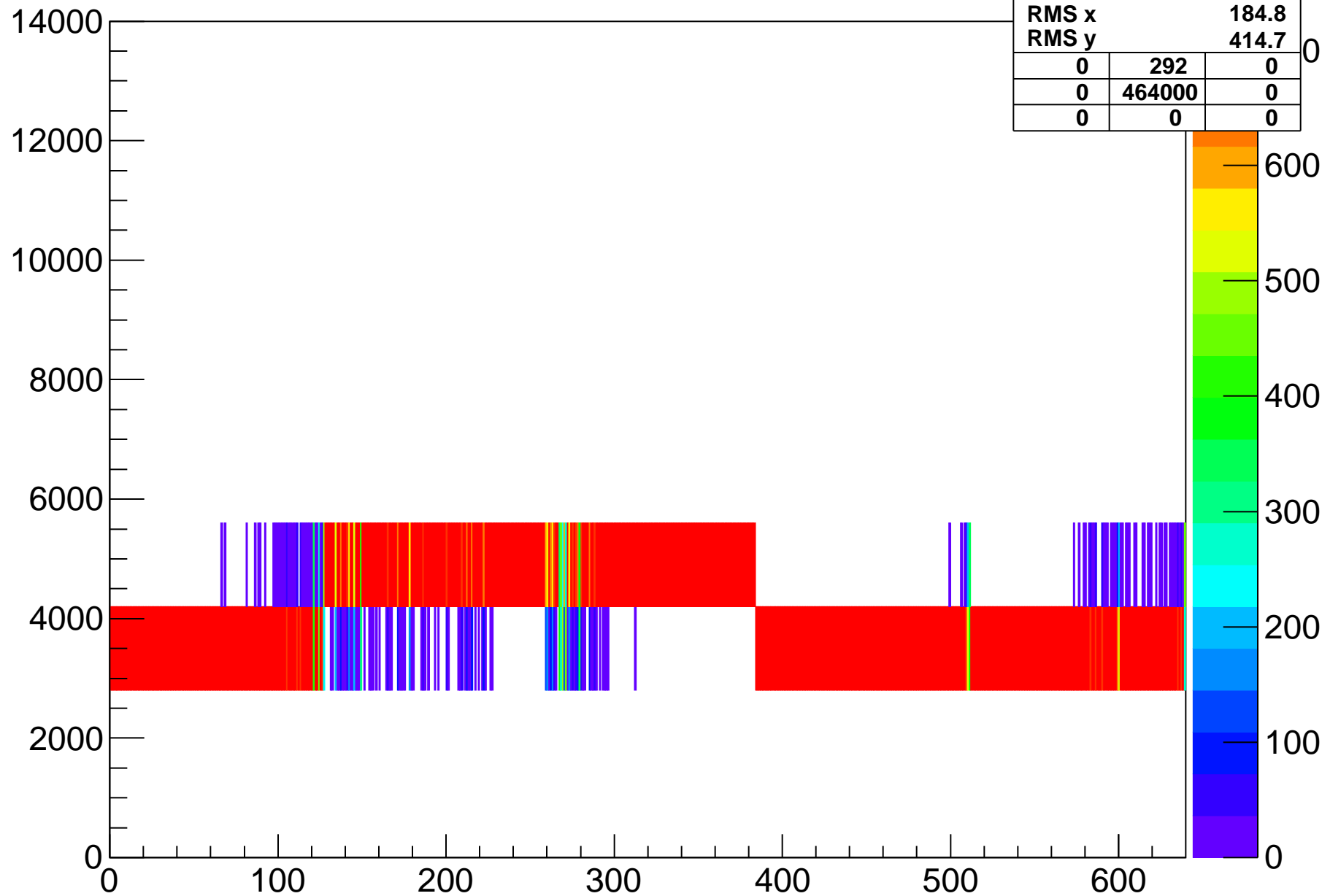
baselinesamples-fpga-3-hyb-0-sample-5
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Entries	464433	
Mean x	319.5	
Mean y	4188	
RMS x	184.8	
RMS y	437.5	
0	433	0
0	464000	0
0	0	0



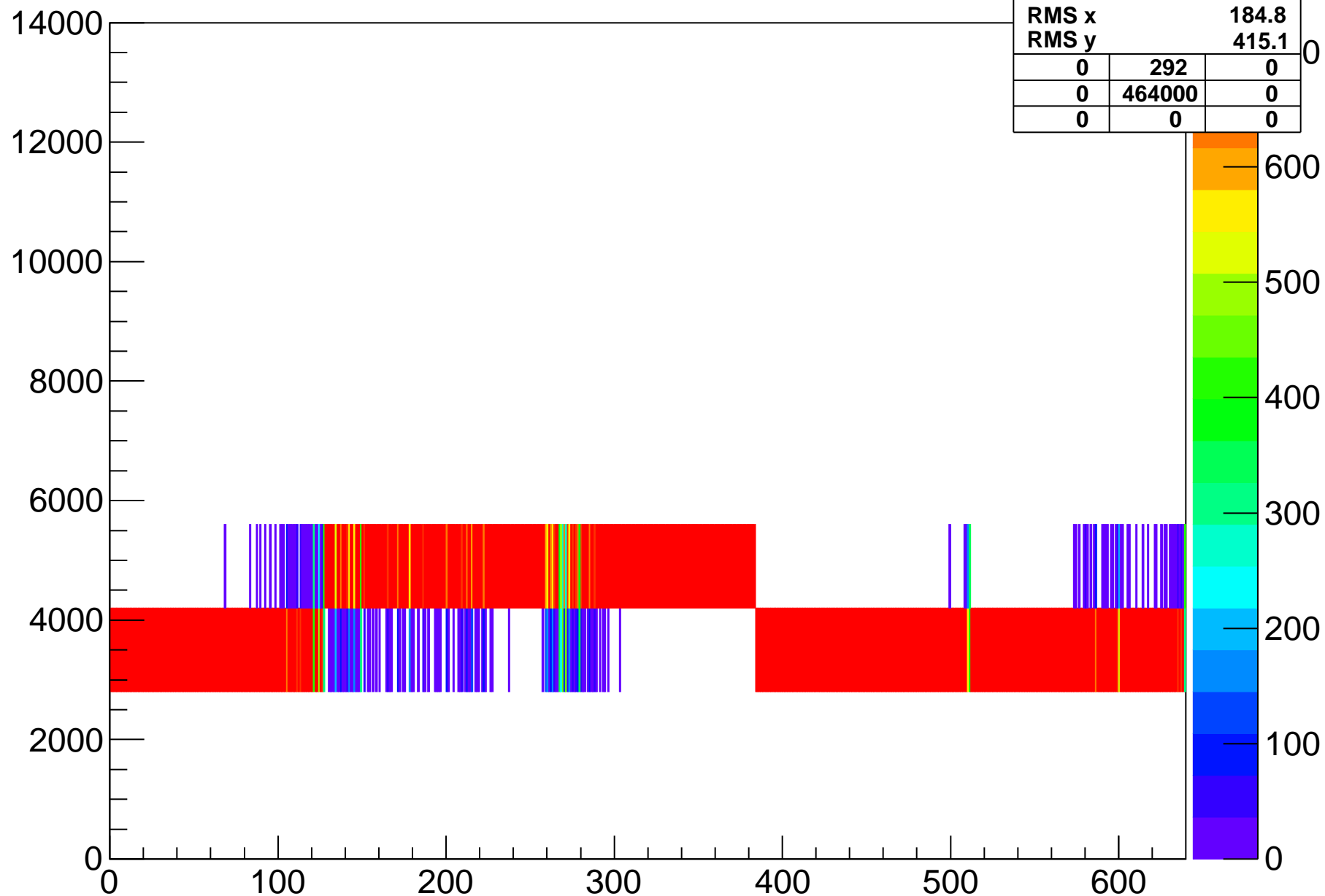
baselinesamples-fpga-3-hyb-1-sample-0

Entries	464292	
Mean x	319.5	
Mean y	4062	
RMS x	184.8	
RMS y	414.7	
0	292	0
0	464000	0
0	0	0



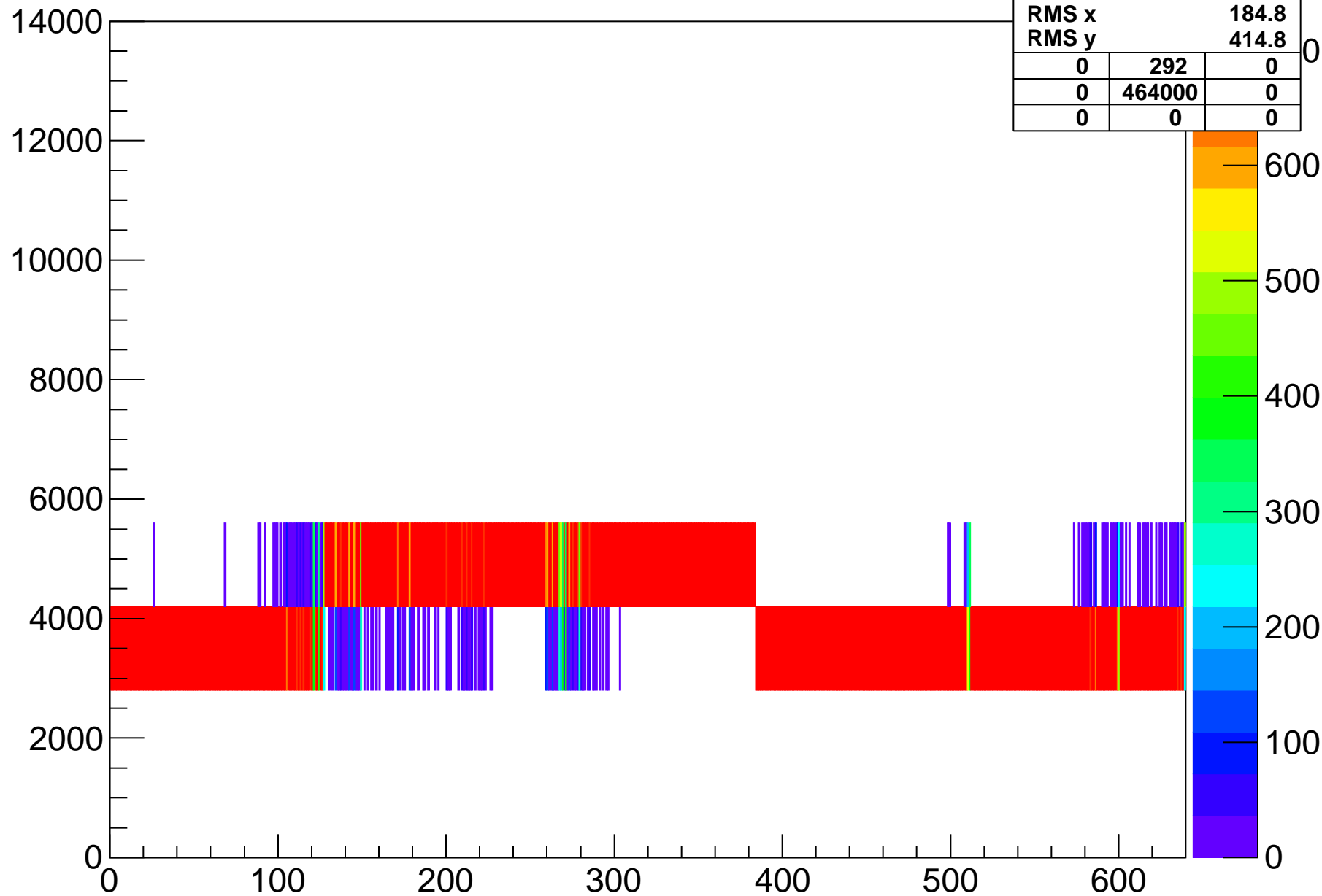
baselinesamples-fpga-3-hyb-1-sample-1

Entries	464292	
Mean x	319.5	
Mean y	4059	
RMS x	184.8	
RMS y	415.1	
0	292	0
0	464000	0
0	0	0



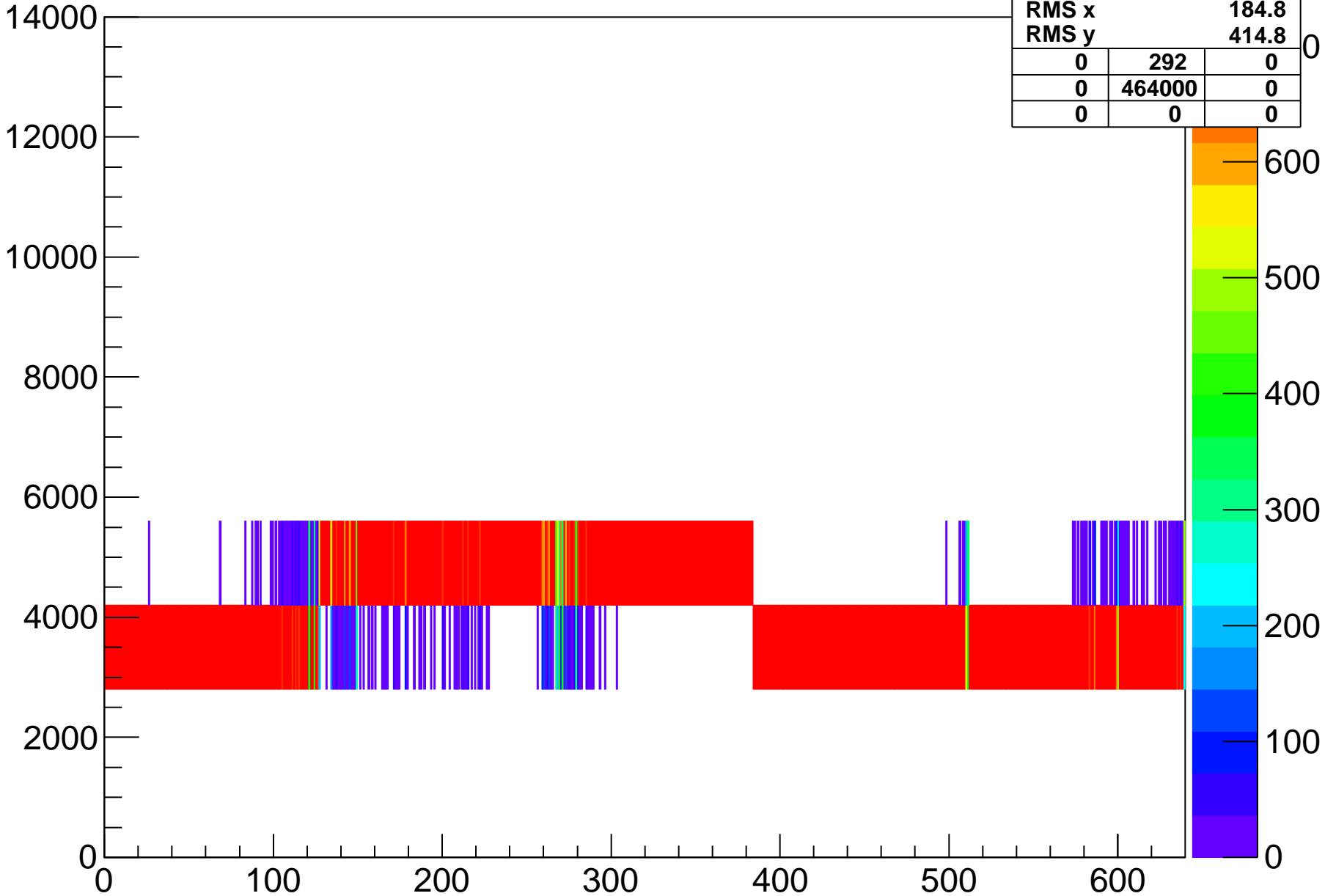
baselinesamples-fpga-3-hyb-1-sample-2

Entries	464292	
Mean x	319.5	
Mean y	4072	
RMS x	184.8	
RMS y	414.8	
0	292	0
0	464000	0
0	0	0



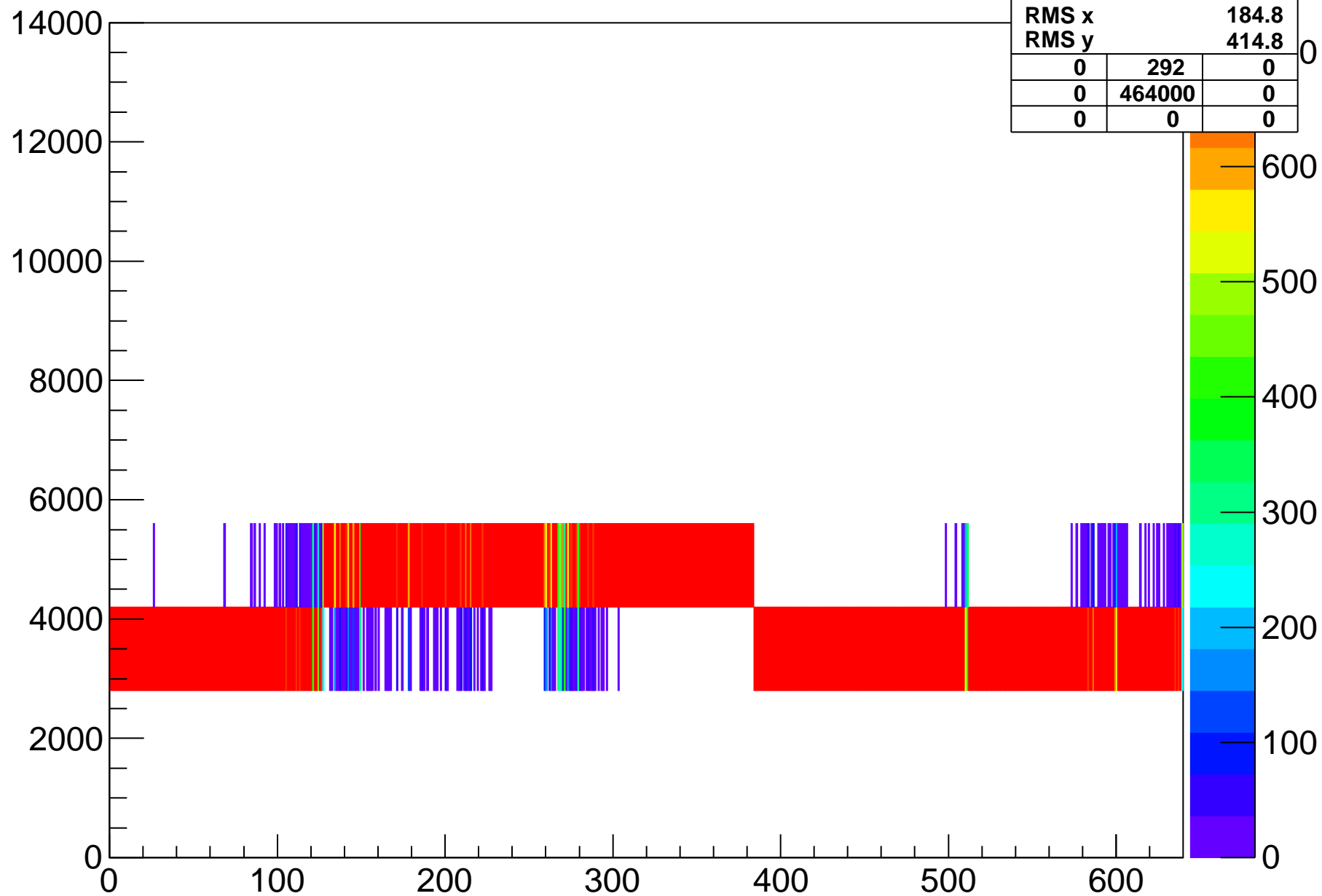
baselinesamples-fpga-3-hyb-1-sample-3

Entries	464292	
Mean x	319.5	
Mean y	4070	
RMS x	184.8	
RMS y	414.8	
0	292	0
0	464000	0
0	0	0



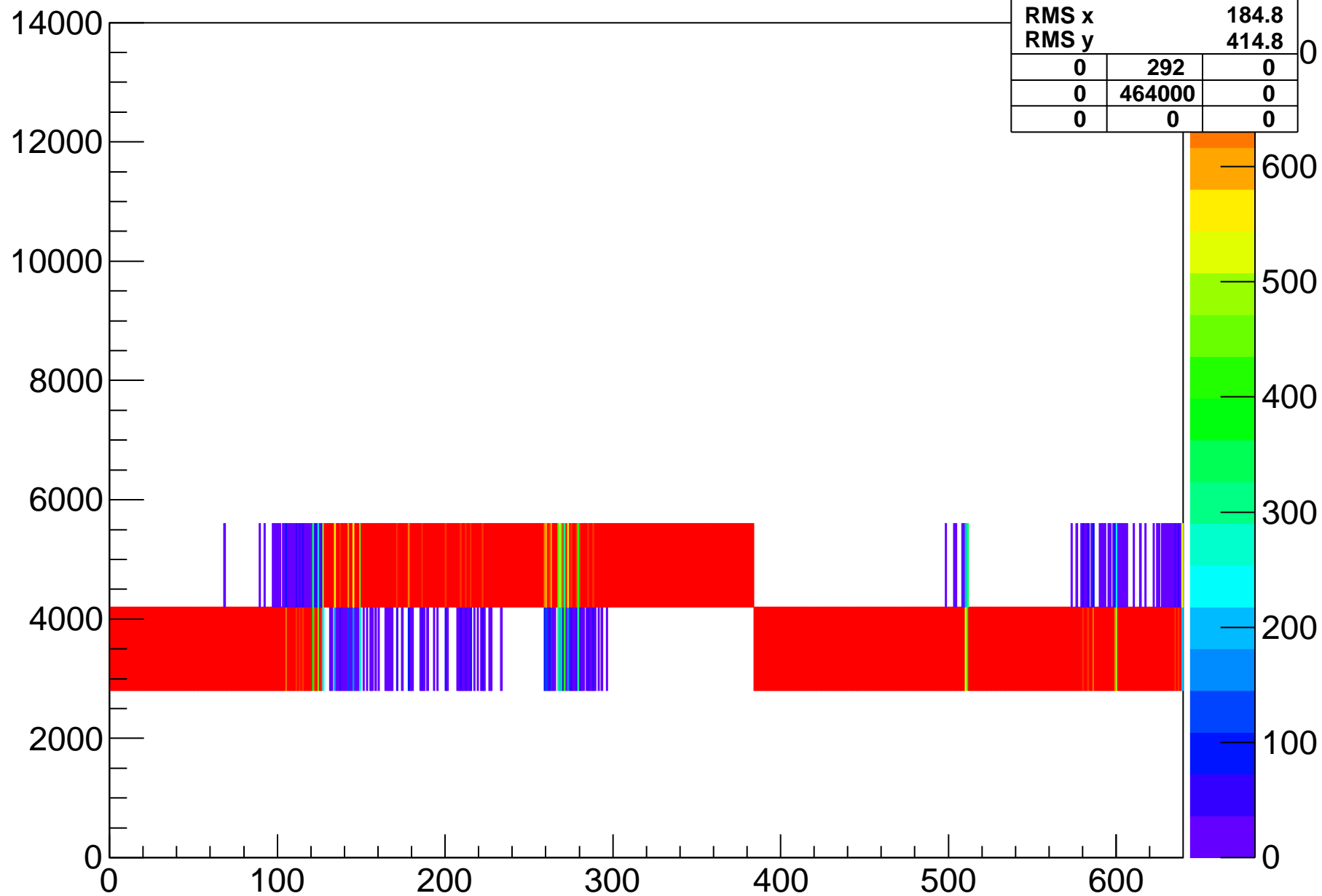
baselinesamples-fpga-3-hyb-1-sample-4

Entries	464292	
Mean x	319.5	
Mean y	4065	
RMS x	184.8	
RMS y	414.8	
0	292	0
0	464000	0
0	0	0



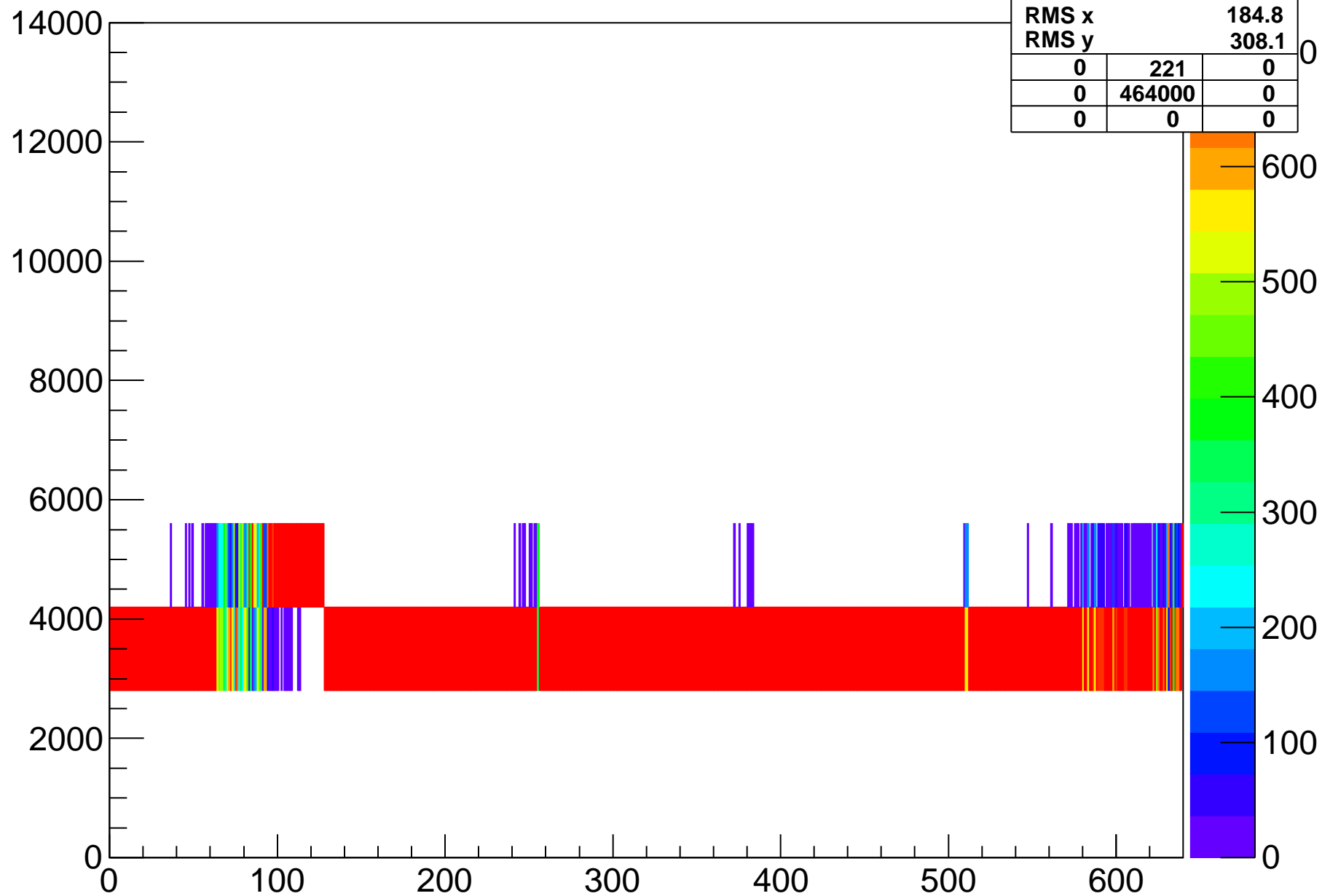
baselinesamples-fpga-3-hyb-1-sample-5

Entries	464292	
Mean x	319.5	
Mean y	4070	
RMS x	184.8	
RMS y	414.8	
0	292	0
0	464000	0
0	0	0



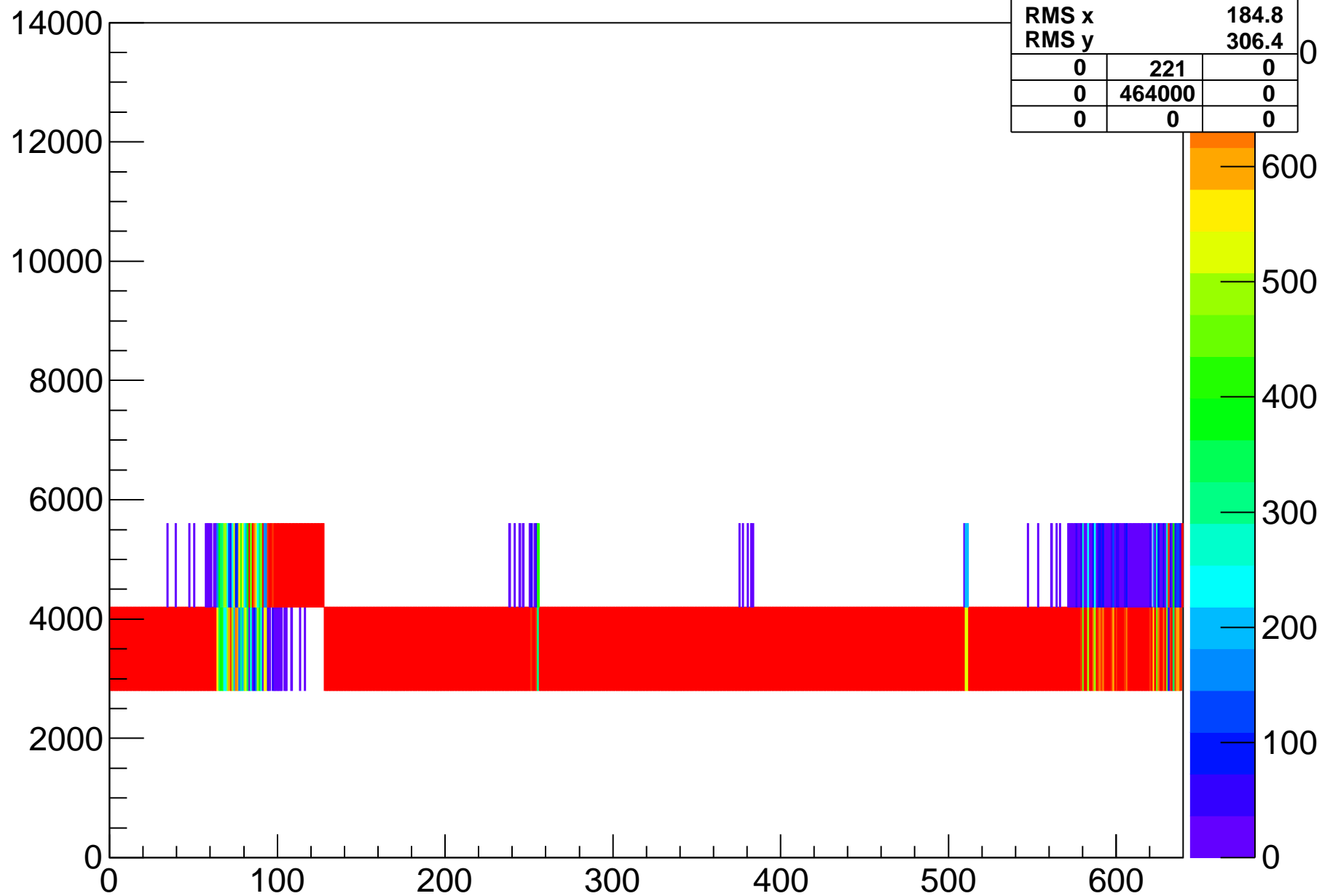
baselinesamples-fpga-3-hyb-2-sample-0

Entries	464221	
Mean x	319.5	
Mean y	3779	
RMS x	184.8	
RMS y	308.1	
0	221	0
0	464000	0
0	0	0



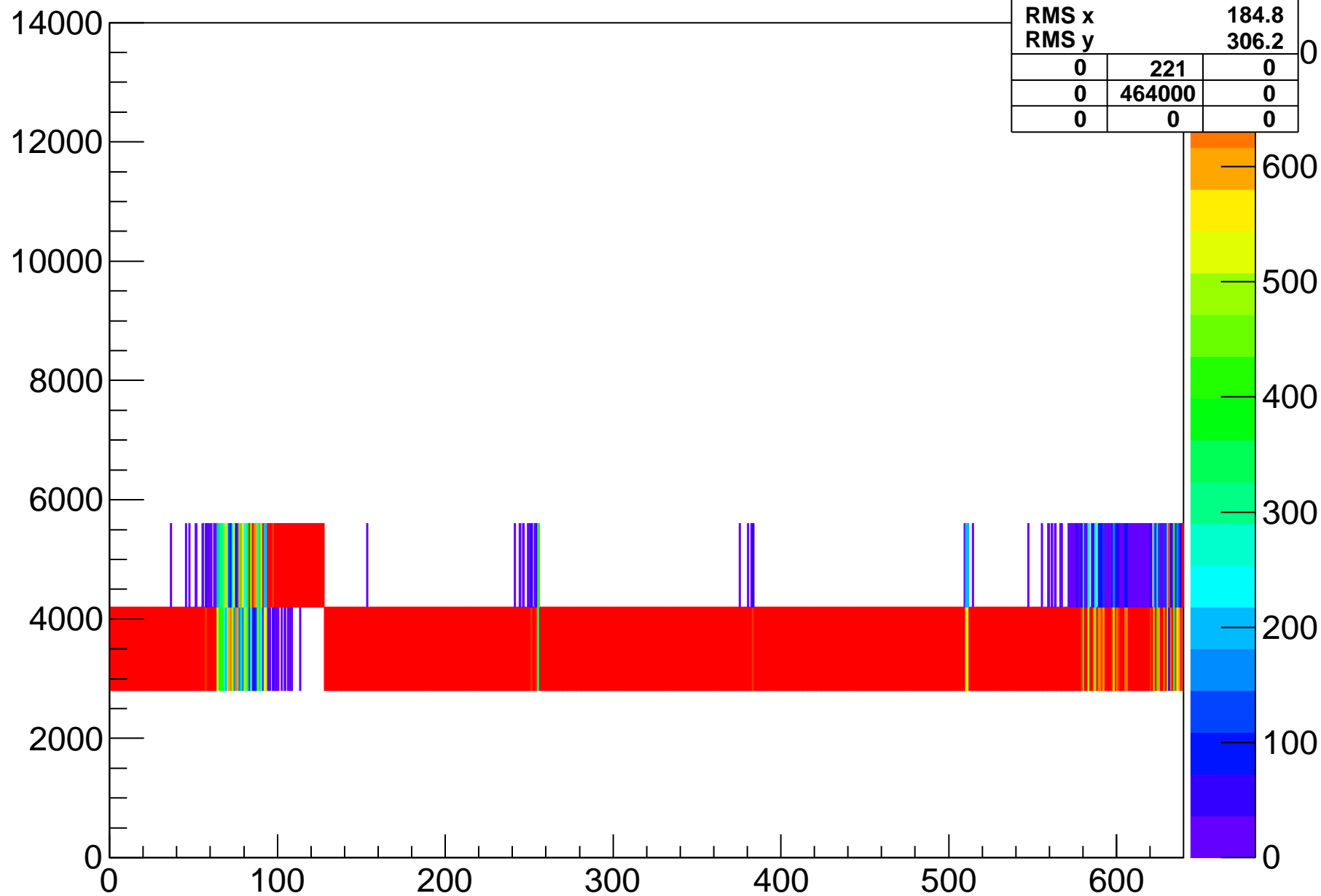
baselinesamples-fpga-3-hyb-2-sample-1

Entries	464221	
Mean x	319.5	
Mean y	3793	
RMS x	184.8	
RMS y	306.4	
0	221	0
0	464000	0
0	0	0



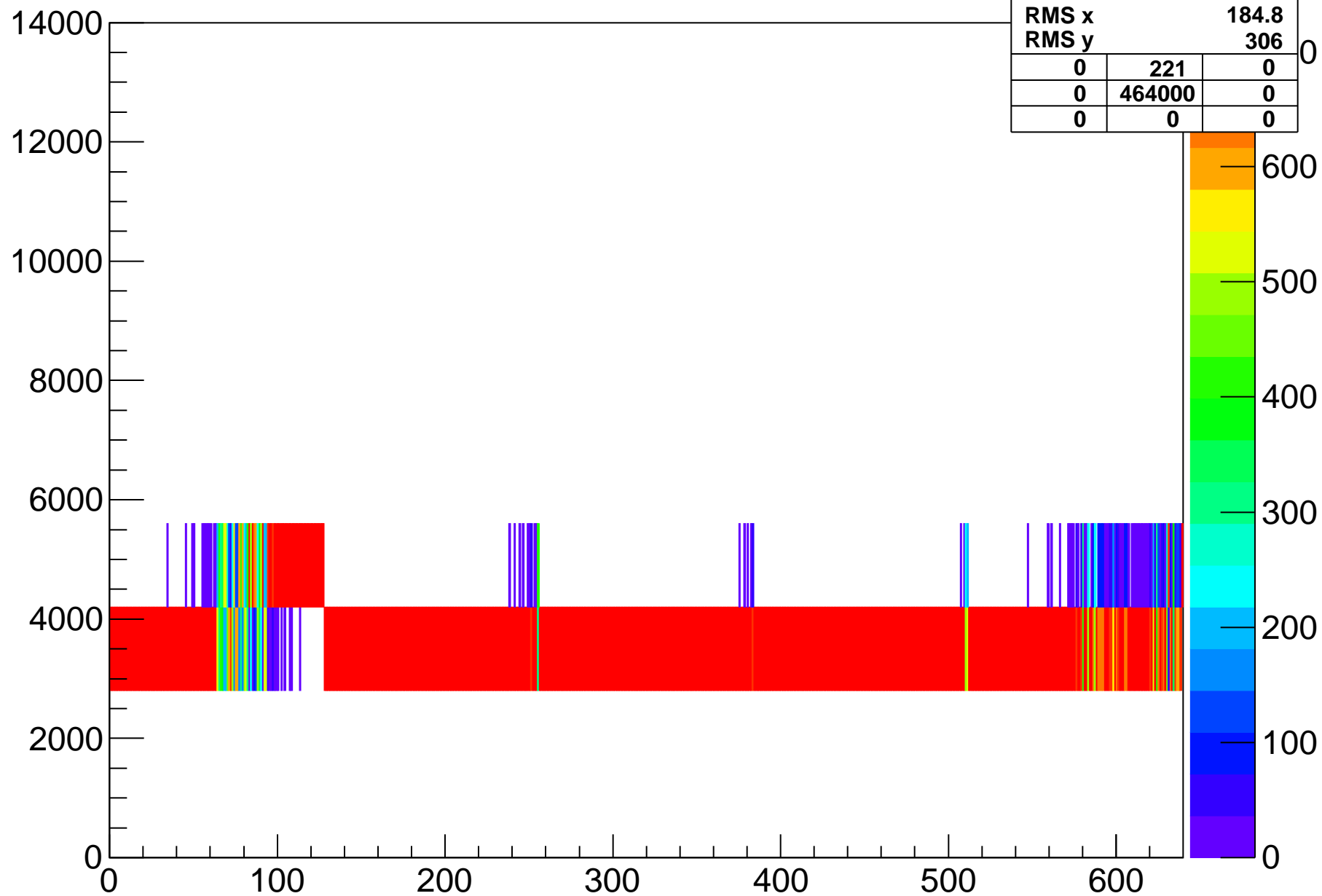
baselinesamples-fpga-3-hyb-2-sample-2

Entries	464221	
Mean x	319.5	
Mean y	3798	
RMS x	184.8	
RMS y	306.2	
0	221	0
0	464000	0
0	0	0



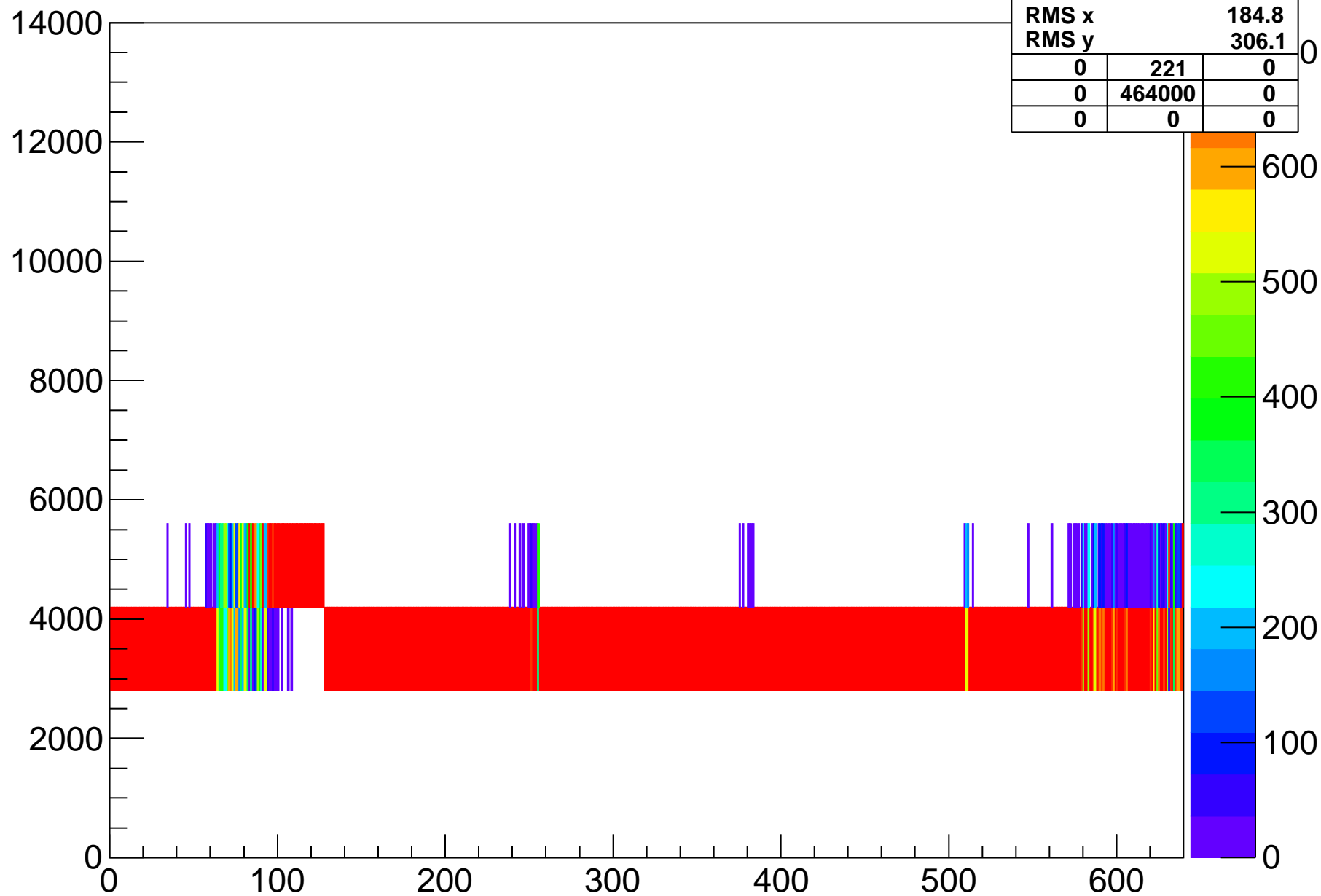
baselinesamples-fpga-3-hyb-2-sample-3

Entries	464221	
Mean x	319.5	
Mean y	3799	
RMS x	184.8	
RMS y	306	
0	221	0
0	464000	0
0	0	0



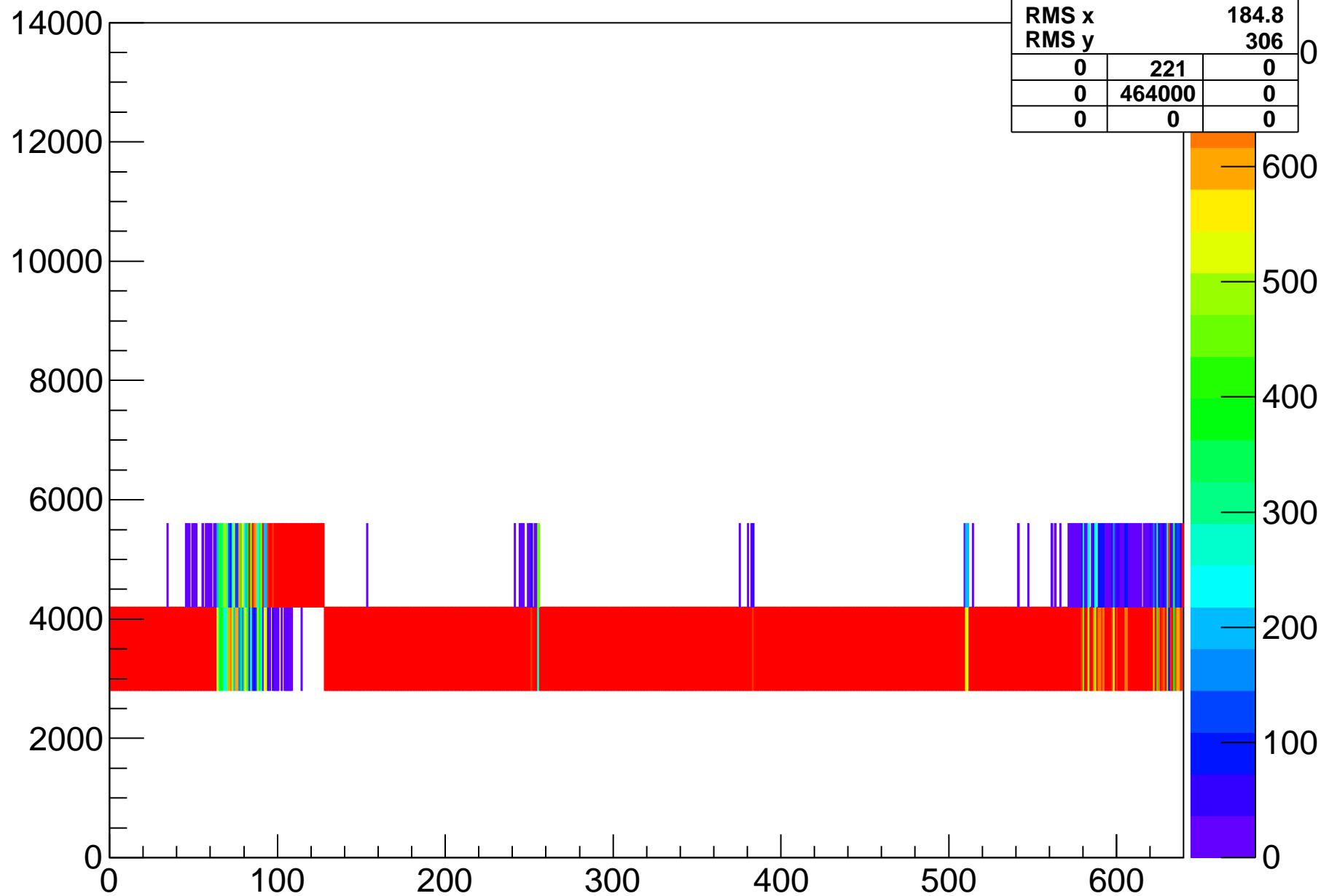
baselinesamples-fpga-3-hyb-2-sample-4

Entries	464221	
Mean x	319.5	
Mean y	3793	
RMS x	184.8	
RMS y	306.1	
0	221	0
0	464000	0
0	0	0



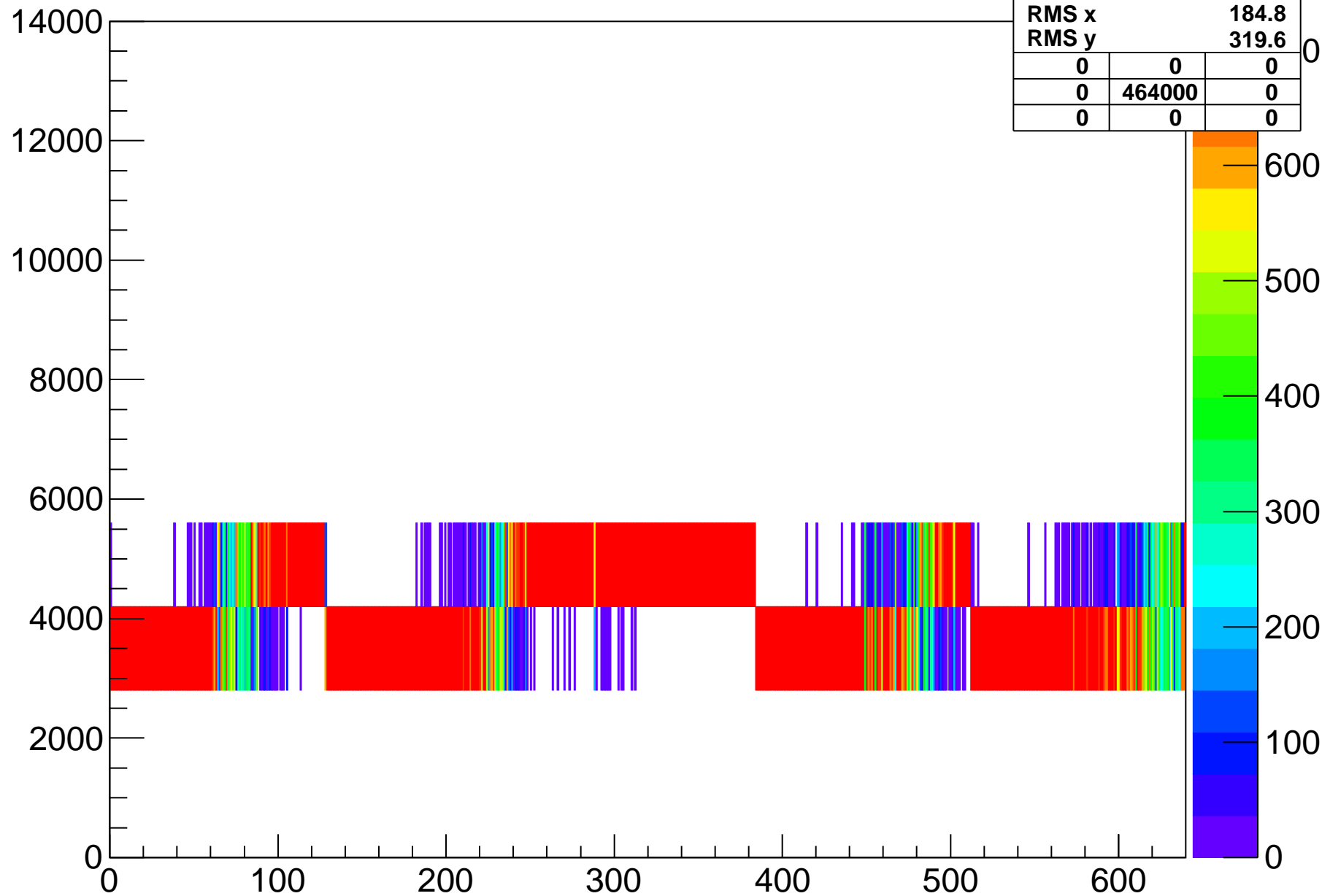
baselinesamples-fpga-3-hyb-2-sample-5

Entries	464221	
Mean x	319.5	
Mean y	3797	
RMS x	184.8	
RMS y	306	
0	221	0
0	464000	0
0	0	0



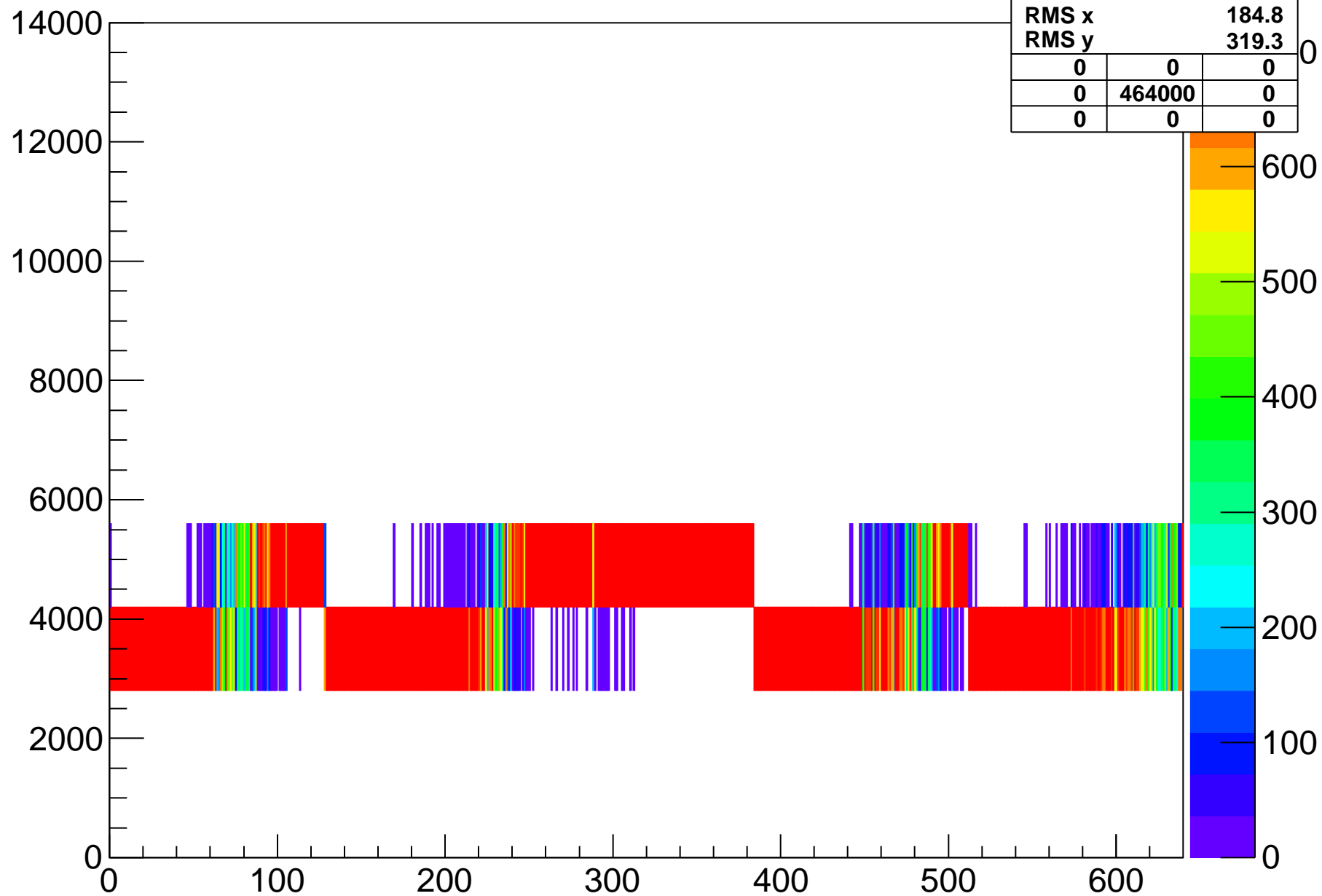
baselinesamples-fpga-3-hyb-3-sample-0

Entries	464000	
Mean x	319.5	
Mean y	4165	
RMS x	184.8	
RMS y	319.6	
0	0	0
0	464000	0
0	0	0



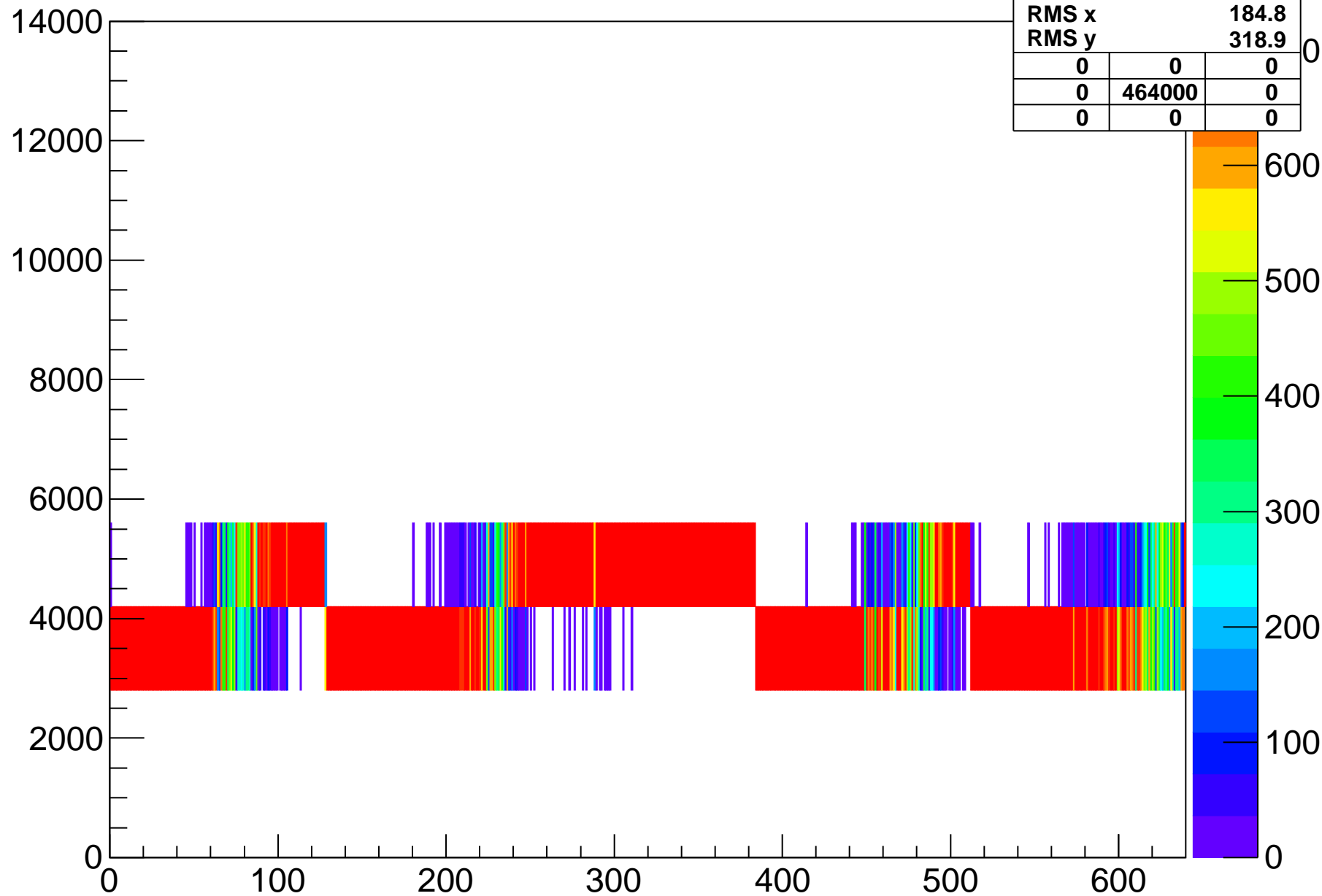
baselinesamples-fpga-3-hyb-3-sample-1

Entries	464000	
Mean x	319.5	
Mean y	4161	
RMS x	184.8	
RMS y	319.3	
0	0	0
0	464000	0
0	0	0



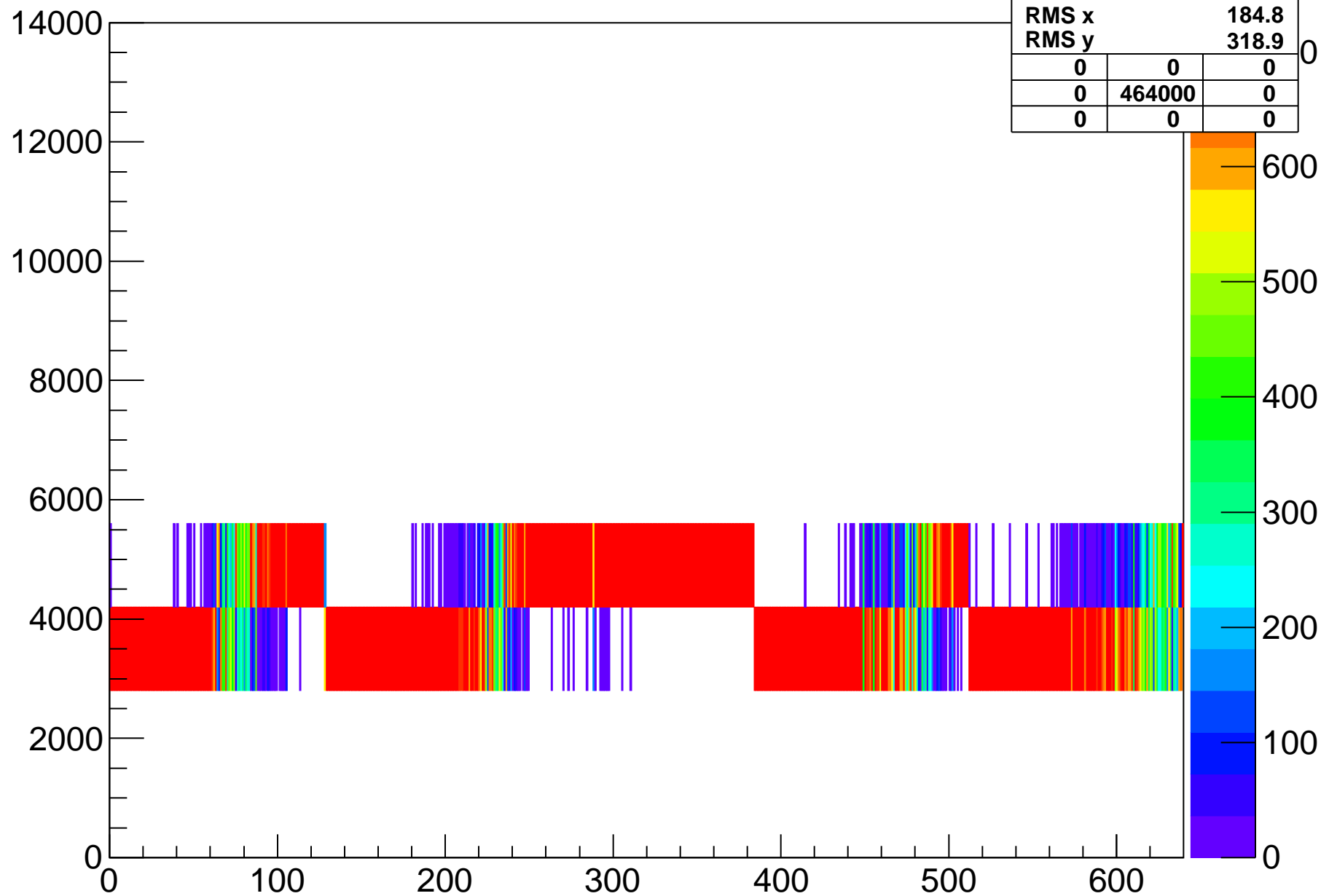
baselinesamples-fpga-3-hyb-3-sample-2

Entries	464000	
Mean x	319.5	
Mean y	4175	
RMS x	184.8	
RMS y	318.9	
0	0	0
0	464000	0
0	0	0



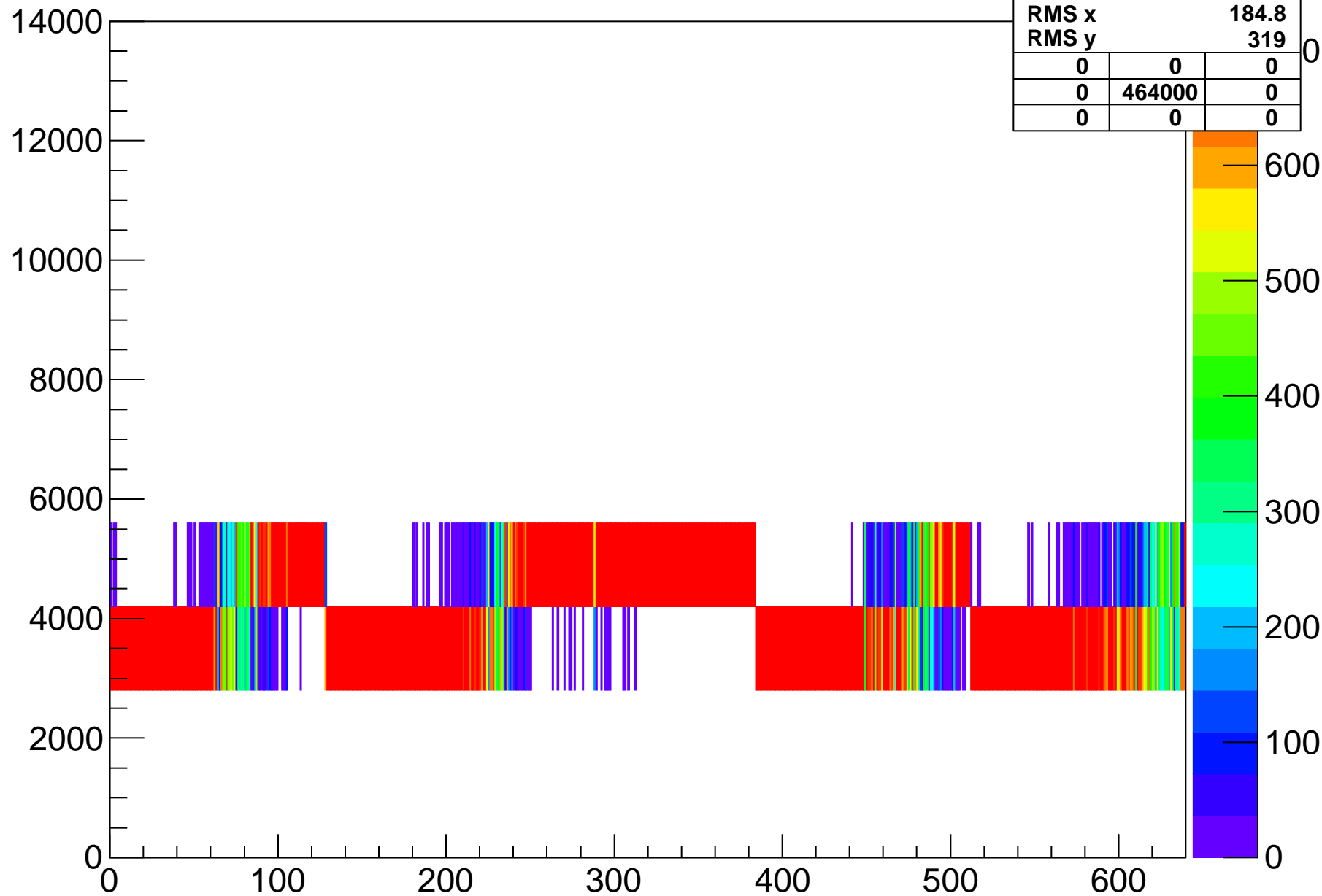
baselinesamples-fpga-3-hyb-3-sample-3

Entries	464000	
Mean x	319.5	
Mean y	4173	
RMS x	184.8	
RMS y	318.9	
0	0	0
0	464000	0
0	0	0



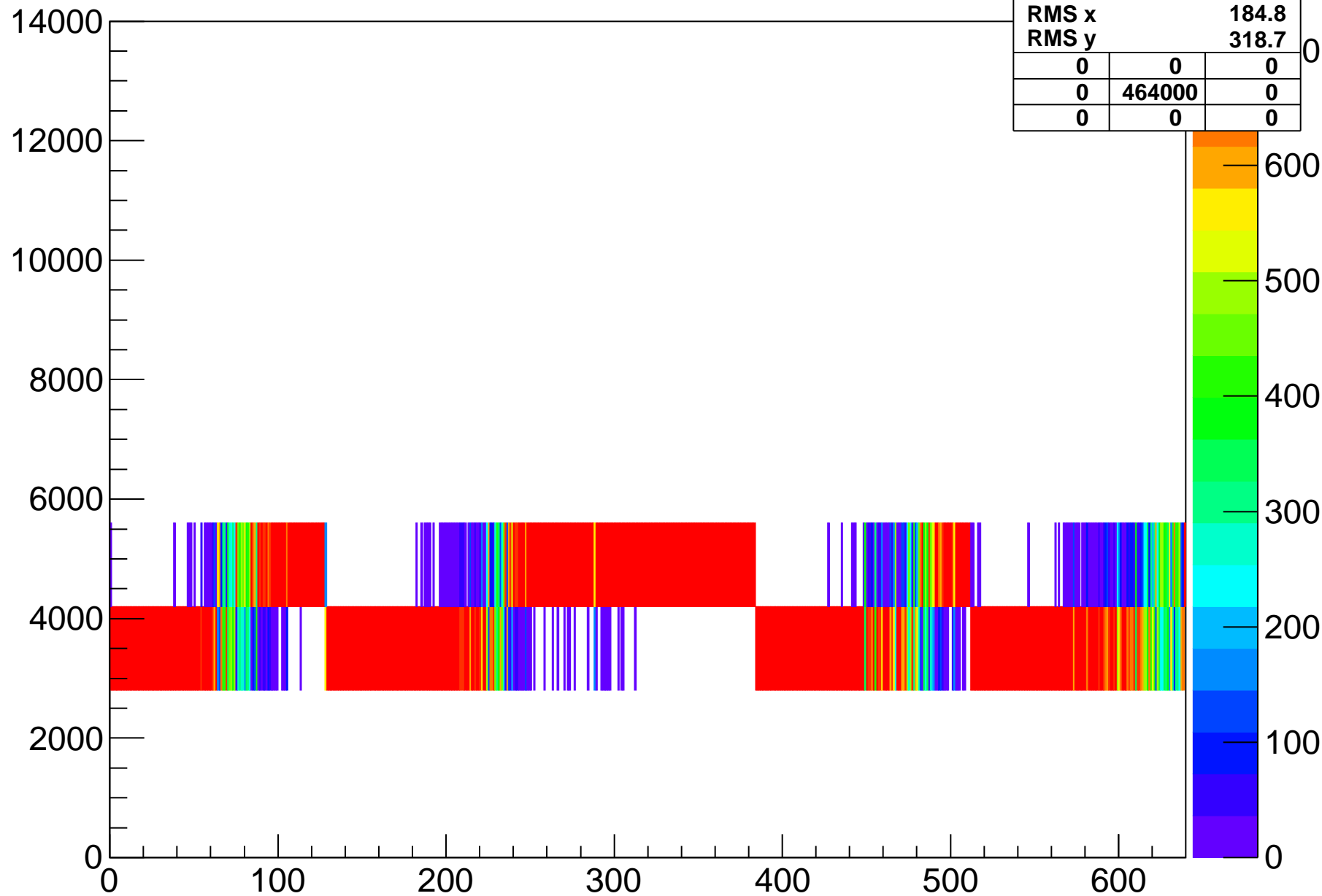
baselinesamples-fpga-3-hyb-3-sample-4

Entries	464000	
Mean x	319.5	
Mean y	4166	
RMS x	184.8	
RMS y	319	
0	0	0
0	464000	0
0	0	0



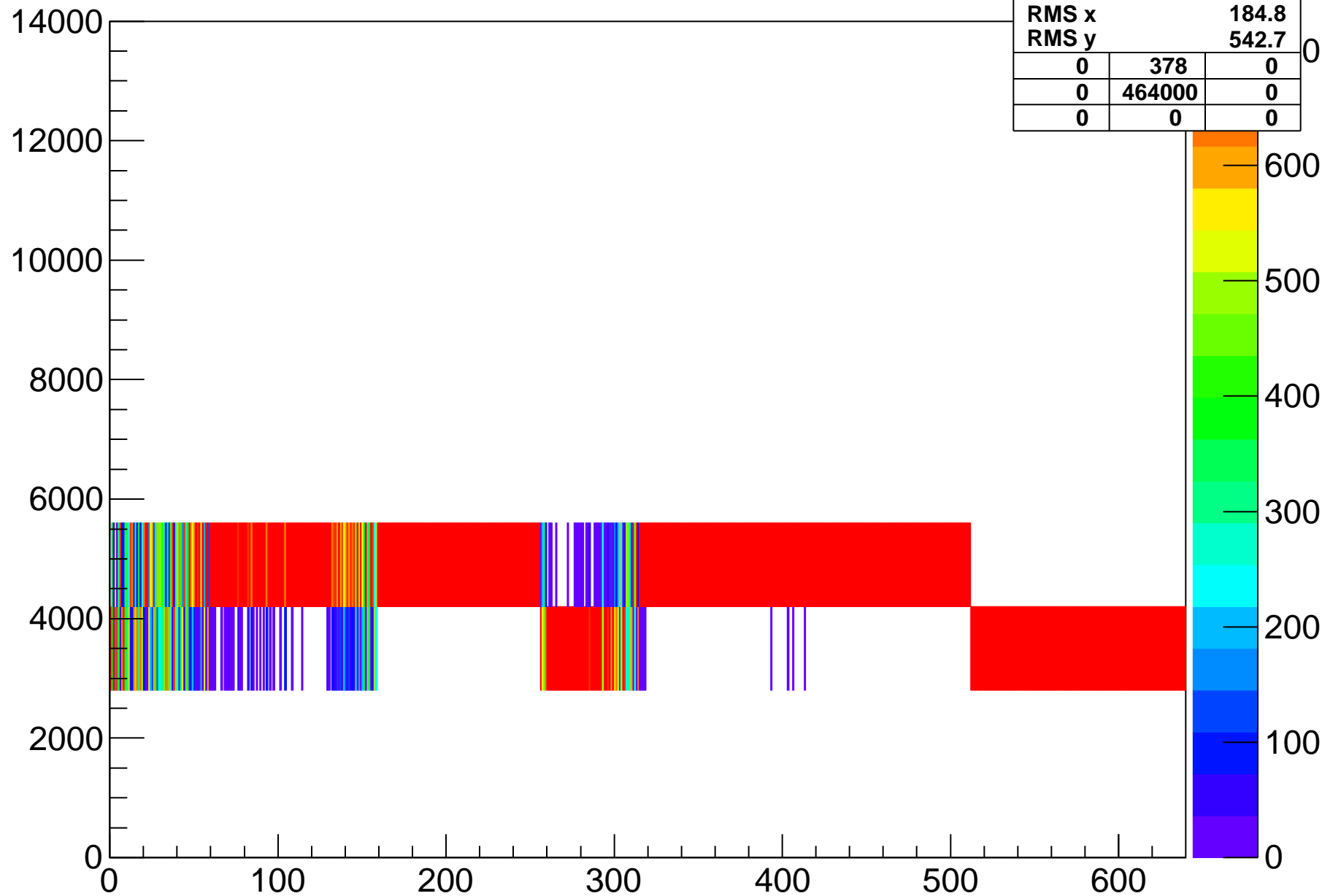
baselinesamples-fpga-3-hyb-3-sample-5

Entries	464000	
Mean x	319.5	
Mean y	4172	
RMS x	184.8	
RMS y	318.7	
0	0	0
0	464000	0
0	0	0



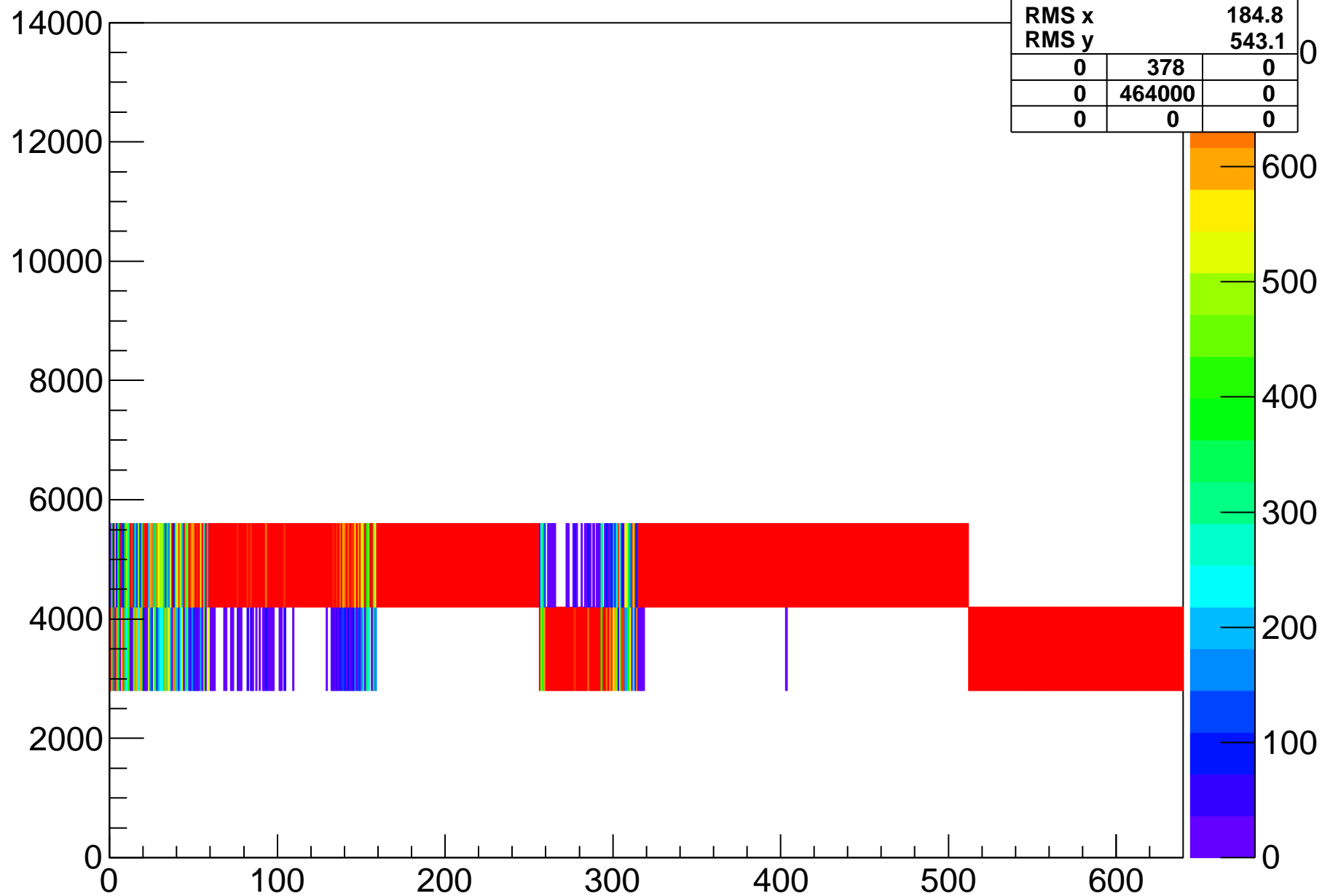
baselinesamples-fpga-4-hyb-0-sample-0

Entries	464378	
Mean x	319.5	
Mean y	4217	
RMS x	184.8	
RMS y	542.7	
0	378	0
0	464000	0
0	0	0



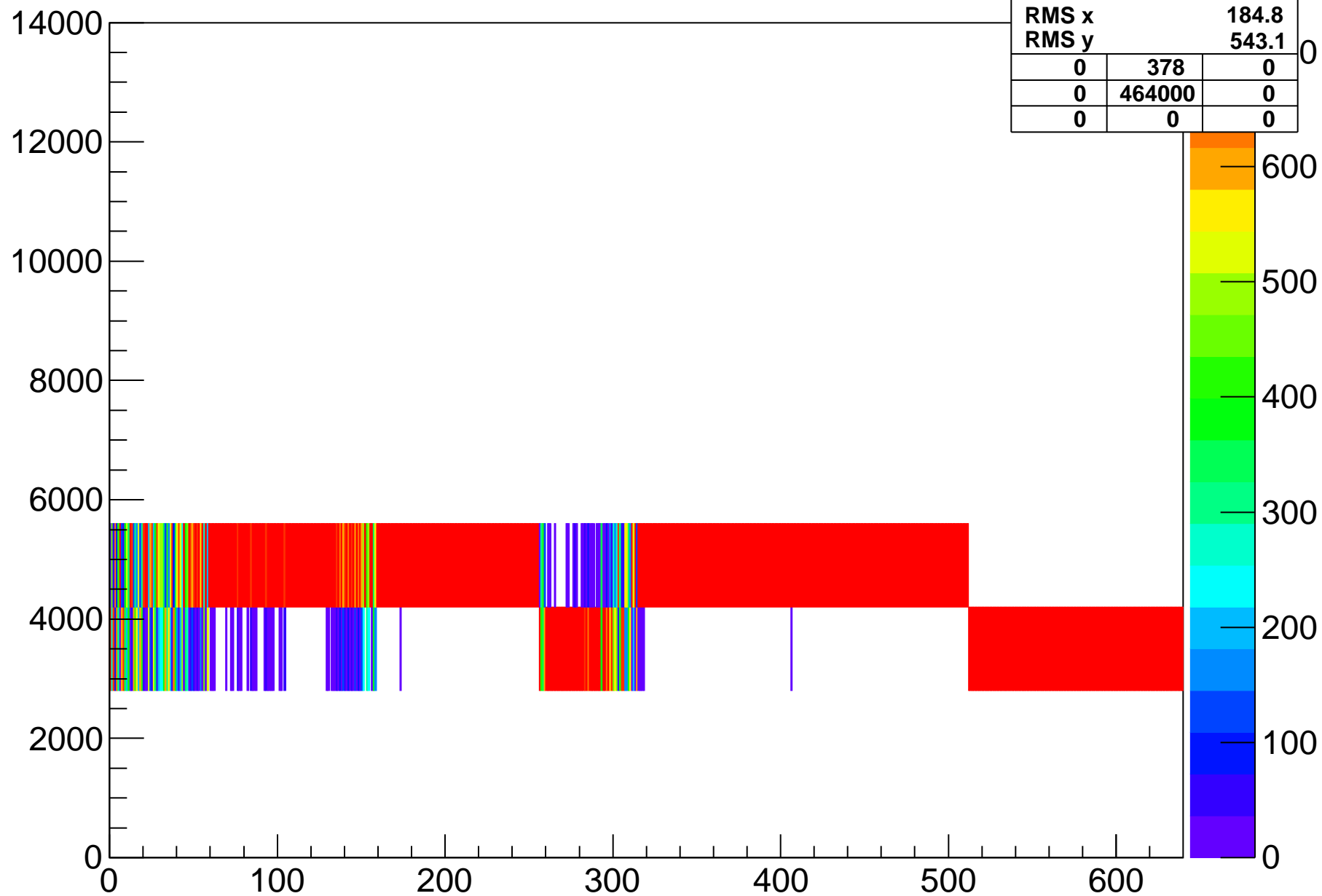
baselinesamples-fpga-4-hyb-0-sample-1

Entries	464378	
Mean x	319.5	
Mean y	4229	
RMS x	184.8	
RMS y	543.1	
0	378	0
0	464000	0
0	0	0



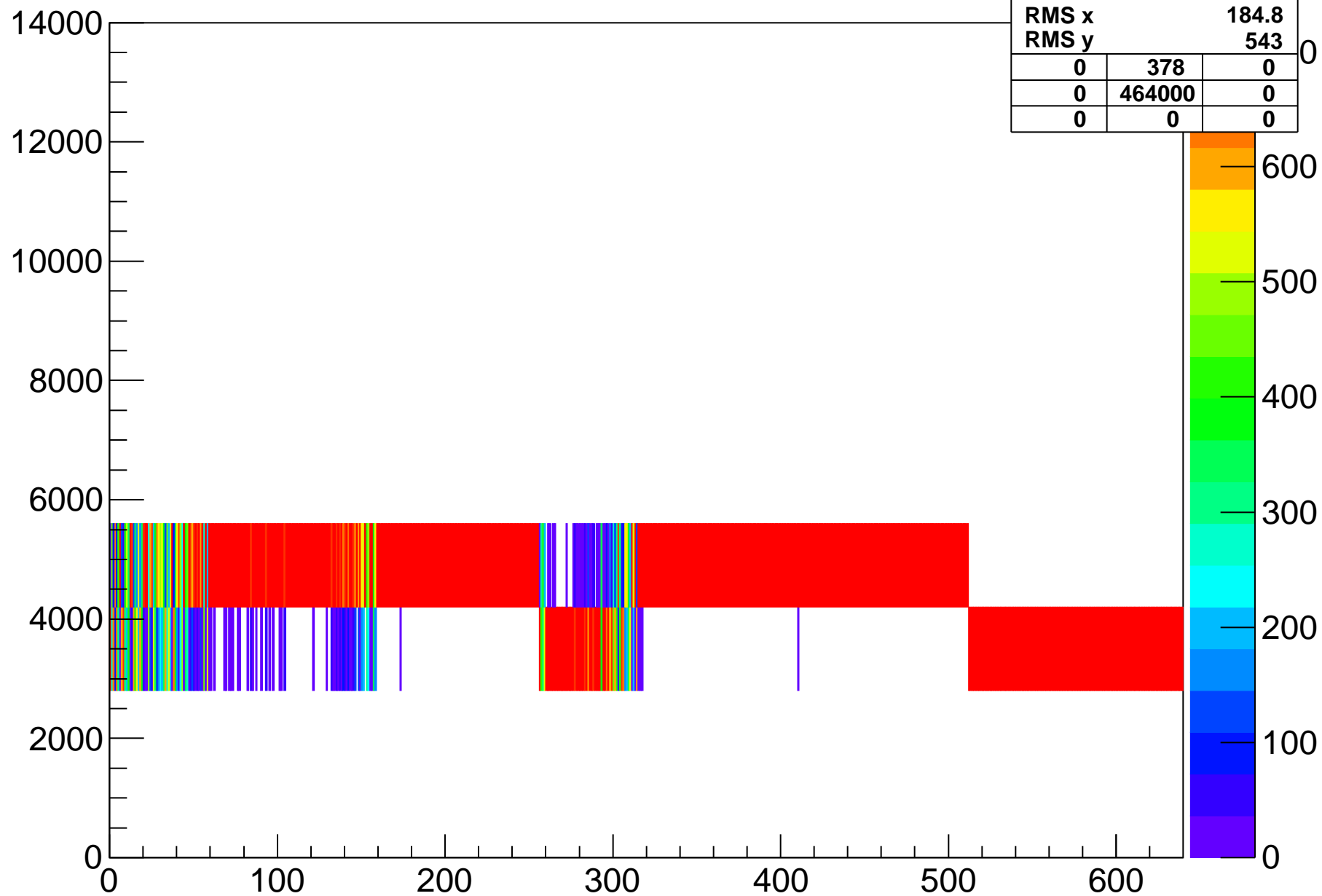
baselinesamples-fpga-4-hyb-0-sample-2

Entries	464378	
Mean x	319.5	
Mean y	4233	
RMS x	184.8	
RMS y	543.1	
0	378	0
0	464000	0
0	0	0



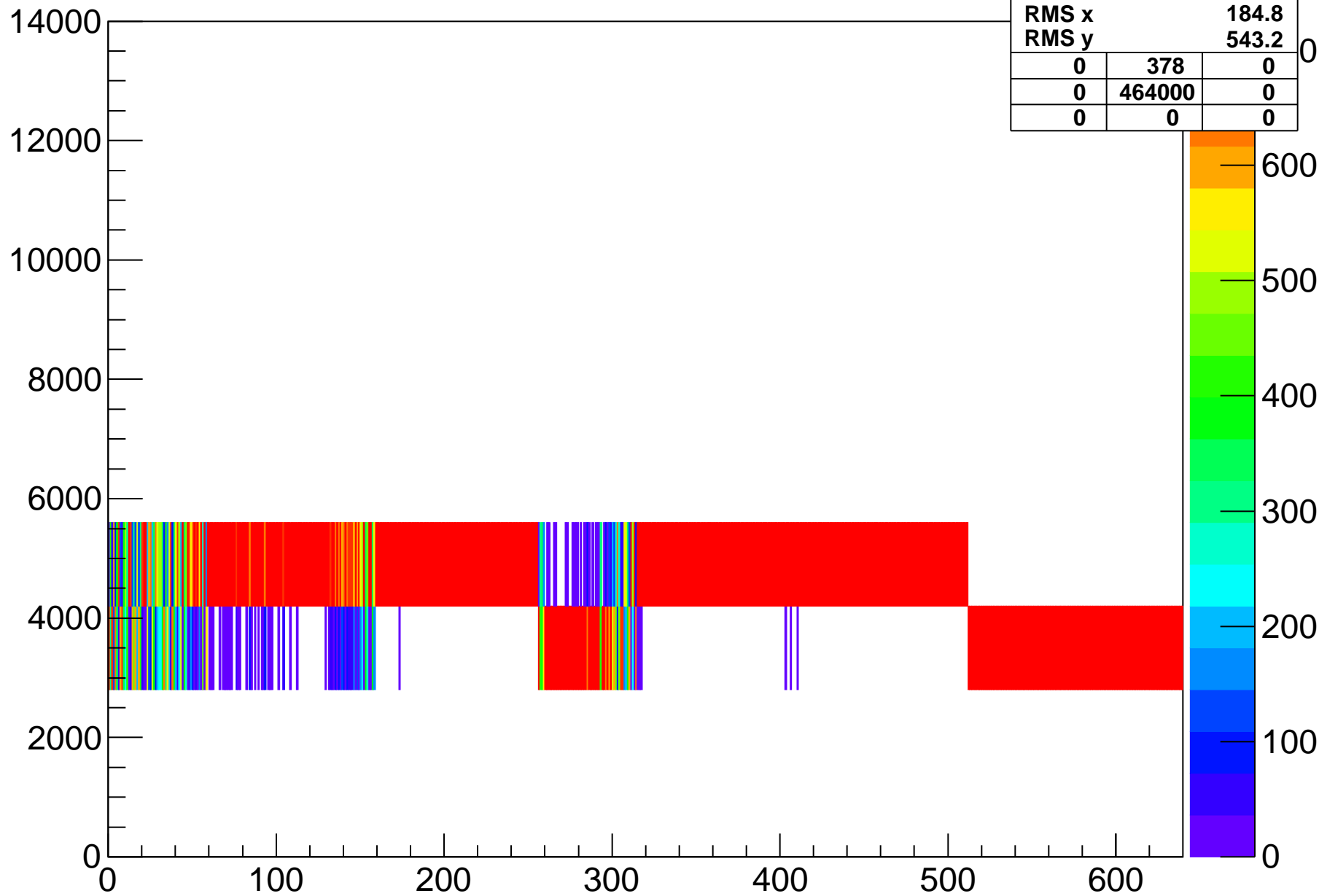
baselinesamples-fpga-4-hyb-0-sample-3

Entries	464378	
Mean x	319.5	
Mean y	4235	
RMS x	184.8	
RMS y	543	
0	378	0
0	464000	0
0	0	0



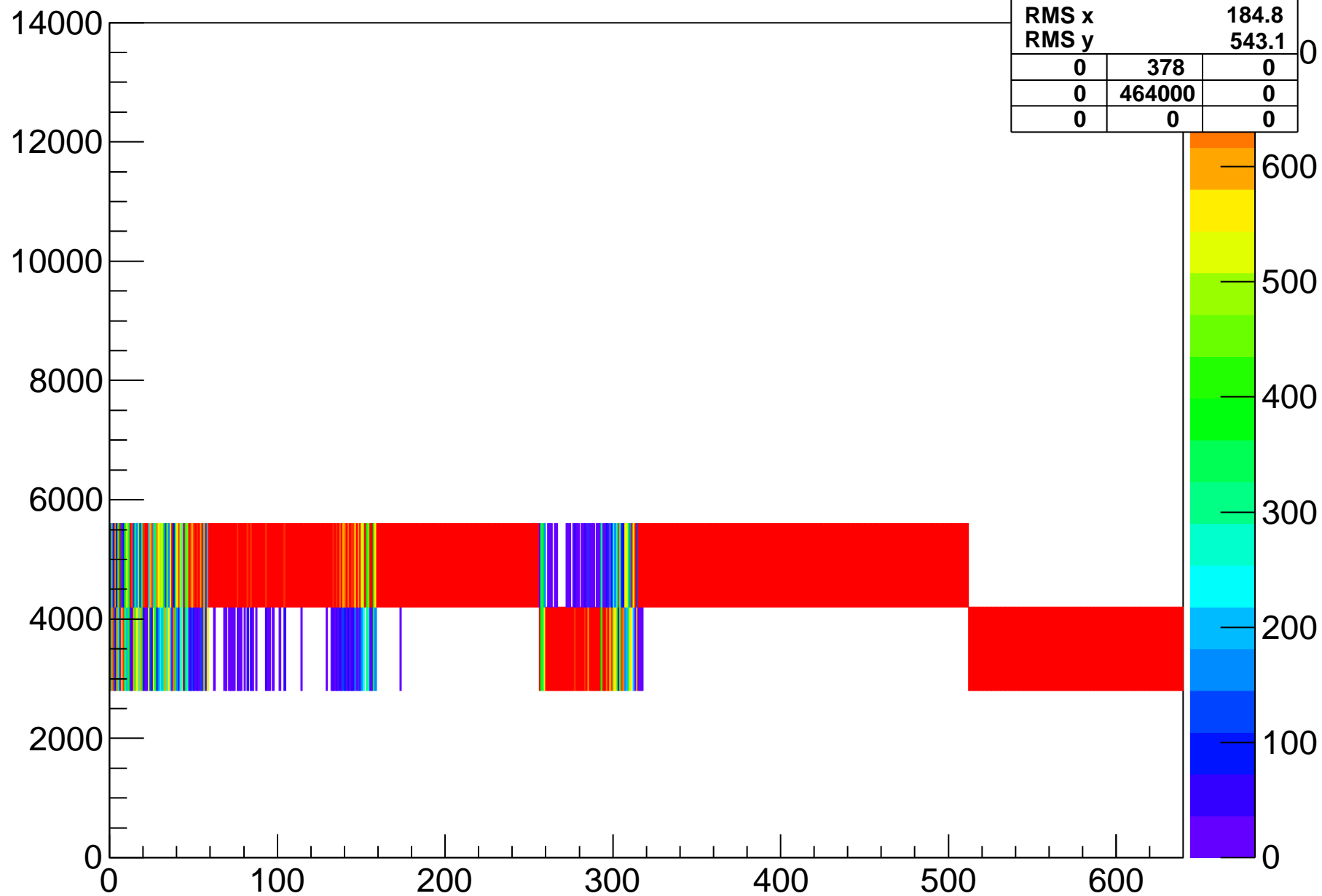
baselinesamples-fpga-4-hyb-0-sample-4

Entries	464378	
Mean x	319.5	
Mean y	4228	
RMS x	184.8	
RMS y	543.2	
0	378	0
0	464000	0
0	0	0



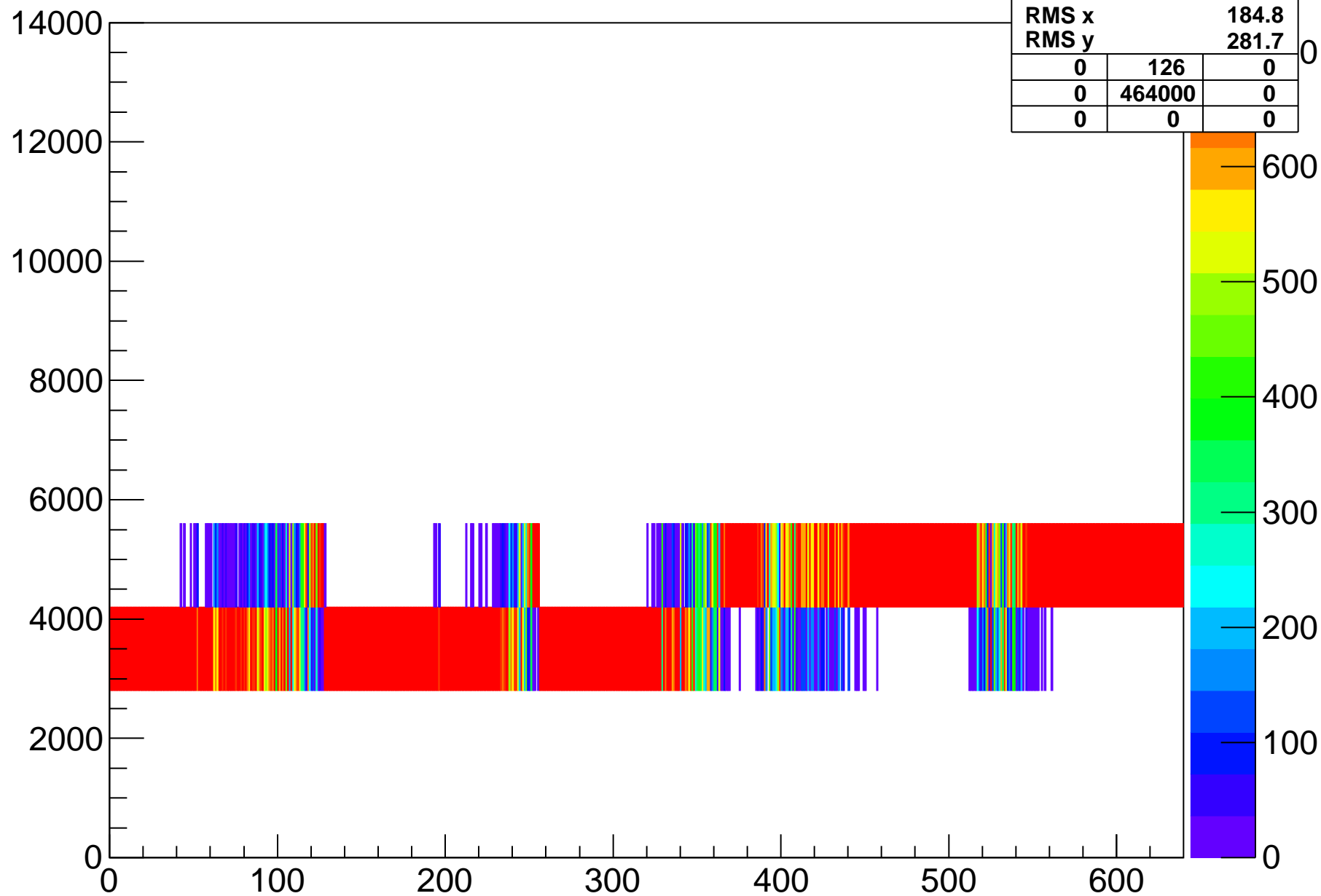
baselinesamples-fpga-4-hyb-0-sample-5

Entries	464378	
Mean x	319.5	
Mean y	4233	
RMS x	184.8	
RMS y	543.1	
0	378	0
0	464000	0
0	0	0



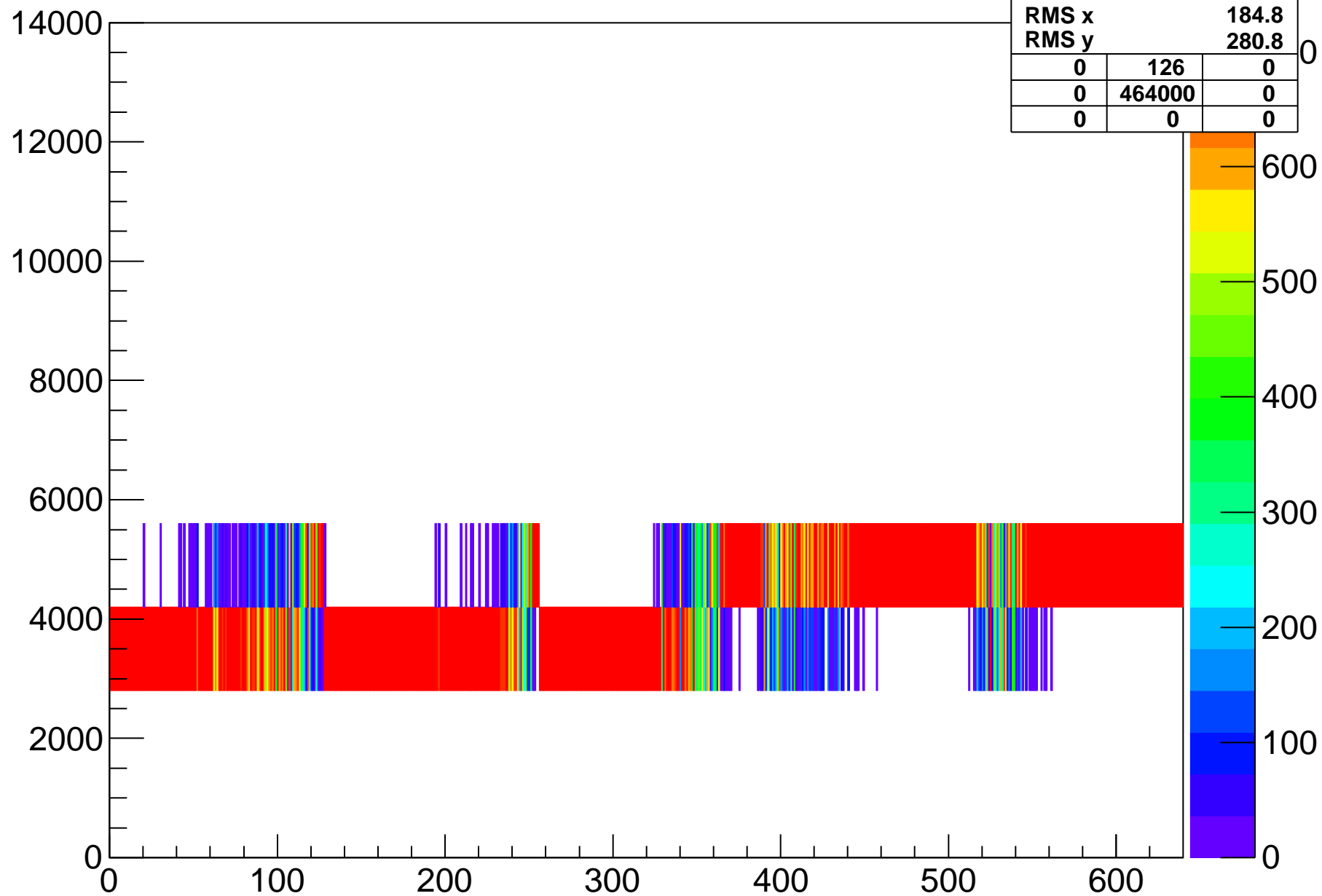
baselinesamples-fpga-4-hyb-1-sample-0

Entries	464126	
Mean x	319.5	
Mean y	4172	
RMS x	184.8	
RMS y	281.7	
0	126	0
0	464000	0
0	0	0



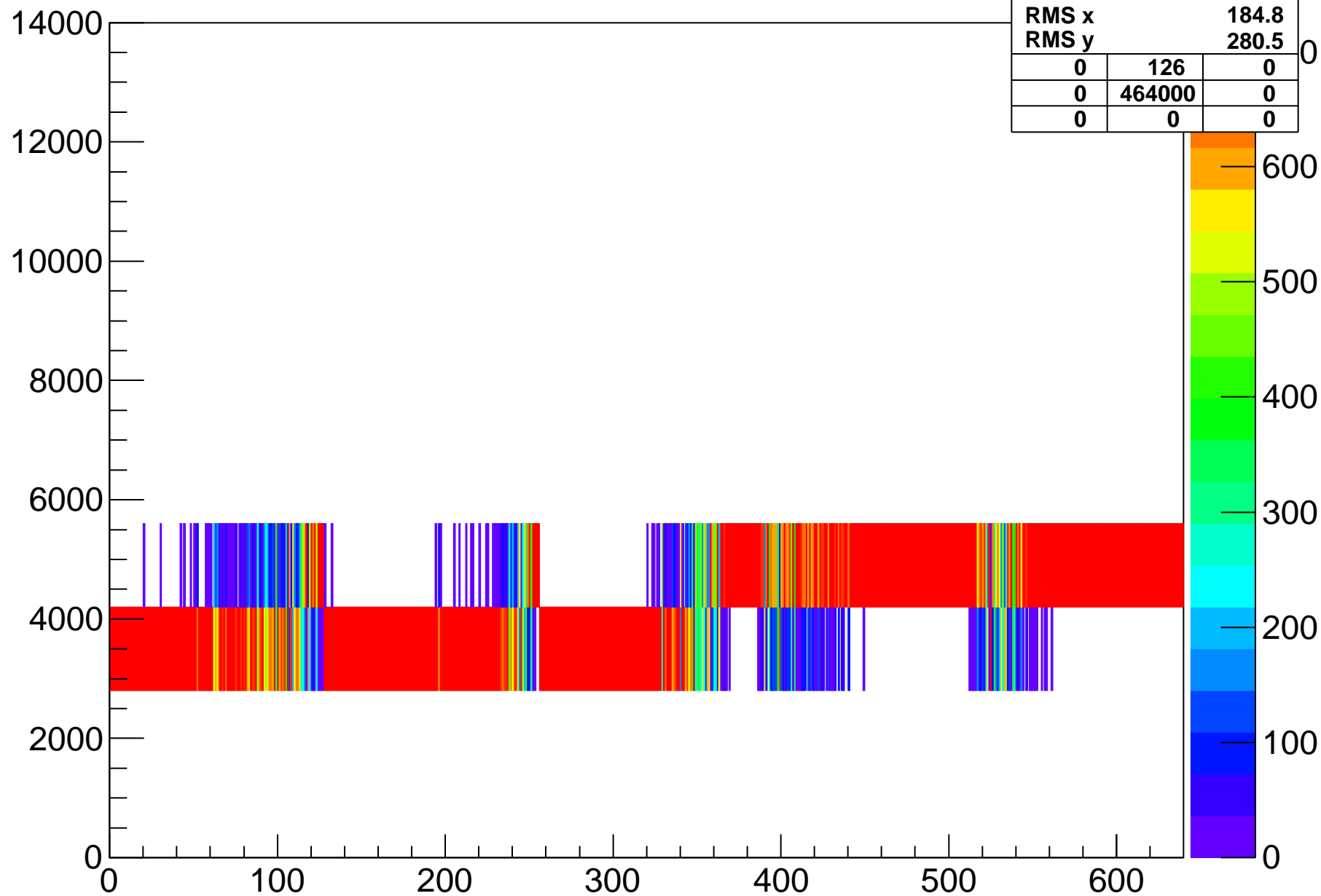
baselinesamples-fpga-4-hyb-1-sample-1

Entries	464126	
Mean x	319.5	
Mean y	4172	
RMS x	184.8	
RMS y	280.8	
0	126	0
0	464000	0
0	0	0



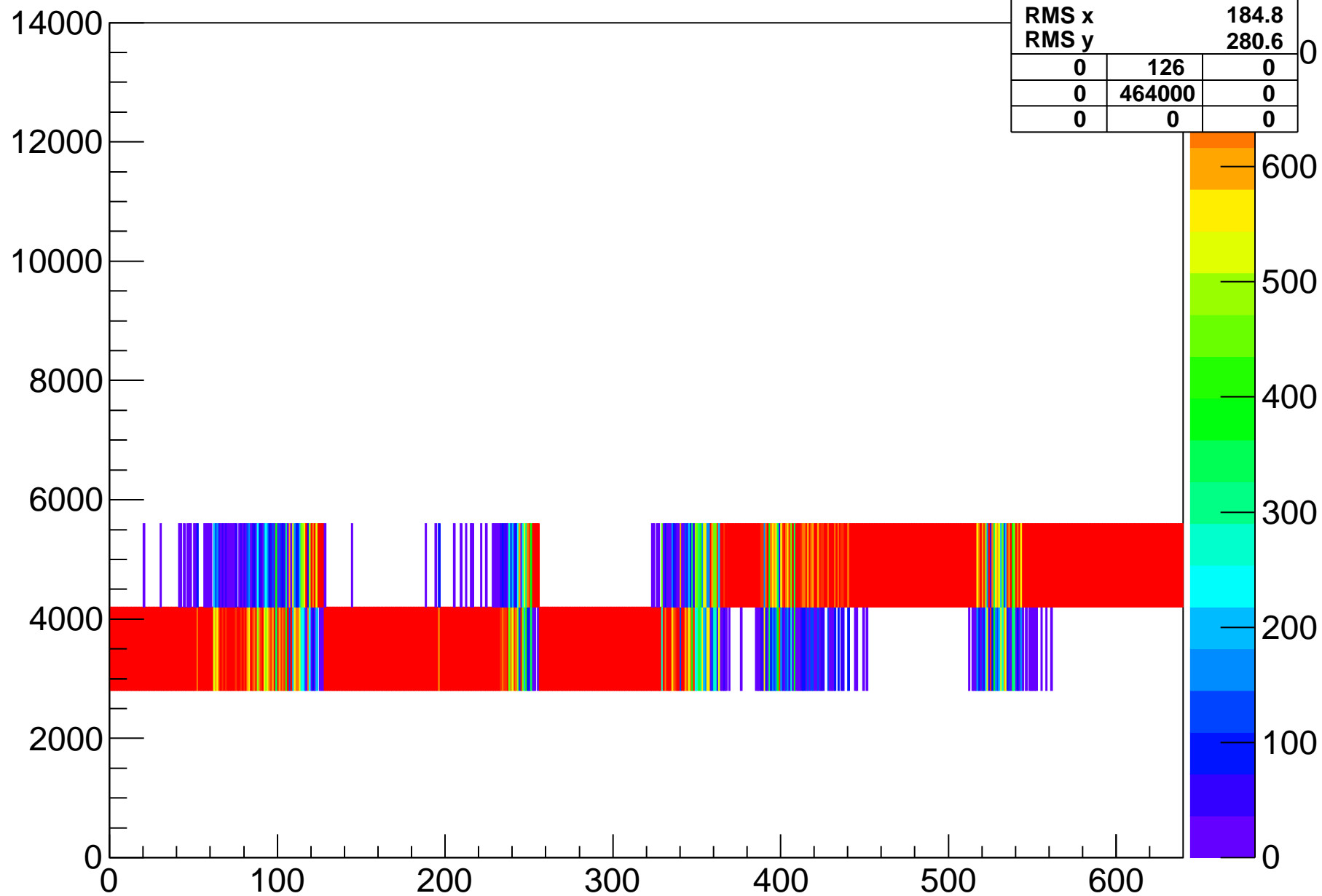
baselinesamples-fpga-4-hyb-1-sample-2

Entries	464126	
Mean x	319.5	
Mean y	4183	
RMS x	184.8	
RMS y	280.5	
0	126	0
0	464000	0
0	0	0



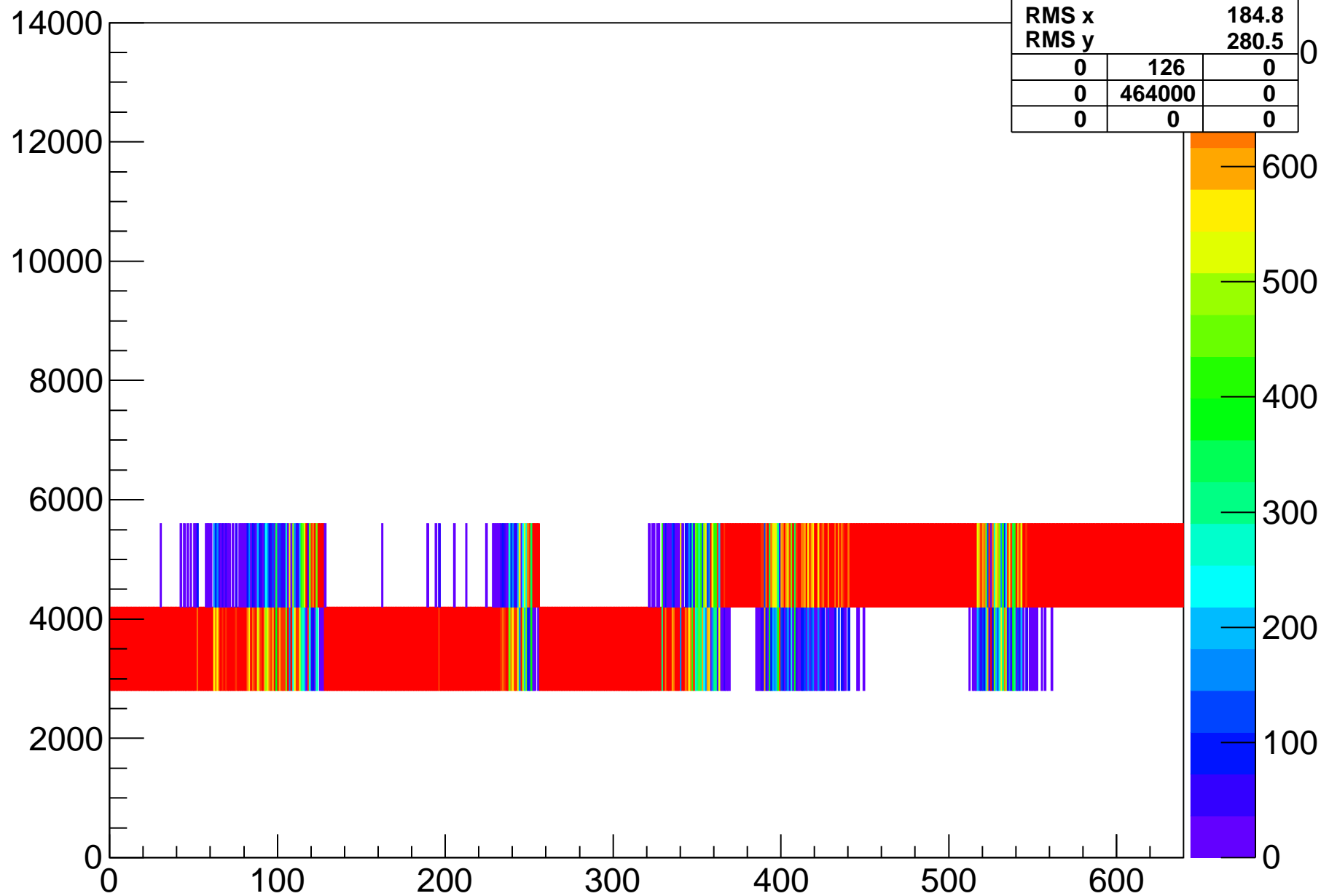
baselinesamples-fpga-4-hyb-1-sample-3

Entries	464126	
Mean x	319.5	
Mean y	4182	
RMS x	184.8	
RMS y	280.6	
0	126	0
0	464000	0
0	0	0



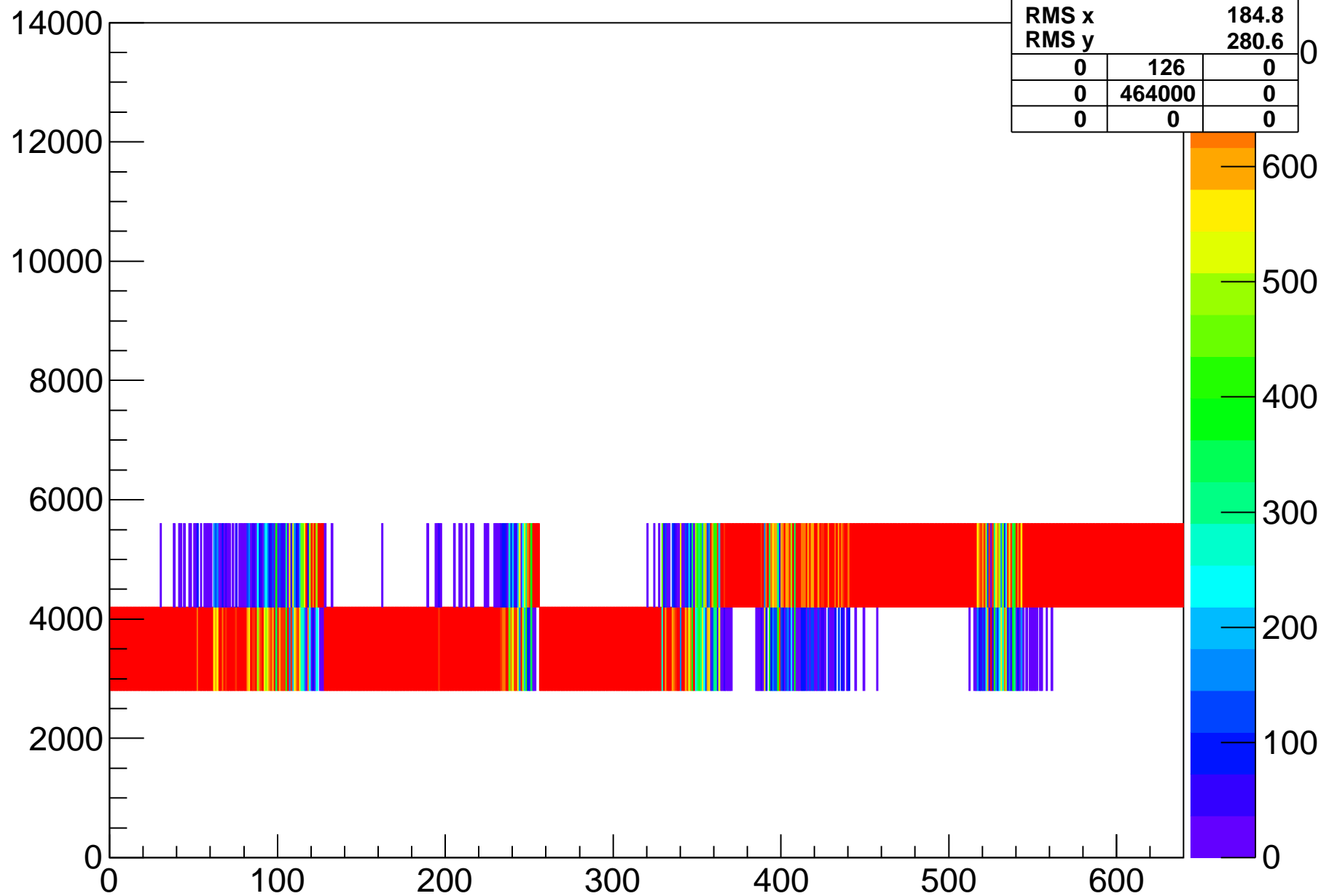
baselinesamples-fpga-4-hyb-1-sample-4

Entries	464126	
Mean x	319.5	
Mean y	4176	
RMS x	184.8	
RMS y	280.5	
0	126	0
0	464000	0
0	0	0



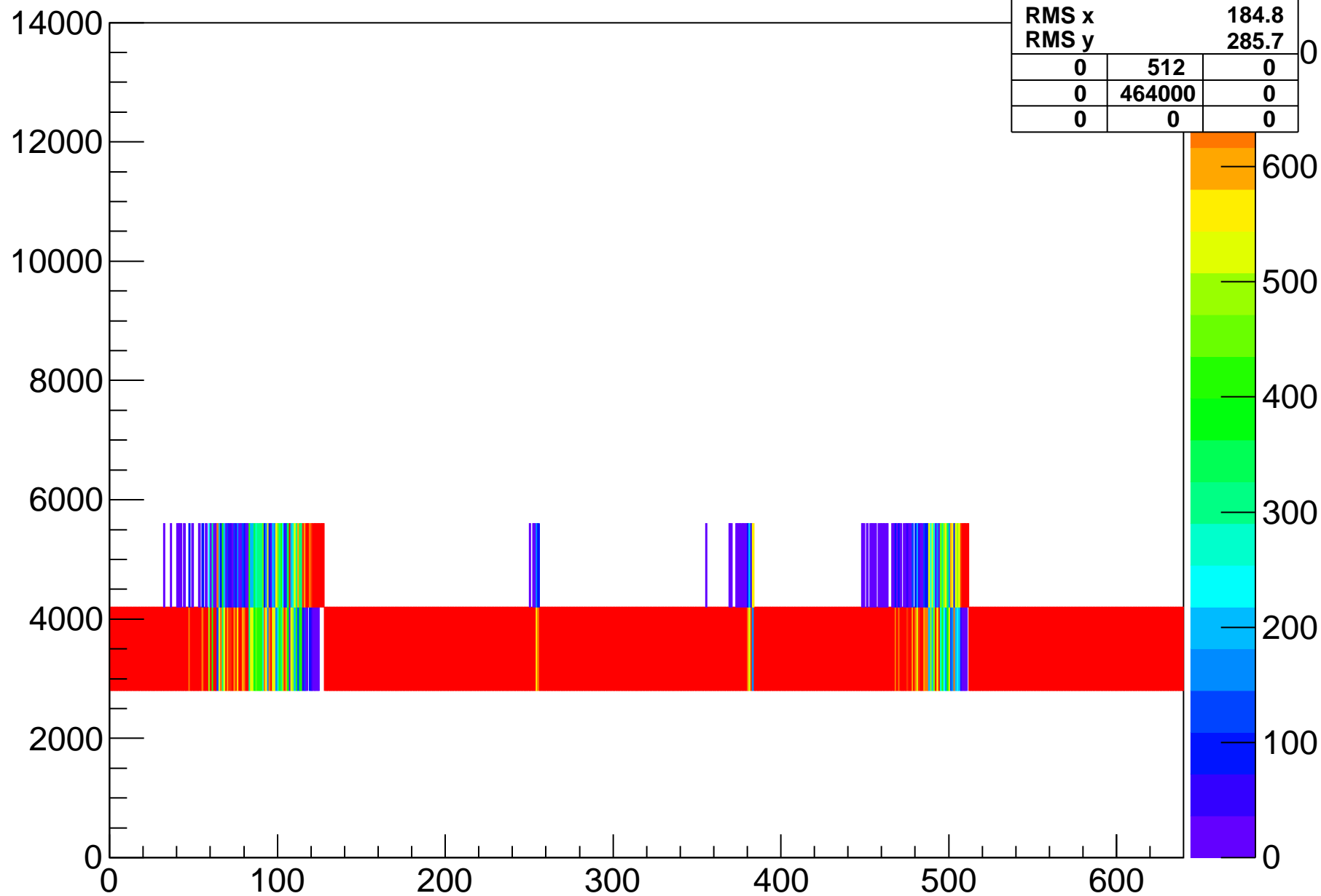
baselinesamples-fpga-4-hyb-1-sample-5

Entries	464126	
Mean x	319.5	
Mean y	4180	
RMS x	184.8	
RMS y	280.6	
0	126	0
0	464000	0
0	0	0



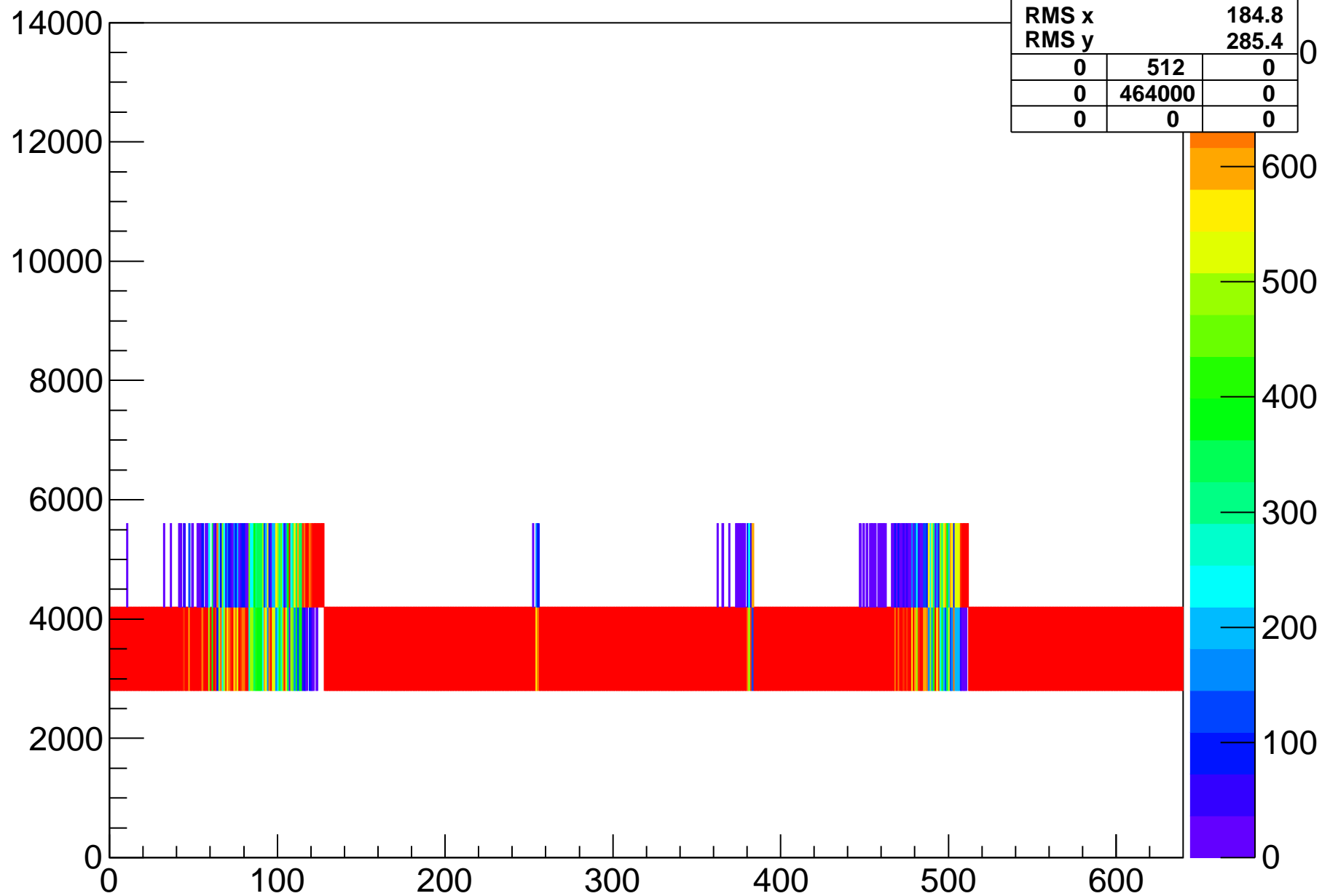
baselinesamples-fpga-4-hyb-2-sample-0

Entries	464512	
Mean x	319.5	
Mean y	3757	
RMS x	184.8	
RMS y	285.7	
0	512	0
0	464000	0
0	0	0



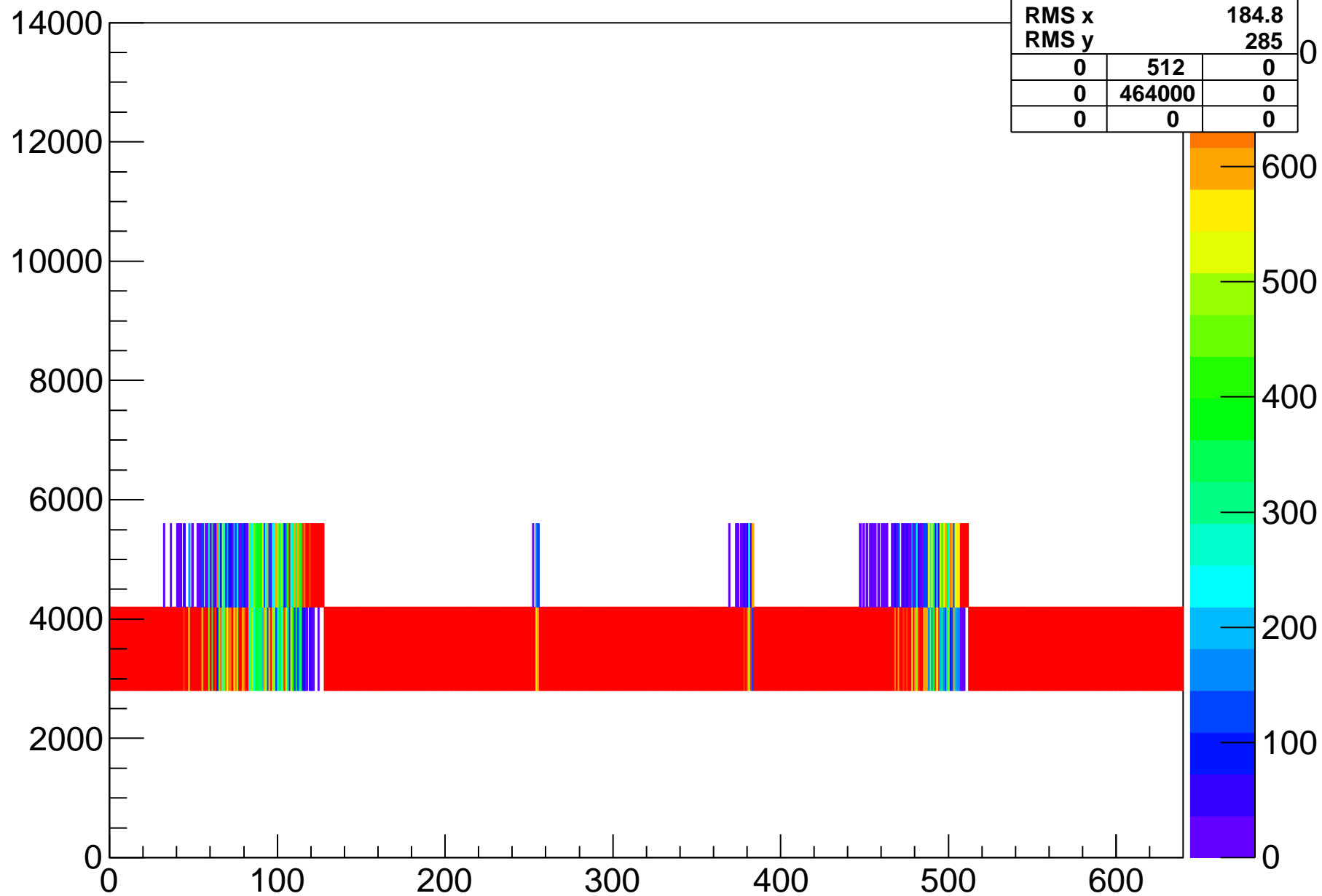
baselinesamples-fpga-4-hyb-2-sample-1

Entries	464512	
Mean x	319.5	
Mean y	3765	
RMS x	184.8	
RMS y	285.4	
0	512	0
0	464000	0
0	0	0



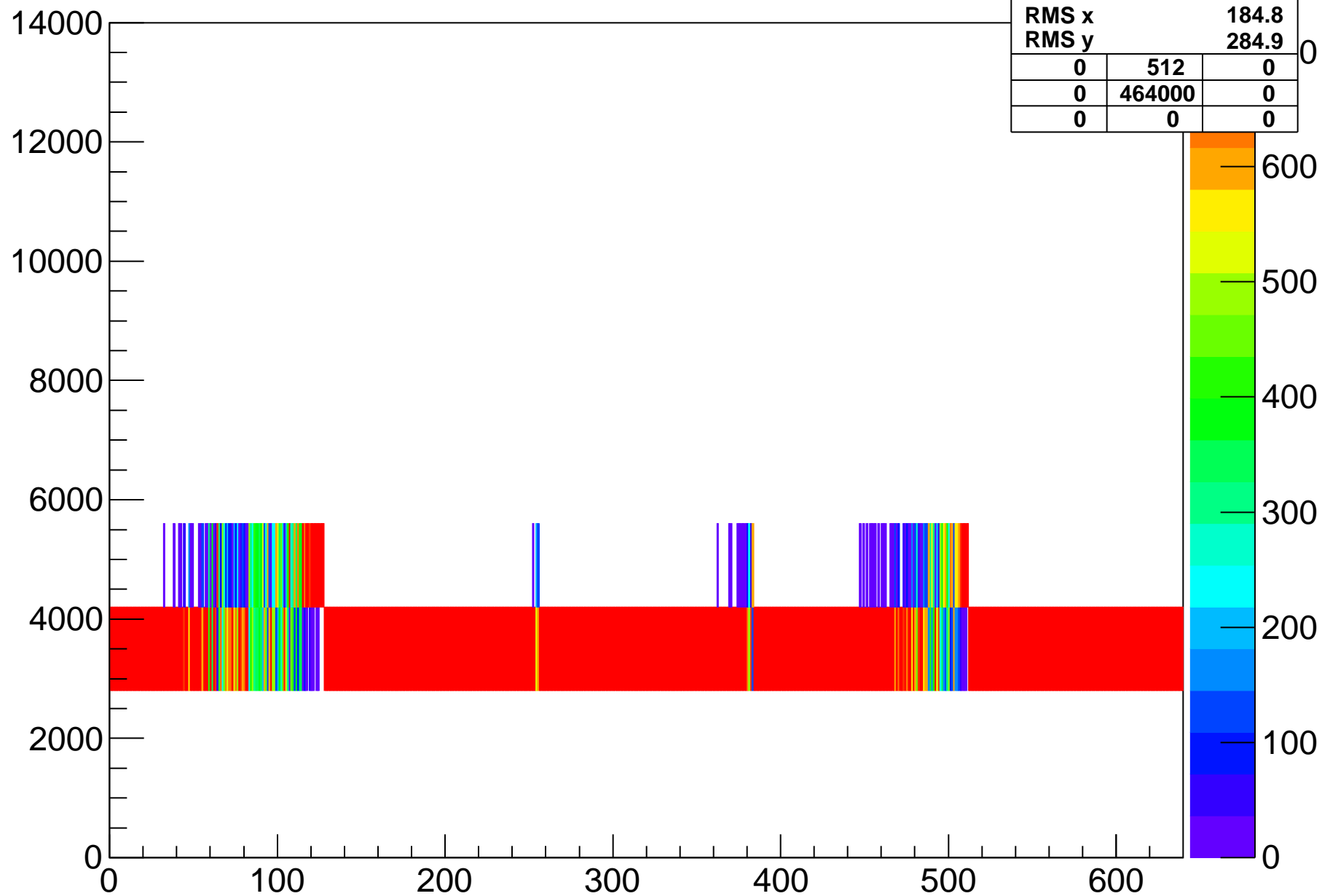
baselinesamples-fpga-4-hyb-2-sample-2

Entries	464512	
Mean x	319.5	
Mean y	3771	
RMS x	184.8	
RMS y	285	
0	512	0
0	464000	0
0	0	0



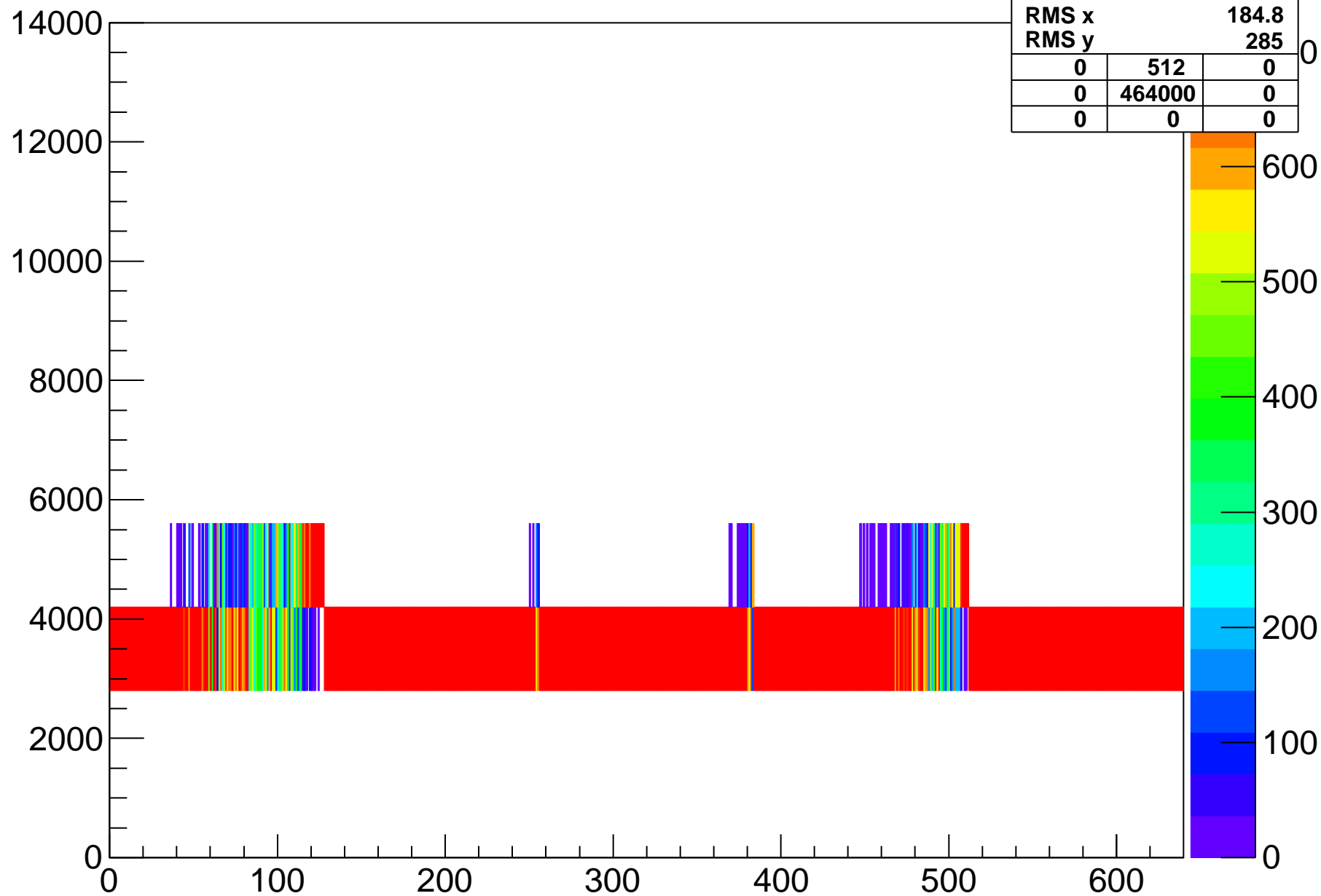
baselinesamples-fpga-4-hyb-2-sample-3

Entries	464512	
Mean x	319.5	
Mean y	3773	
RMS x	184.8	
RMS y	284.9	
0	512	0
0	464000	0
0	0	0



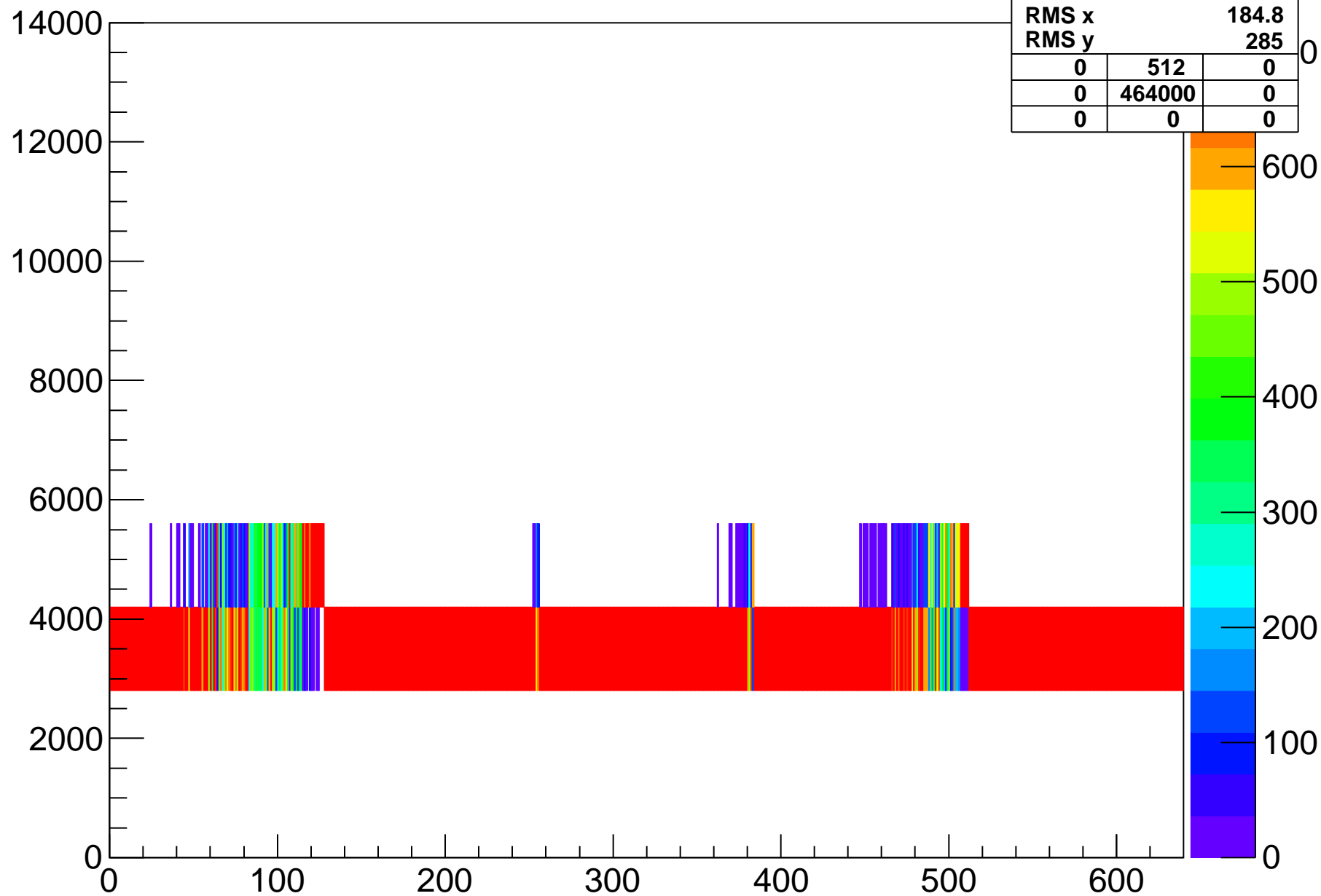
baselinesamples-fpga-4-hyb-2-sample-4

Entries	464512	
Mean x	319.5	
Mean y	3766	
RMS x	184.8	
RMS y	285	
0	512	0
0	464000	0
0	0	0



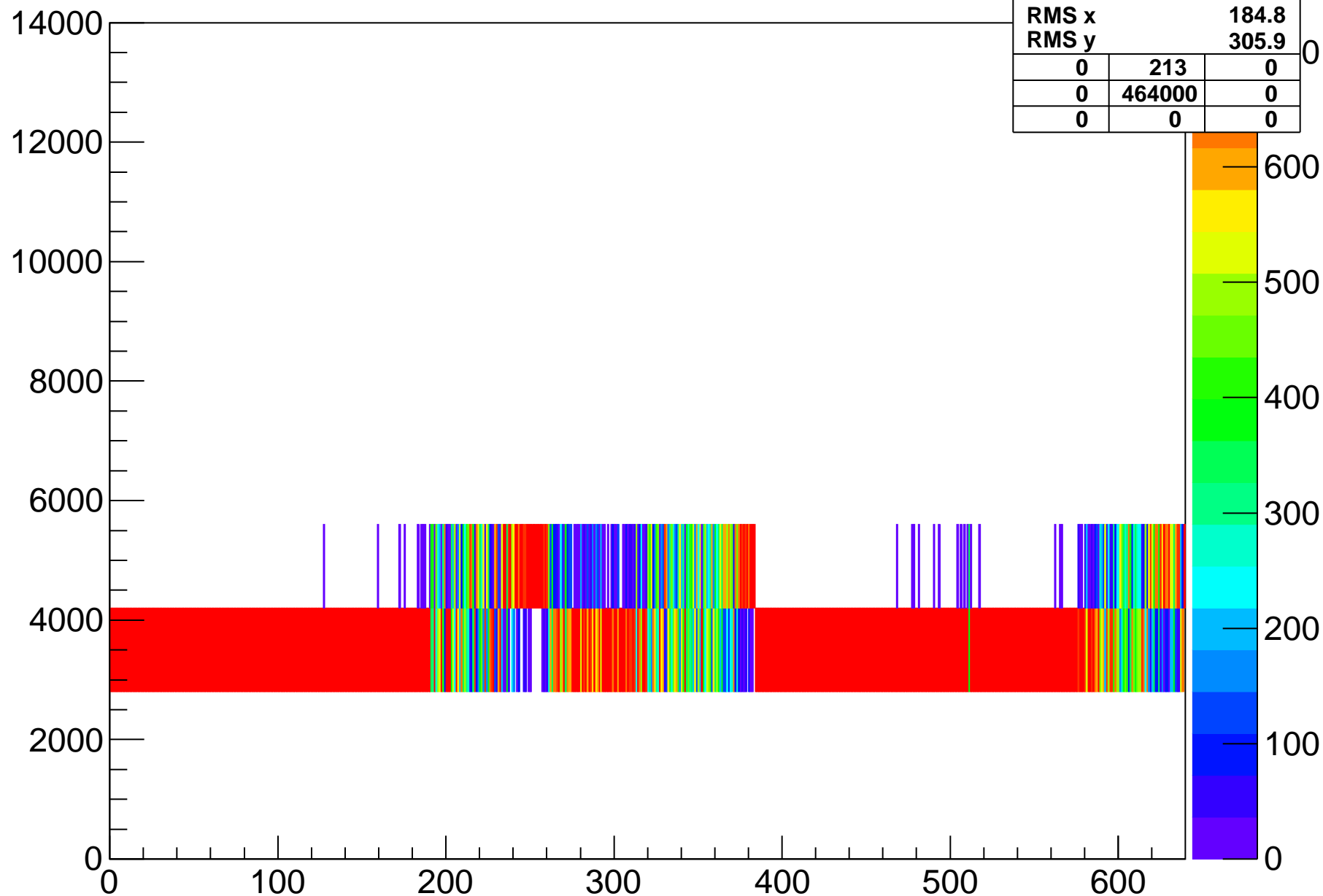
baselinesamples-fpga-4-hyb-2-sample-5

Entries	464512	
Mean x	319.5	
Mean y	3770	
RMS x	184.8	
RMS y	285	
0	512	0
0	464000	0
0	0	0



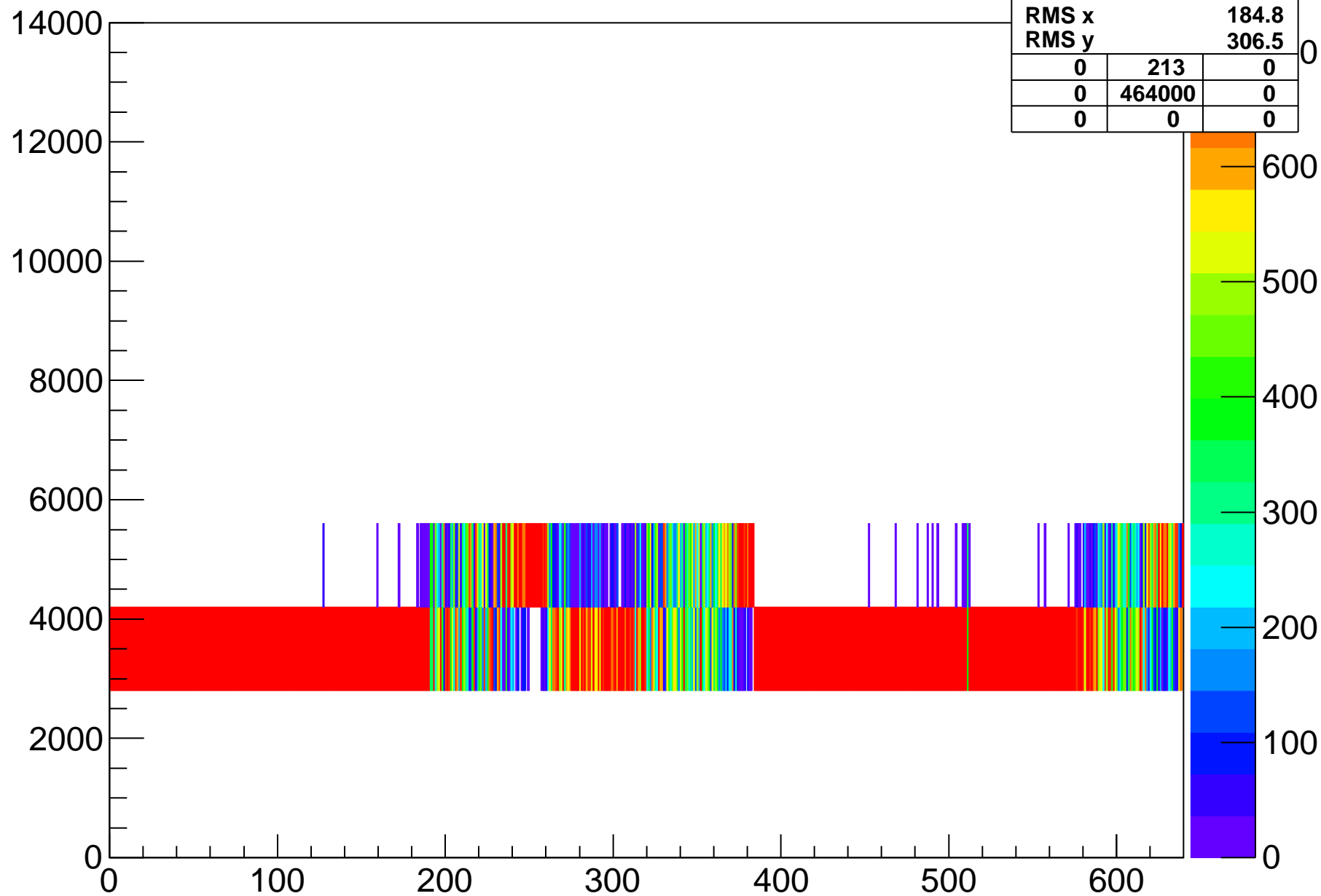
baselinesamples-fpga-4-hyb-3-sample-0

Entries	464213	
Mean x	319.5	
Mean y	3907	
RMS x	184.8	
RMS y	305.9	
0	213	0
0	464000	0
0	0	0



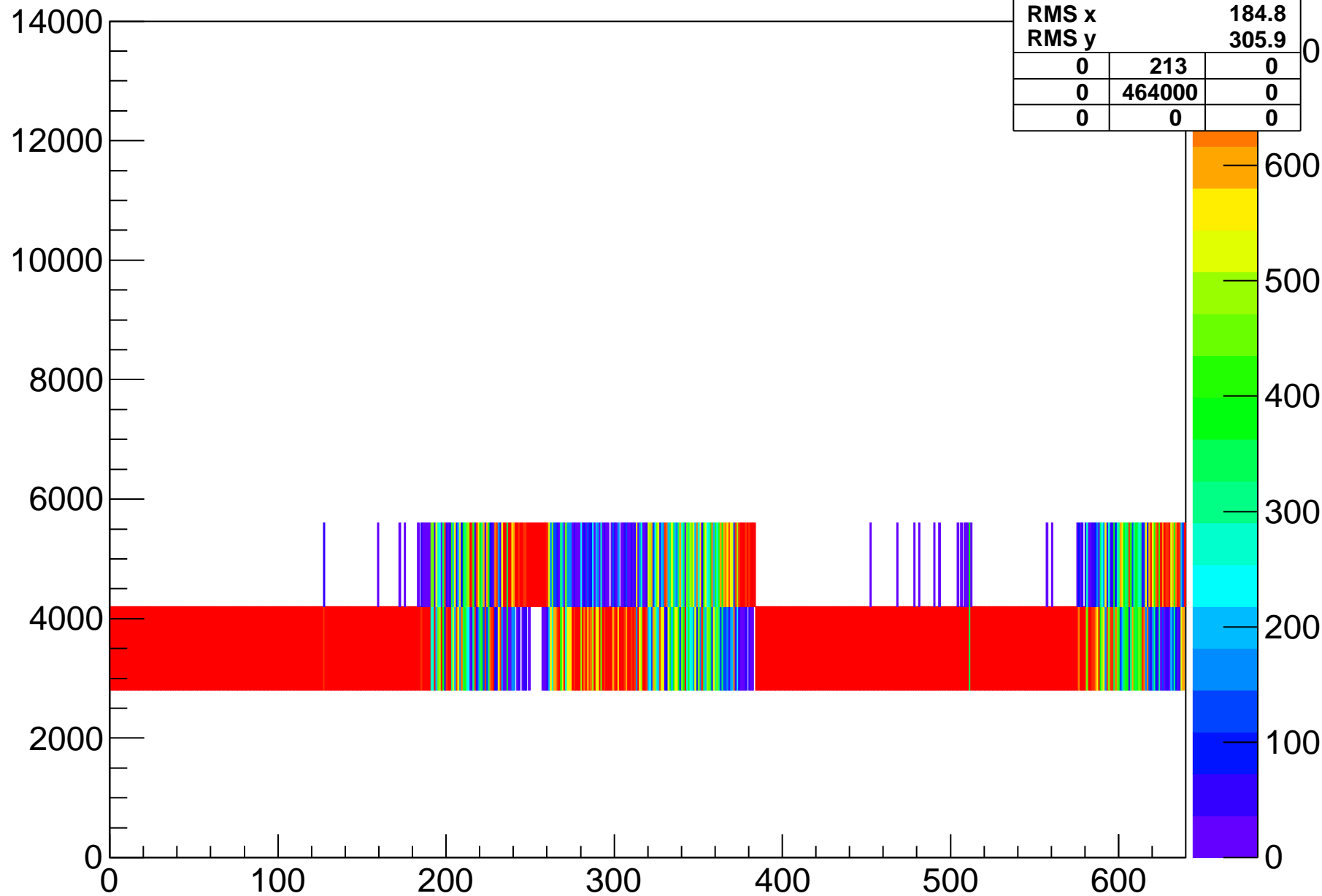
baselinesamples-fpga-4-hyb-3-sample-1

Entries	464213	
Mean x	319.5	
Mean y	3907	
RMS x	184.8	
RMS y	306.5	
0	213	0
0	464000	0
0	0	0



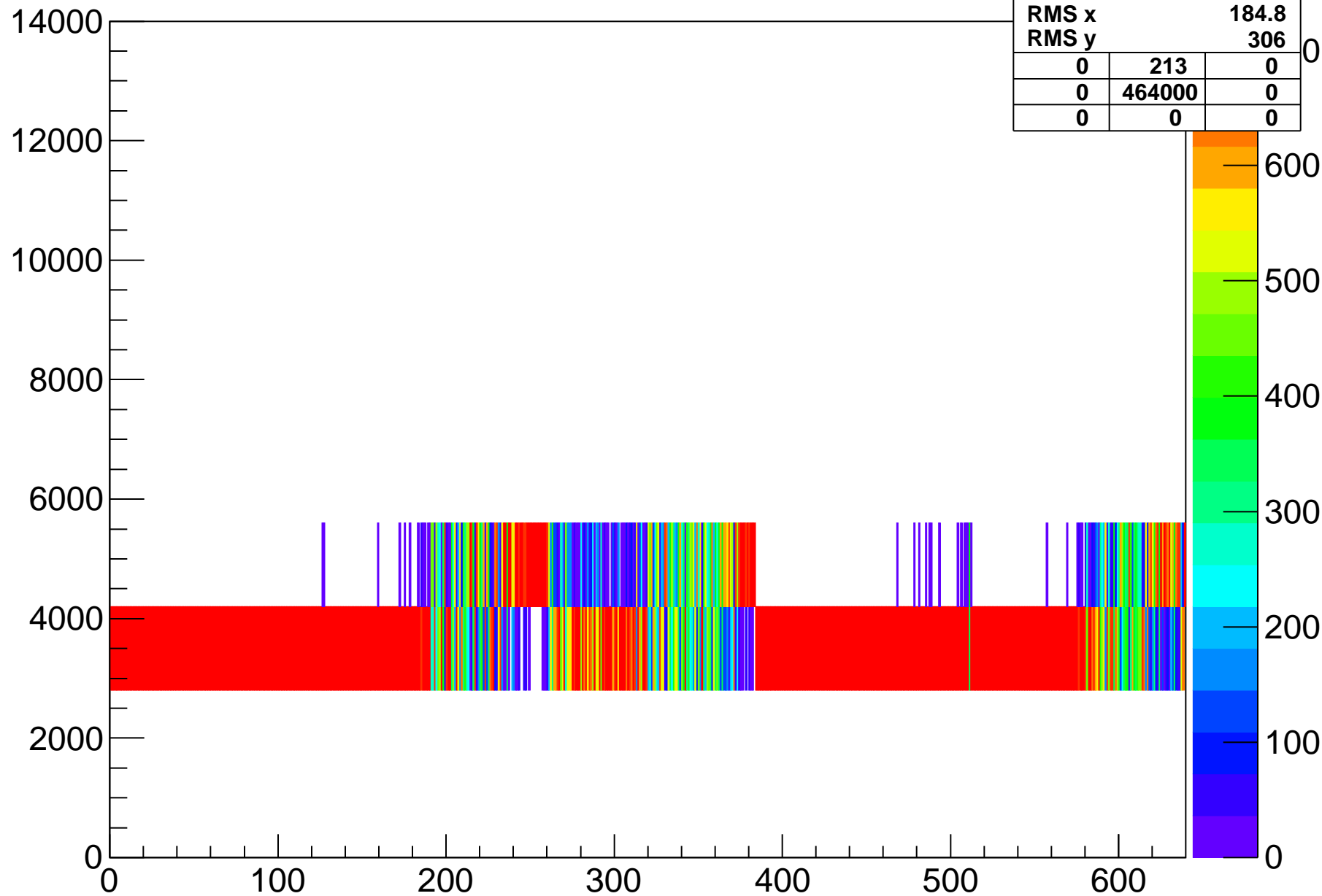
baselinesamples-fpga-4-hyb-3-sample-2

Entries	464213	
Mean x	319.5	
Mean y	3919	
RMS x	184.8	
RMS y	305.9	
0	213	0
0	464000	0
0	0	0



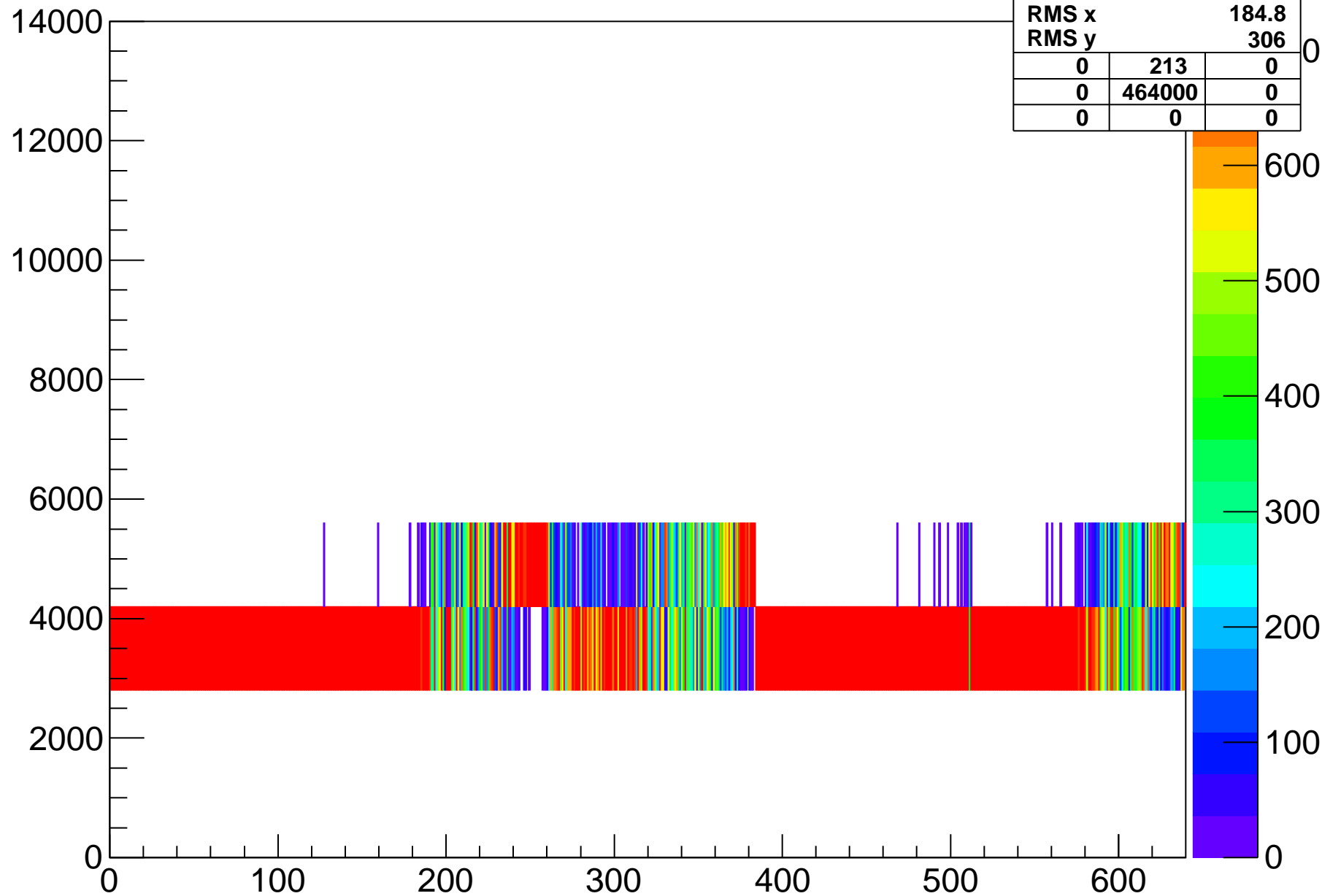
baselinesamples-fpga-4-hyb-3-sample-3

Entries	464213	
Mean x	319.5	
Mean y	3918	
RMS x	184.8	
RMS y	306	
0	213	0
0	464000	0
0	0	0



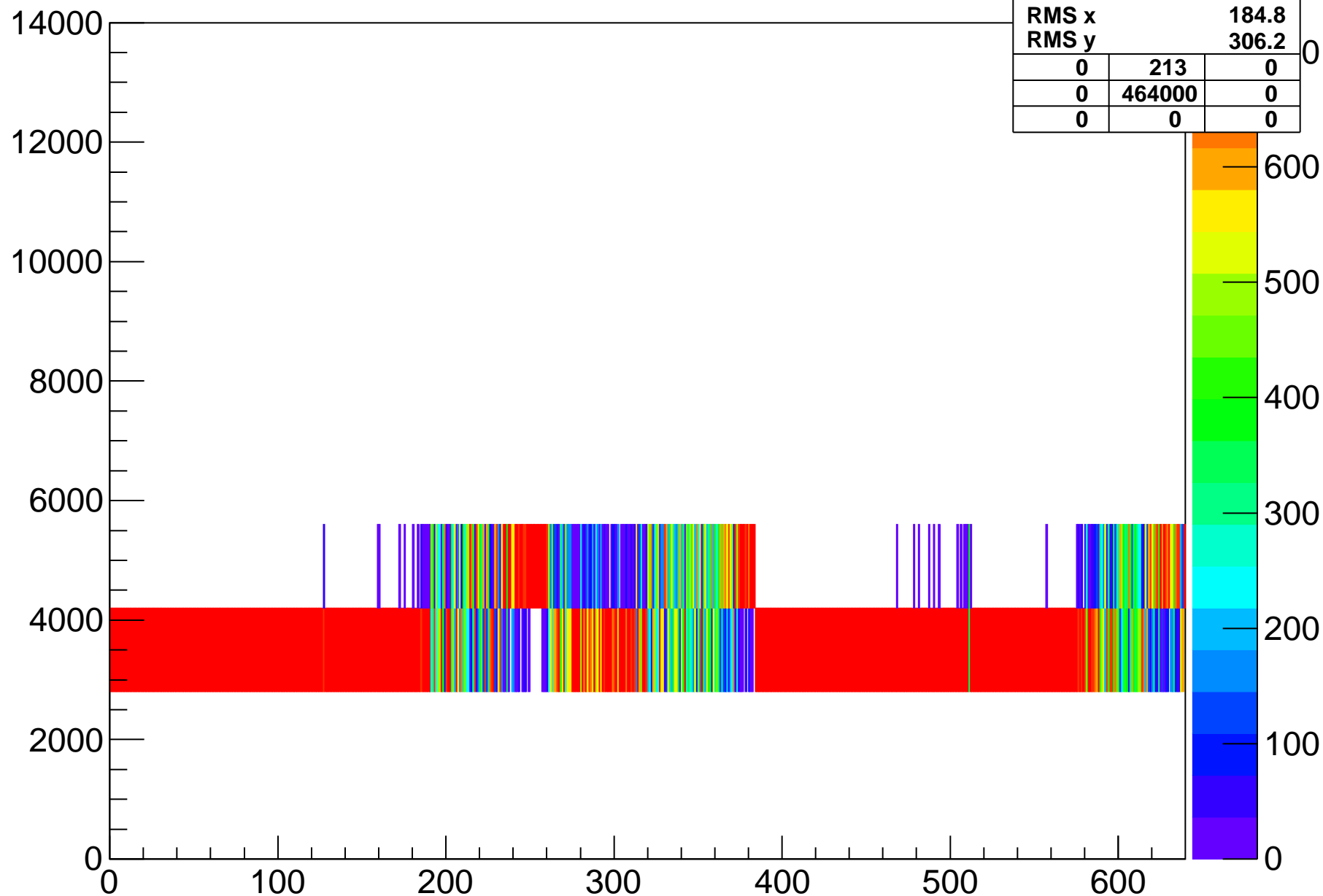
baselinesamples-fpga-4-hyb-3-sample-4

Entries	464213	
Mean x	319.5	
Mean y	3912	
RMS x	184.8	
RMS y	306	
0	213	0
0	464000	0
0	0	0



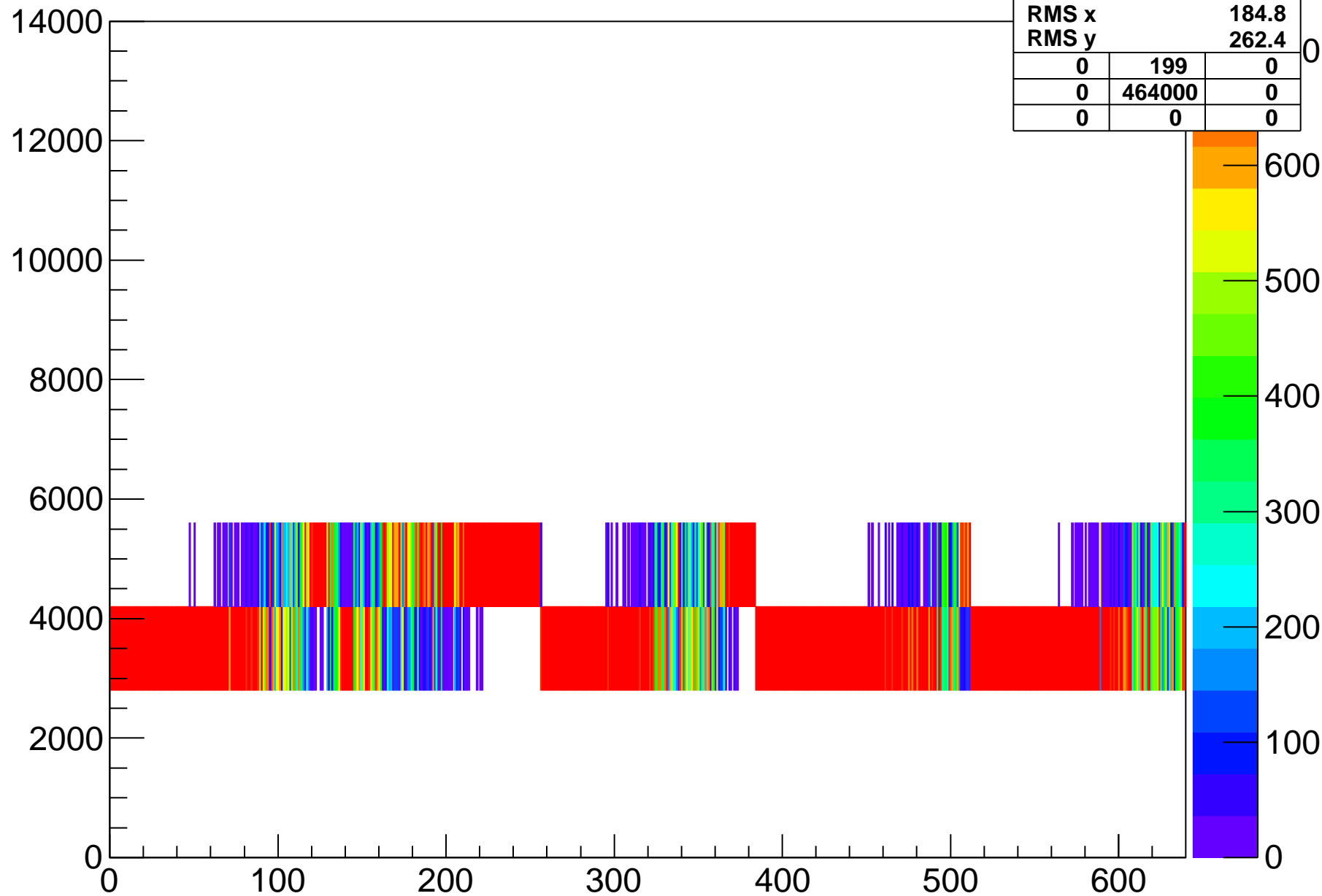
baselinesamples-fpga-4-hyb-3-sample-5

Entries	464213	
Mean x	319.5	
Mean y	3916	
RMS x	184.8	
RMS y	306.2	
0	213	0
0	464000	0
0	0	0



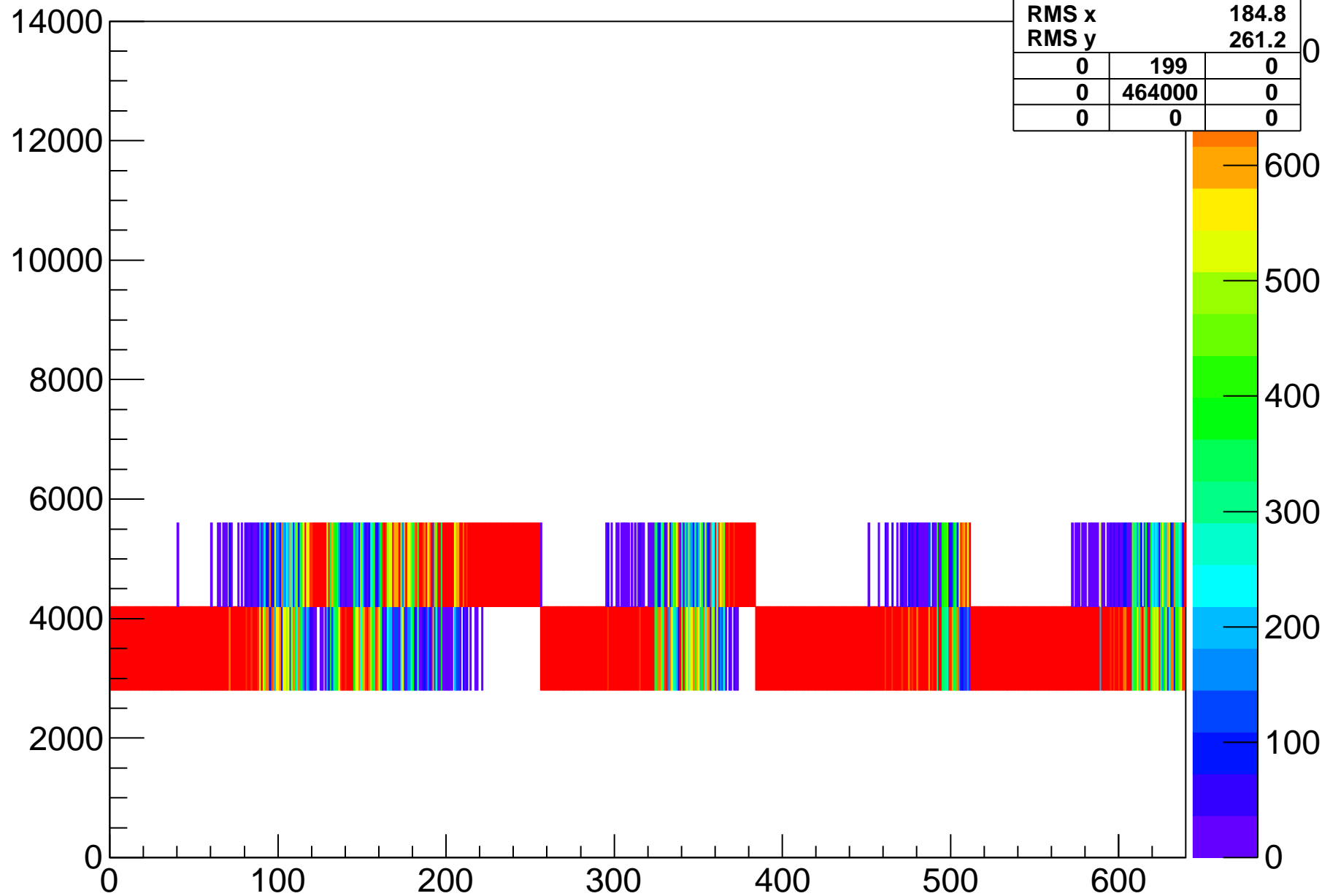
baselinesamples-fpga-5-hyb-0-sample-0

Entries			464199
Mean x			319.5
Mean y			4052
RMS x			184.8
RMS y			262.4
0	199	0	0
0	464000	0	0
0	0	0	0



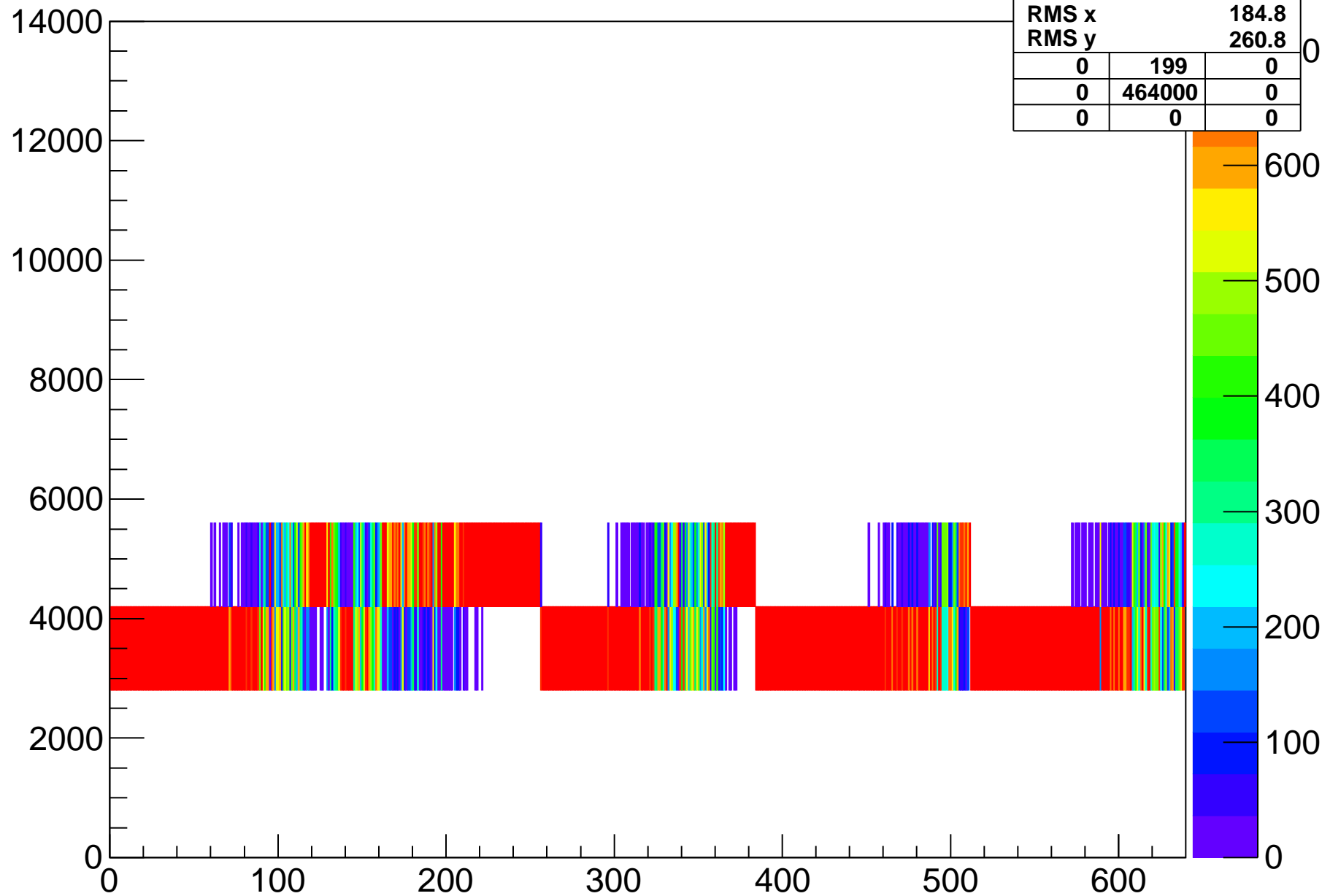
baselinesamples-fpga-5-hyb-0-sample-1

Entries	464199	
Mean x	319.5	
Mean y	4049	
RMS x	184.8	
RMS y	261.2	
0	199	0
0	464000	0
0	0	0



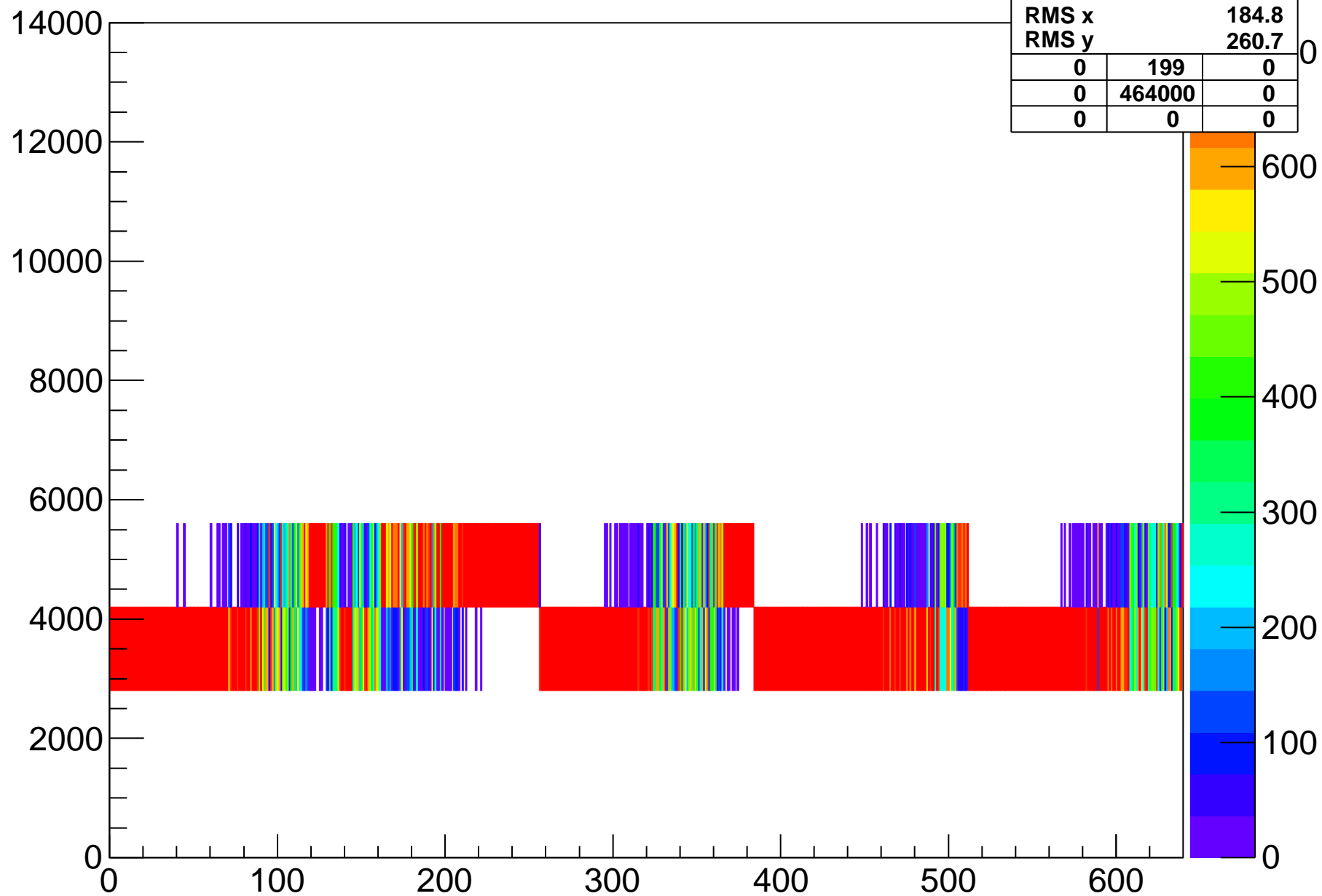
baselinesamples-fpga-5-hyb-0-sample-2

Entries	464199	
Mean x	319.5	
Mean y	4061	
RMS x	184.8	
RMS y	260.8	
0	199	0
0	464000	0
0	0	0



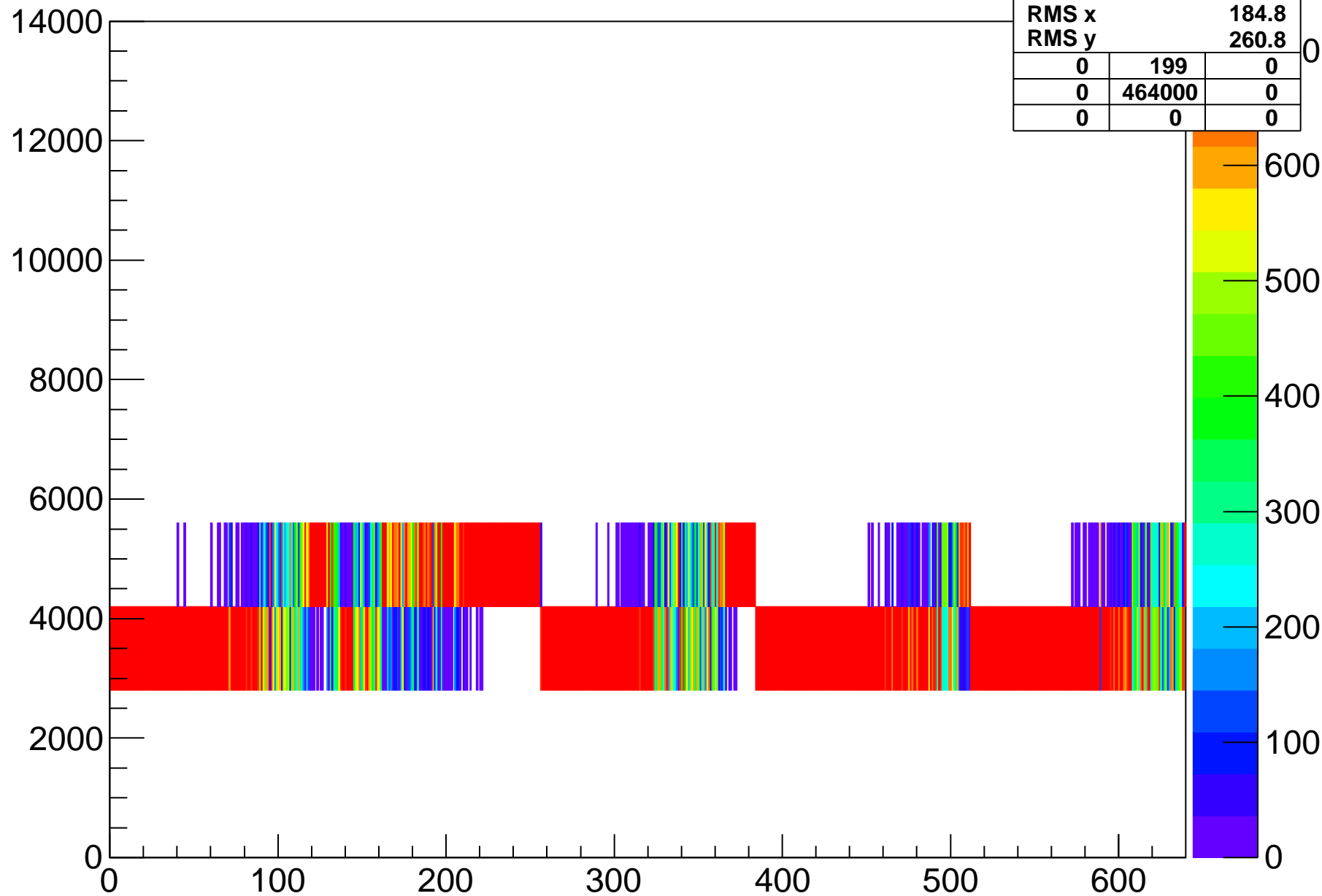
baselinesamples-fpga-5-hyb-0-sample-3

Entries	464199	
Mean x	319.5	
Mean y	4063	
RMS x	184.8	
RMS y	260.7	
0	199	0
0	464000	0
0	0	0



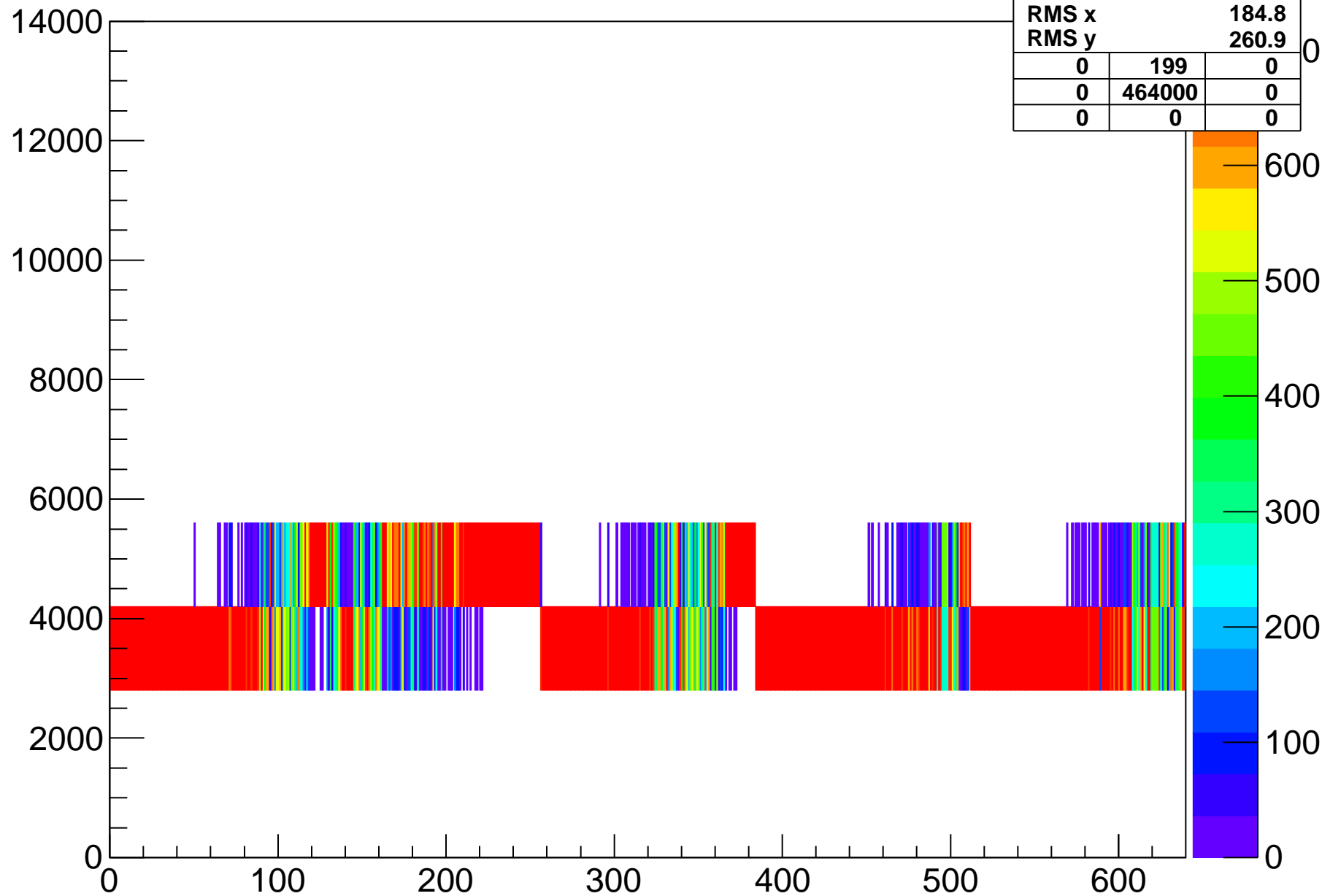
baselinesamples-fpga-5-hyb-0-sample-4

Entries	464199	
Mean x	319.5	
Mean y	4059	
RMS x	184.8	
RMS y	260.8	
0	199	0
0	464000	0
0	0	0



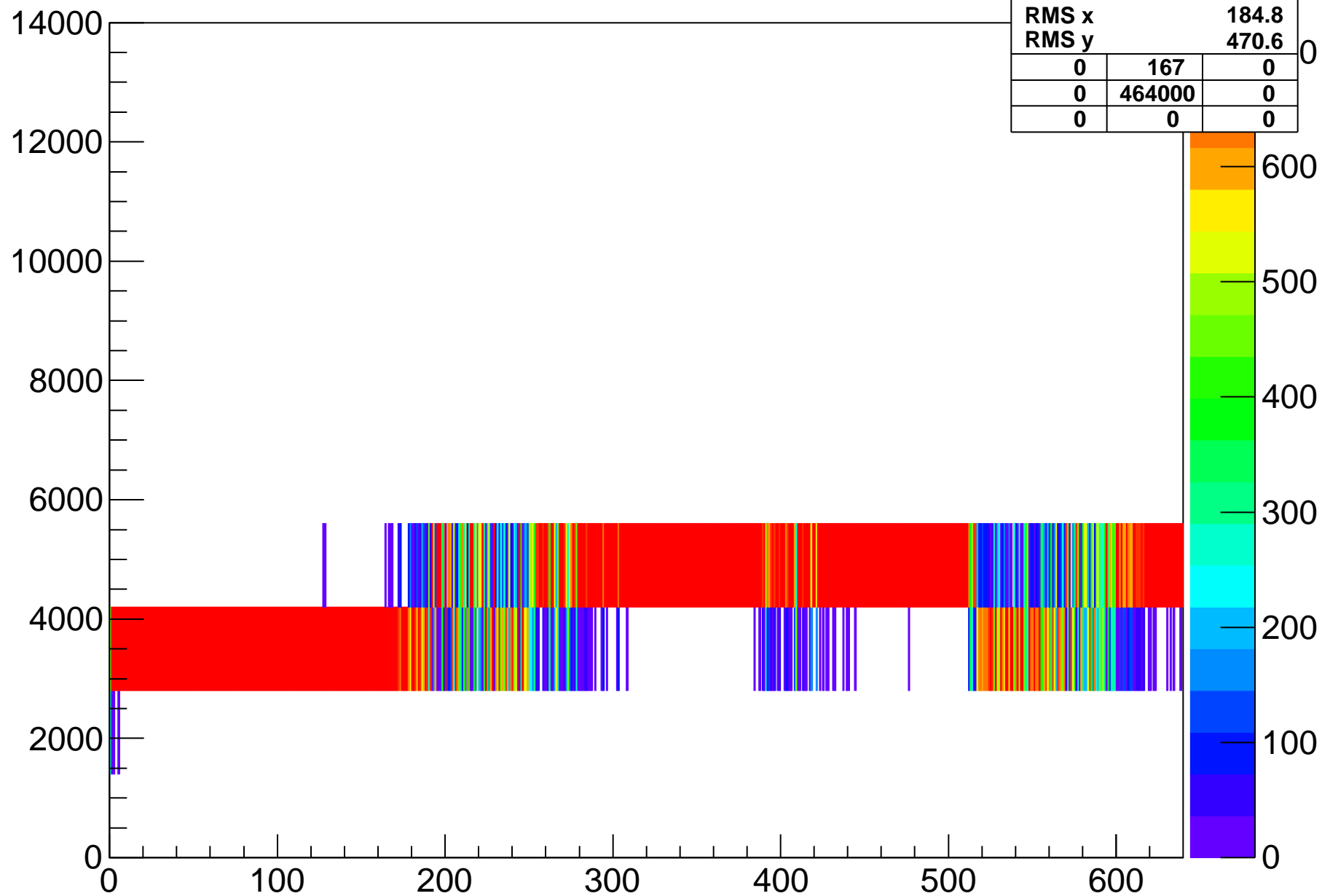
baselinesamples-fpga-5-hyb-0-sample-5

Entries	464199	
Mean x	319.5	
Mean y	4060	
RMS x	184.8	
RMS y	260.9	
0	199	0
0	464000	0
0	0	0



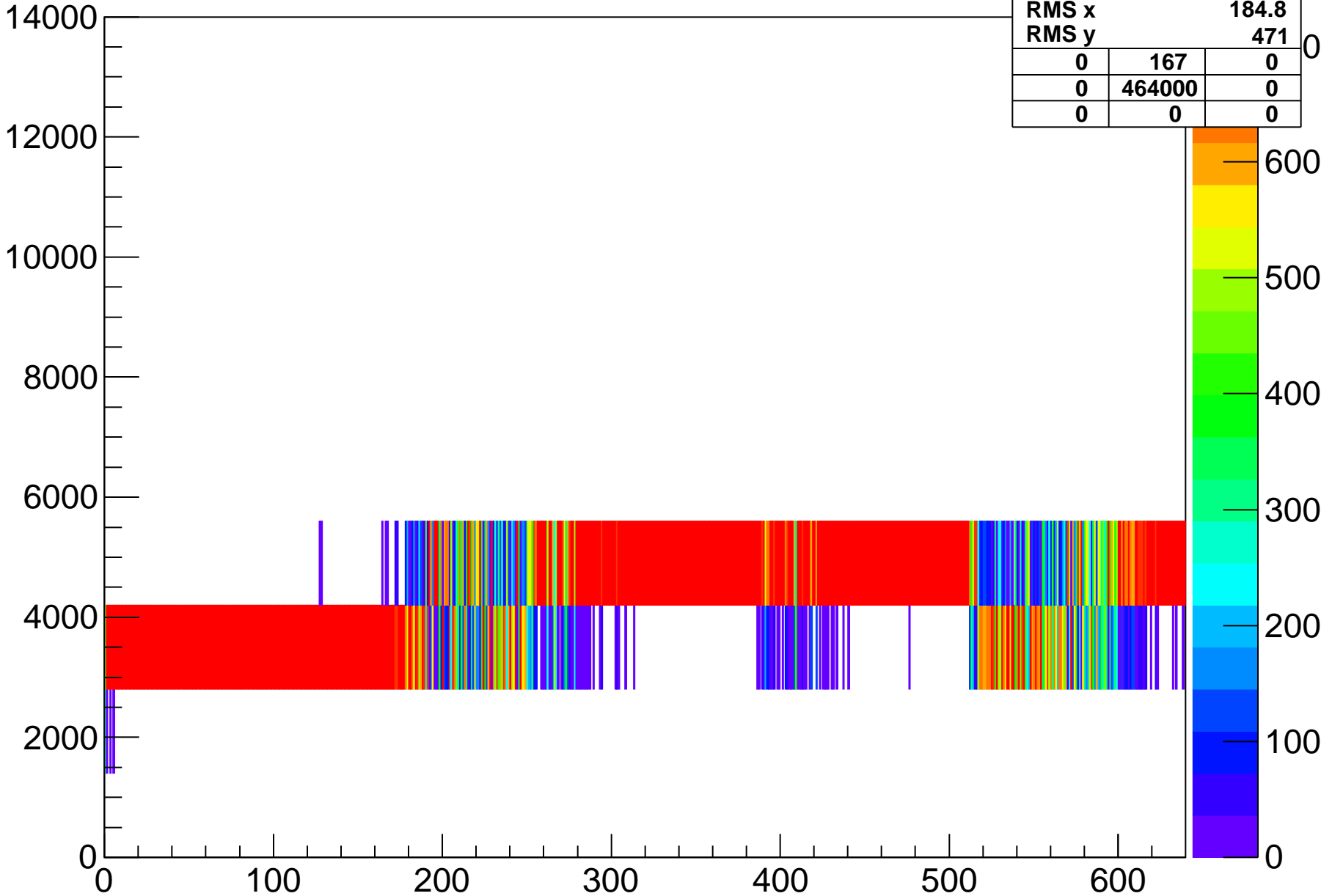
baselinesamples-fpga-5-hyb-1-sample-0

Entries	464167	
Mean x	319.5	
Mean y	4137	
RMS x	184.8	
RMS y	470.6	
0	167	0
0	464000	0
0	0	0



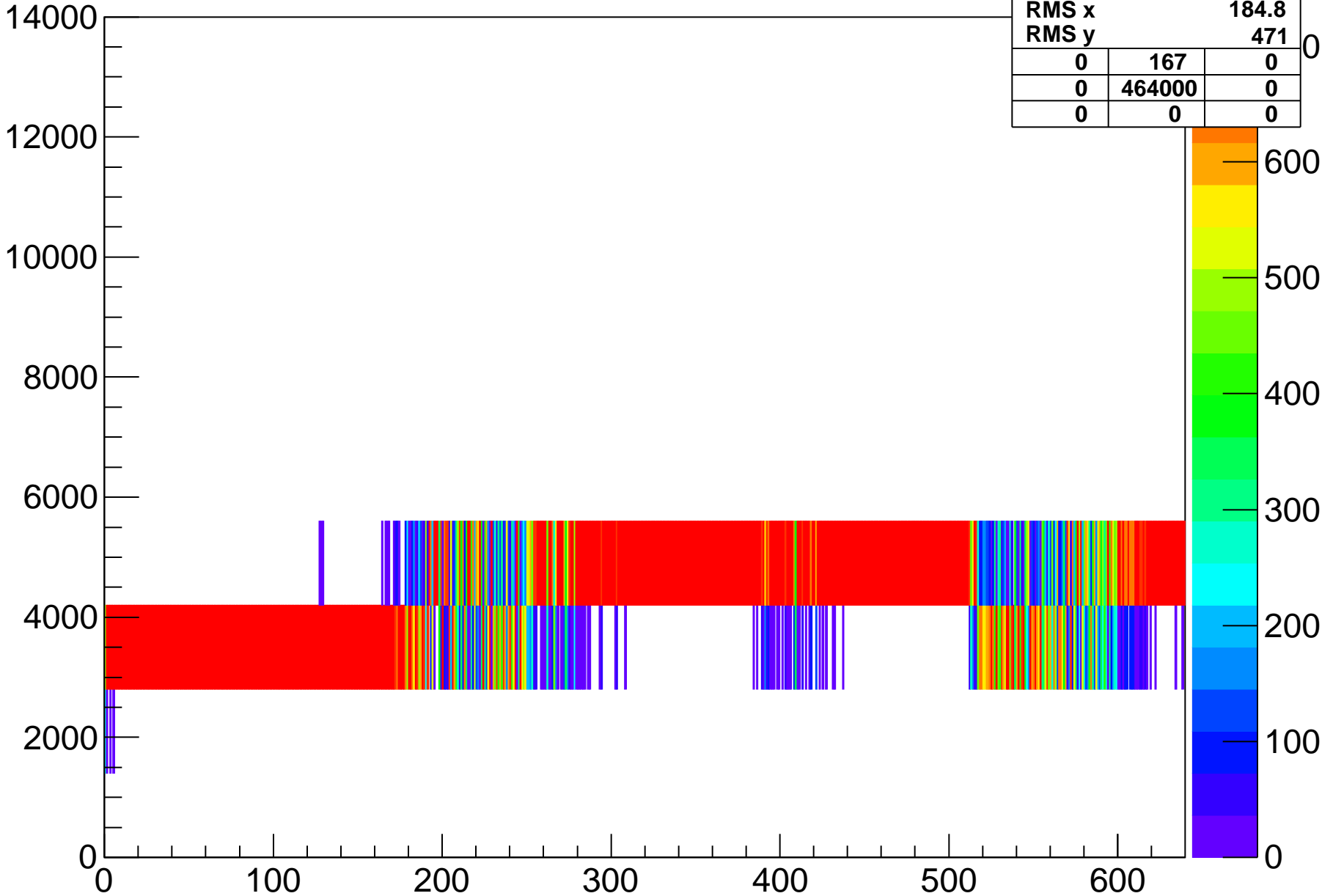
baselinesamples-fpga-5-hyb-1-sample-1
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Entries	464167	
Mean x	319.5	
Mean y	4142	
RMS x	184.8	
RMS y	471	
0	167	0
0	464000	0
0	0	0



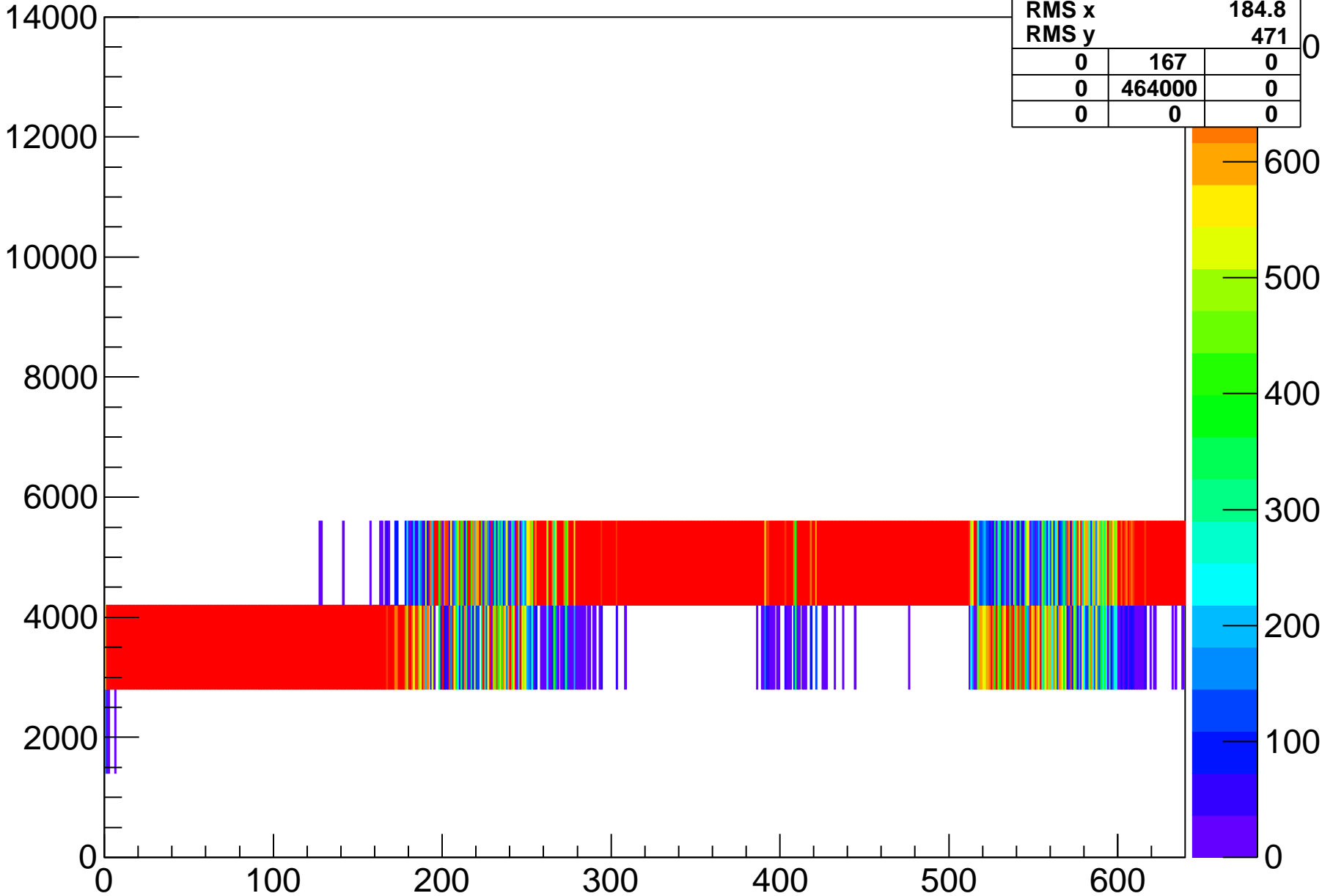
baselinesamples-fpga-5-hyb-1-sample-2

Entries	464167	
Mean x	319.5	
Mean y	4149	
RMS x	184.8	
RMS y	471	
0	167	0
0	464000	0
0	0	0



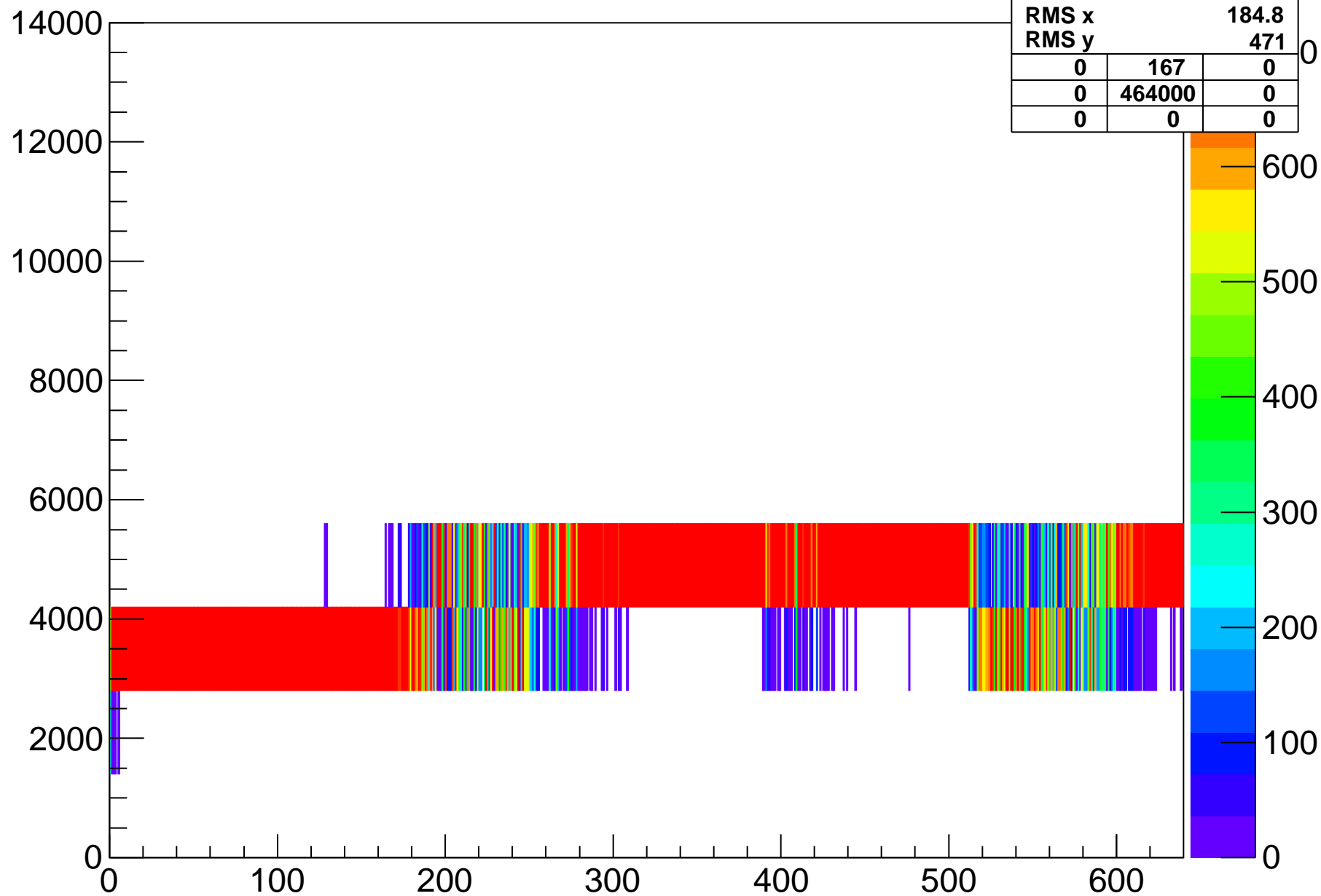
baselinesamples-fpga-5-hyb-1-sample-3
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Entries	464167	
Mean x	319.5	
Mean y	4153	
RMS x	184.8	
RMS y	471	
0	167	0
0	464000	0
0	0	0



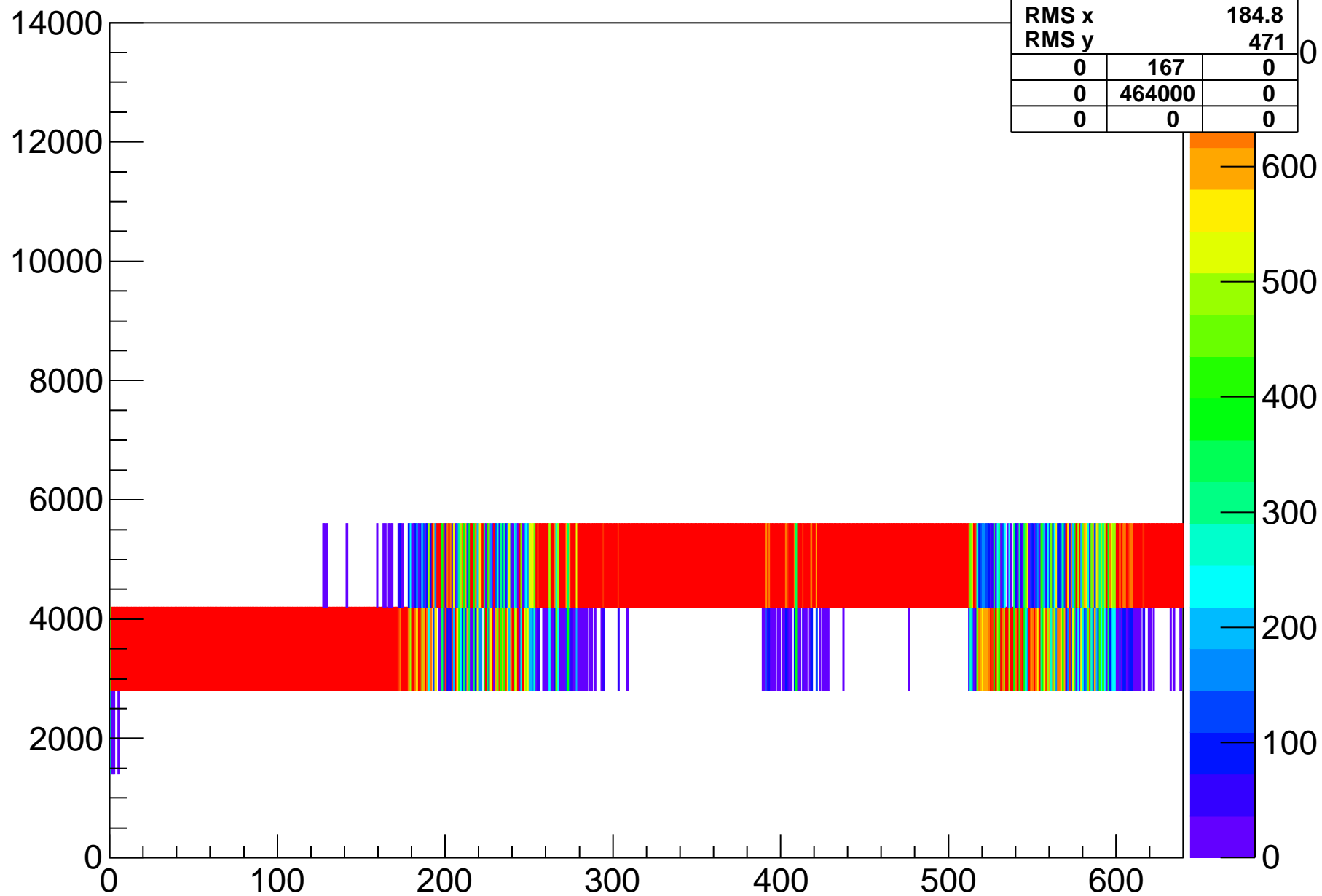
baselinesamples-fpga-5-hyb-1-sample-4

Entries	464167	
Mean x	319.5	
Mean y	4148	
RMS x	184.8	
RMS y	471	
0	167	0
0	464000	0
0	0	0



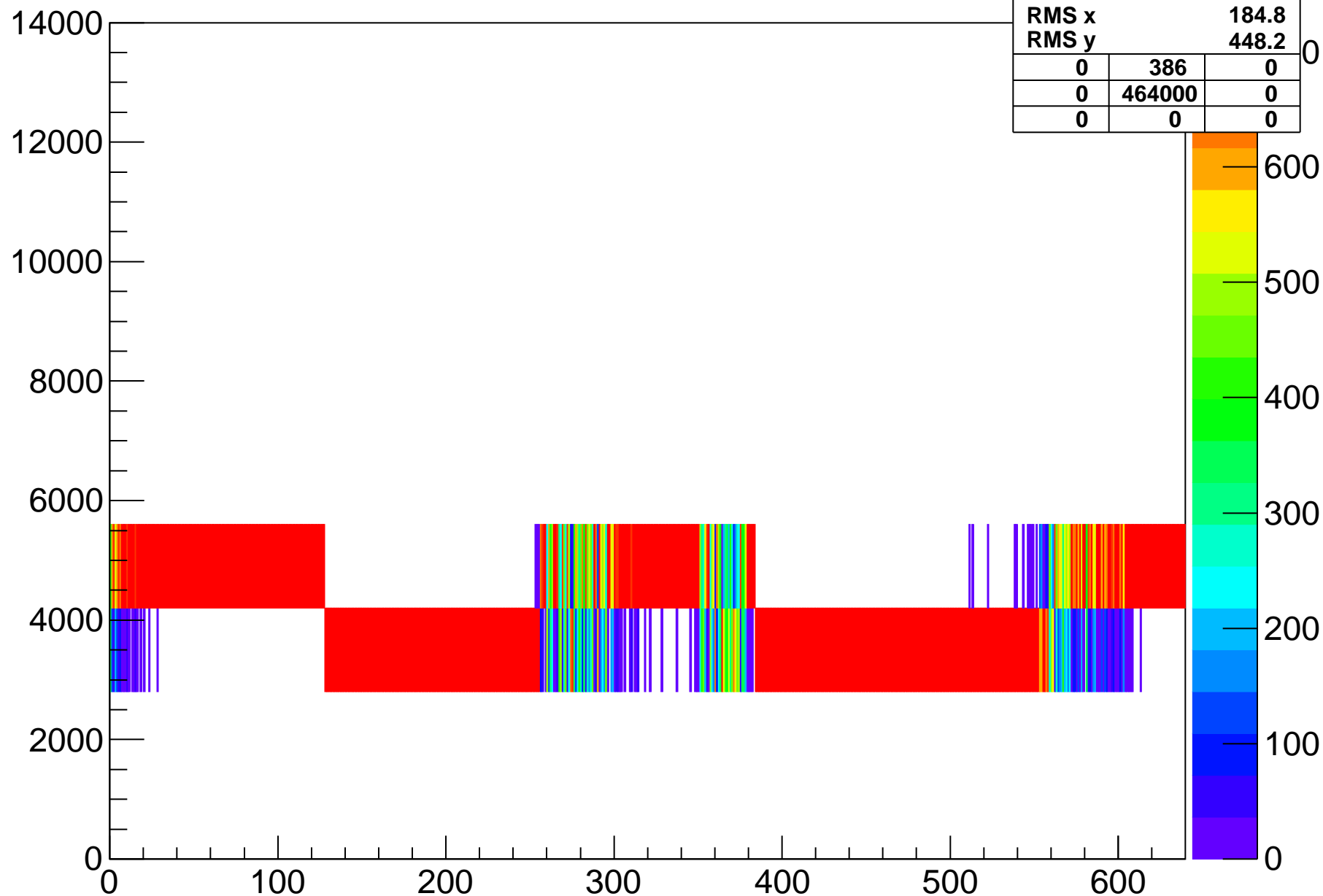
baselinesamples-fpga-5-hyb-1-sample-5

Entries	464167	
Mean x	319.5	
Mean y	4149	
RMS x	184.8	
RMS y	471	
0	167	0
0	464000	0
0	0	0



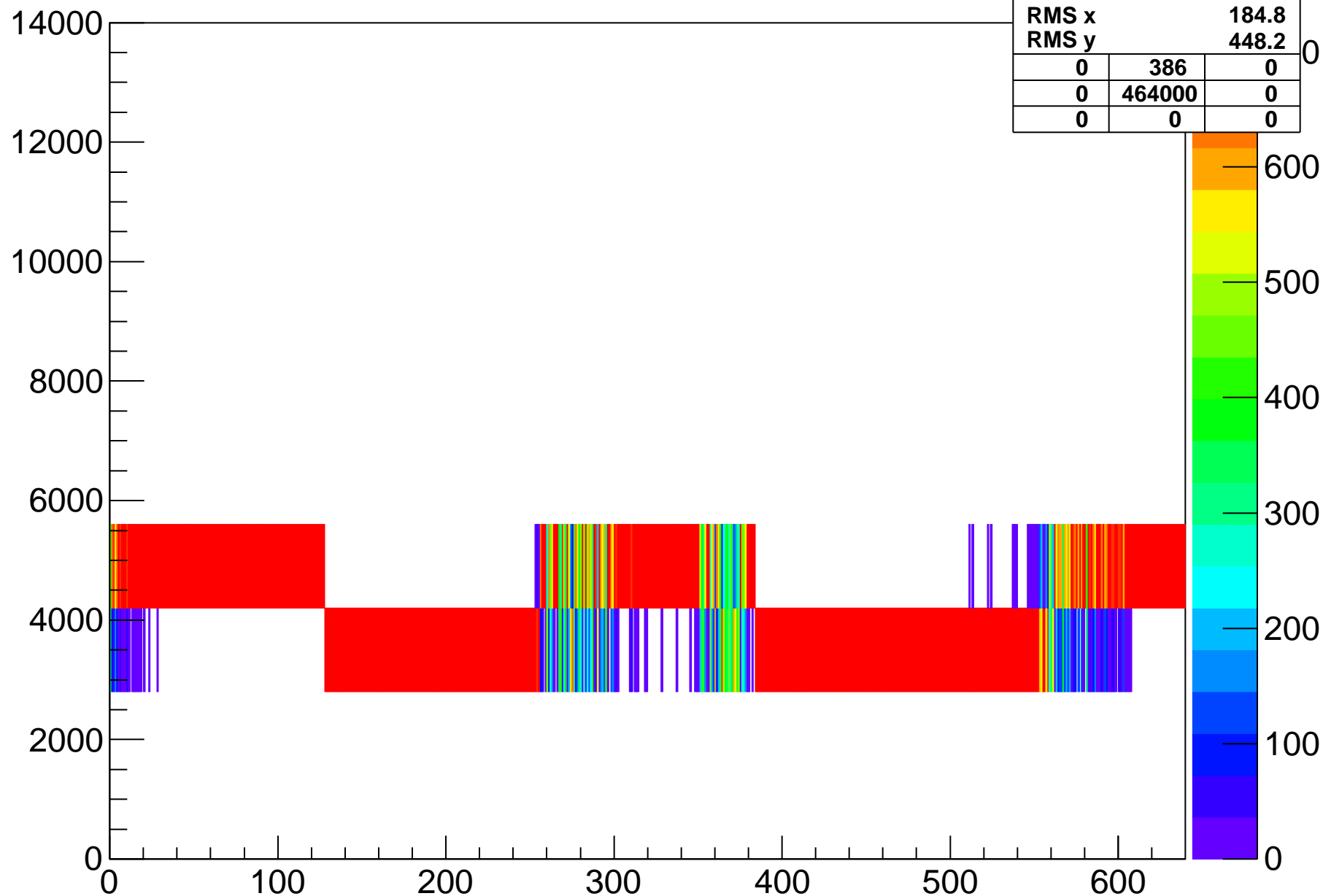
baselinesamples-fpga-5-hyb-2-sample-0

Entries	464386	
Mean x	319.5	
Mean y	4030	
RMS x	184.8	
RMS y	448.2	
0	386	0
0	464000	0
0	0	0



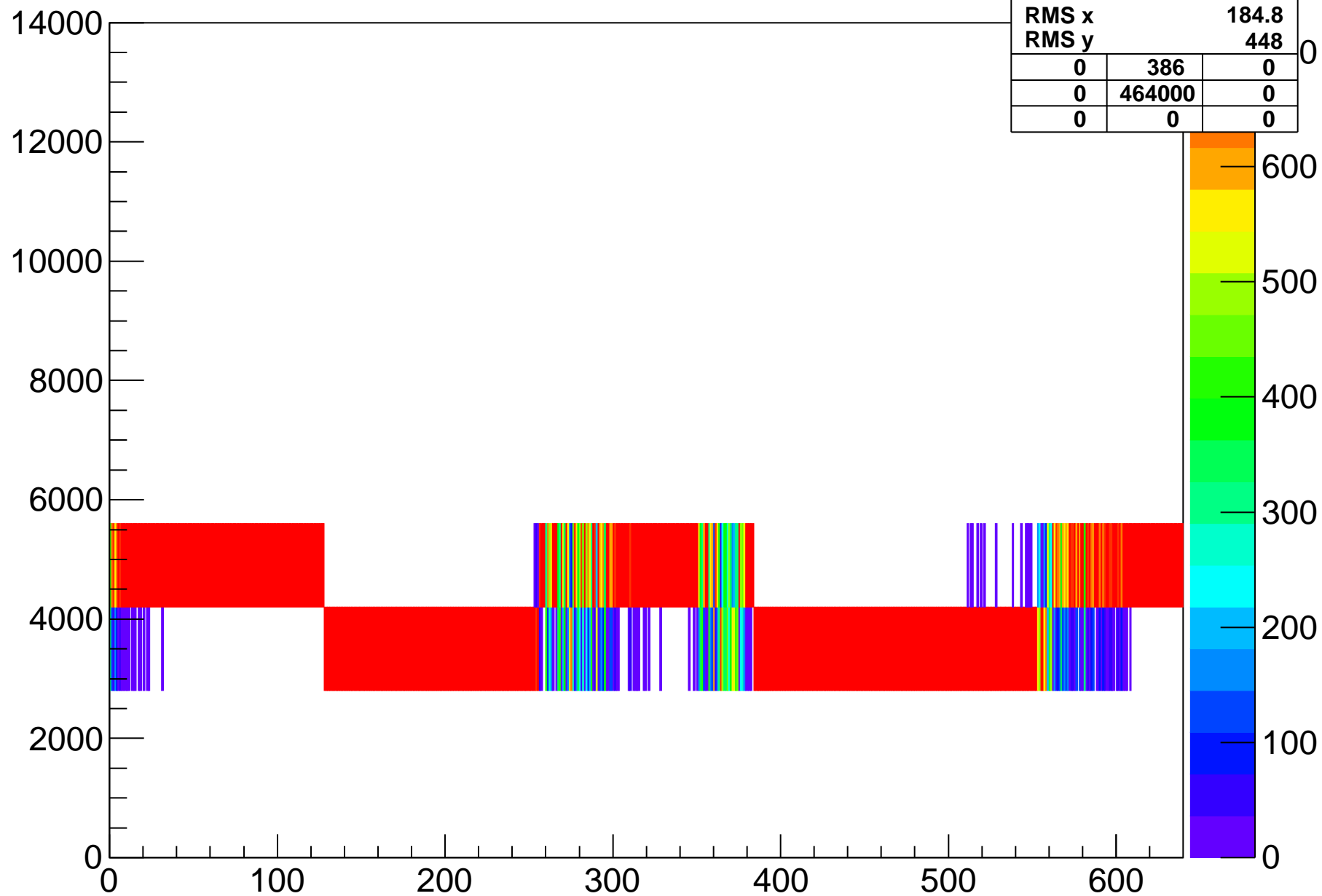
baselinesamples-fpga-5-hyb-2-sample-1

Entries	464386	
Mean x	319.5	
Mean y	4041	
RMS x	184.8	
RMS y	448.2	
0	386	0
0	464000	0
0	0	0



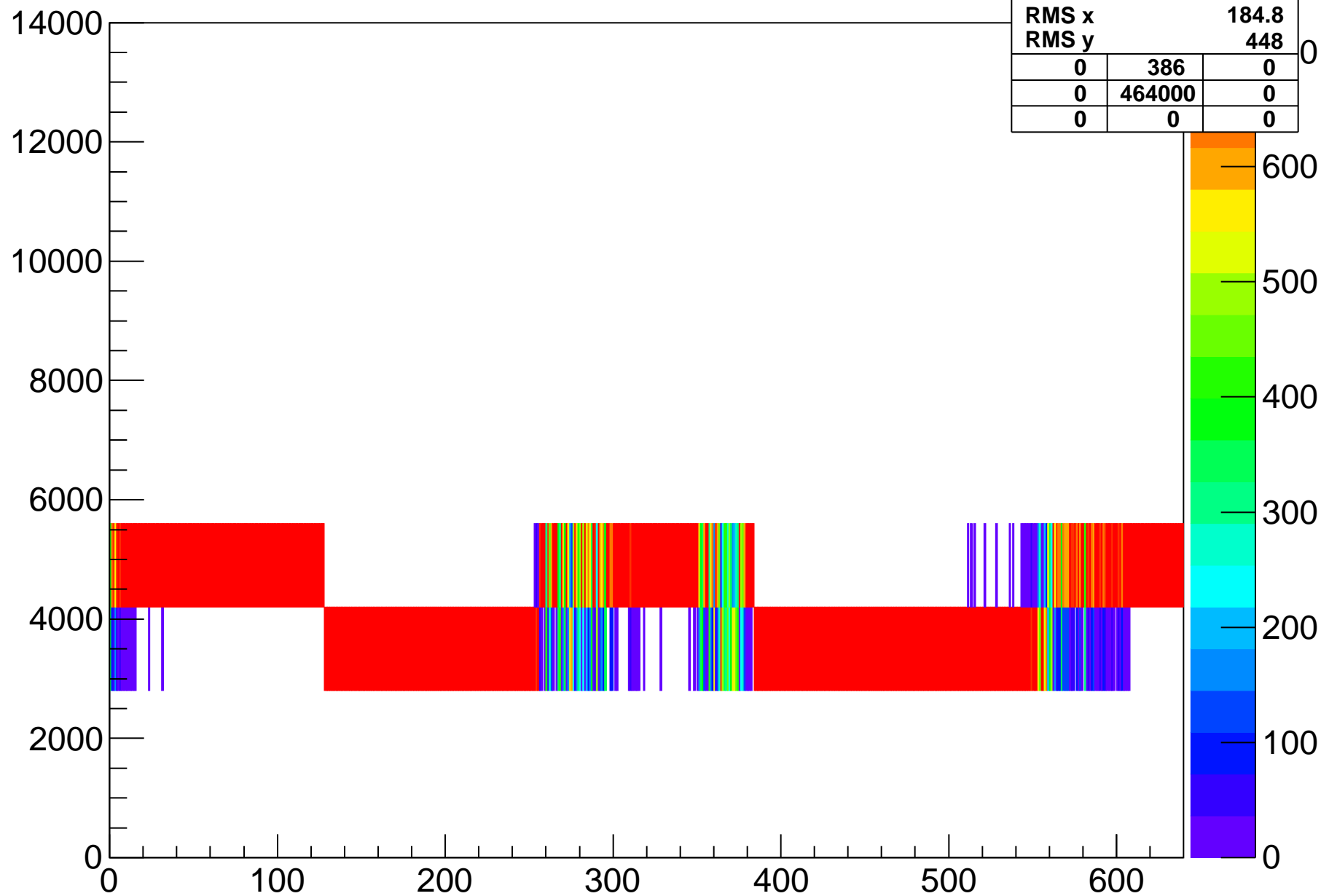
baselinesamples-fpga-5-hyb-2-sample-2

Entries	464386	
Mean x	319.5	
Mean y	4045	
RMS x	184.8	
RMS y	448	
0	386	0
0	464000	0
0	0	0



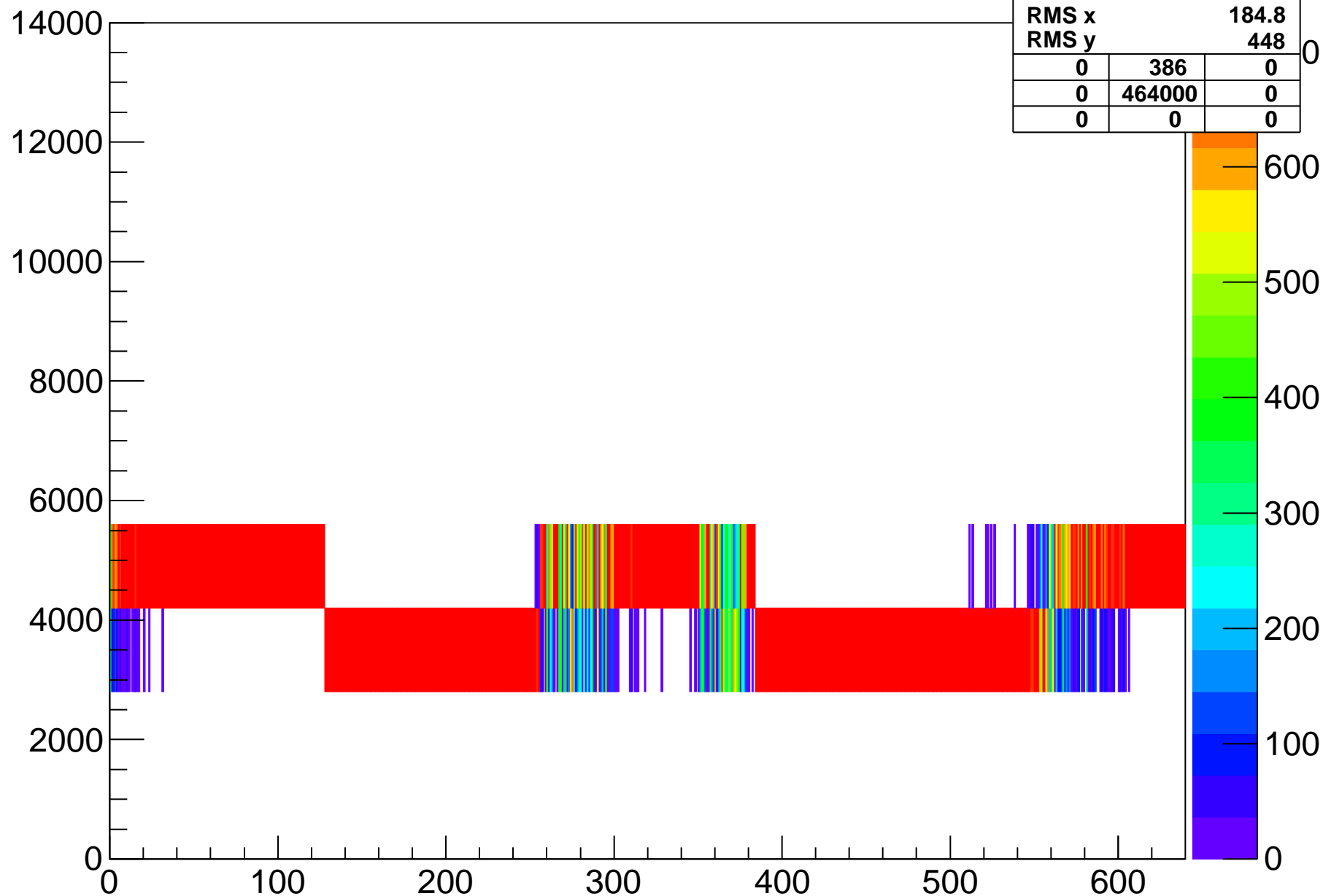
baselinesamples-fpga-5-hyb-2-sample-3

Entries	464386	
Mean x	319.5	
Mean y	4049	
RMS x	184.8	
RMS y	448	
0	386	0
0	464000	0
0	0	0



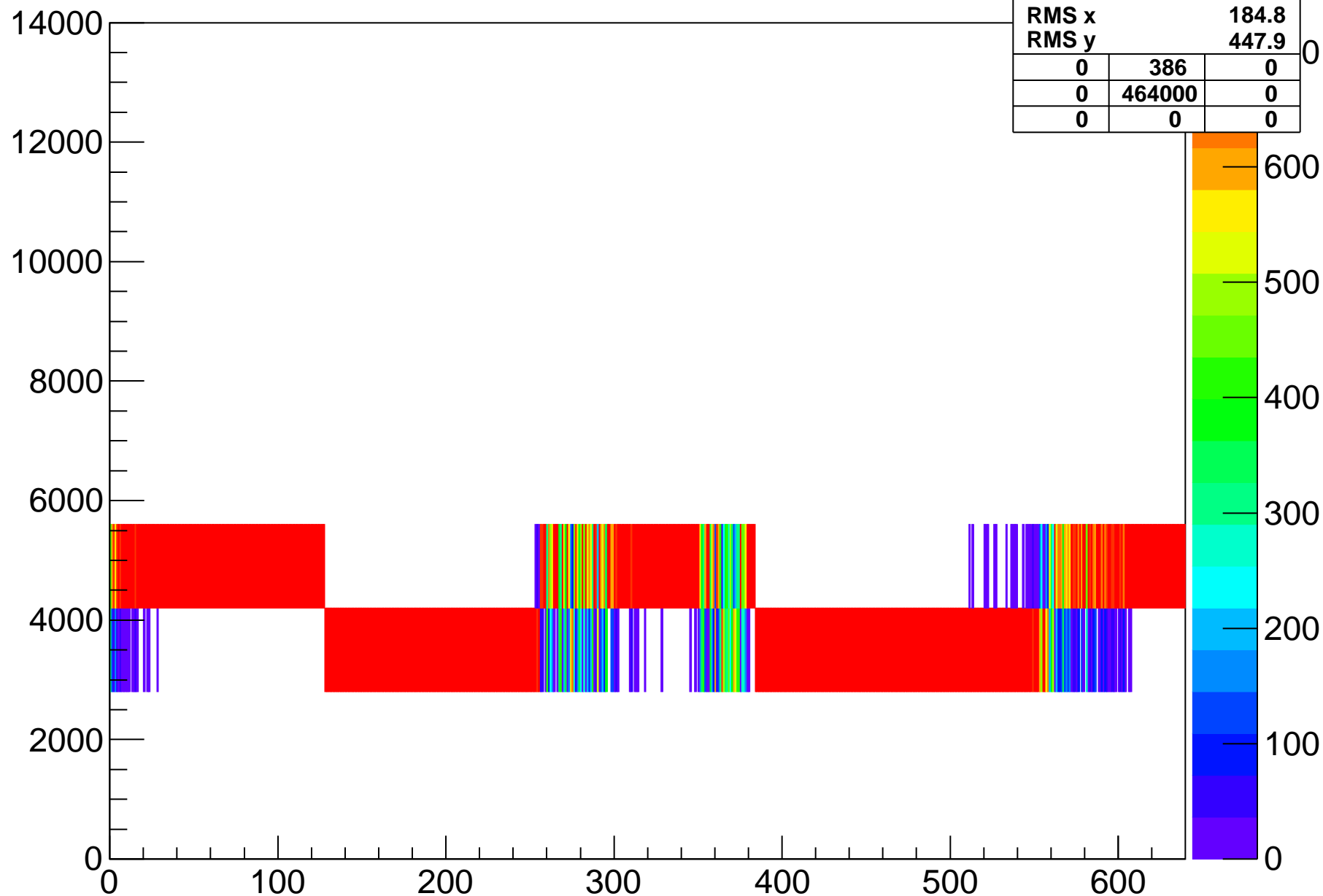
baselinesamples-fpga-5-hyb-2-sample-4

Entries	464386	
Mean x	319.5	
Mean y	4045	
RMS x	184.8	
RMS y	448	
0	386	0
0	464000	0
0	0	0



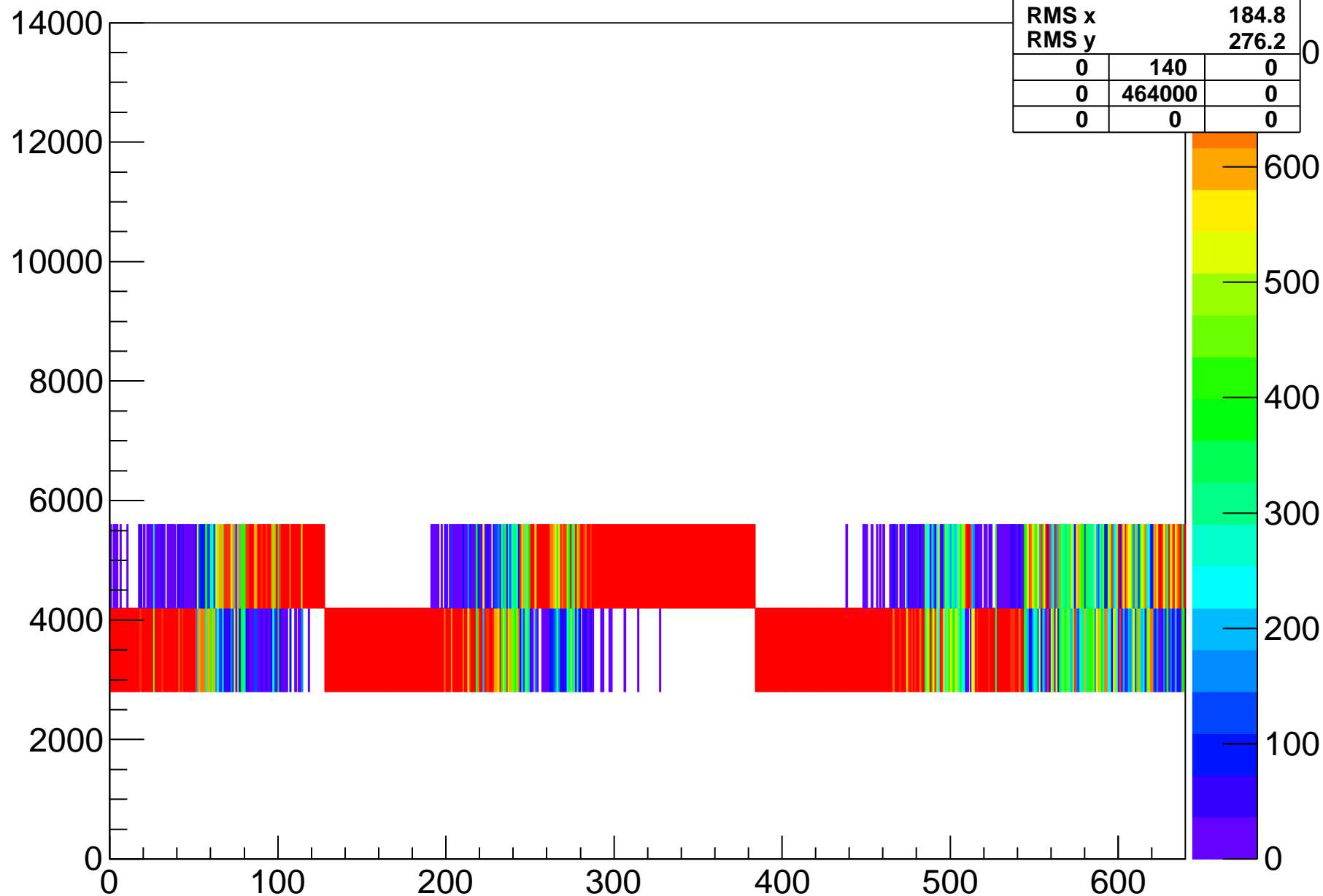
baselinesamples-fpga-5-hyb-2-sample-5

Entries	464386	
Mean x	319.5	
Mean y	4046	
RMS x	184.8	
RMS y	447.9	
0	386	0
0	464000	0
0	0	0



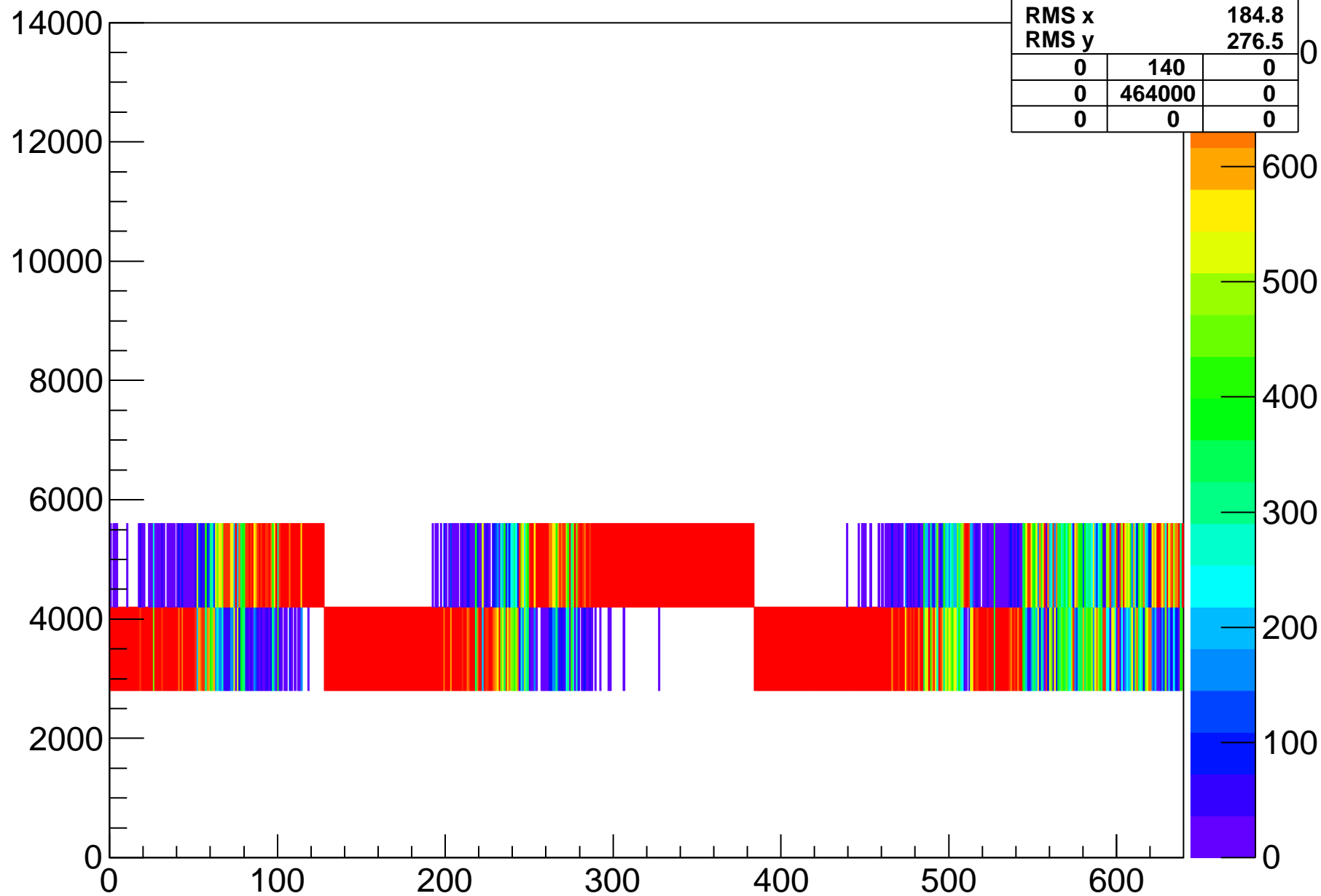
baselinesamples-fpga-5-hyb-3-sample-0

Entries	464140	
Mean x	319.5	
Mean y	4148	
RMS x	184.8	
RMS y	276.2	
0	140	0
0	464000	0
0	0	0



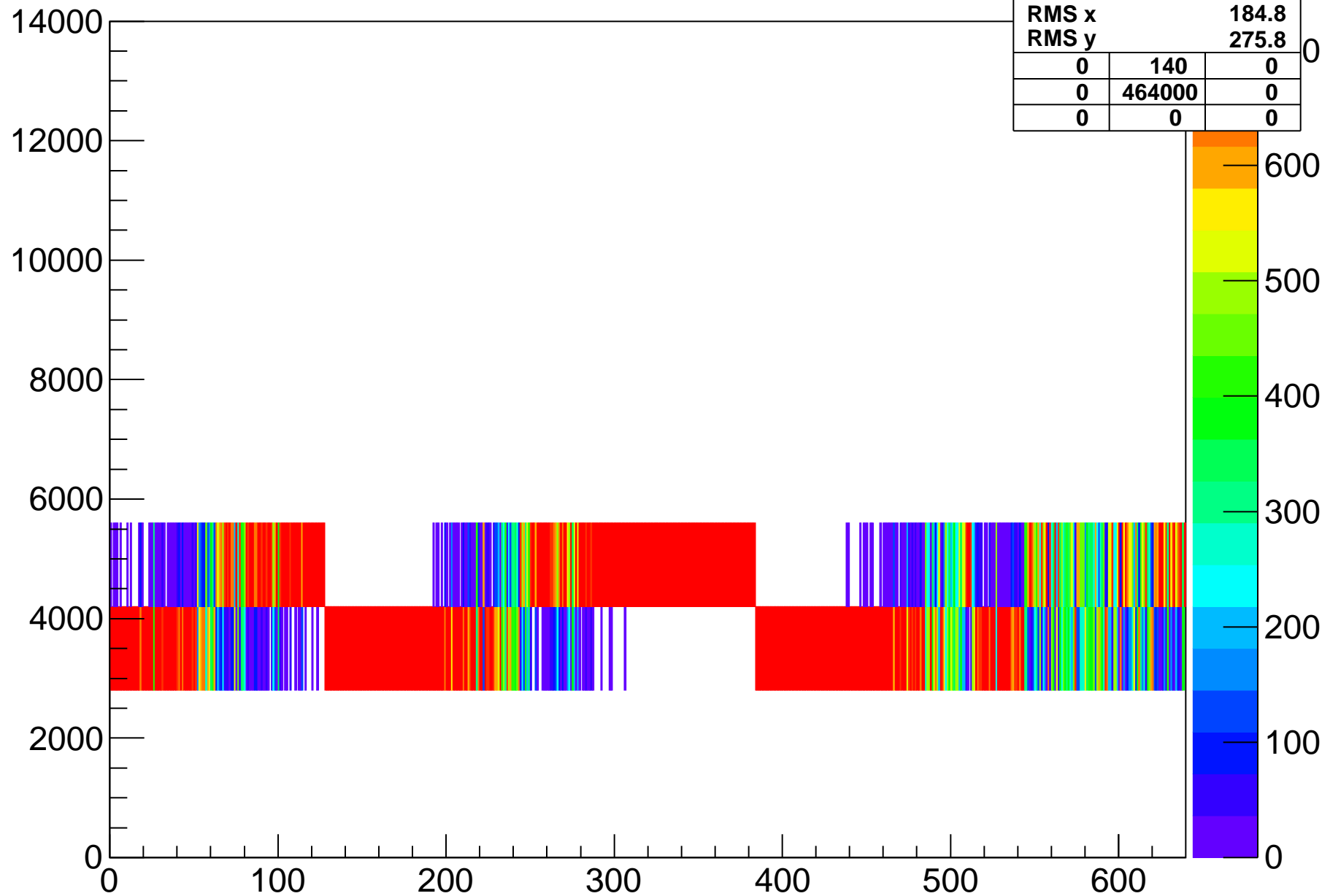
baselinesamples-fpga-5-hyb-3-sample-1

Entries	464140	
Mean x	319.5	
Mean y	4144	
RMS x	184.8	
RMS y	276.5	
0	140	0
0	464000	0
0	0	0



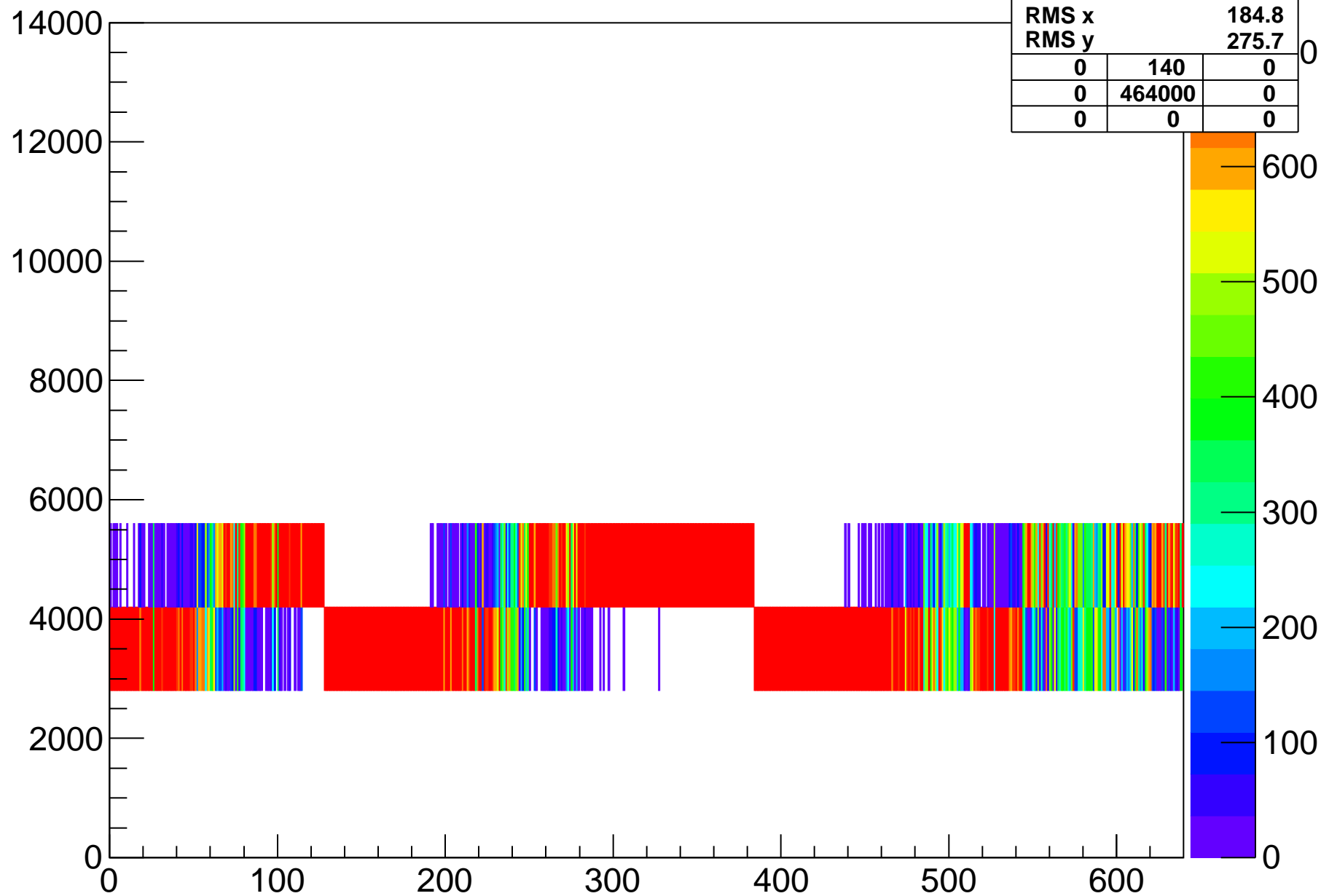
baselinesamples-fpga-5-hyb-3-sample-2

Entries	464140	
Mean x	319.5	
Mean y	4158	
RMS x	184.8	
RMS y	275.8	
0	140	0
0	464000	0
0	0	0



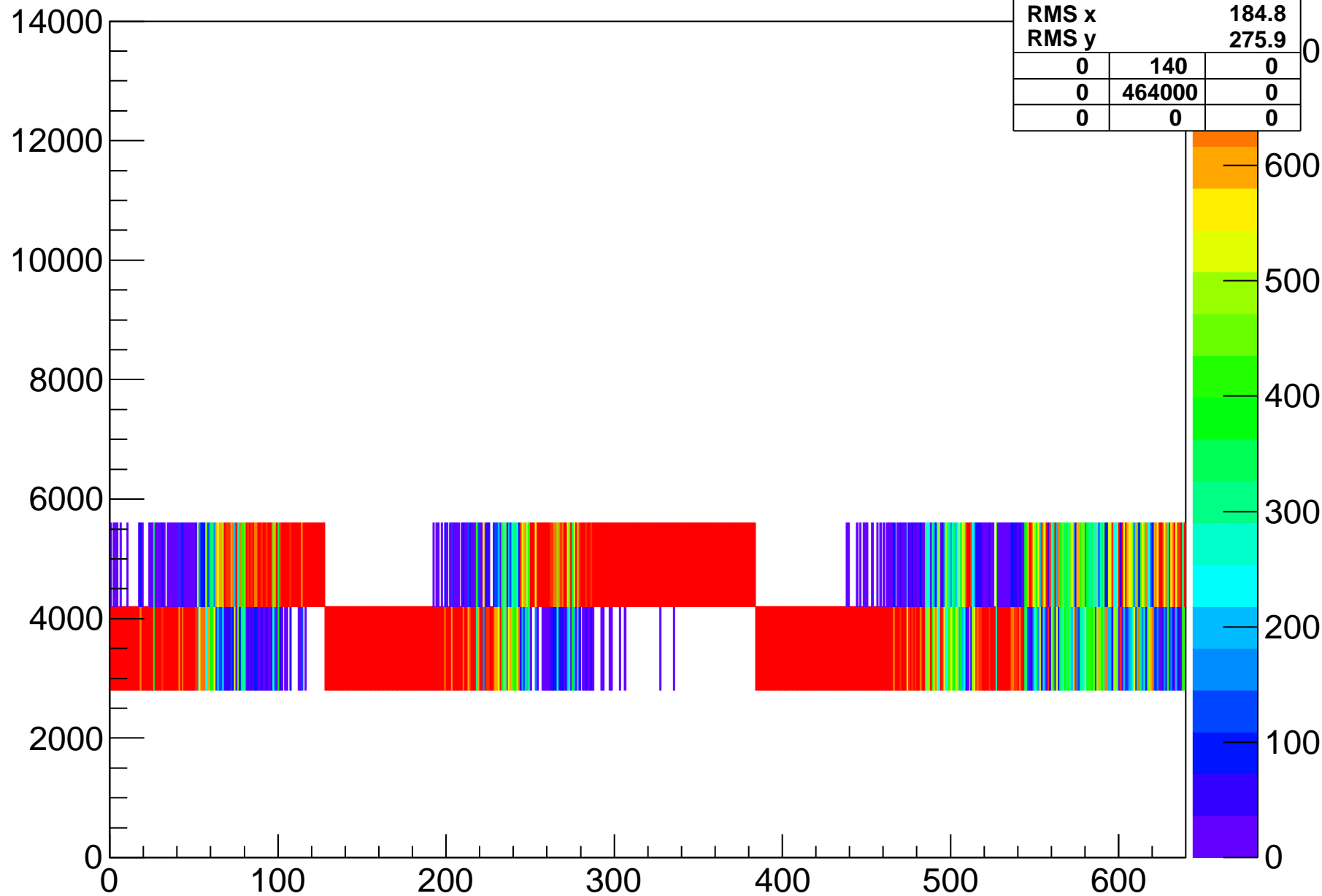
baselinesamples-fpga-5-hyb-3-sample-3

Entries	464140	
Mean x	319.5	
Mean y	4159	
RMS x	184.8	
RMS y	275.7	
0	140	0
0	464000	0
0	0	0



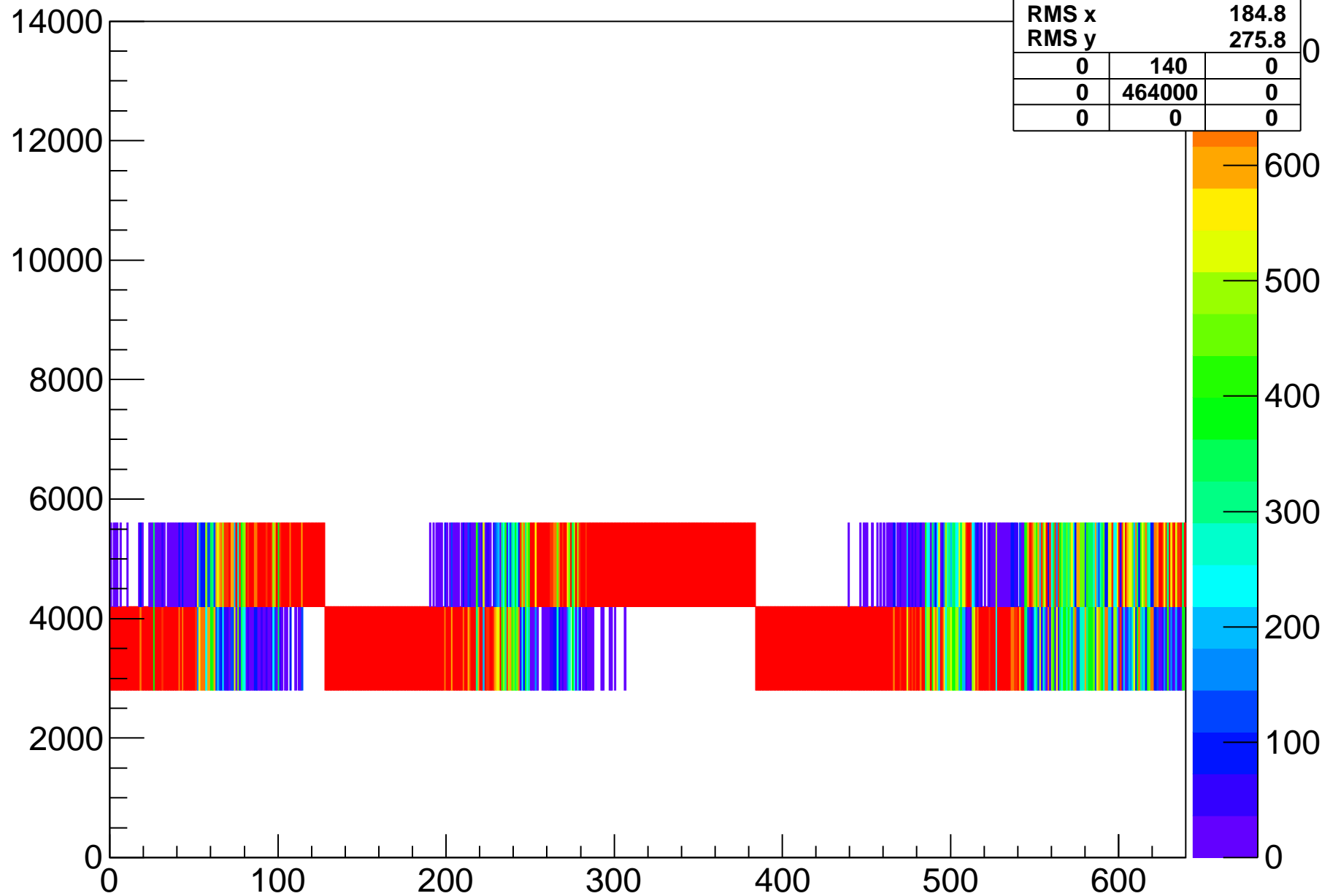
baselinesamples-fpga-5-hyb-3-sample-4

Entries	464140	
Mean x	319.5	
Mean y	4154	
RMS x	184.8	
RMS y	275.9	
0	140	0
0	464000	0
0	0	0



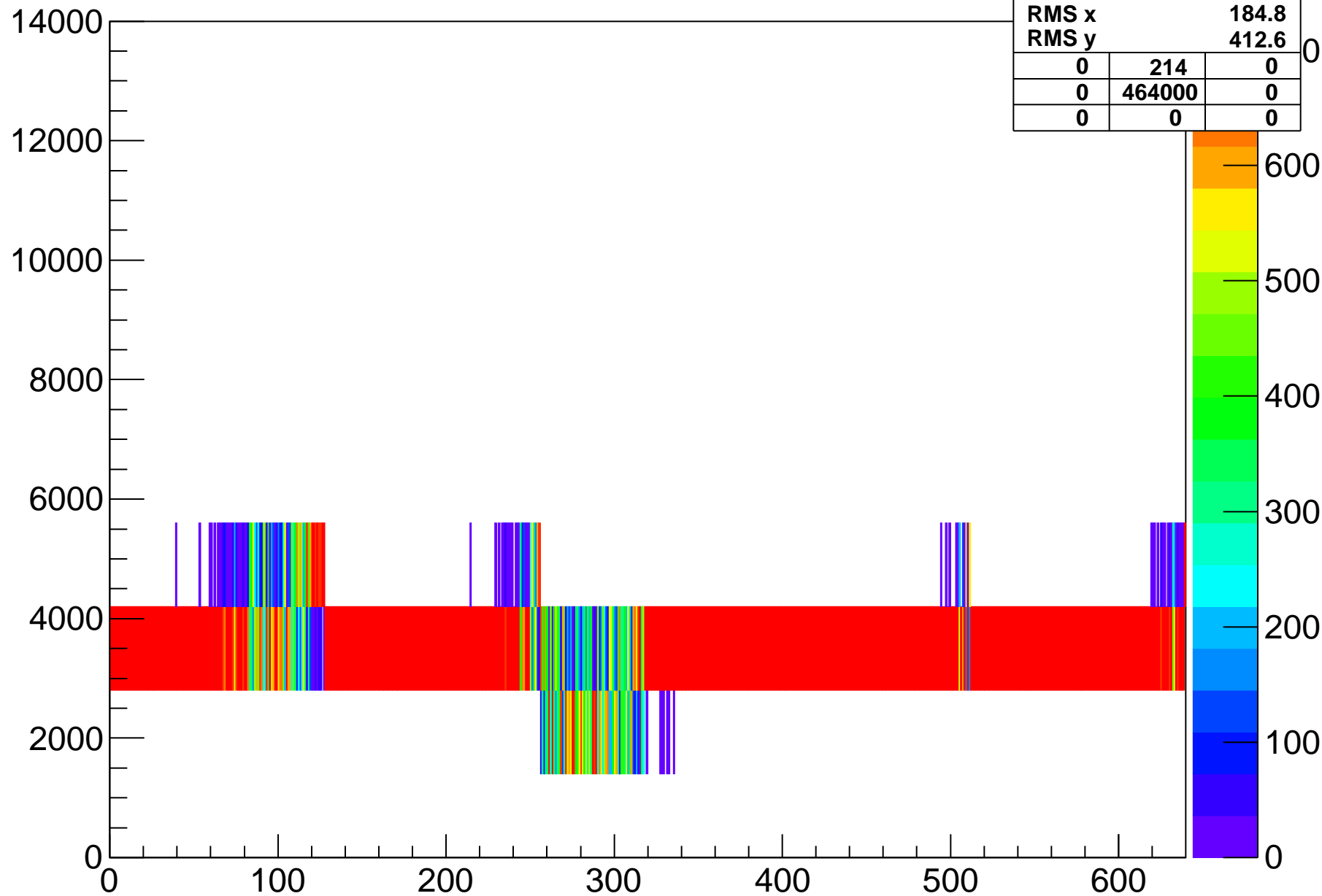
baselinesamples-fpga-5-hyb-3-sample-5

Entries	464140	
Mean x	319.5	
Mean y	4156	
RMS x	184.8	
RMS y	275.8	
0	140	0
0	464000	0
0	0	0



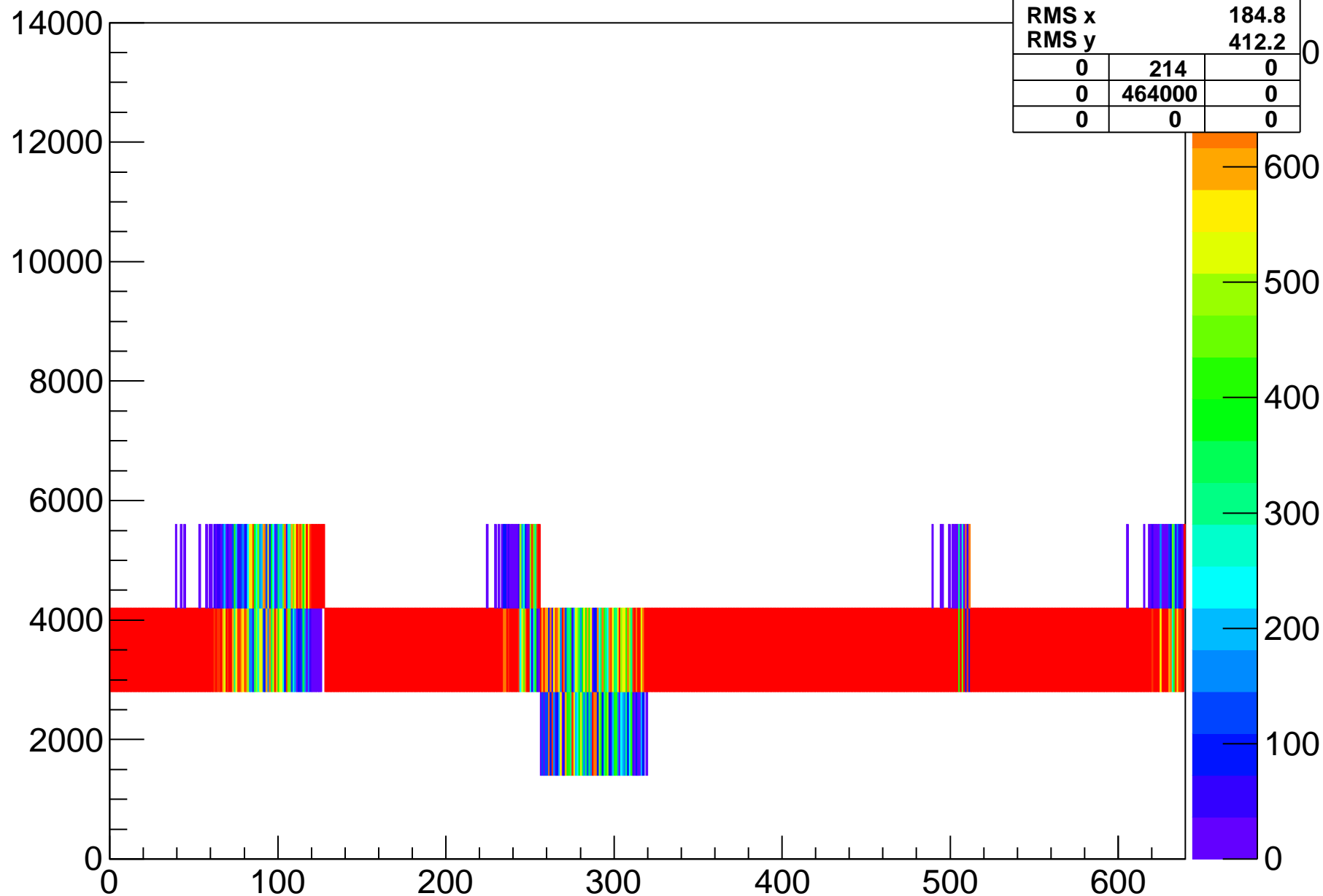
baselinesamples-fpga-6-hyb-0-sample-0

Entries	464214	
Mean x	319.5	
Mean y	3674	
RMS x	184.8	
RMS y	412.6	
0	214	0
0	464000	0
0	0	0



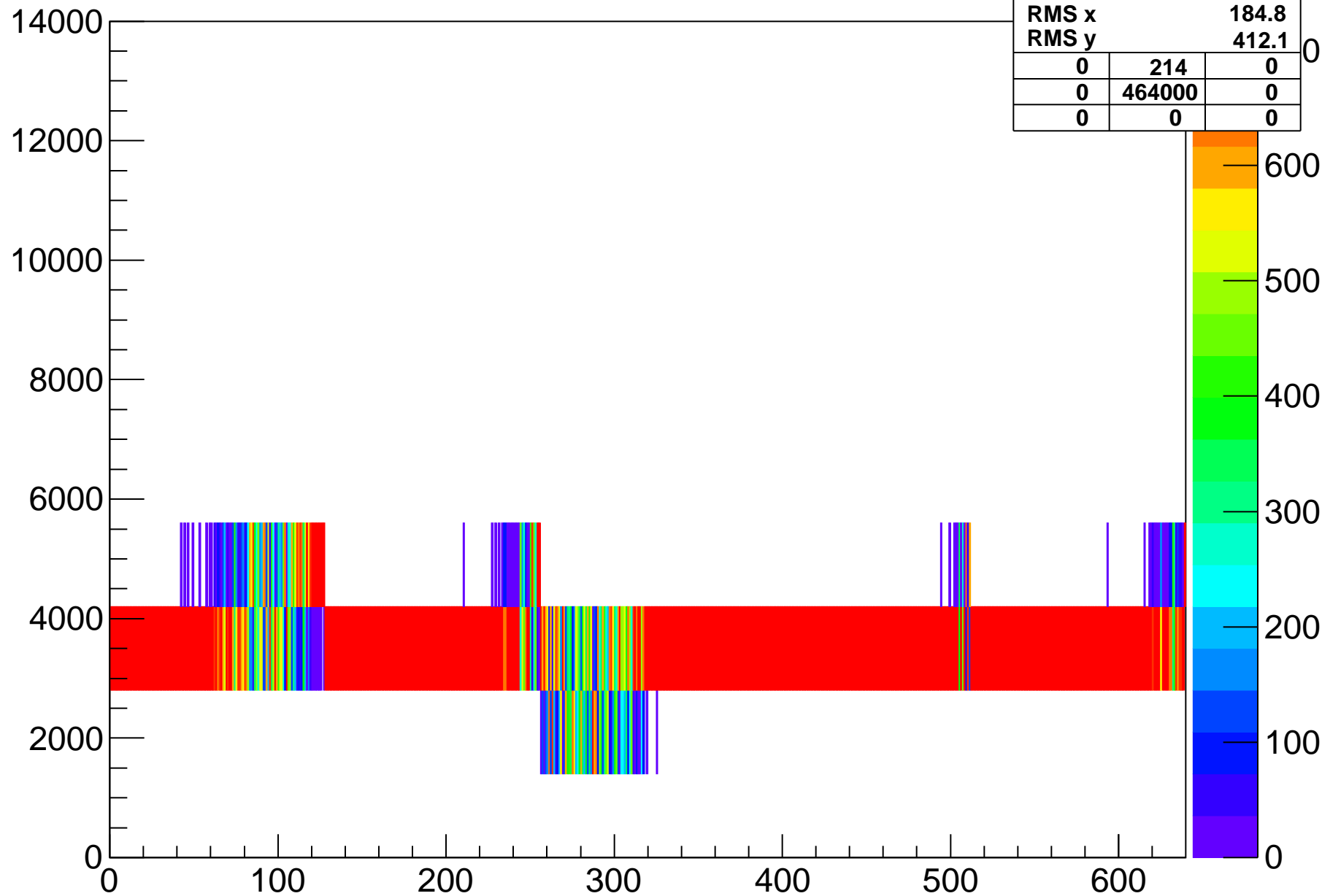
baselinesamples-fpga-6-hyb-0-sample-1

Entries	464214	
Mean x	319.5	
Mean y	3714	
RMS x	184.8	
RMS y	412.2	
0	214	0
0	464000	0
0	0	0



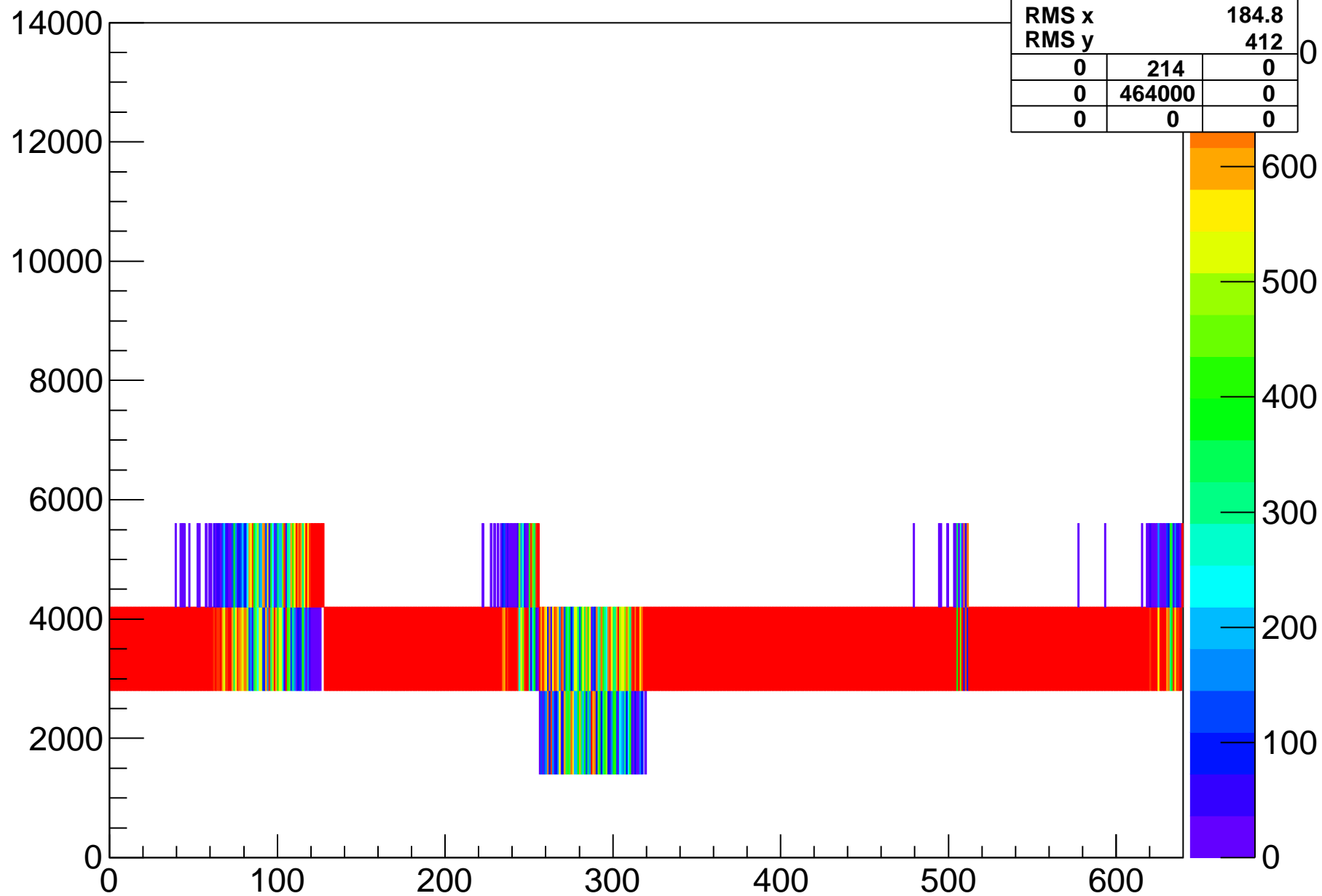
baselinesamples-fpga-6-hyb-0-sample-2

Entries	464214	
Mean x	319.5	
Mean y	3711	
RMS x	184.8	
RMS y	412.1	
0	214	0
0	464000	0
0	0	0



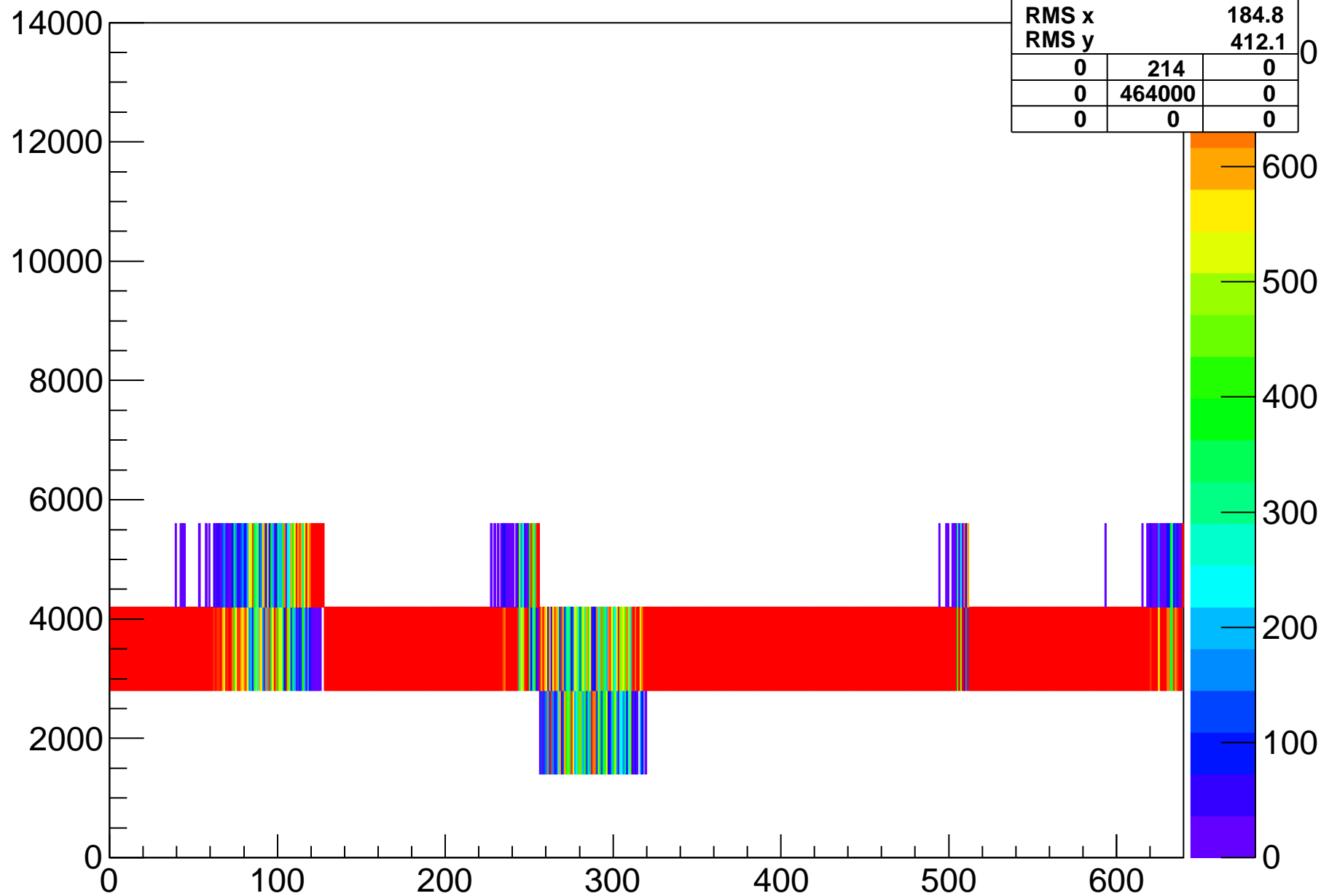
baselinesamples-fpga-6-hyb-0-sample-3

Entries	464214	
Mean x	319.5	
Mean y	3715	
RMS x	184.8	
RMS y	412	
0	214	0
0	464000	0
0	0	0



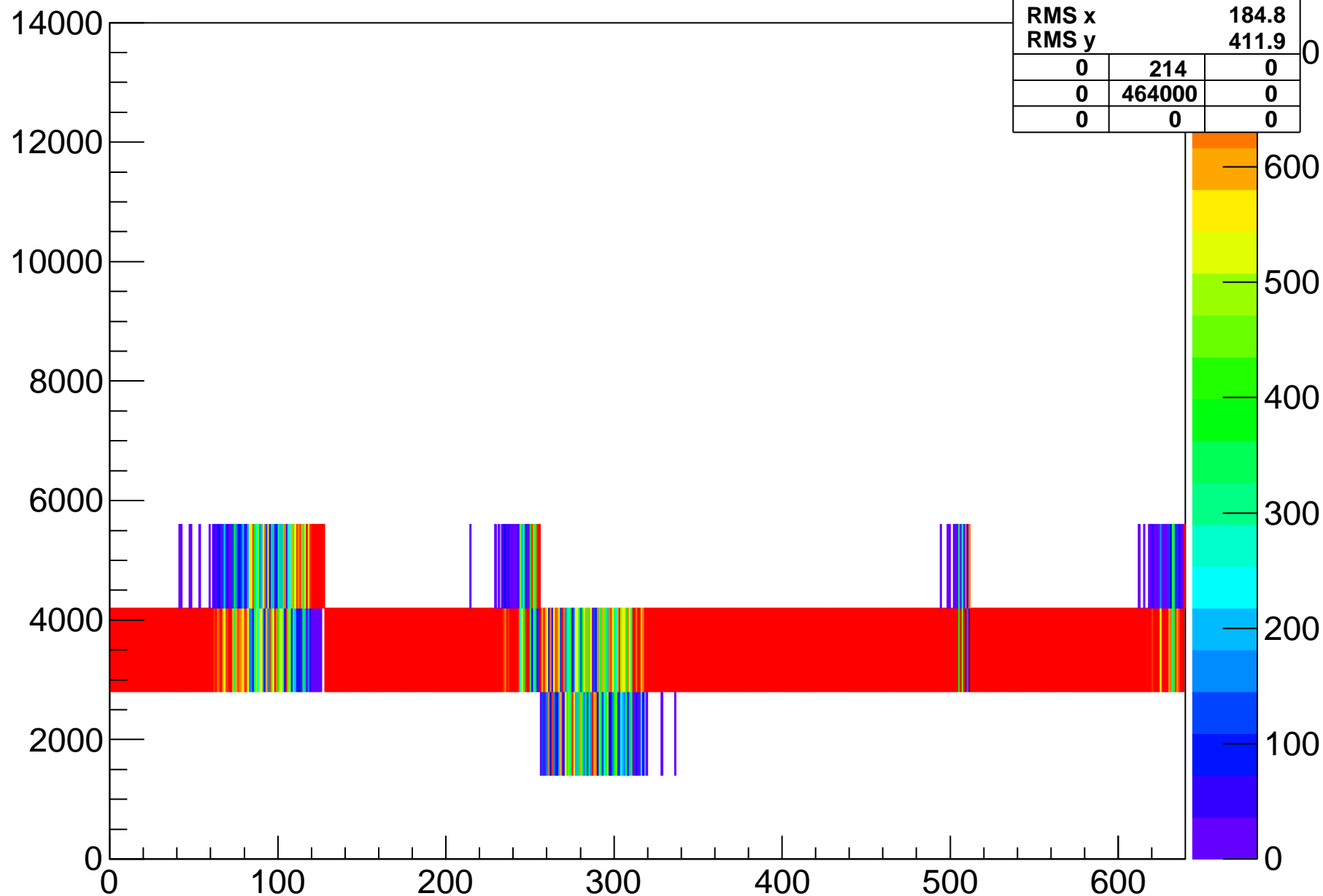
baselinesamples-fpga-6-hyb-0-sample-4

Entries	464214	
Mean x	319.5	
Mean y	3708	
RMS x	184.8	
RMS y	412.1	
0	214	0
0	464000	0
0	0	0



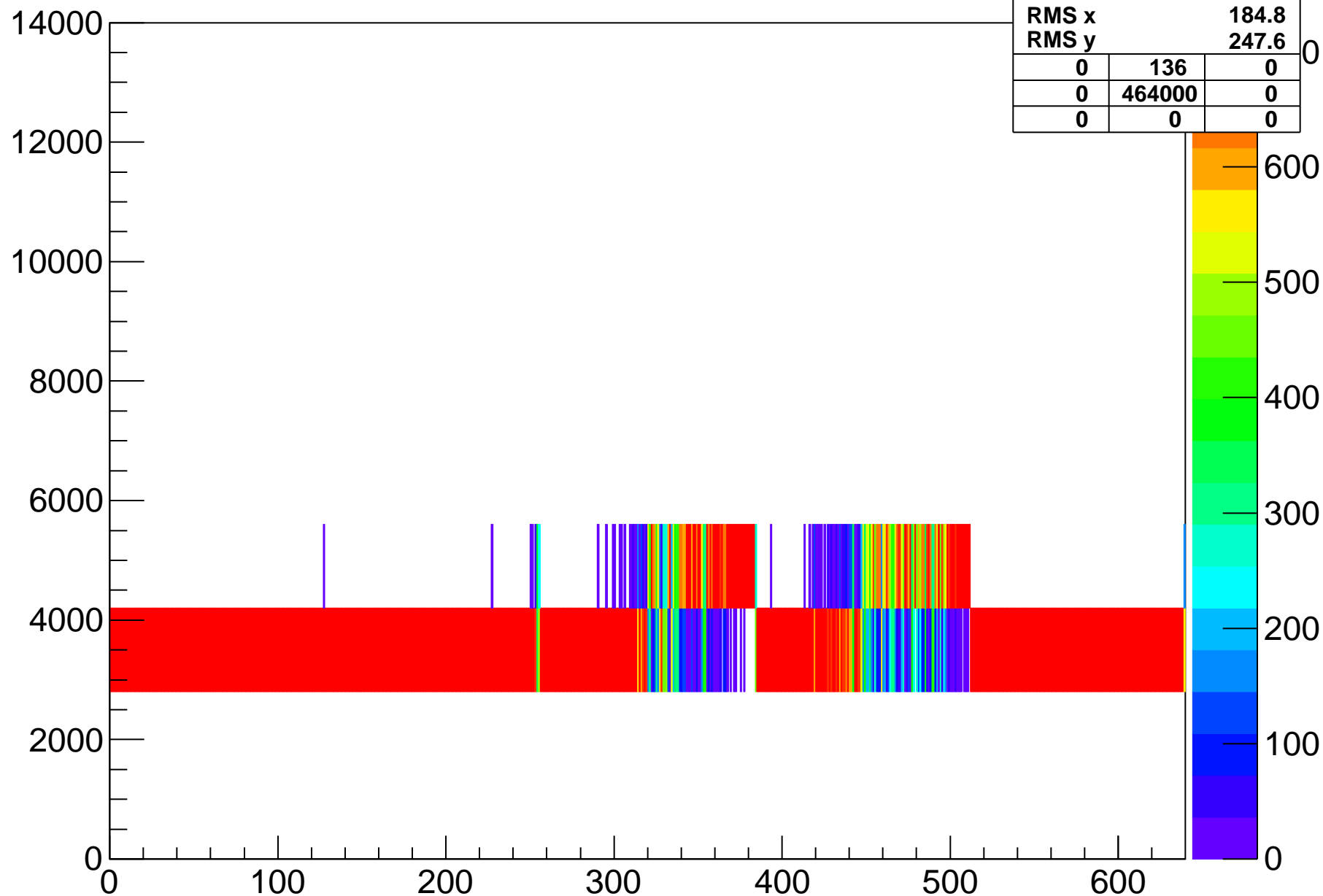
baselinesamples-fpga-6-hyb-0-sample-5

Entries	464214	
Mean x	319.5	
Mean y	3712	
RMS x	184.8	
RMS y	411.9	
0	214	0
0	464000	0
0	0	0



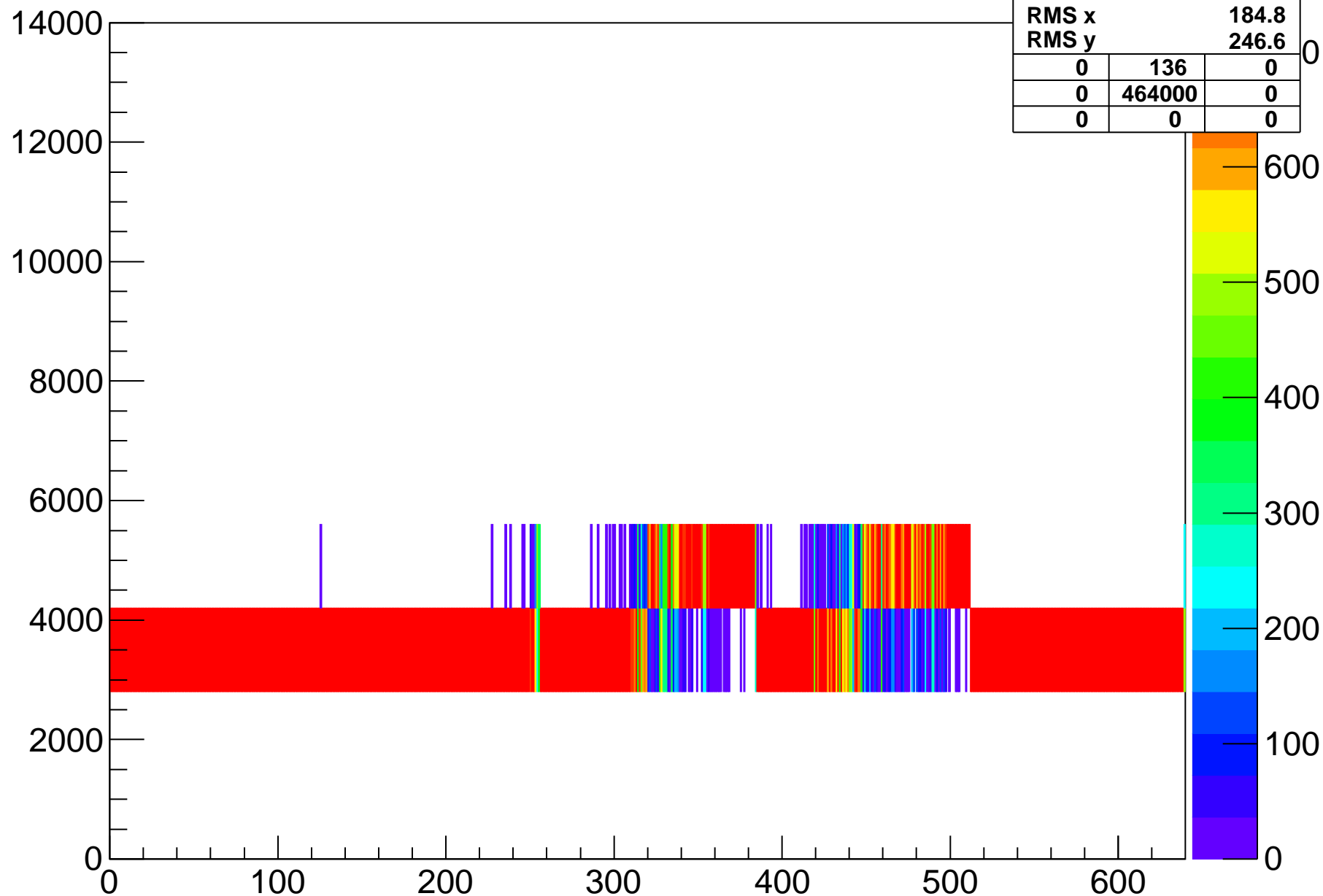
baselinesamples-fpga-6-hyb-1-sample-0

Entries	464136	
Mean x	319.5	
Mean y	3893	
RMS x	184.8	
RMS y	247.6	
0	136	0
0	464000	0
0	0	0



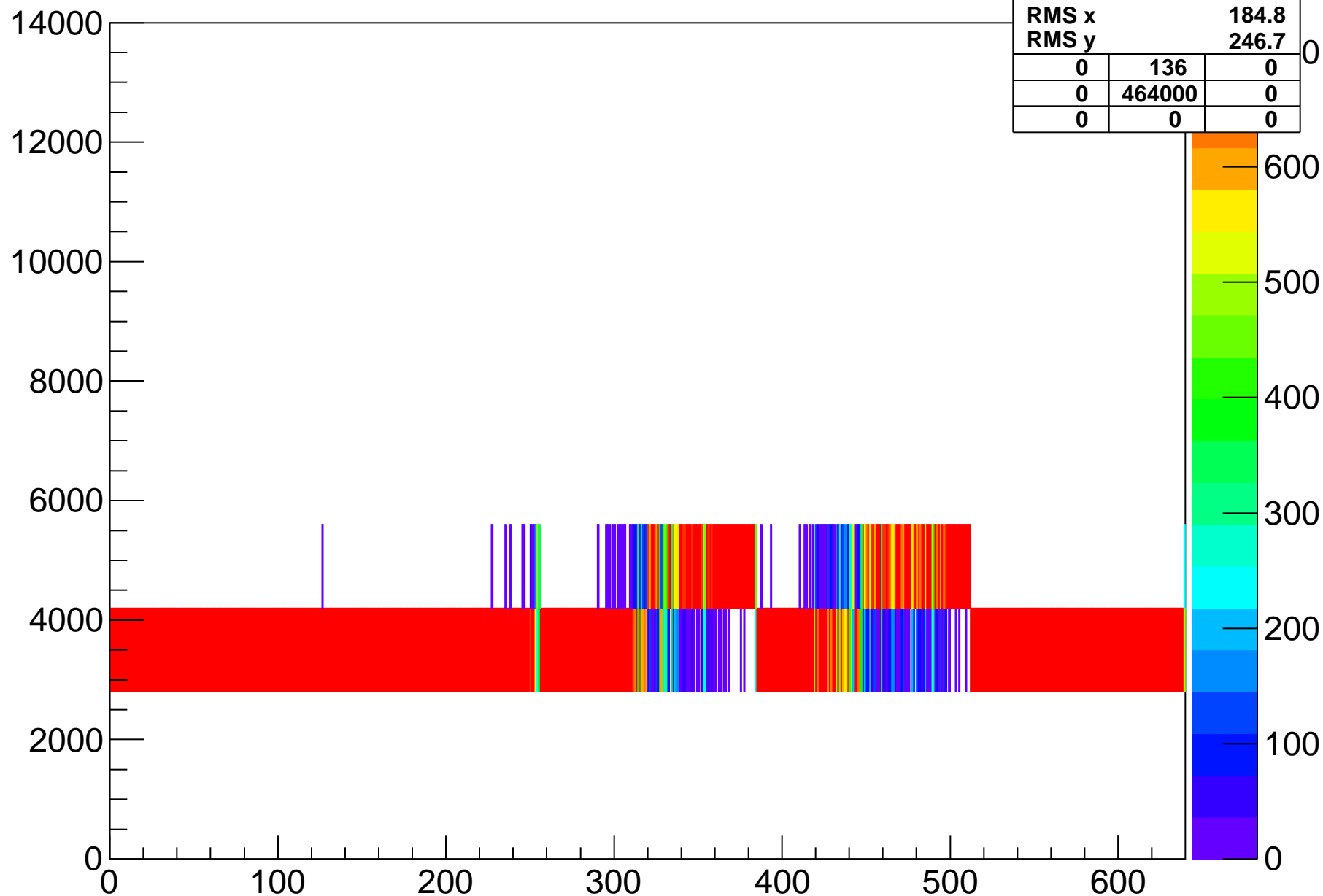
baselinesamples-fpga-6-hyb-1-sample-1

Entries	464136	
Mean x	319.5	
Mean y	3928	
RMS x	184.8	
RMS y	246.6	
0	136	0
0	464000	0
0	0	0



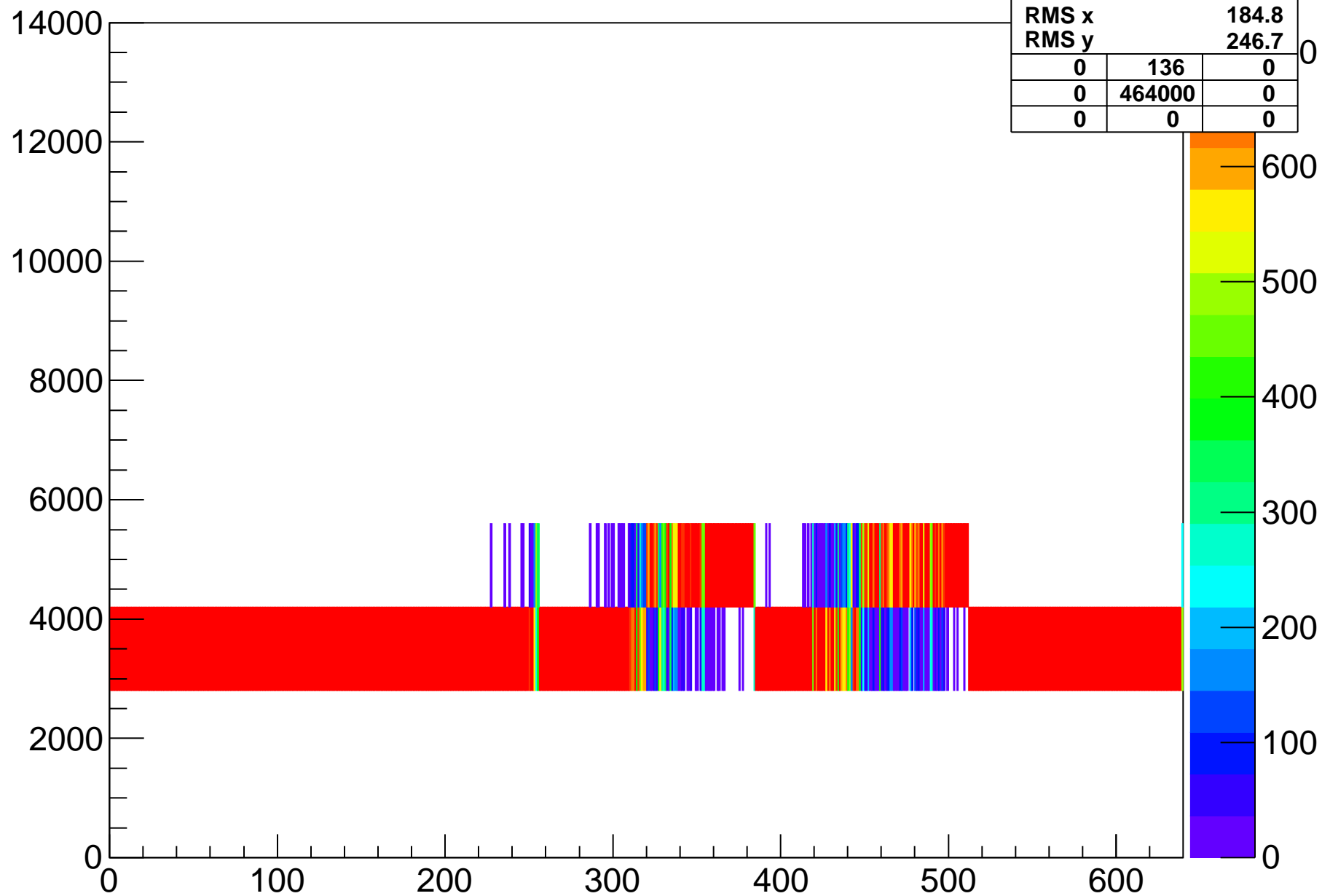
baselinesamples-fpga-6-hyb-1-sample-2

Entries	464136	
Mean x	319.5	
Mean y	3927	
RMS x	184.8	
RMS y	246.7	
0	136	0
0	464000	0
0	0	0



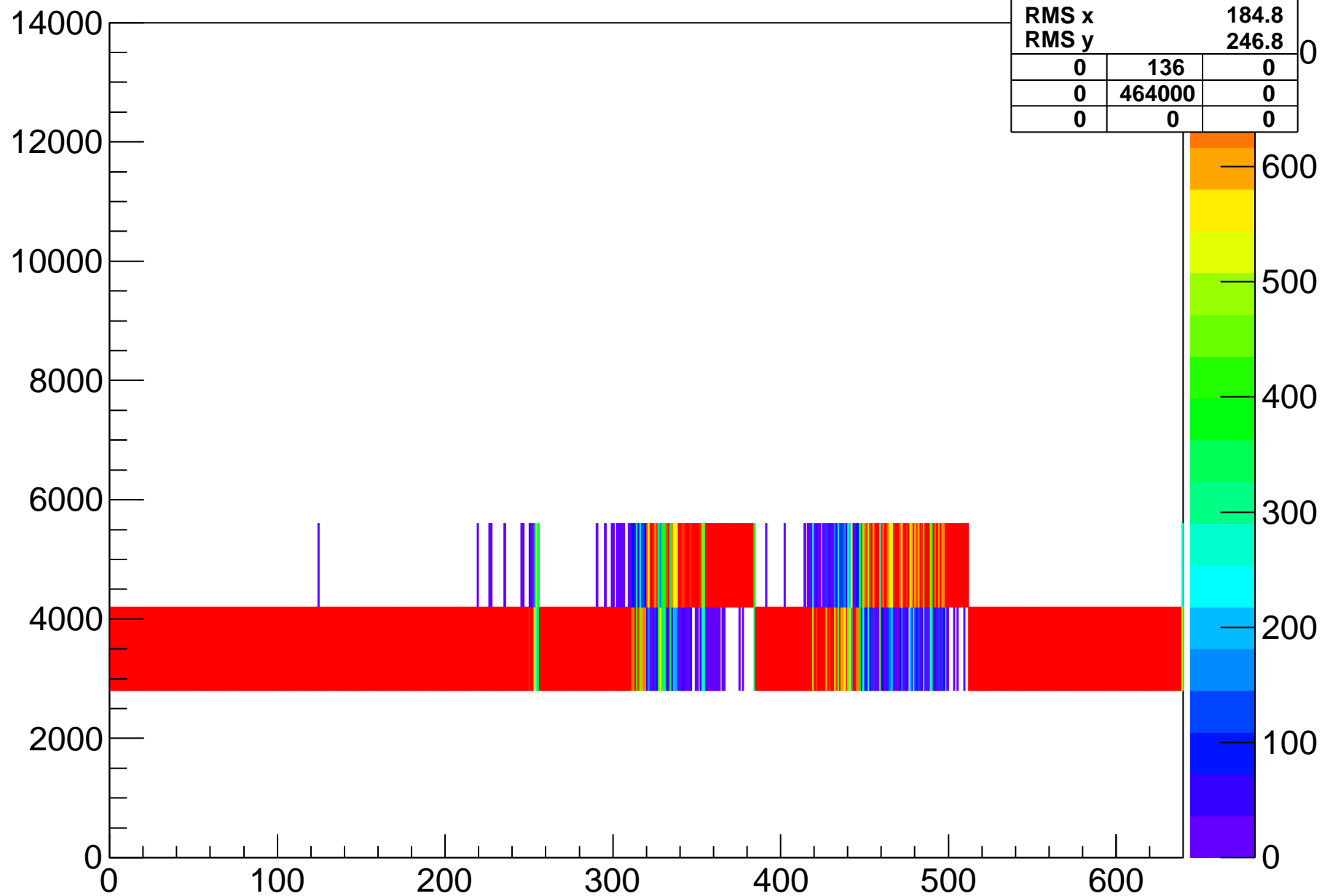
baselinesamples-fpga-6-hyb-1-sample-3

Entries	464136	
Mean x	319.5	
Mean y	3930	
RMS x	184.8	
RMS y	246.7	
0	136	0
0	464000	0
0	0	0



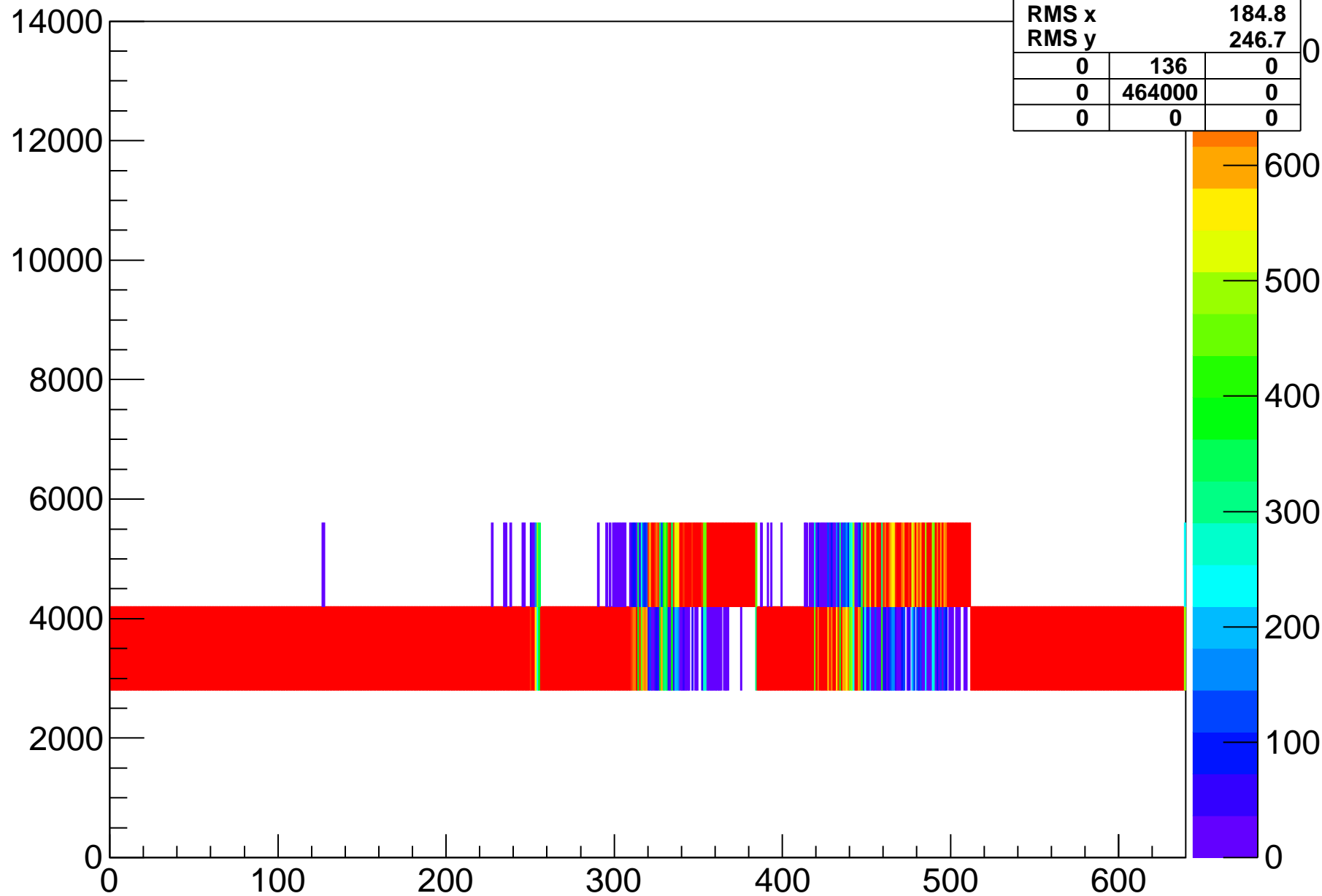
baselinesamples-fpga-6-hyb-1-sample-4

Entries	464136	
Mean x	319.5	
Mean y	3924	
RMS x	184.8	
RMS y	246.8	
0	136	0
0	464000	0
0	0	0



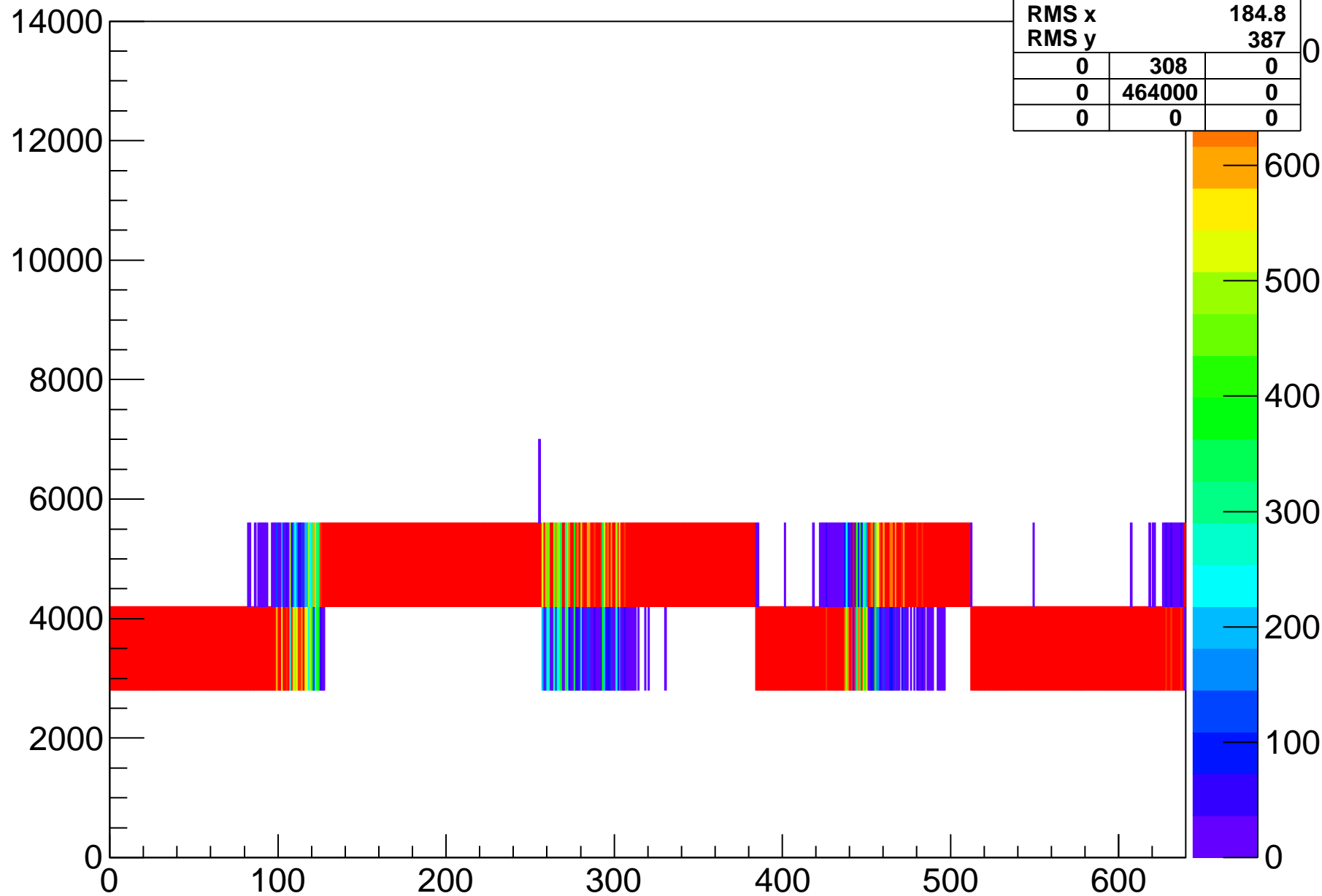
baselinesamples-fpga-6-hyb-1-sample-5

Entries	464136	
Mean x	319.5	
Mean y	3927	
RMS x	184.8	
RMS y	246.7	
0	136	0
0	464000	0
0	0	0



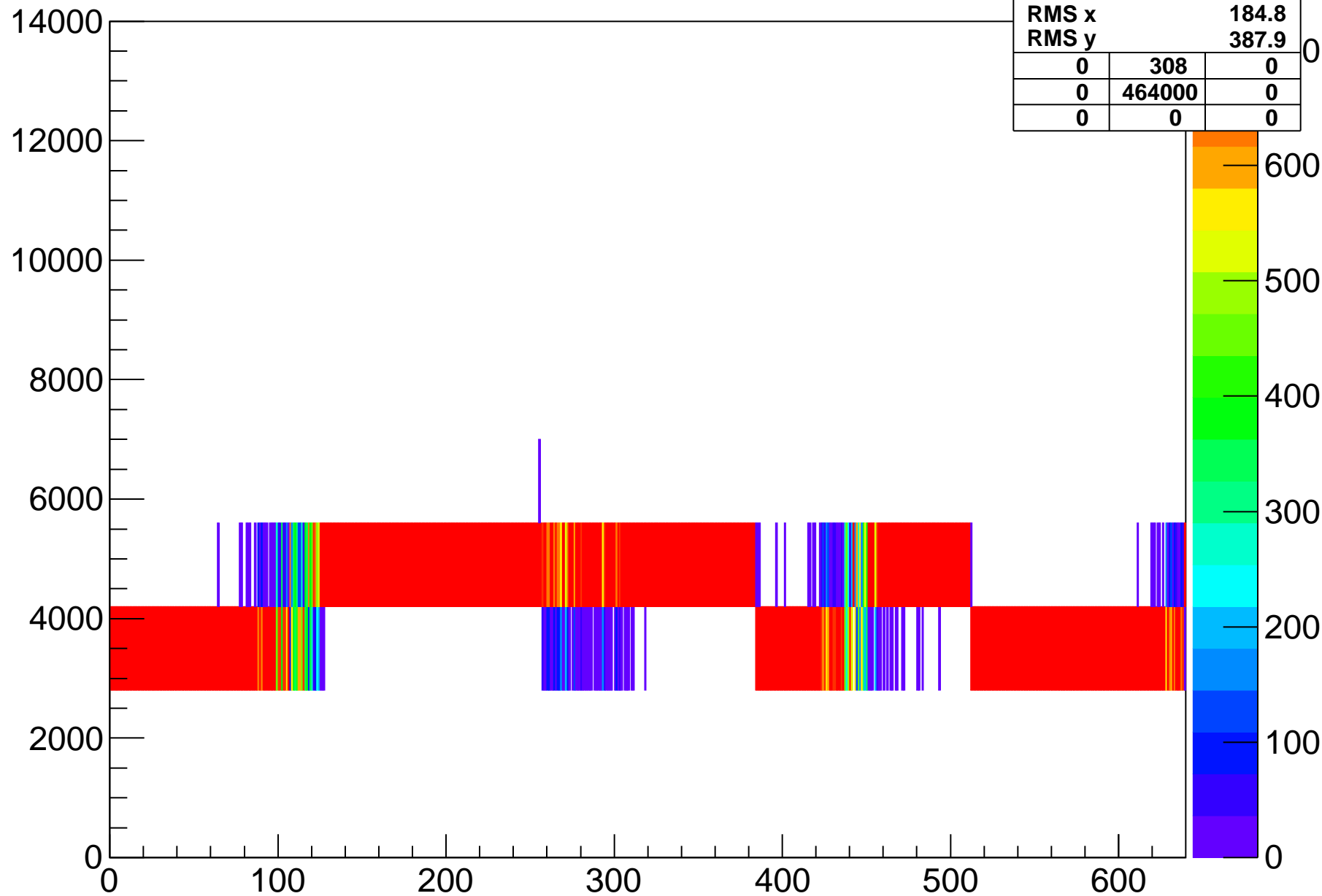
baselinesamples-fpga-6-hyb-2-sample-0

Entries	464308	
Mean x	319.5	
Mean y	4231	
RMS x	184.8	
RMS y	387	
0	308	0
0	464000	0
0	0	0



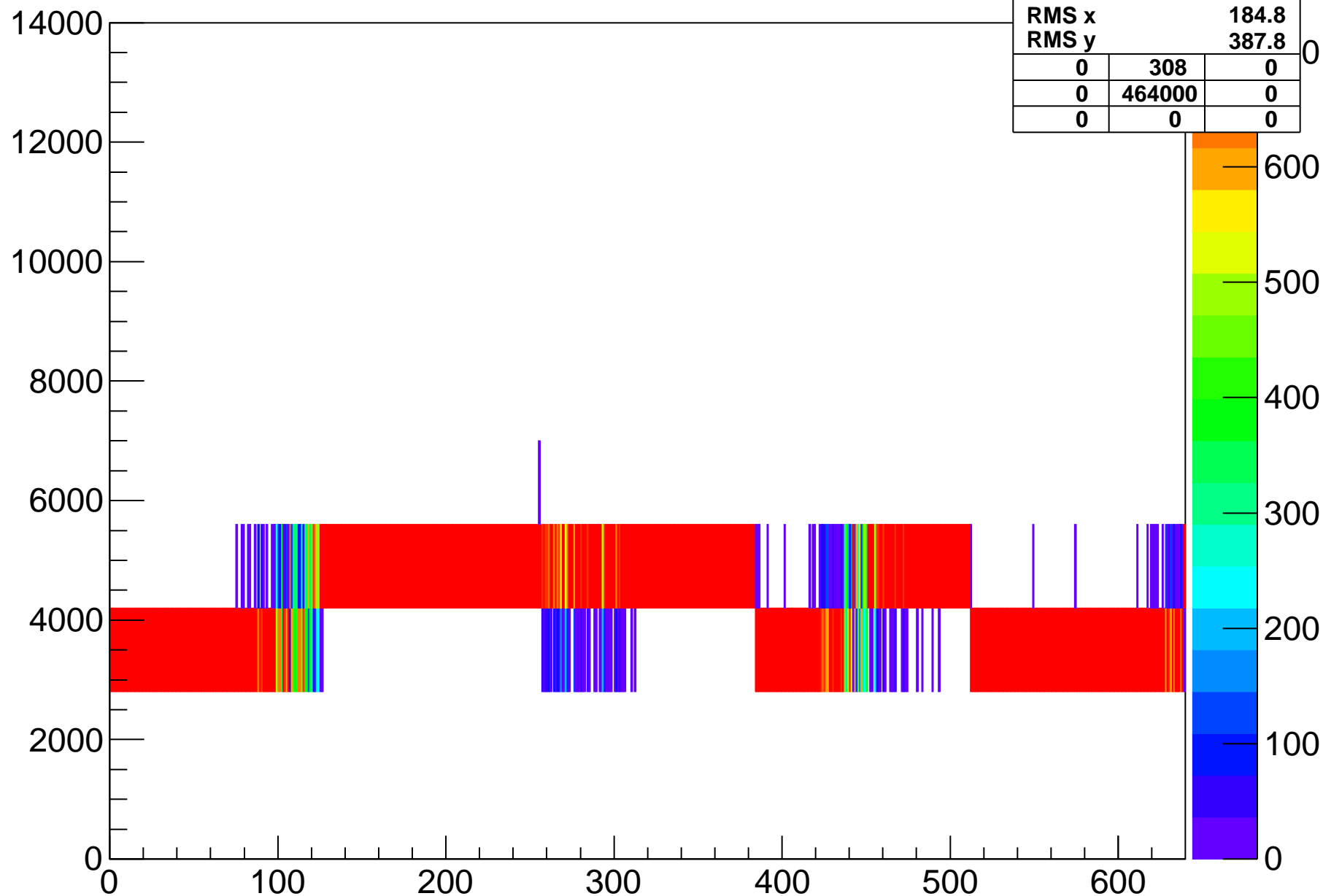
baselinesamples-fpga-6-hyb-2-sample-1

Entries	464308	
Mean x	319.5	
Mean y	4276	
RMS x	184.8	
RMS y	387.9	
0	308	0
0	464000	0
0	0	0



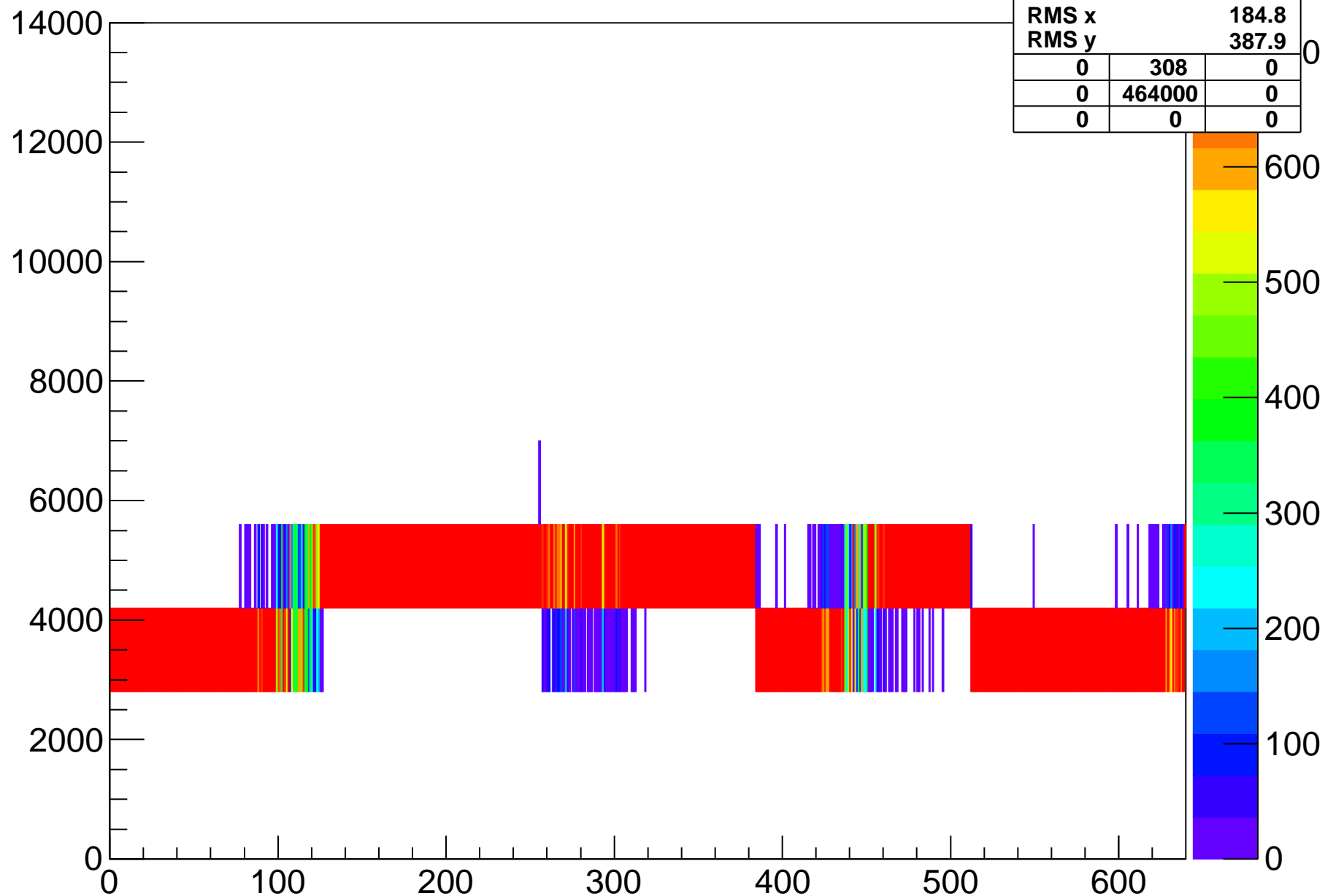
baselinesamples-fpga-6-hyb-2-sample-2

Entries	464308	
Mean x	319.5	
Mean y	4272	
RMS x	184.8	
RMS y	387.8	
0	308	0
0	464000	0
0	0	0



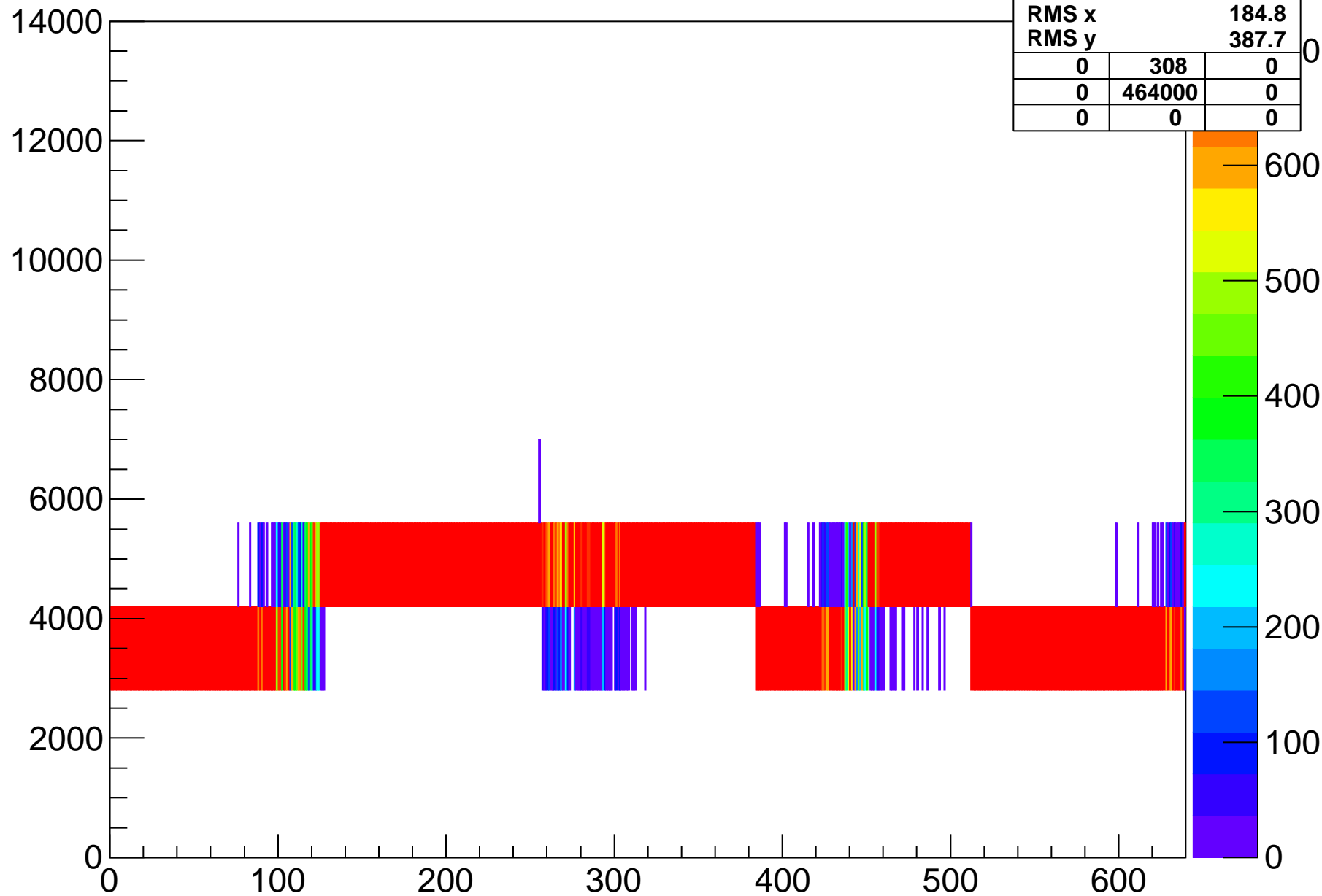
baselinesamples-fpga-6-hyb-2-sample-3

Entries	464308	
Mean x	319.5	
Mean y	4276	
RMS x	184.8	
RMS y	387.9	
0	308	0
0	464000	0
0	0	0



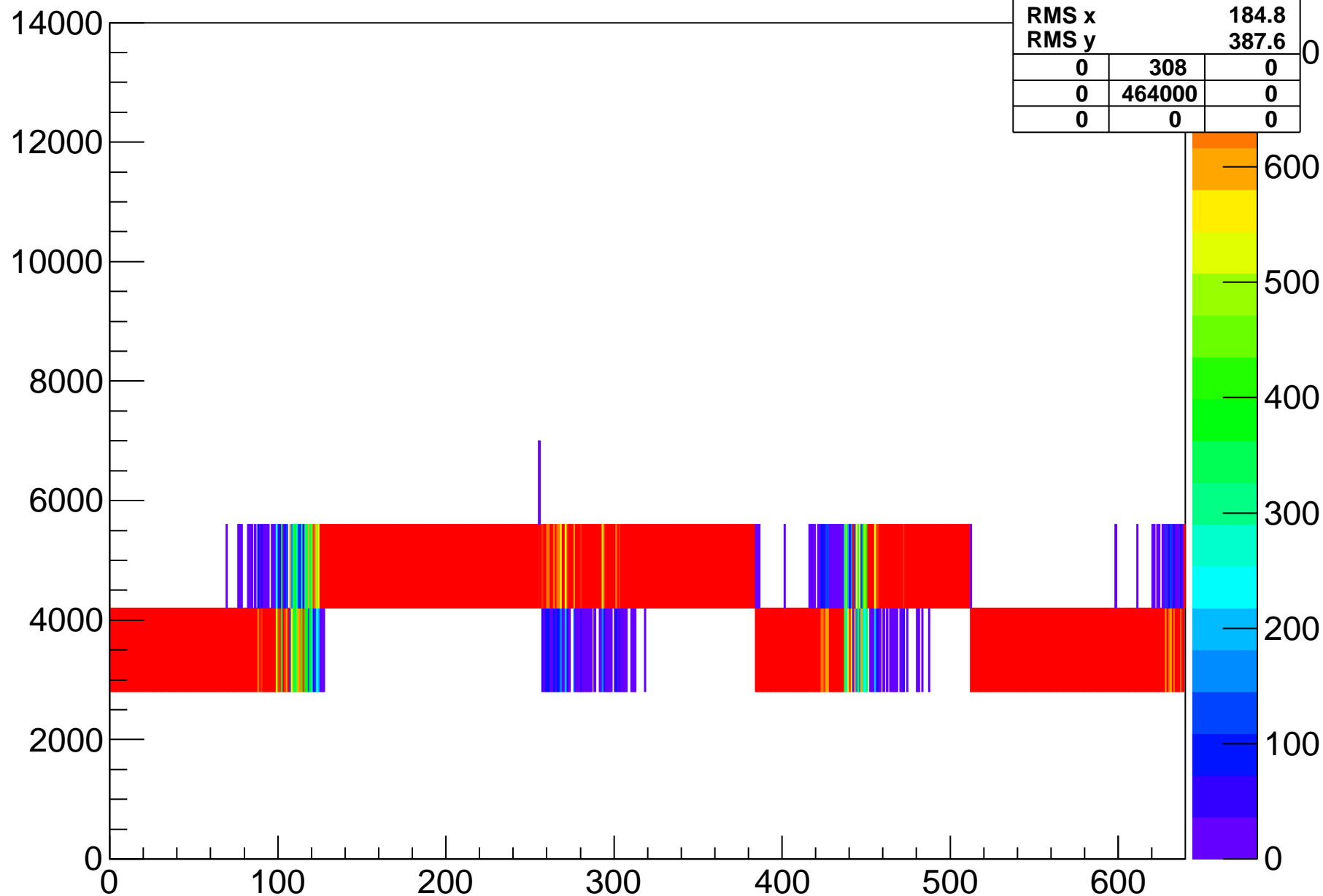
baselinesamples-fpga-6-hyb-2-sample-4

Entries	464308	
Mean x	319.5	
Mean y	4269	
RMS x	184.8	
RMS y	387.7	
0	308	0
0	464000	0
0	0	0



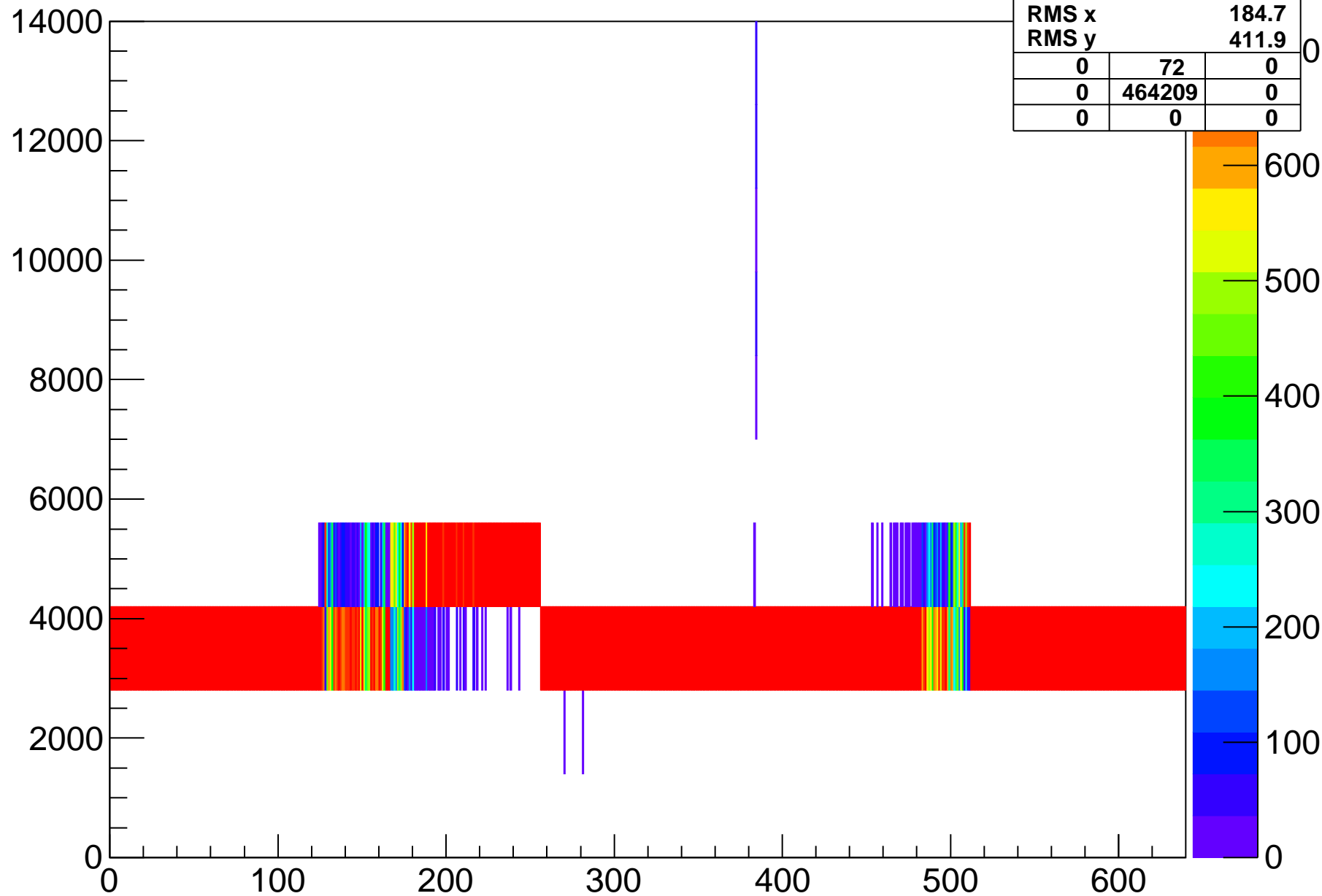
baselinesamples-fpga-6-hyb-2-sample-5

Entries	464308	
Mean x	319.5	
Mean y	4273	
RMS x	184.8	
RMS y	387.6	
0	308	0
0	464000	0
0	0	0



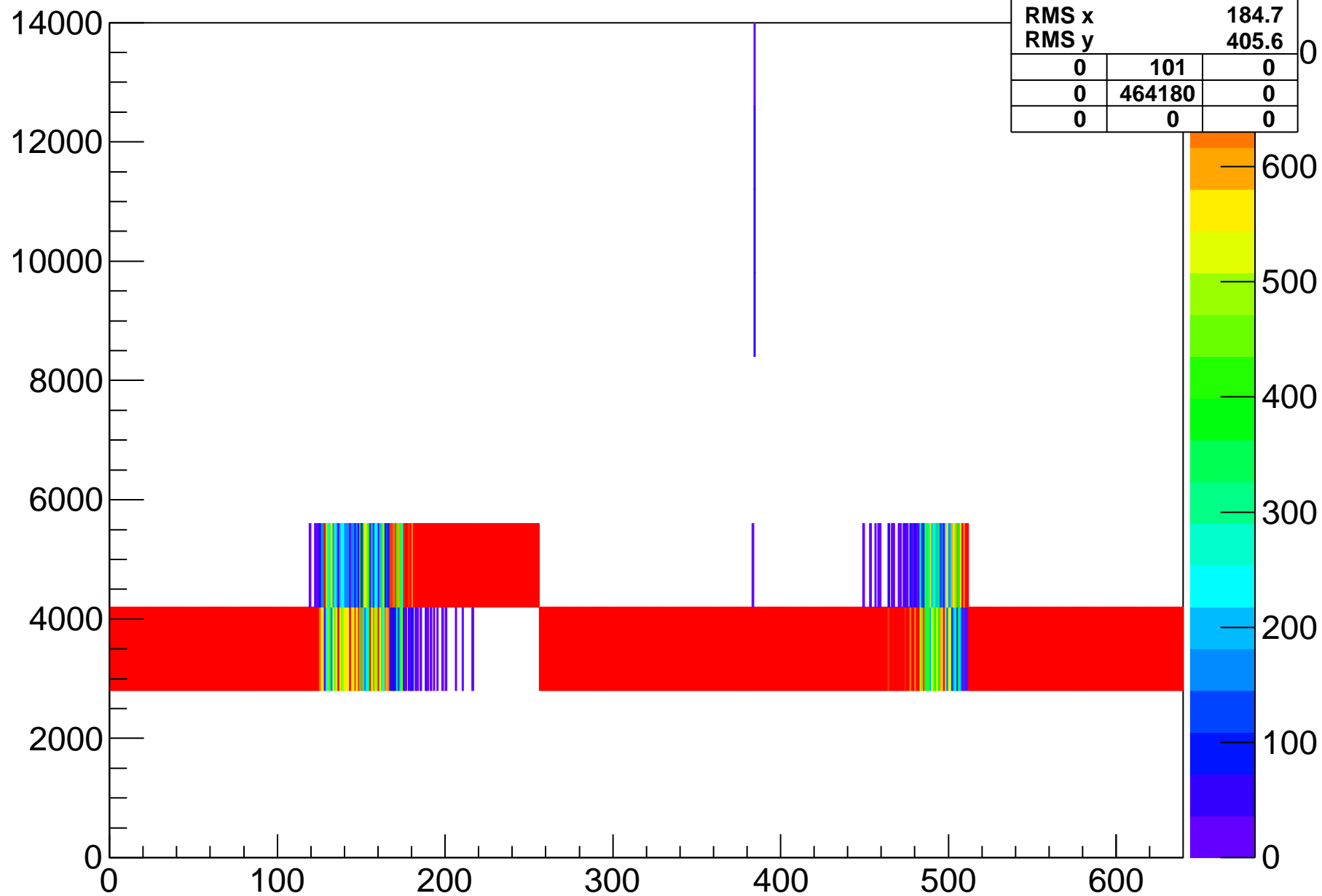
baselinesamples-fpga-6-hyb-3-sample-0

Entries	464281	
Mean x	319.5	
Mean y	3752	
RMS x	184.7	
RMS y	411.9	
0	72	0
0	464209	0
0	0	0



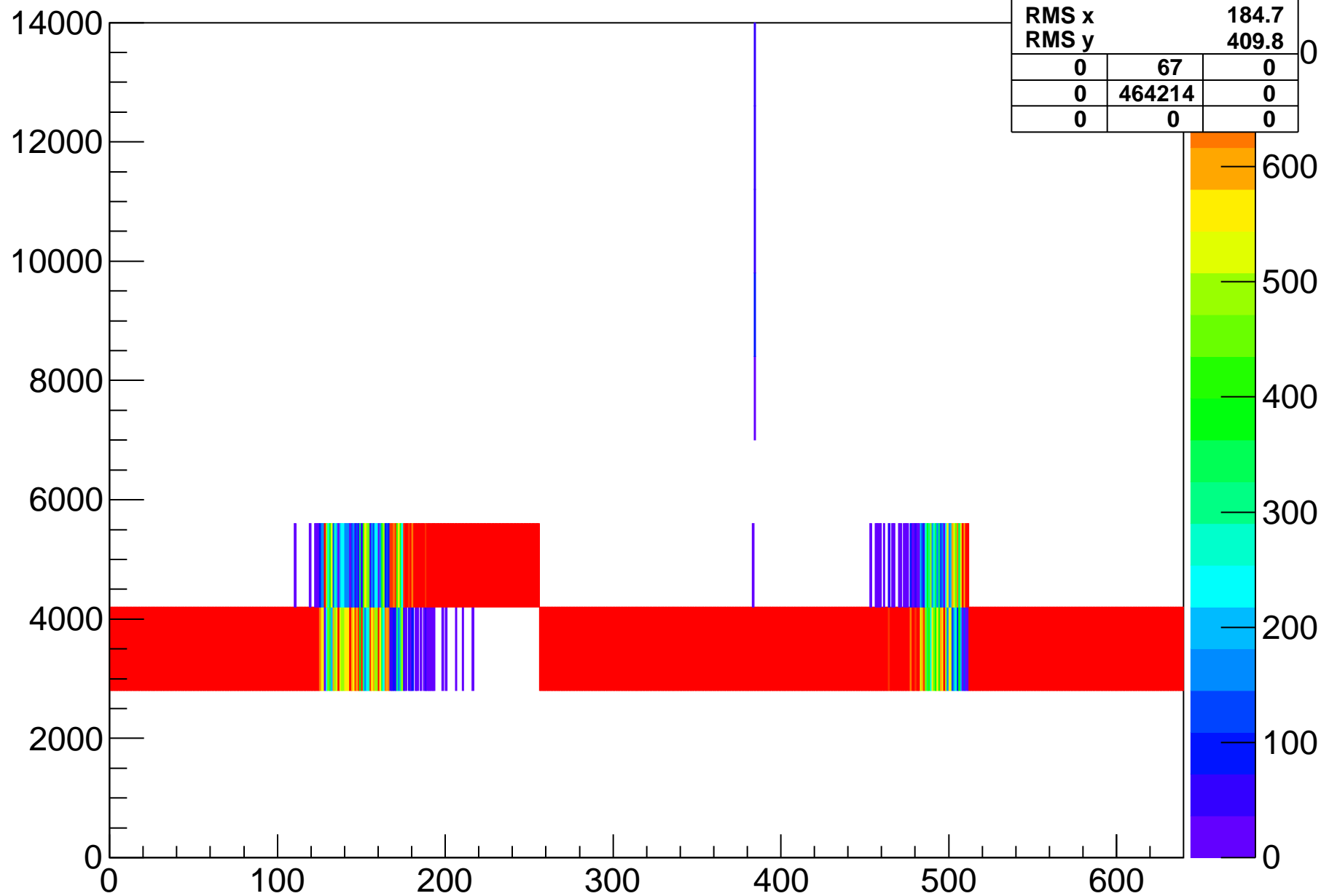
baselinesamples-fpga-6-hyb-3-sample-1

Entries	464281	
Mean x	319.5	
Mean y	3792	
RMS x	184.7	
RMS y	405.6	
0	101	0
0	464180	0
0	0	0



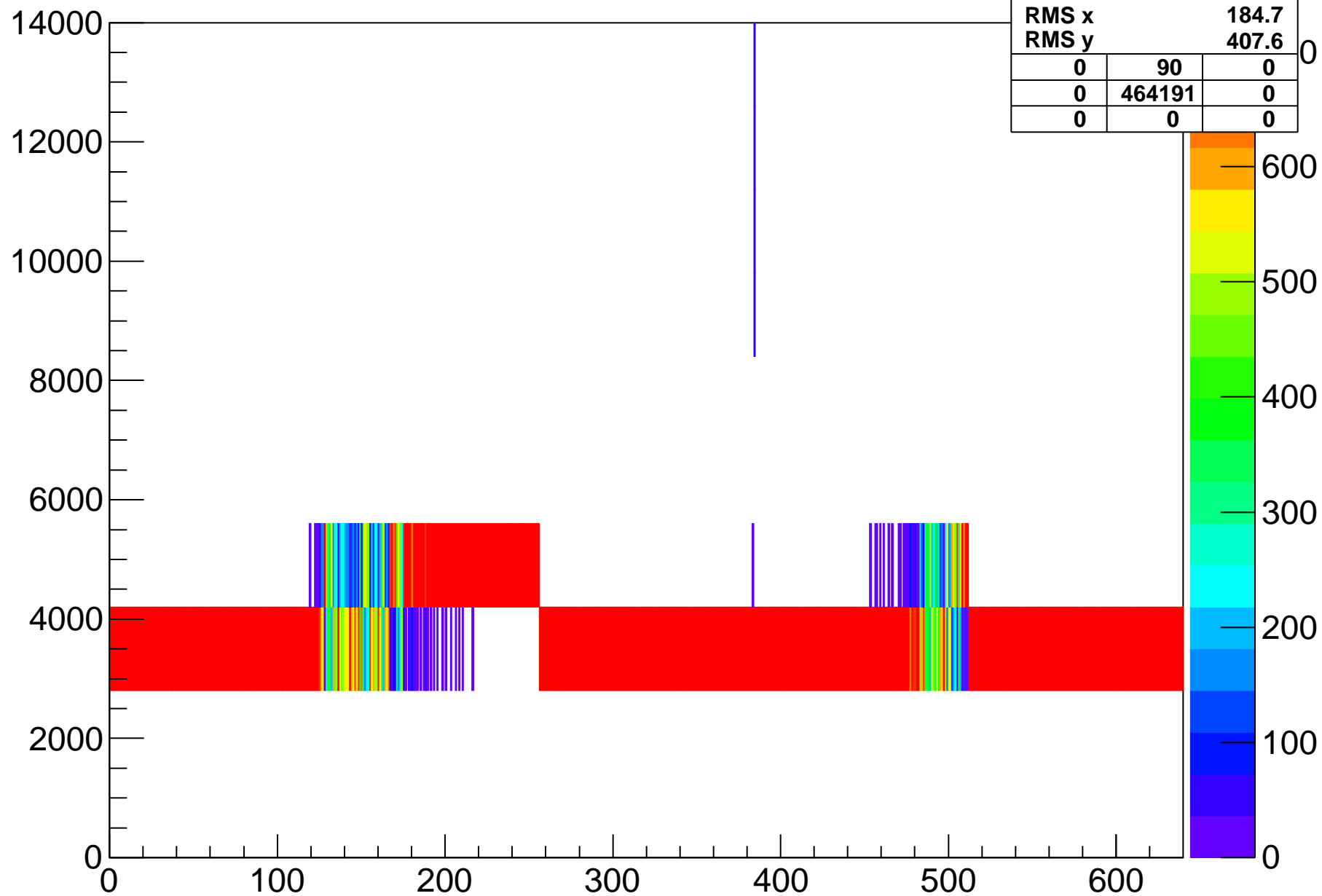
baselinesamples-fpga-6-hyb-3-sample-2

Entries	464281	
Mean x	319.5	
Mean y	3790	
RMS x	184.7	
RMS y	409.8	
0	67	0
0	464214	0
0	0	0



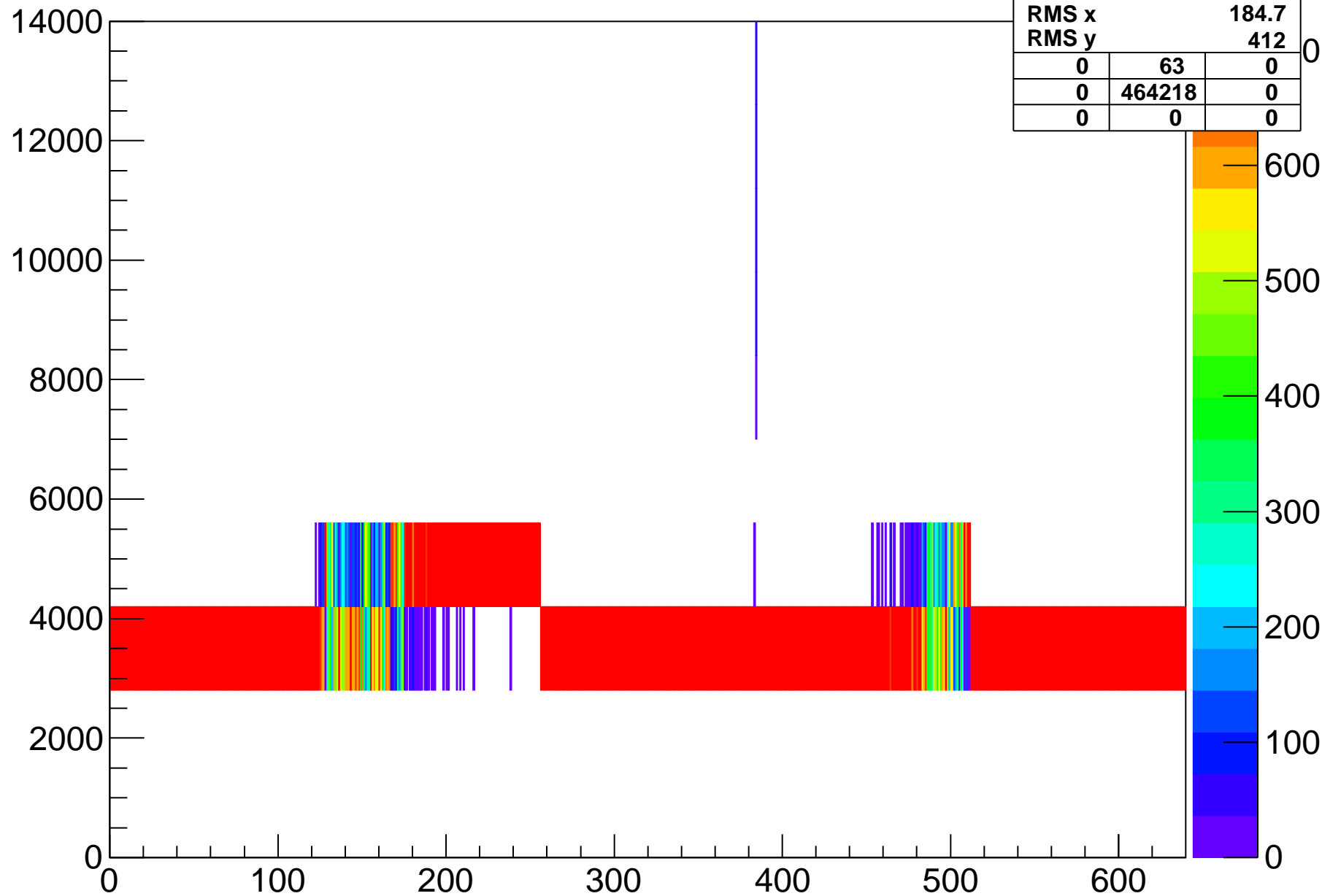
baselinesamples-fpga-6-hyb-3-sample-3

Entries	464281	
Mean x	319.5	
Mean y	3793	
RMS x	184.7	
RMS y	407.6	
0	90	0
0	464191	0
0	0	0



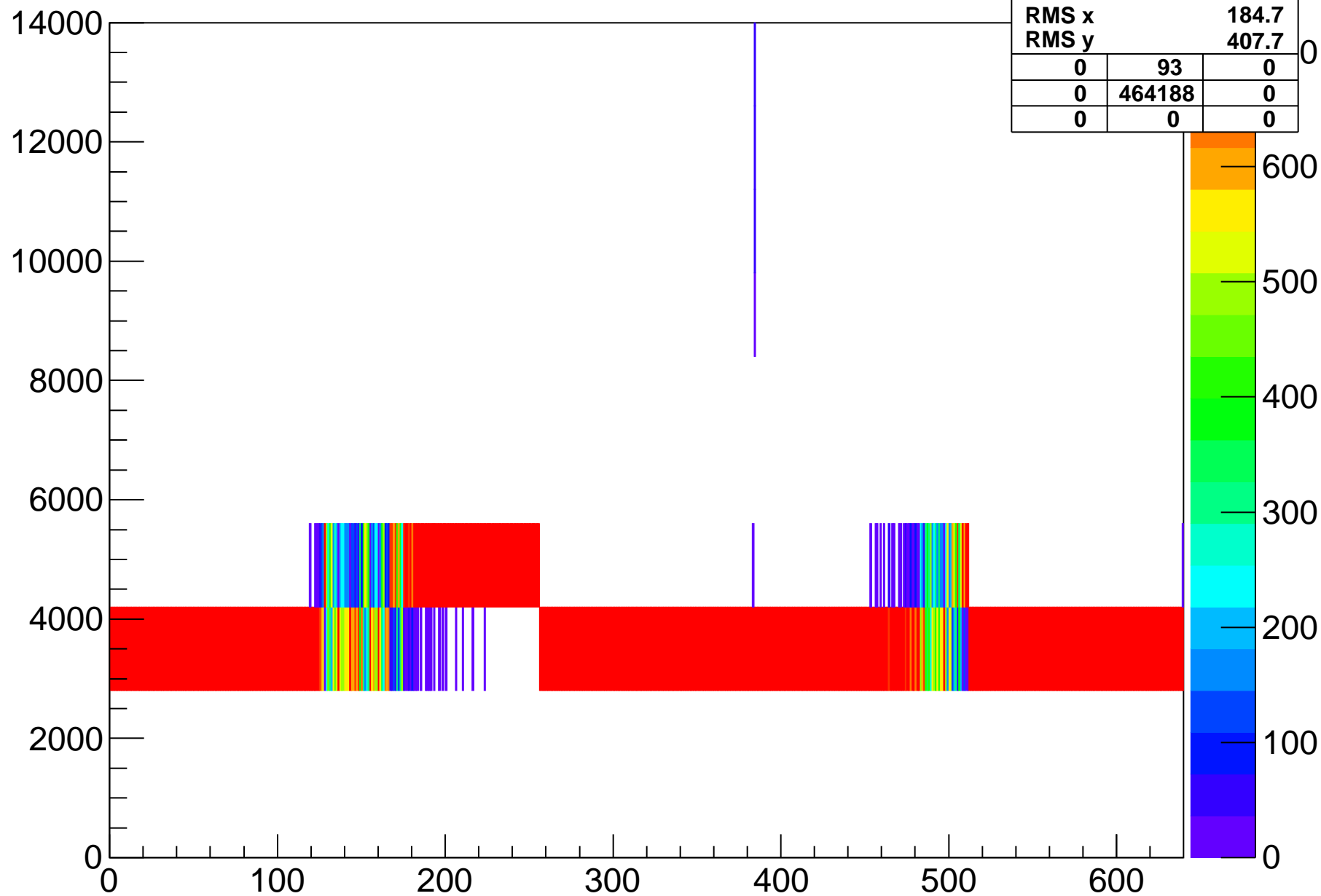
baselinesamples-fpga-6-hyb-3-sample-4

Entries	464281	
Mean x	319.5	
Mean y	3787	
RMS x	184.7	
RMS y	412	
0	63	0
0	464218	0
0	0	0



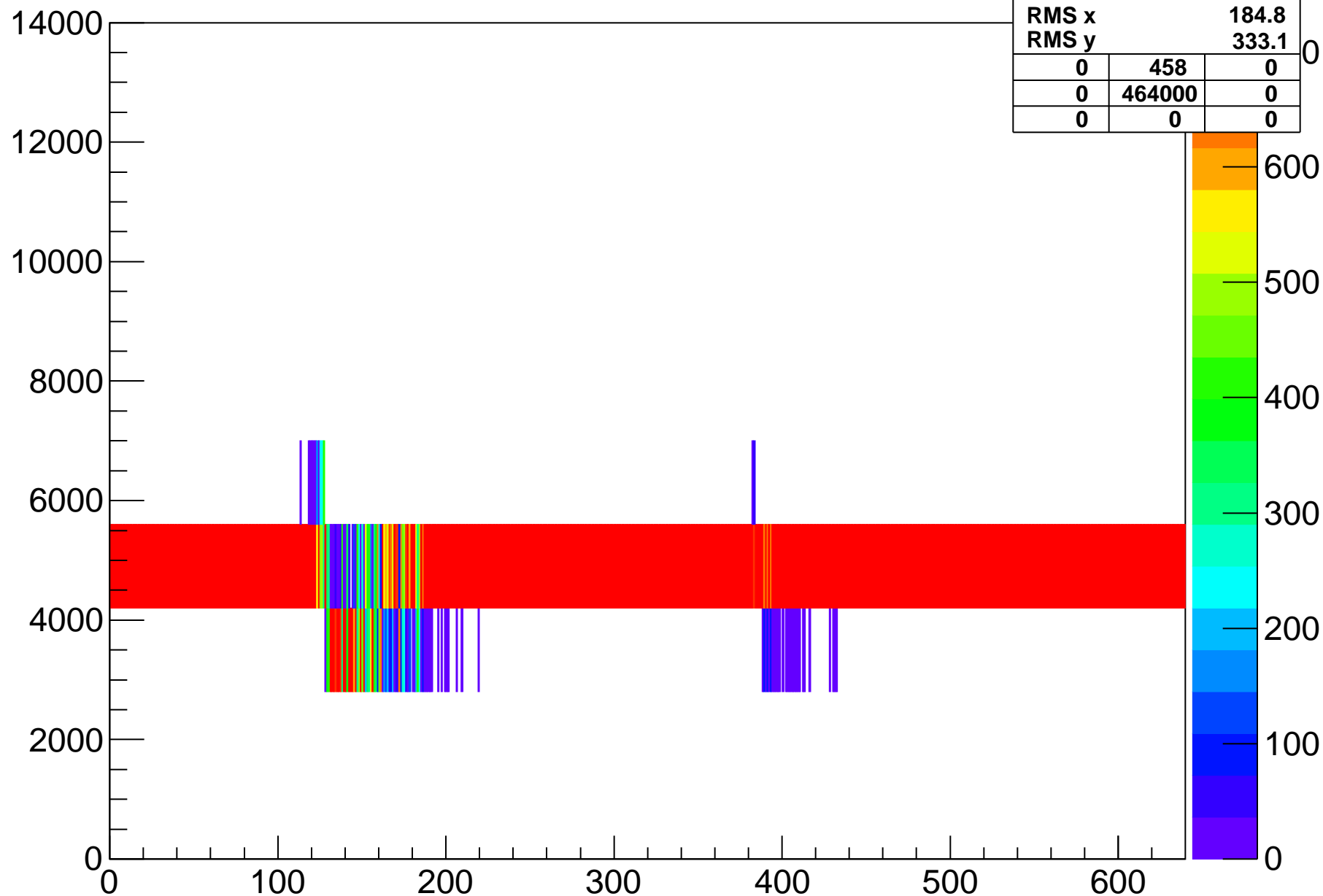
baselinesamples-fpga-6-hyb-3-sample-5

Entries	464281	
Mean x	319.5	
Mean y	3791	
RMS x	184.7	
RMS y	407.7	
0	93	0
0	464188	0
0	0	0



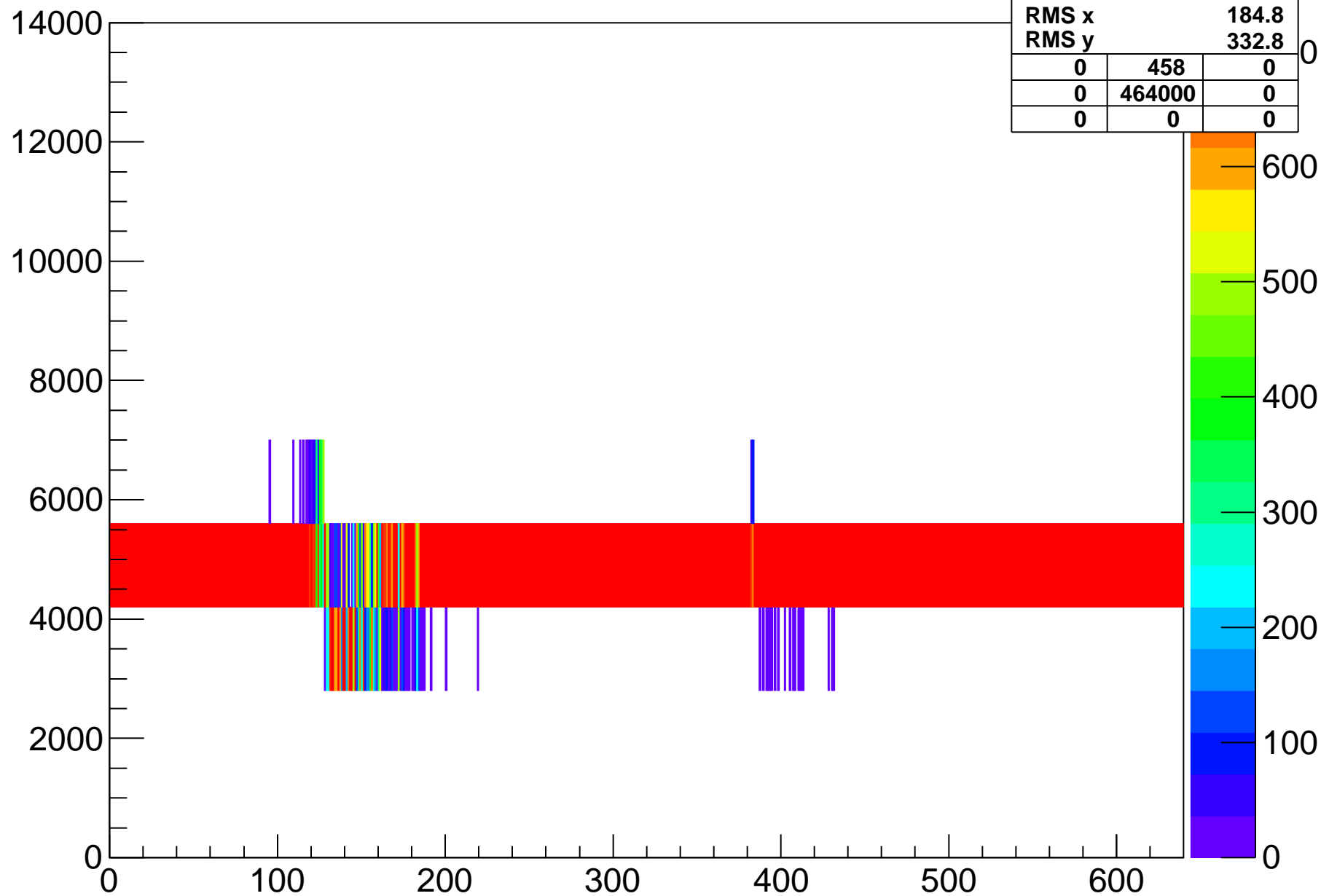
baselinesamples-fpga-7-hyb-0-sample-0

Entries	464458	
Mean x	319.5	
Mean y	4748	
RMS x	184.8	
RMS y	333.1	
0	458	0
0	464000	0
0	0	0



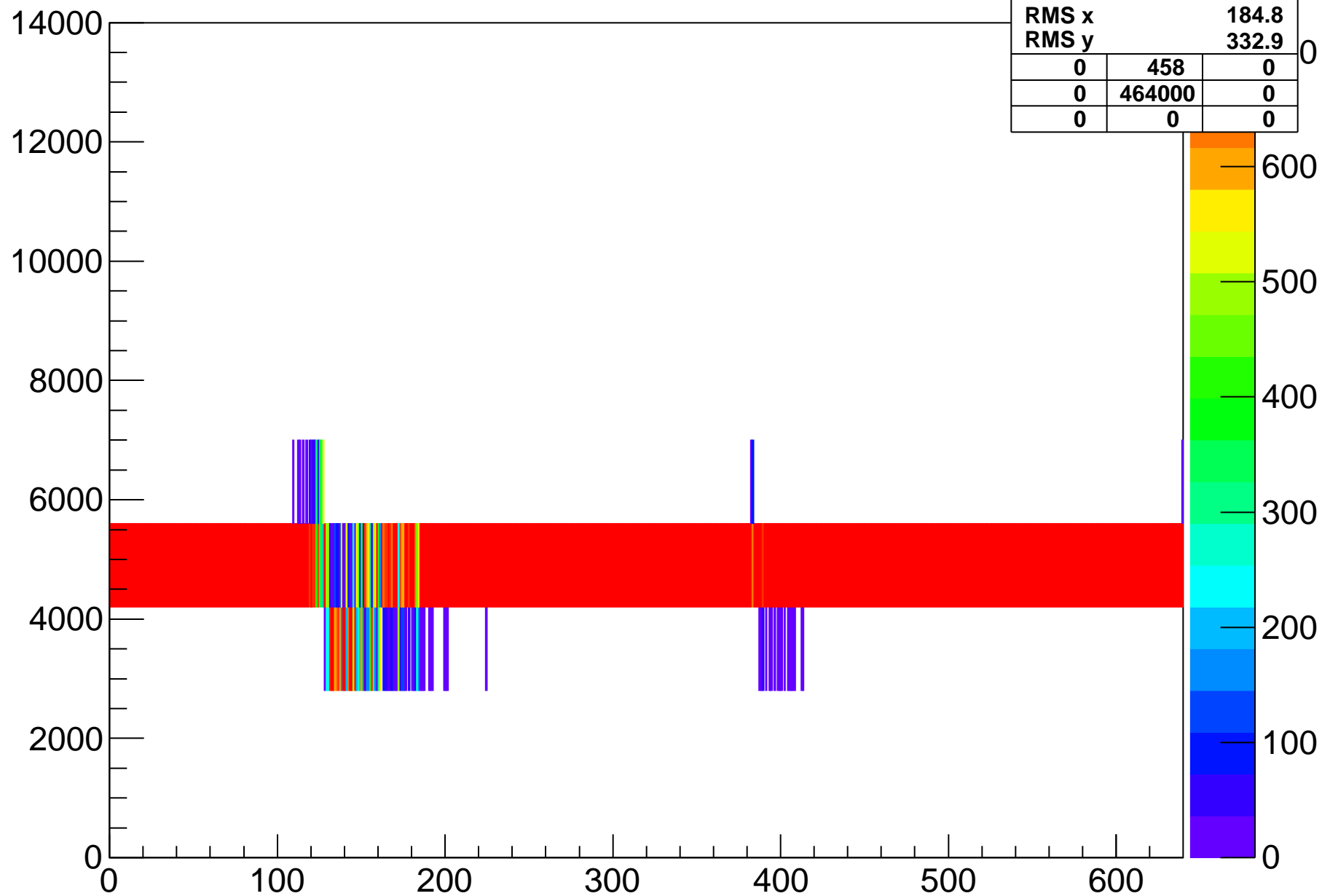
baselinesamples-fpga-7-hyb-0-sample-1

Entries	464458	
Mean x	319.5	
Mean y	4783	
RMS x	184.8	
RMS y	332.8	
0	458	0
0	464000	0
0	0	0



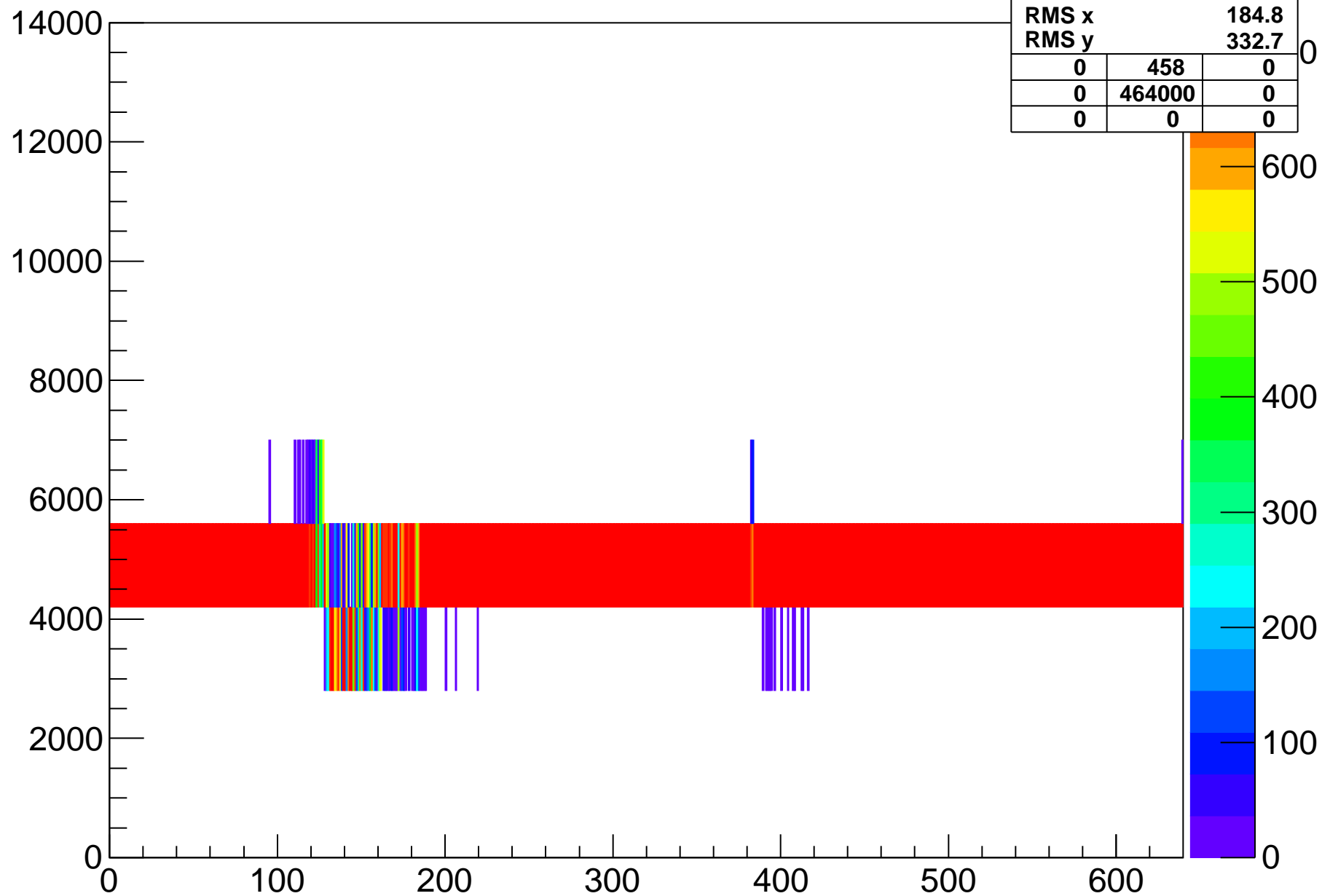
baselinesamples-fpga-7-hyb-0-sample-2

Entries	464458	
Mean x	319.5	
Mean y	4780	
RMS x	184.8	
RMS y	332.9	
0	458	0
0	464000	0
0	0	0



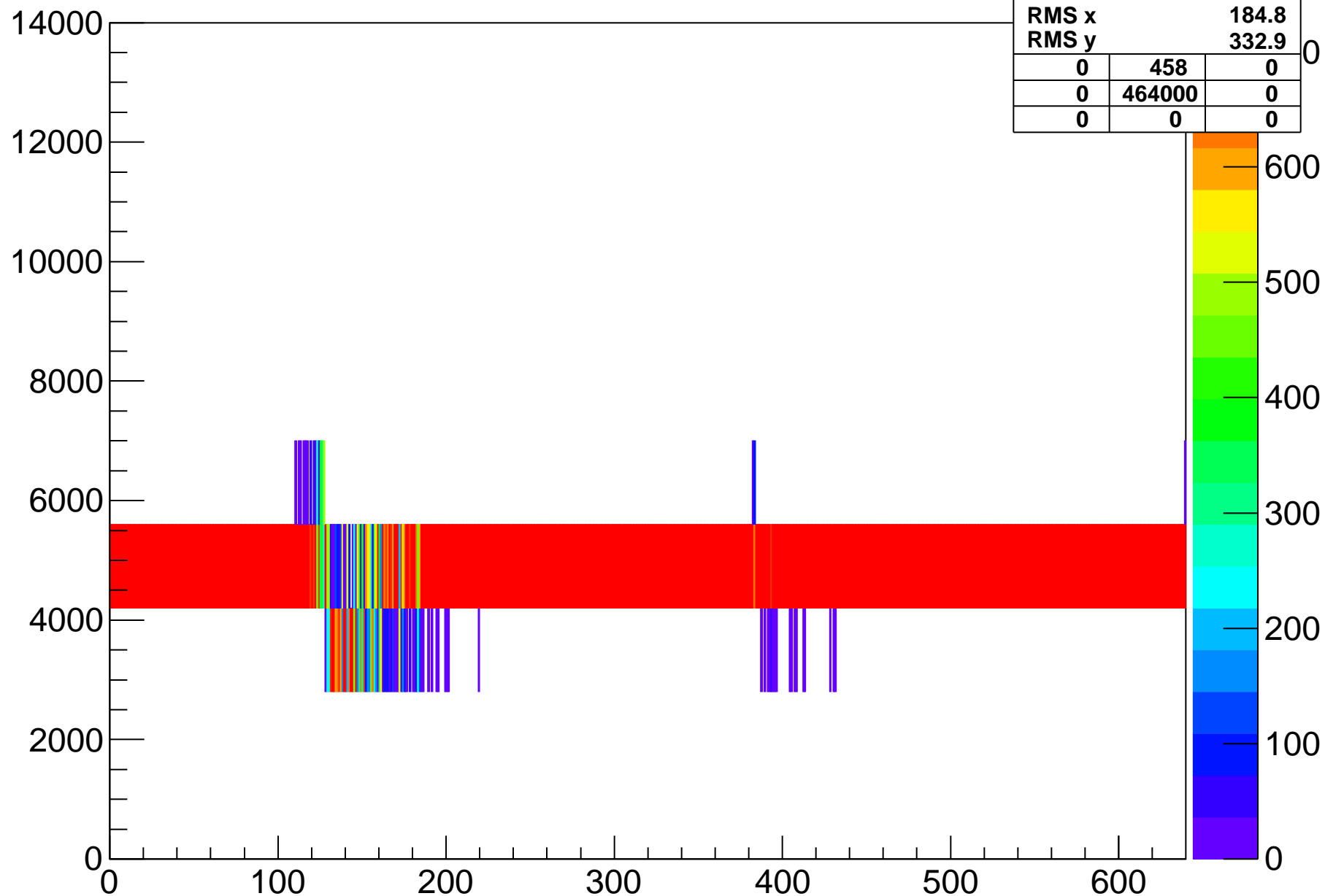
baselinesamples-fpga-7-hyb-0-sample-3

Entries	464458	
Mean x	319.5	
Mean y	4786	
RMS x	184.8	
RMS y	332.7	
0	458	0
0	464000	0
0	0	0



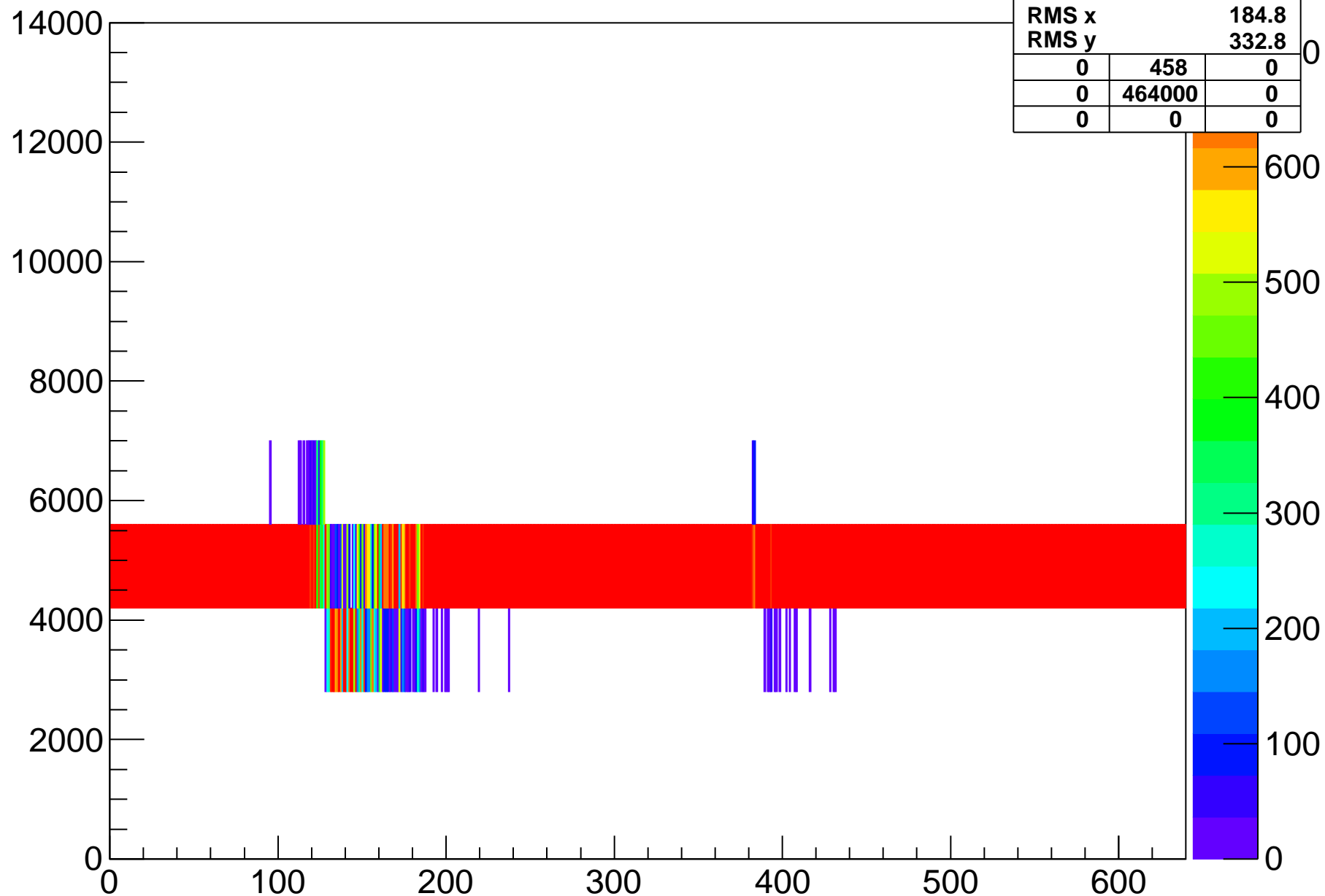
baselinesamples-fpga-7-hyb-0-sample-4

Entries	464458	
Mean x	319.5	
Mean y	4778	
RMS x	184.8	
RMS y	332.9	
0	458	0
0	464000	0
0	0	0



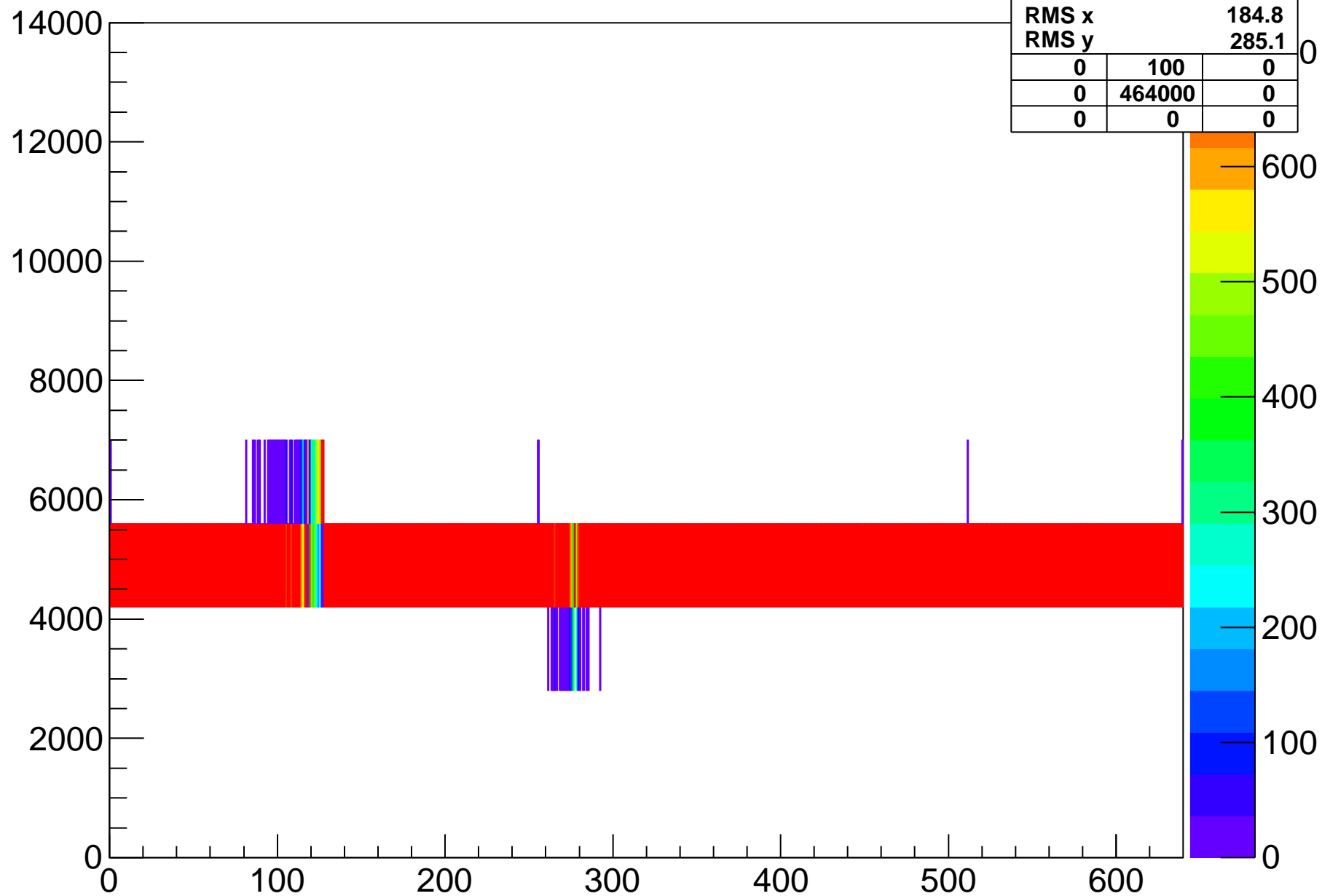
baselinesamples-fpga-7-hyb-0-sample-5

Entries	464458	
Mean x	319.5	
Mean y	4778	
RMS x	184.8	
RMS y	332.8	
0	458	0
0	464000	0
0	0	0



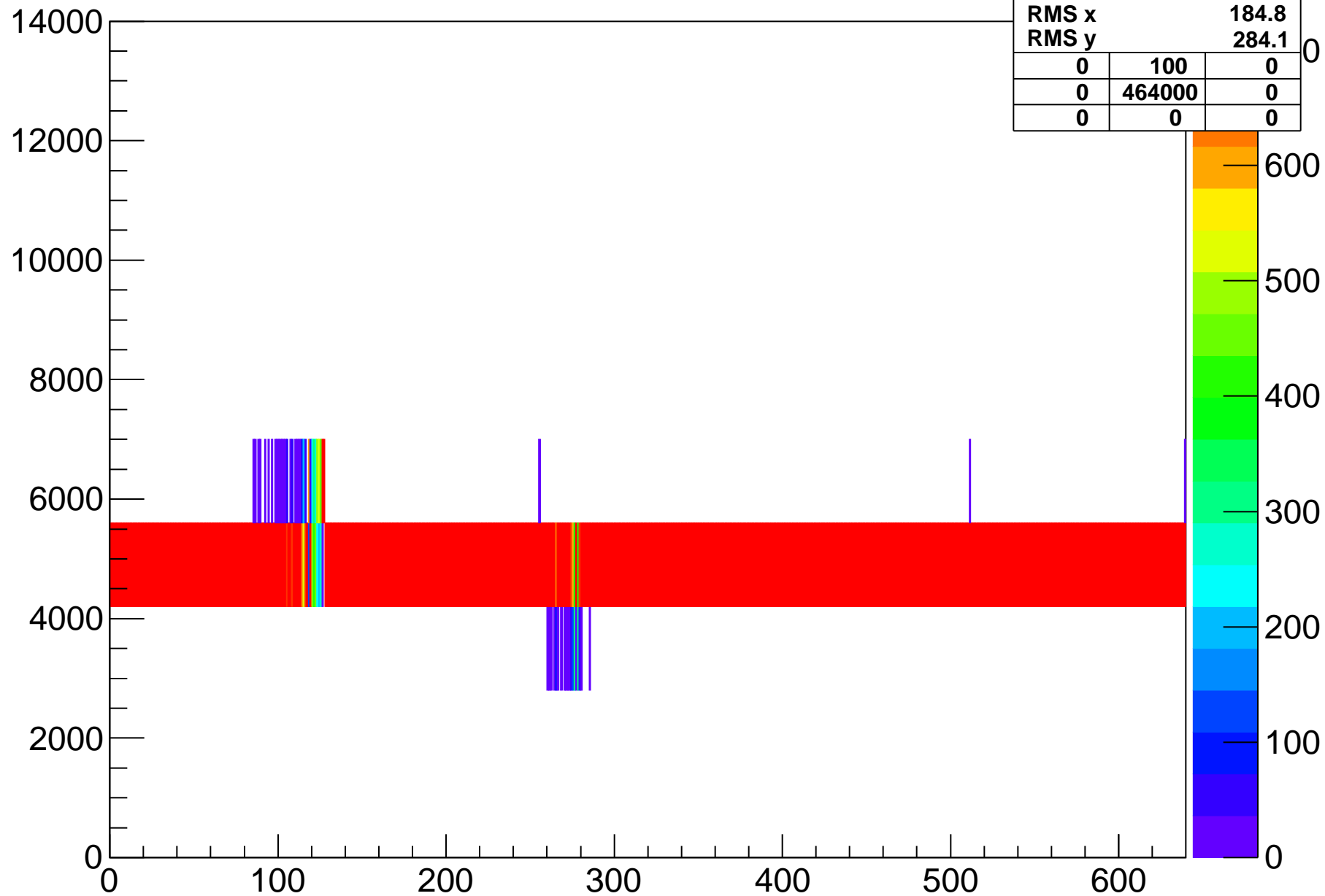
baselinesamples-fpga-7-hyb-1-sample-0

Entries	464100	
Mean x	319.5	
Mean y	4866	
RMS x	184.8	
RMS y	285.1	
0	100	0
0	464000	0
0	0	0



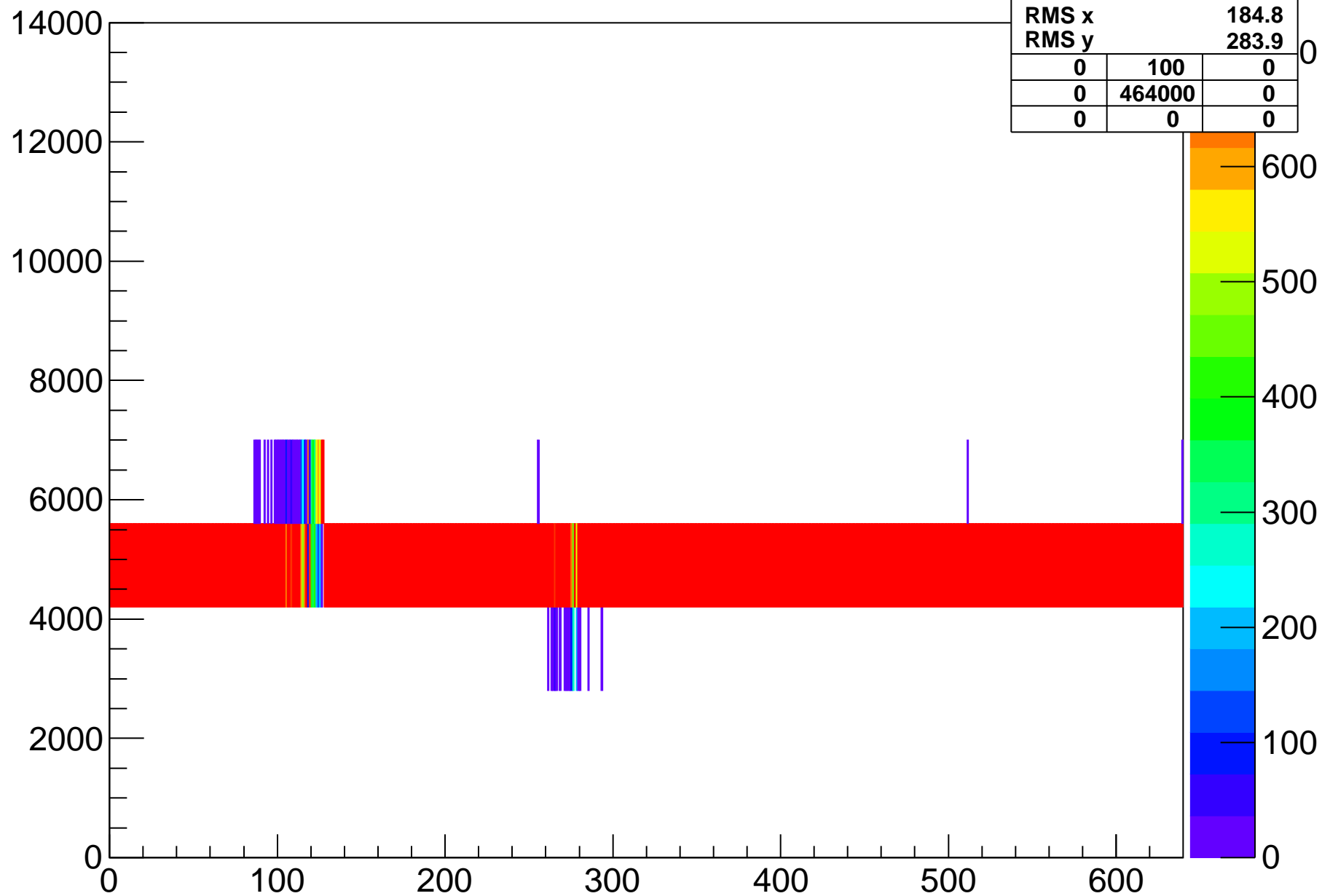
baselinesamples-fpga-7-hyb-1-sample-1

Entries	464100	
Mean x	319.5	
Mean y	4862	
RMS x	184.8	
RMS y	284.1	
0	100	0
0	464000	0
0	0	0



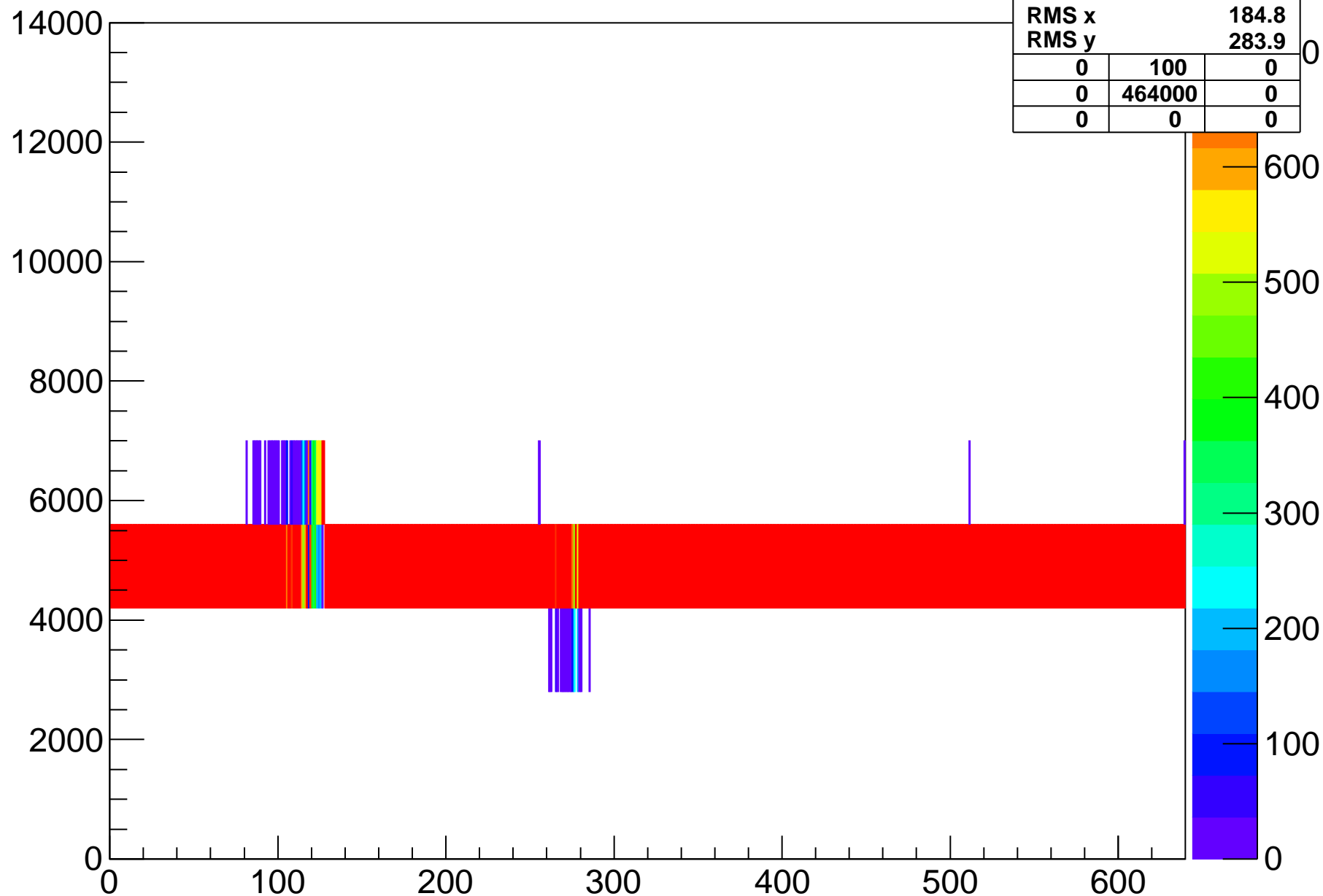
baselinesamples-fpga-7-hyb-1-sample-2

Entries	464100	
Mean x	319.5	
Mean y	4877	
RMS x	184.8	
RMS y	283.9	
0	100	0
0	464000	0
0	0	0



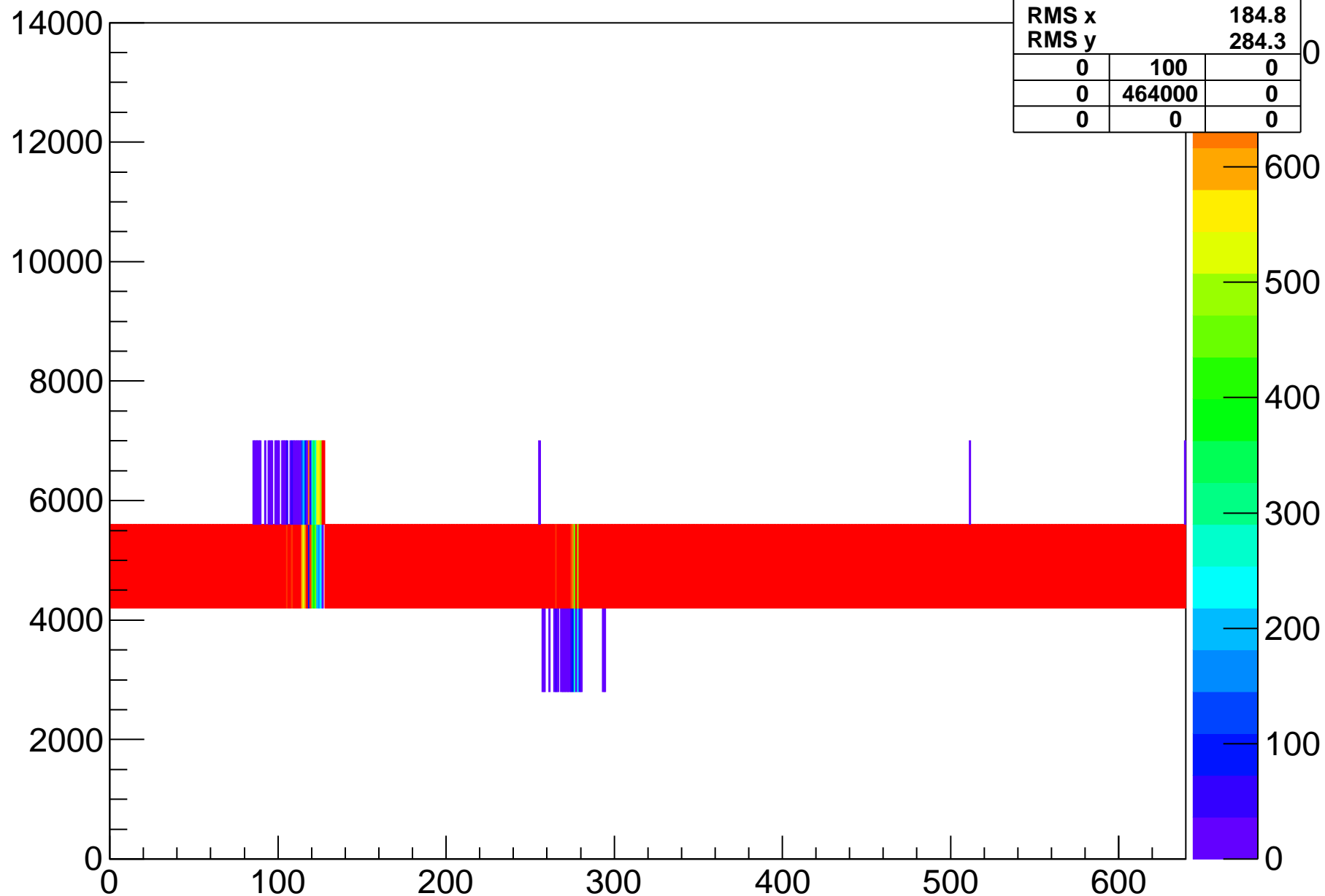
baselinesamples-fpga-7-hyb-1-sample-3

Entries	464100	
Mean x	319.5	
Mean y	4879	
RMS x	184.8	
RMS y	283.9	
0	100	0
0	464000	0
0	0	0



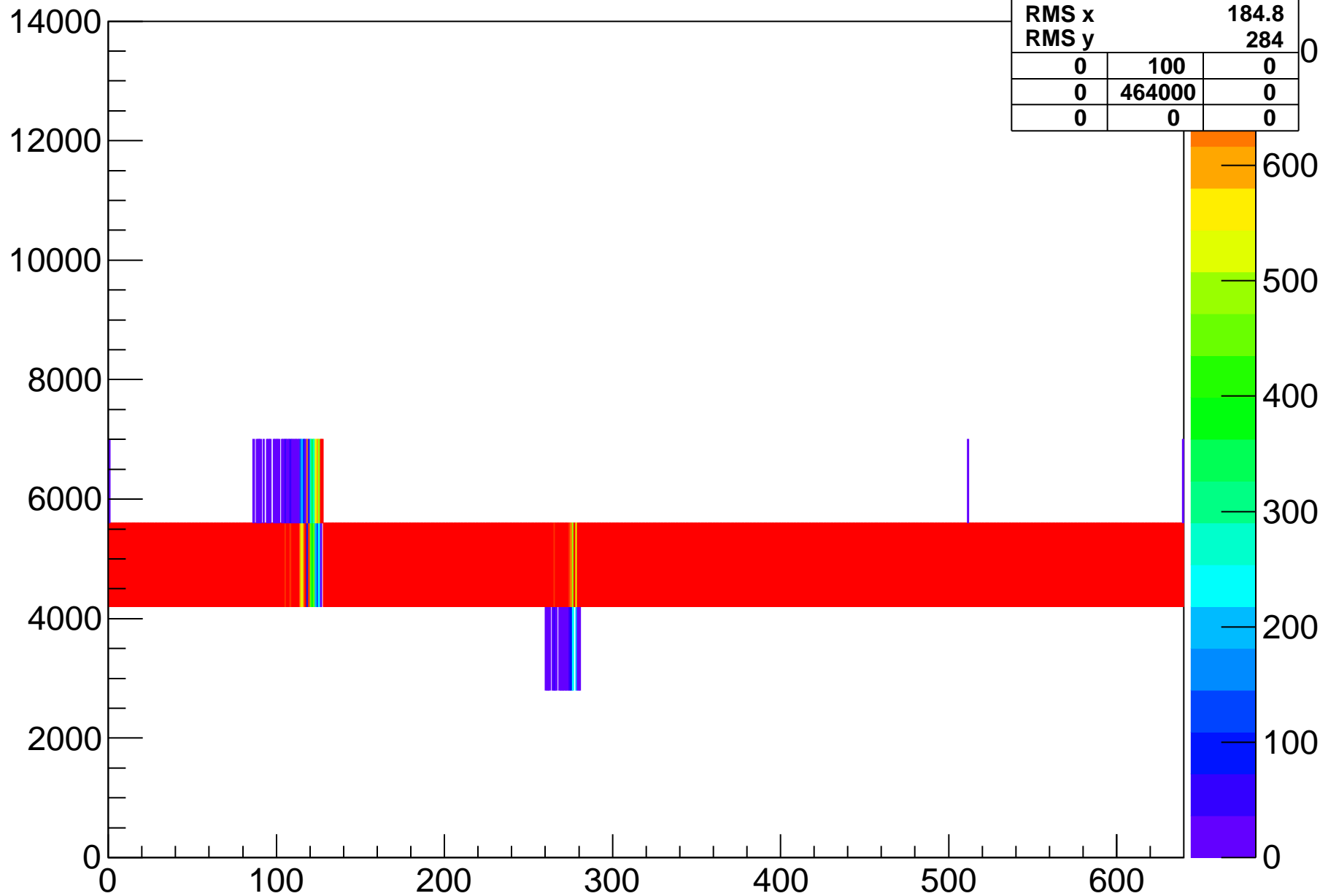
baselinesamples-fpga-7-hyb-1-sample-4

Entries	464100	
Mean x	319.5	
Mean y	4870	
RMS x	184.8	
RMS y	284.3	
0	100	0
0	464000	0
0	0	0



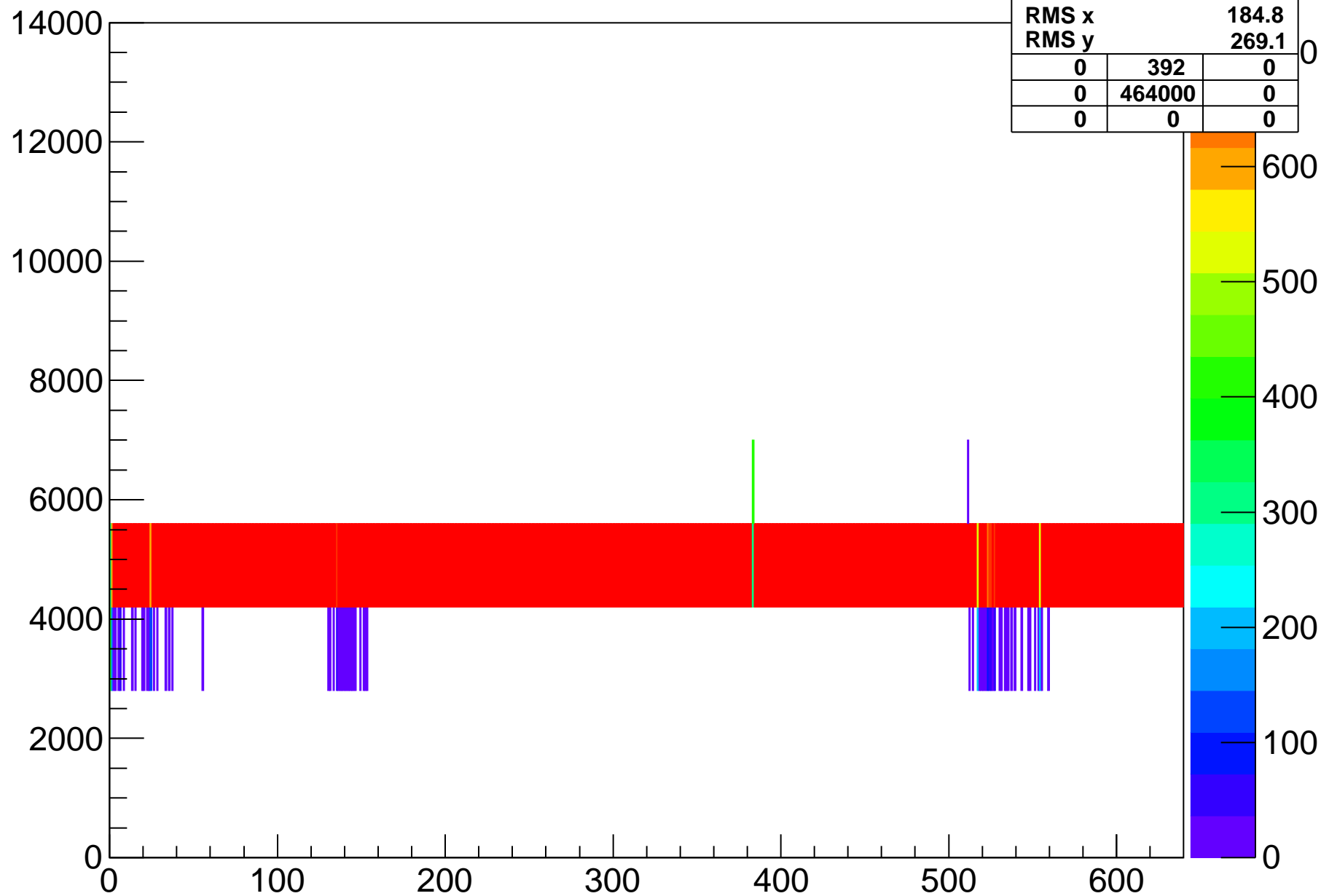
baselinesamples-fpga-7-hyb-1-sample-5

Entries			464100
Mean x			319.5
Mean y			4874
RMS x			184.8
RMS y			284
0	100	0	0
0	464000	0	0
0	0	0	0



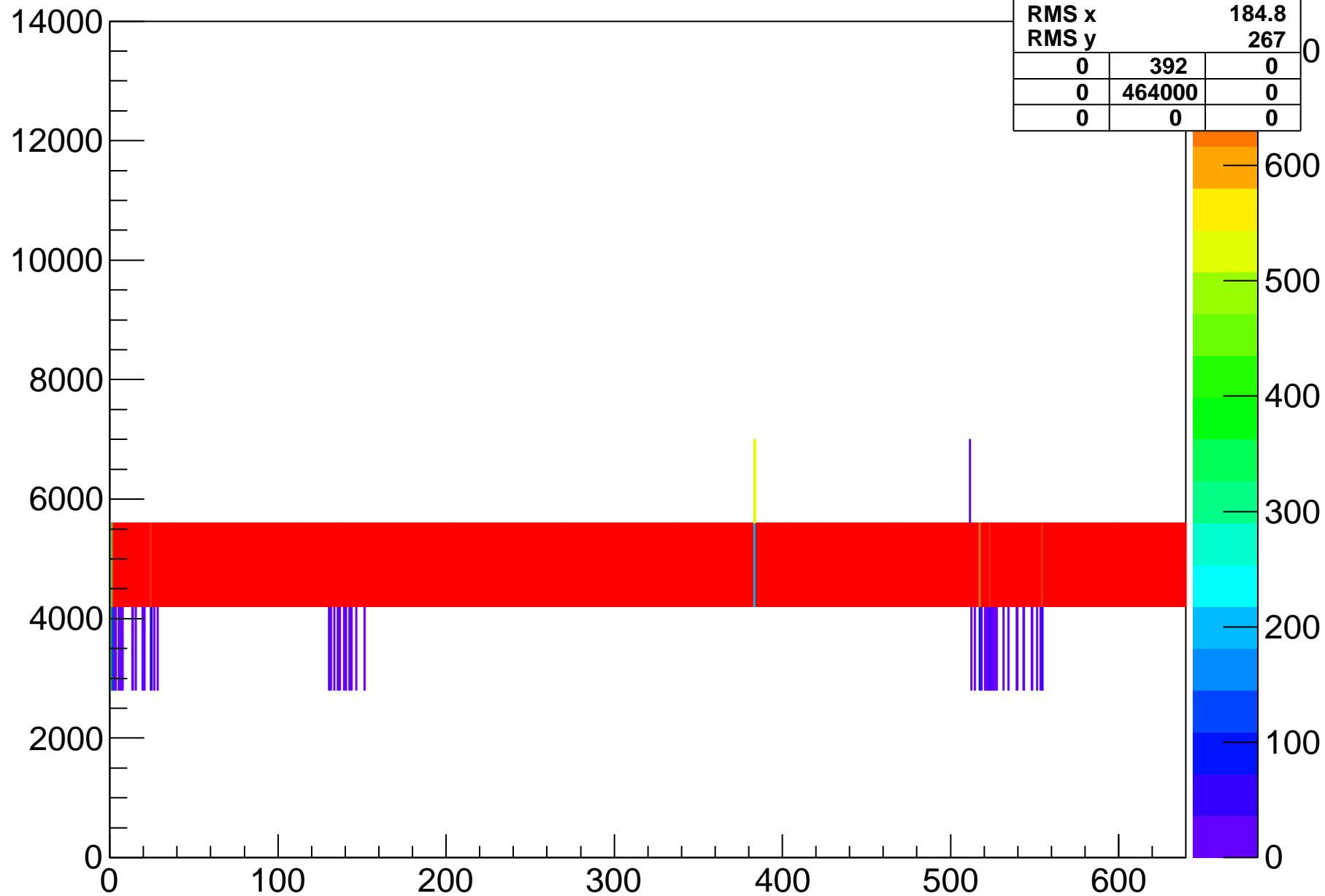
baselinesamples-fpga-7-hyb-2-sample-0

Entries	464392	
Mean x	319.5	
Mean y	4733	
RMS x	184.8	
RMS y	269.1	
0	392	0
0	464000	0
0	0	0



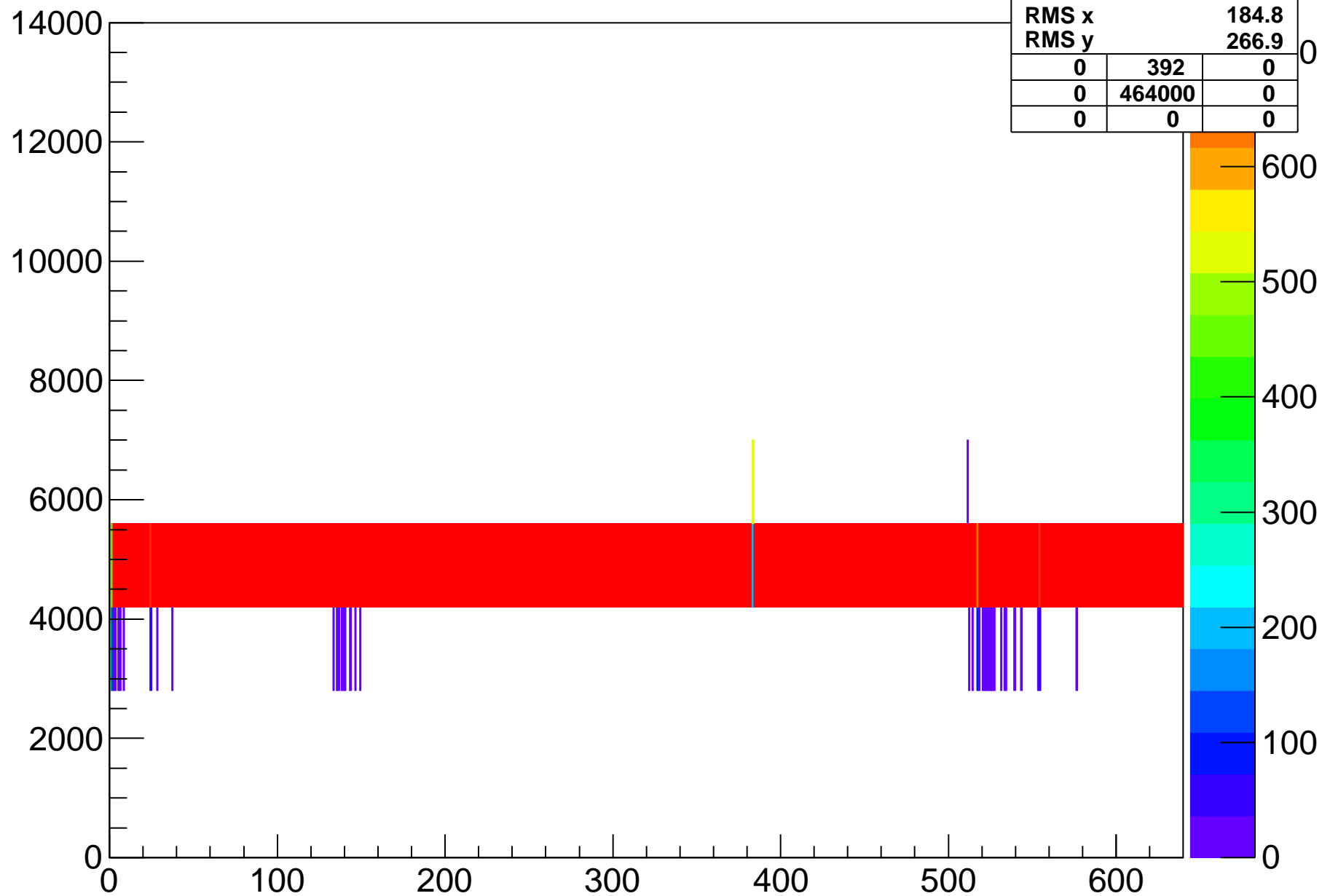
baselinesamples-fpga-7-hyb-2-sample-1

Entries	464392	
Mean x	319.5	
Mean y	4758	
RMS x	184.8	
RMS y	267	
0	392	0
0	464000	0
0	0	0



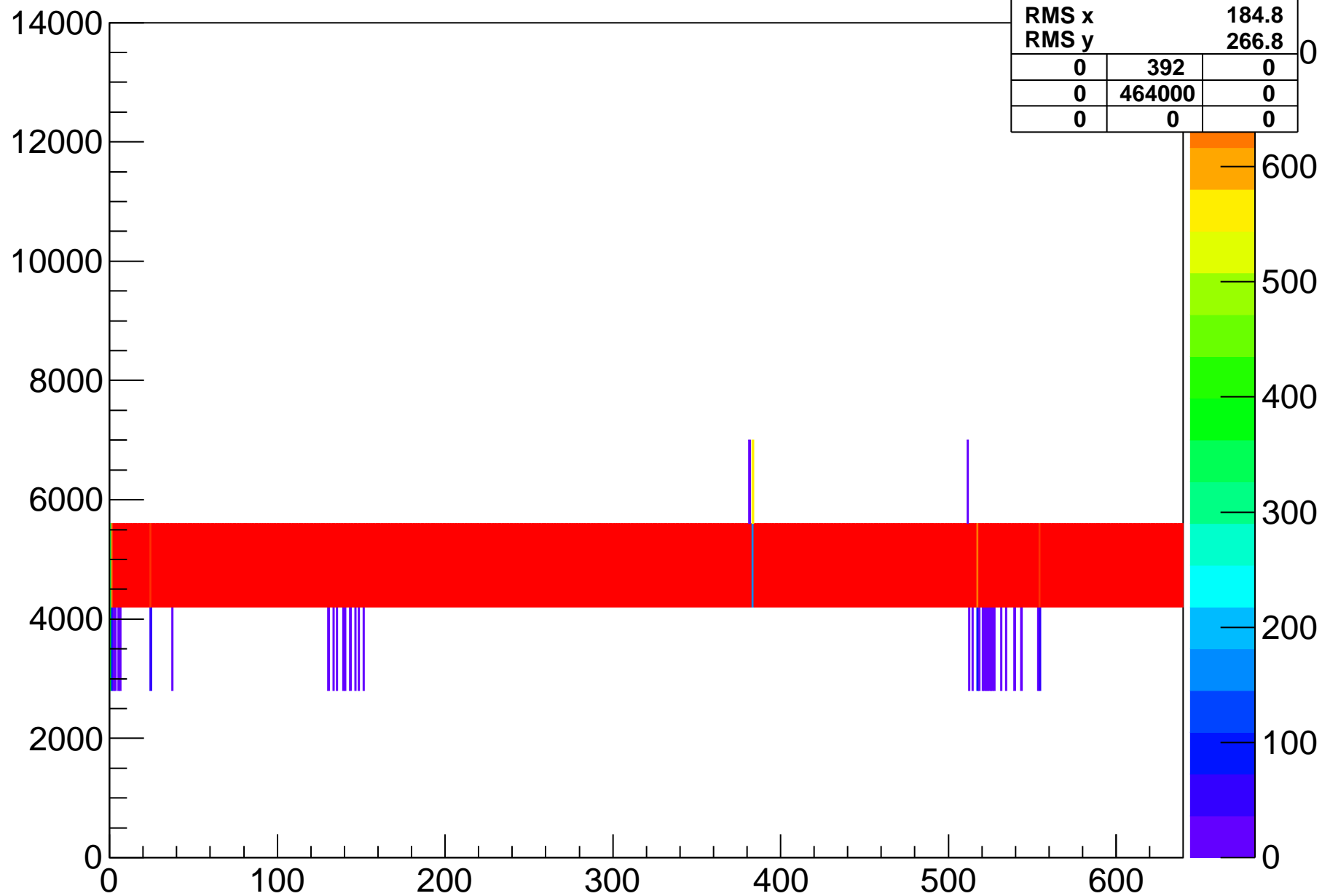
baselinesamples-fpga-7-hyb-2-sample-2

Entries	464392	
Mean x	319.5	
Mean y	4763	
RMS x	184.8	
RMS y	266.9	
0	392	0
0	464000	0
0	0	0



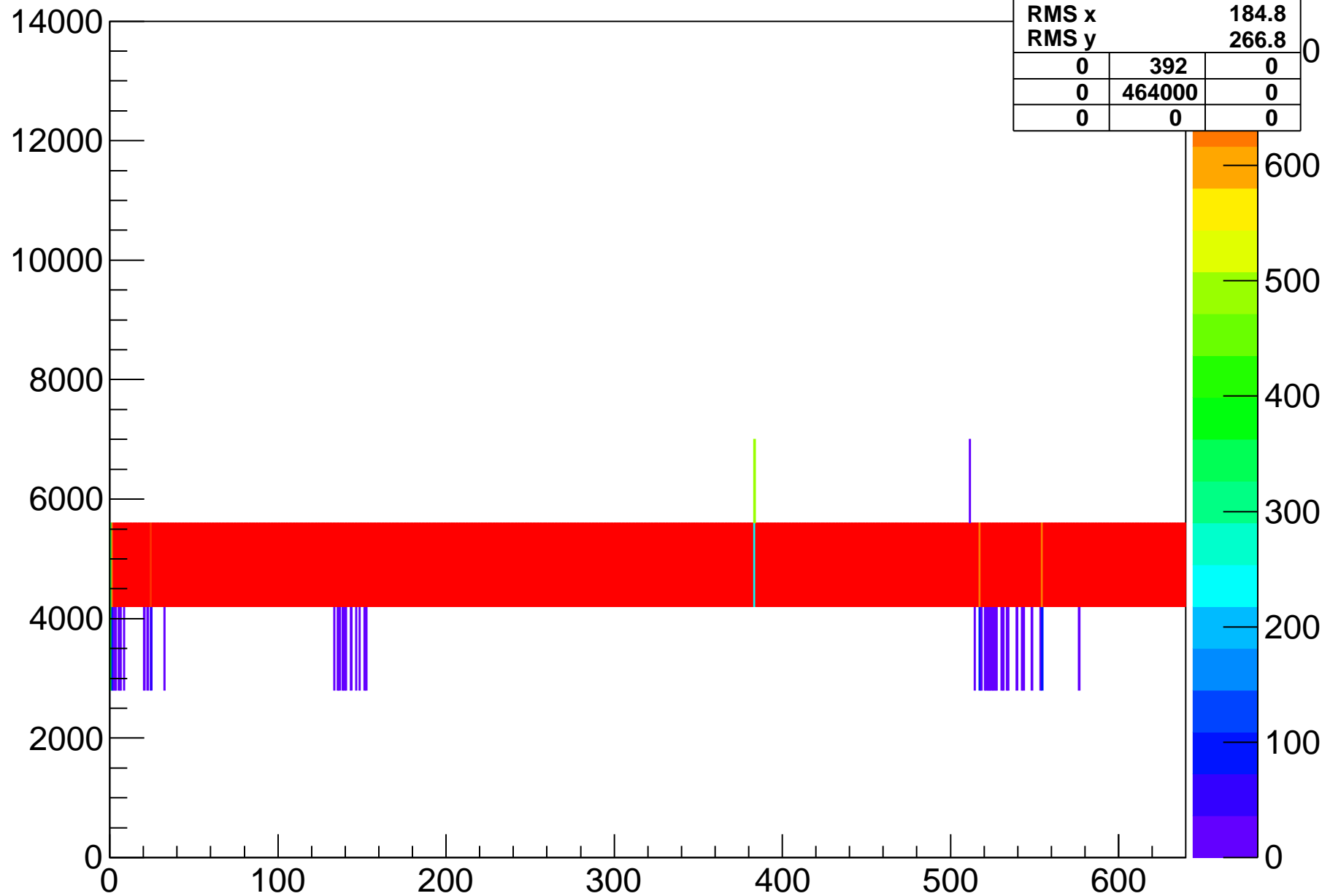
baselinesamples-fpga-7-hyb-2-sample-3

Entries	464392	
Mean x	319.5	
Mean y	4768	
RMS x	184.8	
RMS y	266.8	
0	392	0
0	464000	0
0	0	0



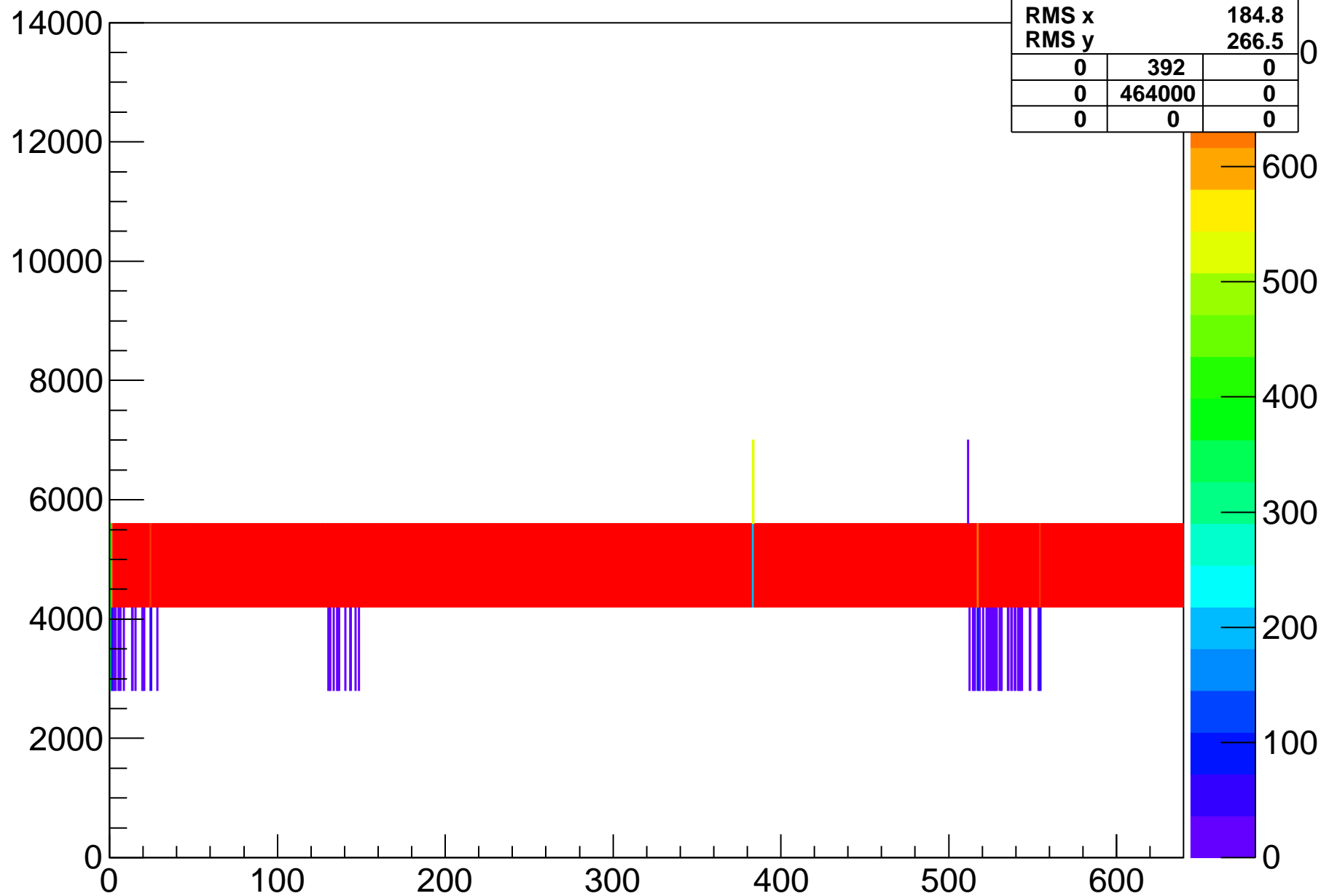
baselinesamples-fpga-7-hyb-2-sample-4

Entries	464392	
Mean x	319.5	
Mean y	4759	
RMS x	184.8	
RMS y	266.8	
0	392	0
0	464000	0
0	0	0



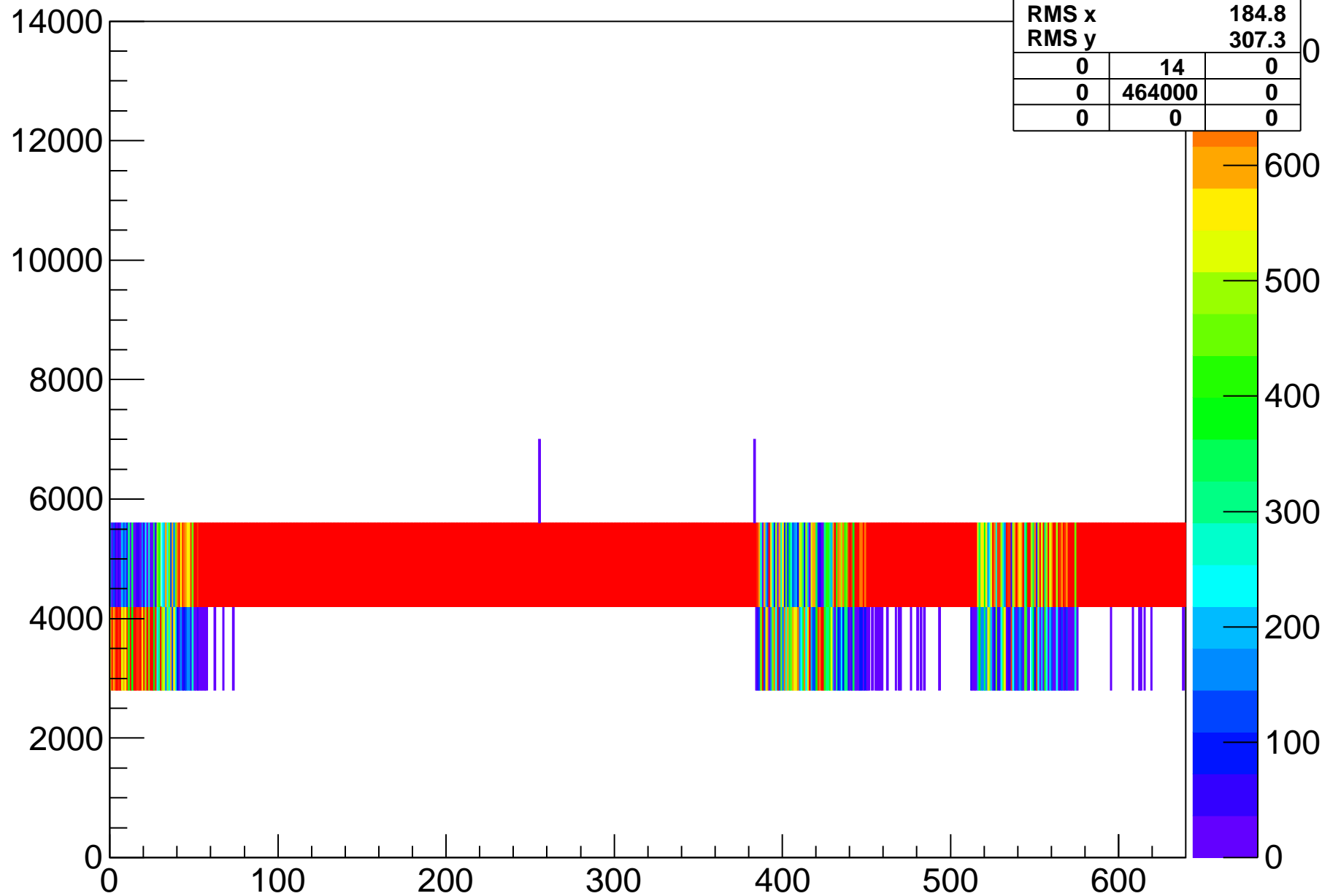
baselinesamples-fpga-7-hyb-2-sample-5

Entries	464392	
Mean x	319.5	
Mean y	4762	
RMS x	184.8	
RMS y	266.5	
0	392	0
0	464000	0
0	0	0



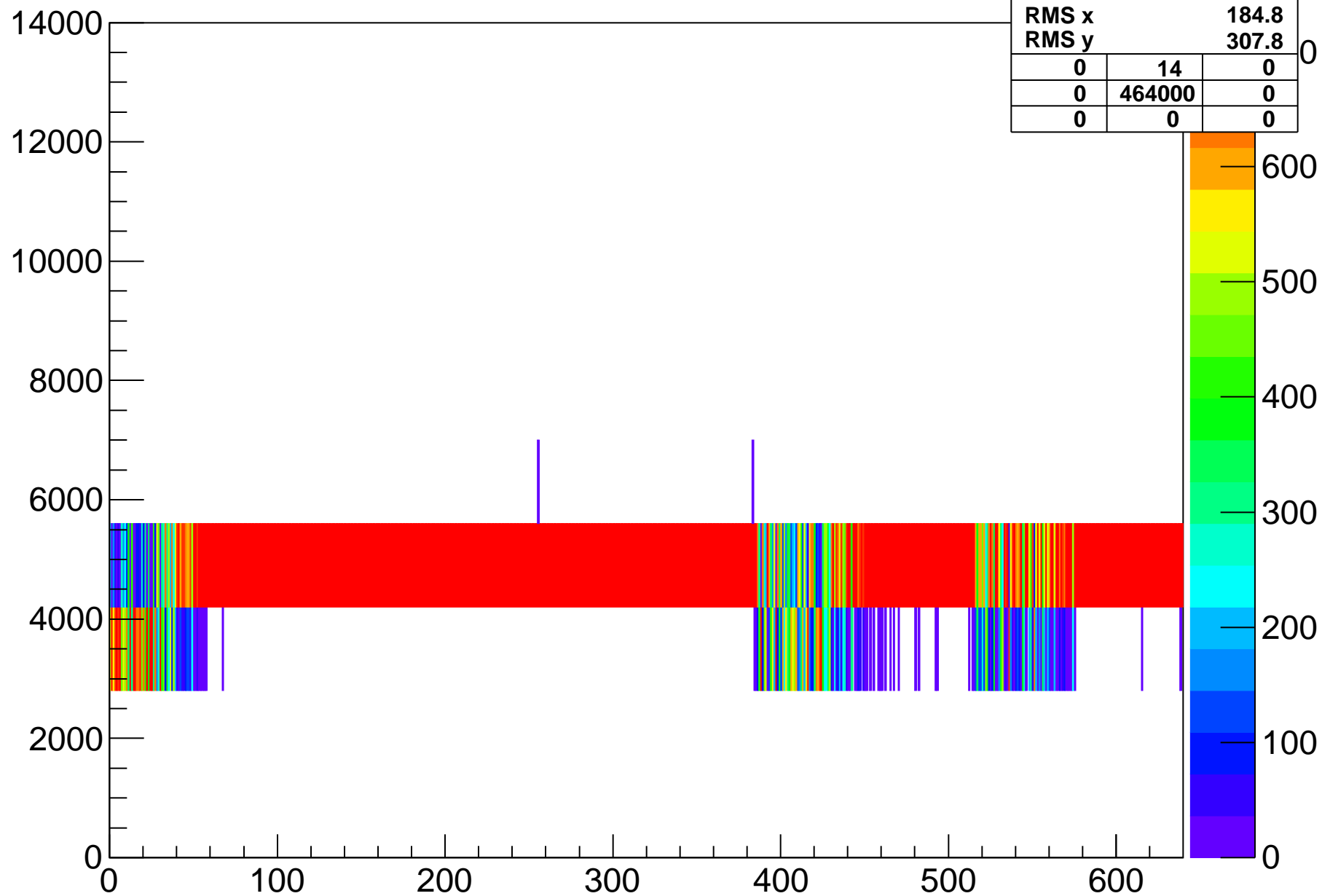
baselinesamples-fpga-7-hyb-3-sample-0

Entries	464014	
Mean x	319.5	
Mean y	4577	
RMS x	184.8	
RMS y	307.3	
0	14	0
0	464000	0
0	0	0



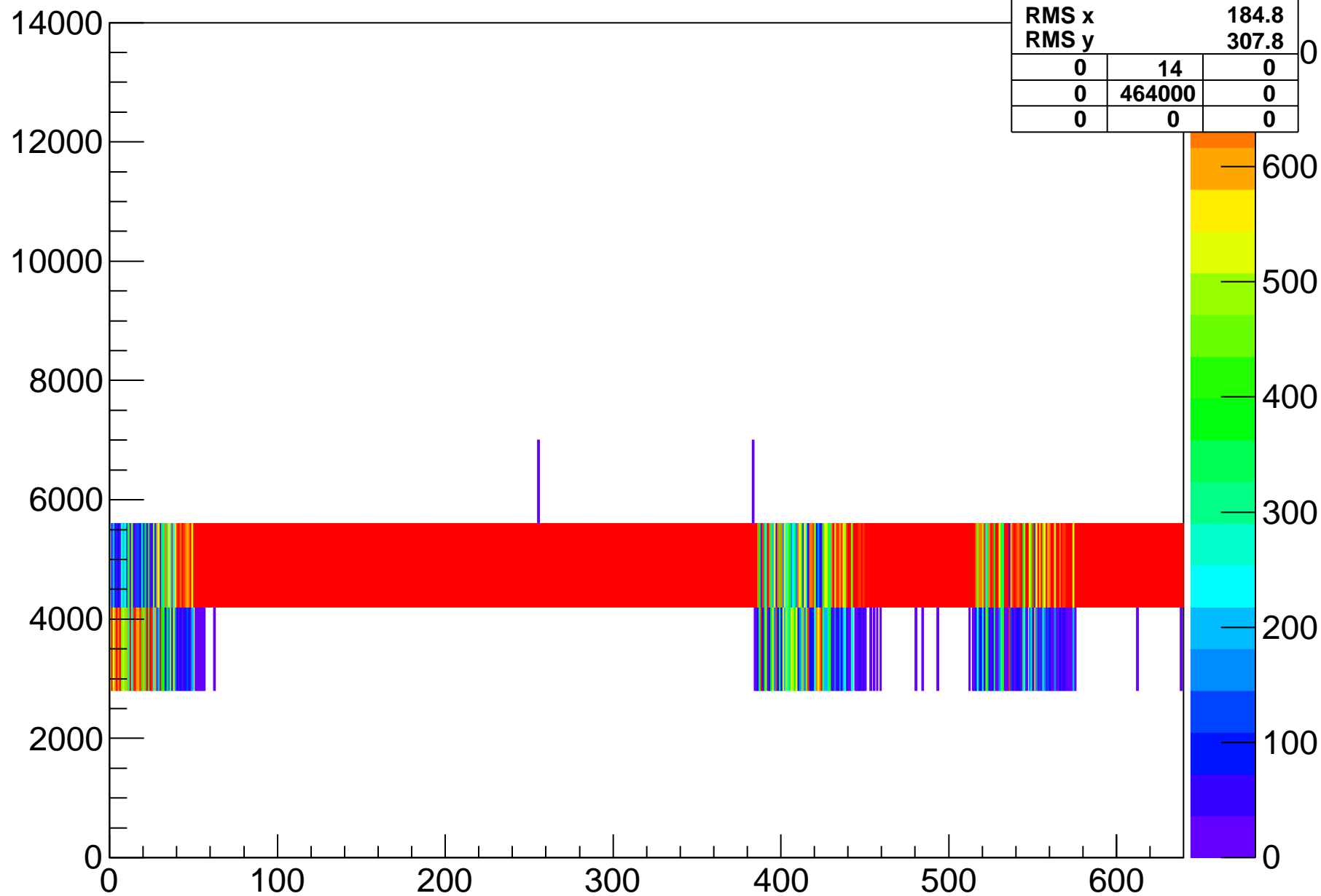
baselinesamples-fpga-7-hyb-3-sample-1

Entries	464014	
Mean x	319.5	
Mean y	4589	
RMS x	184.8	
RMS y	307.8	
0	14	0
0	464000	0
0	0	0



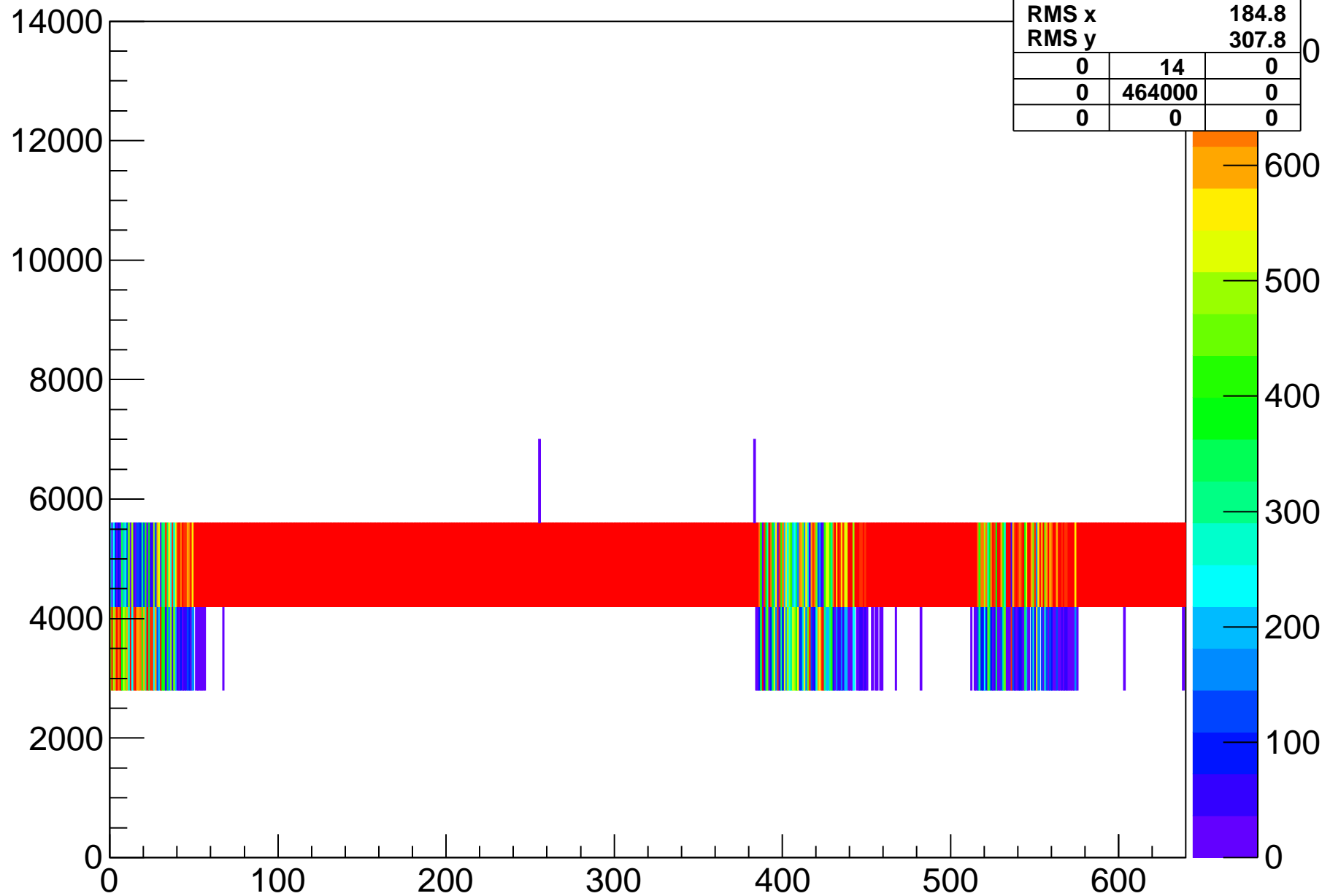
baselinesamples-fpga-7-hyb-3-sample-2

Entries	464014	
Mean x	319.5	
Mean y	4599	
RMS x	184.8	
RMS y	307.8	
0	14	0
0	464000	0
0	0	0



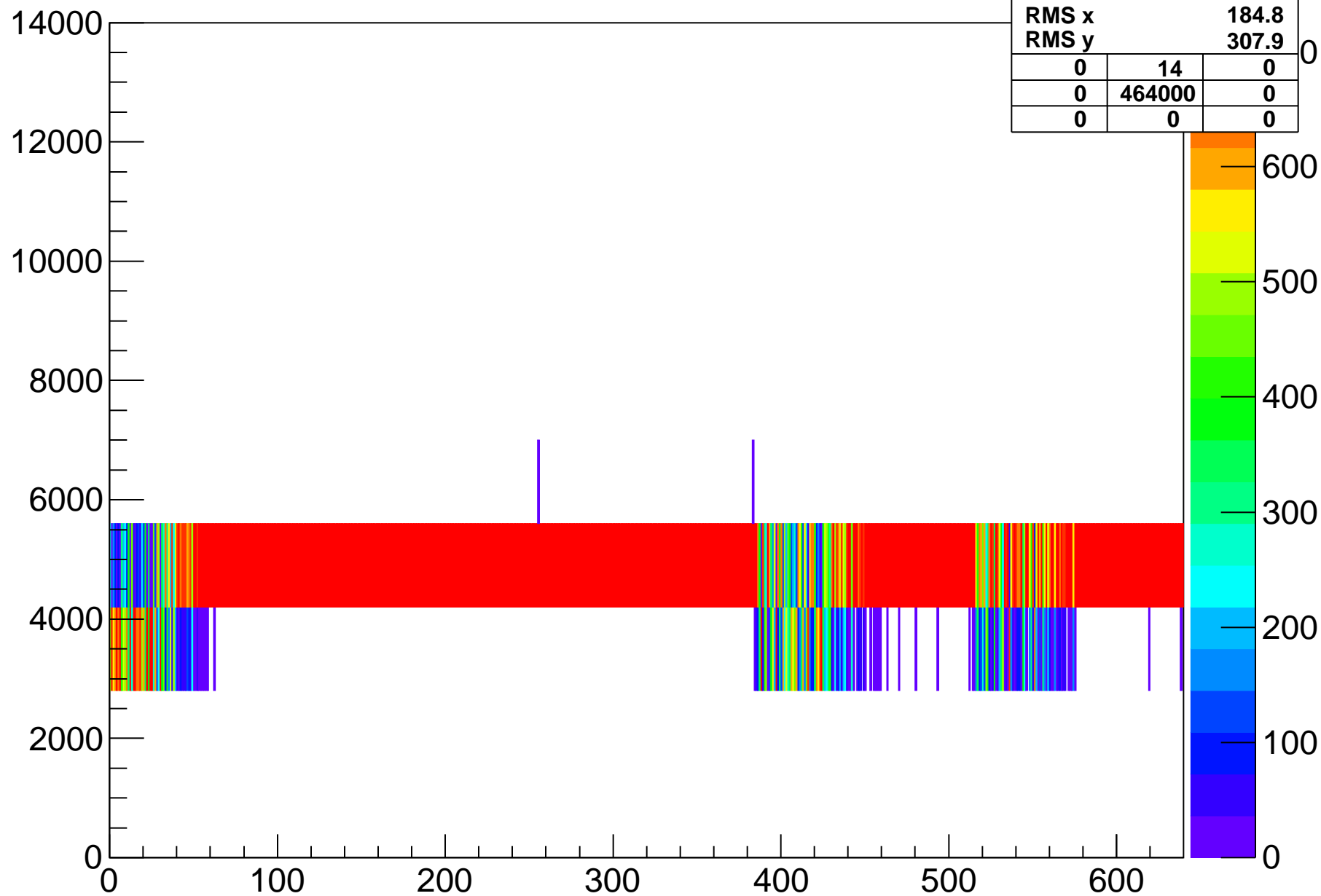
baselinesamples-fpga-7-hyb-3-sample-3

Entries	464014	
Mean x	319.5	
Mean y	4603	
RMS x	184.8	
RMS y	307.8	
0	14	0
0	464000	0
0	0	0



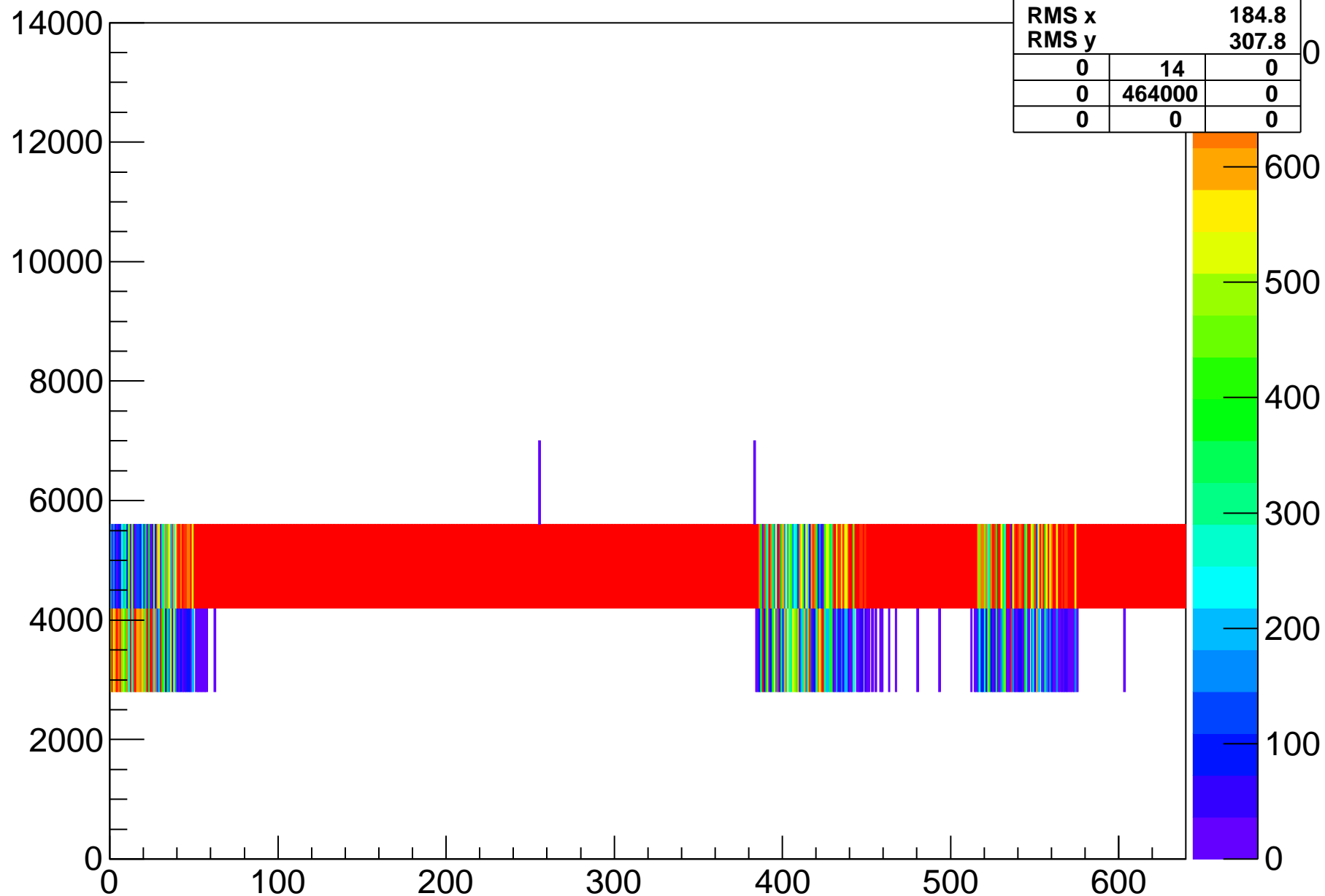
baselinesamples-fpga-7-hyb-3-sample-4

Entries	464014	
Mean x	319.5	
Mean y	4594	
RMS x	184.8	
RMS y	307.9	
0	14	0
0	464000	0
0	0	0



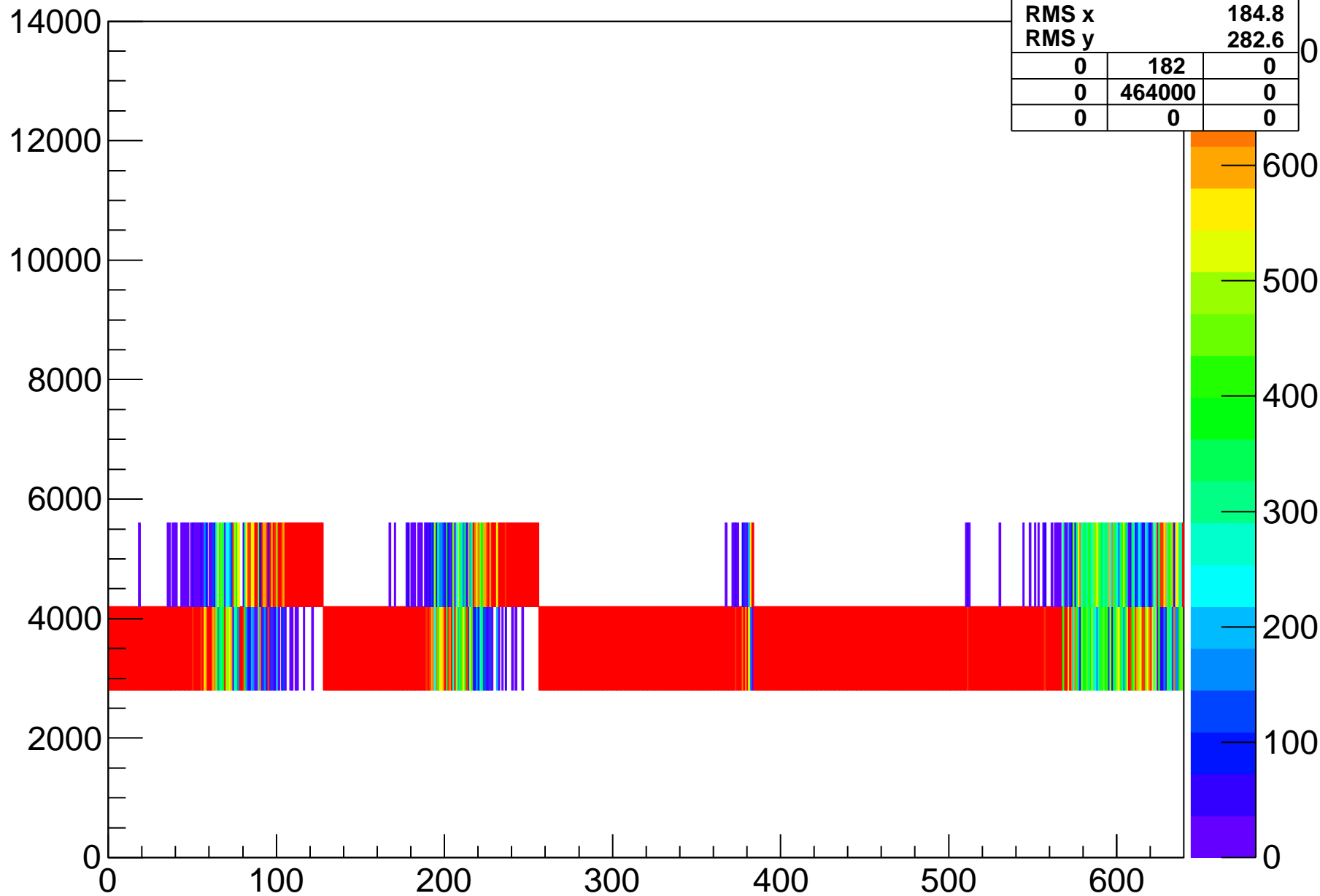
baselinesamples-fpga-7-hyb-3-sample-5

Entries	464014	
Mean x	319.5	
Mean y	4597	
RMS x	184.8	
RMS y	307.8	
0	14	0
0	464000	0
0	0	0



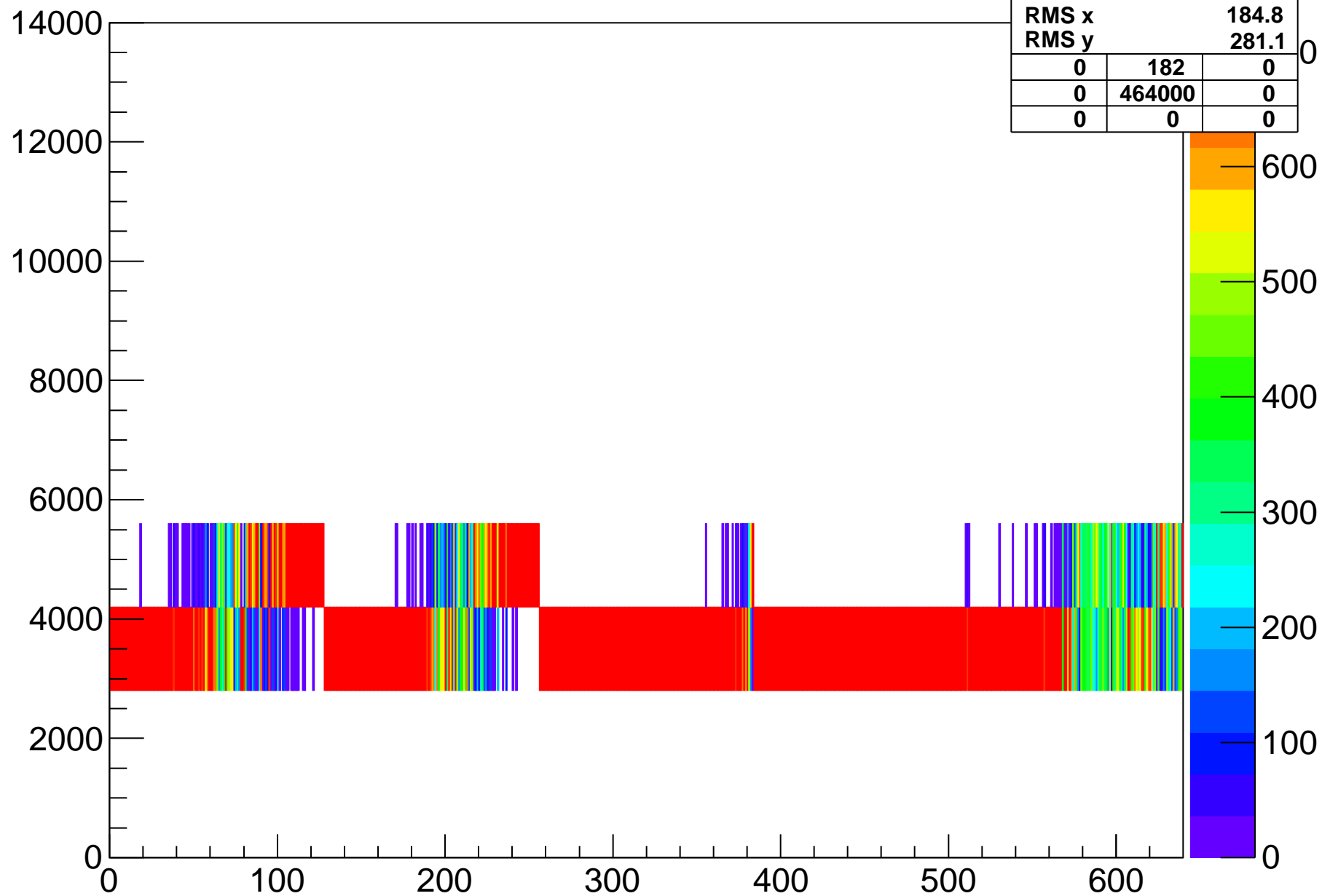
baselinesamples-fpga-8-hyb-0-sample-0

Entries	464182	
Mean x	319.5	
Mean y	3939	
RMS x	184.8	
RMS y	282.6	
0	182	0
0	464000	0
0	0	0



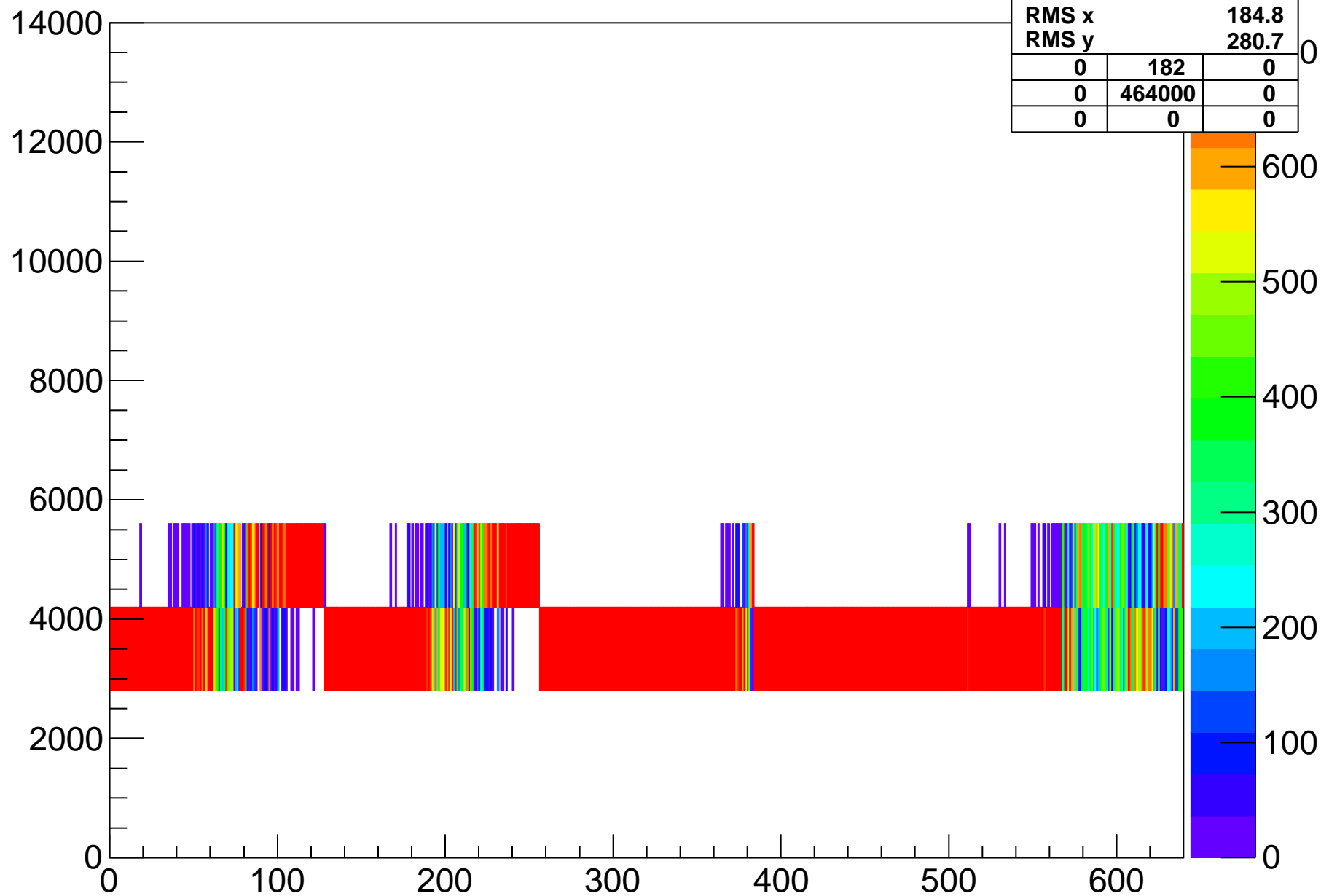
baselinesamples-fpga-8-hyb-0-sample-1

Entries	464182	
Mean x	319.5	
Mean y	3942	
RMS x	184.8	
RMS y	281.1	
0	182	0
0	464000	0
0	0	0



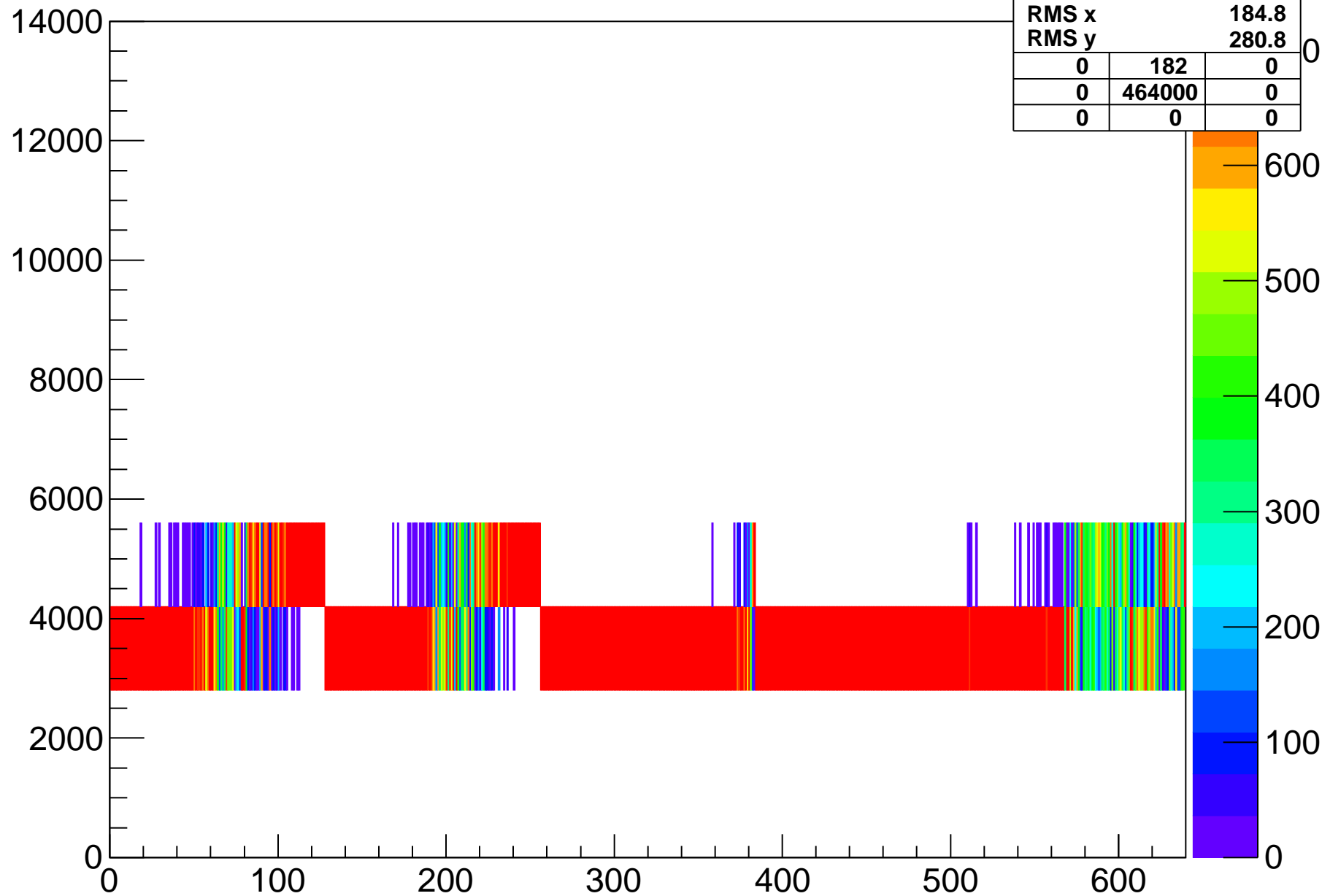
baselinesamples-fpga-8-hyb-0-sample-2

Entries	464182	
Mean x	319.5	
Mean y	3951	
RMS x	184.8	
RMS y	280.7	
0	182	0
0	464000	0
0	0	0



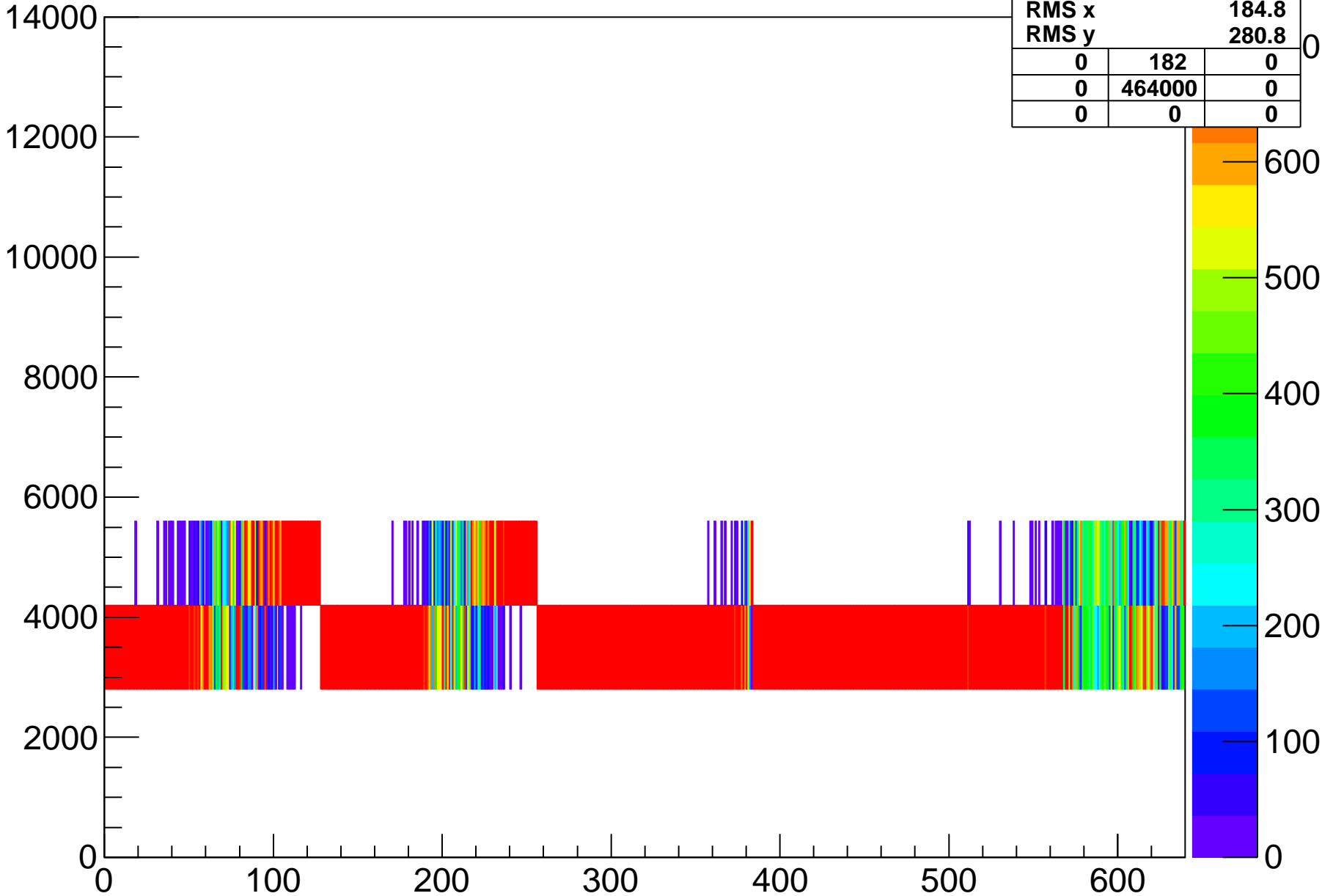
baselinesamples-fpga-8-hyb-0-sample-3

Entries	464182	
Mean x	319.5	
Mean y	3952	
RMS x	184.8	
RMS y	280.8	
0	182	0
0	464000	0
0	0	0



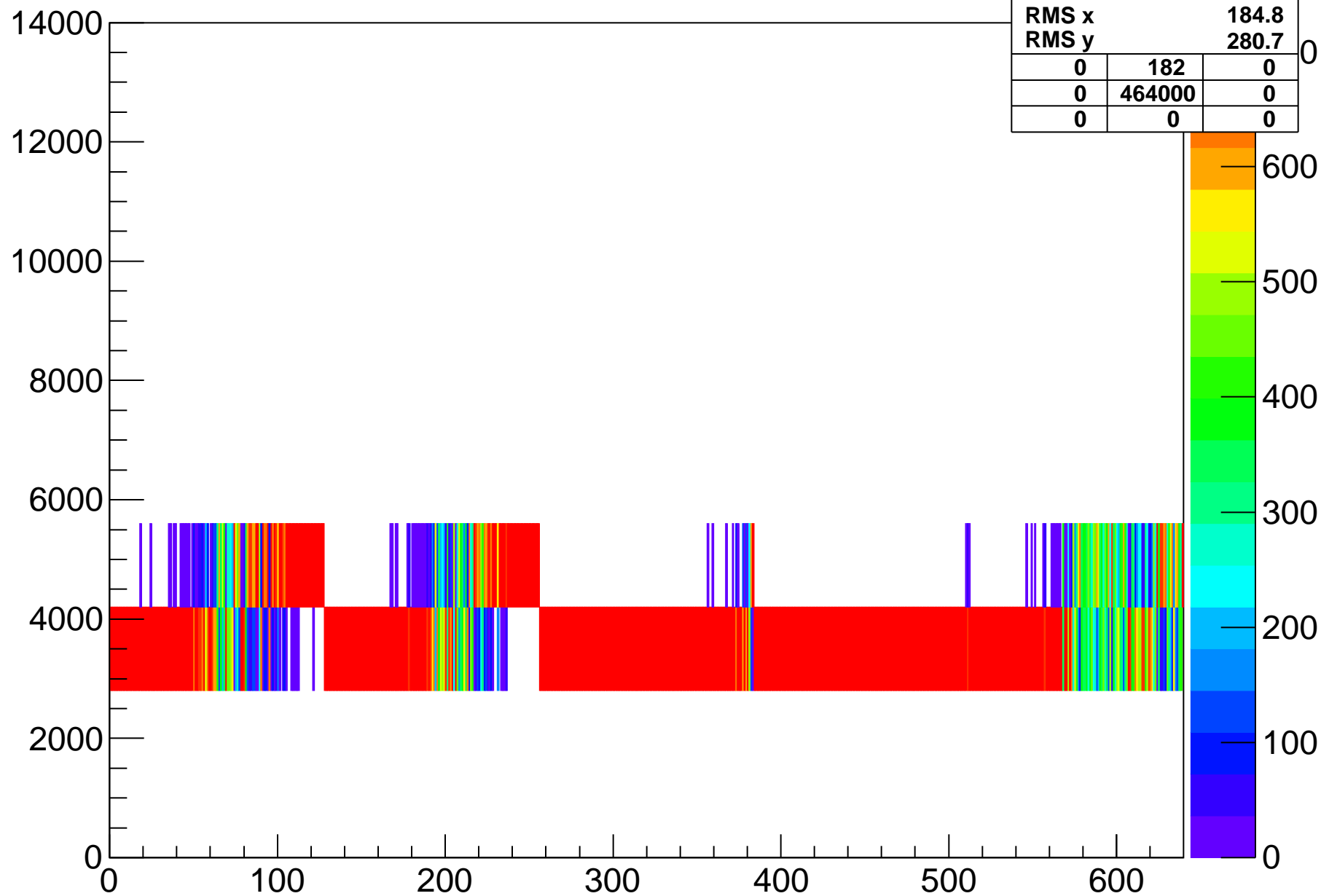
baselinesamples-fpga-8-hyb-0-sample-4
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Entries	464182	
Mean x	319.5	
Mean y	3945	
RMS x	184.8	
RMS y	280.8	
0	182	0
0	464000	0
0	0	0



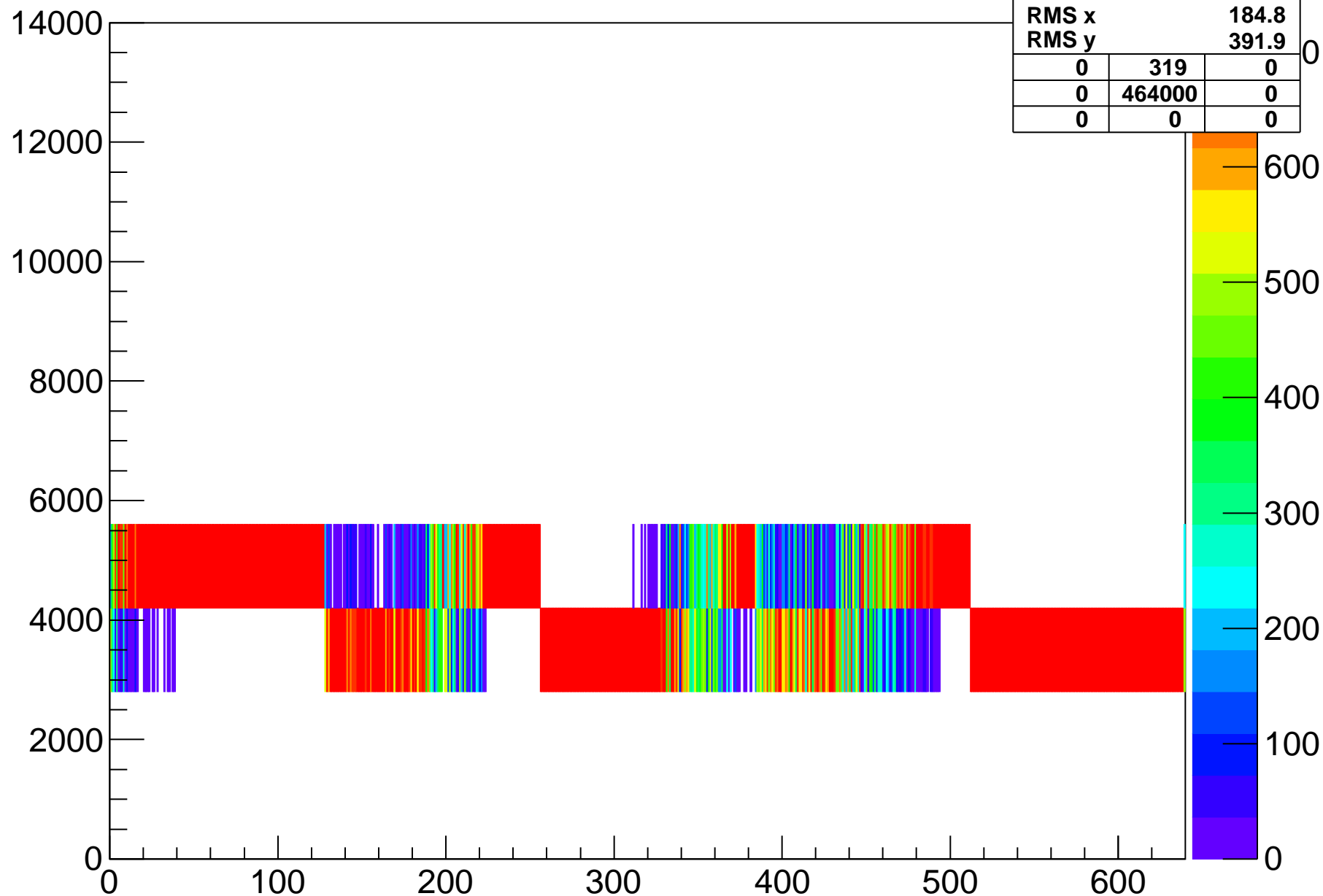
baselinesamples-fpga-8-hyb-0-sample-5

Entries	464182	
Mean x	319.5	
Mean y	3949	
RMS x	184.8	
RMS y	280.7	
0	182	0
0	464000	0
0	0	0



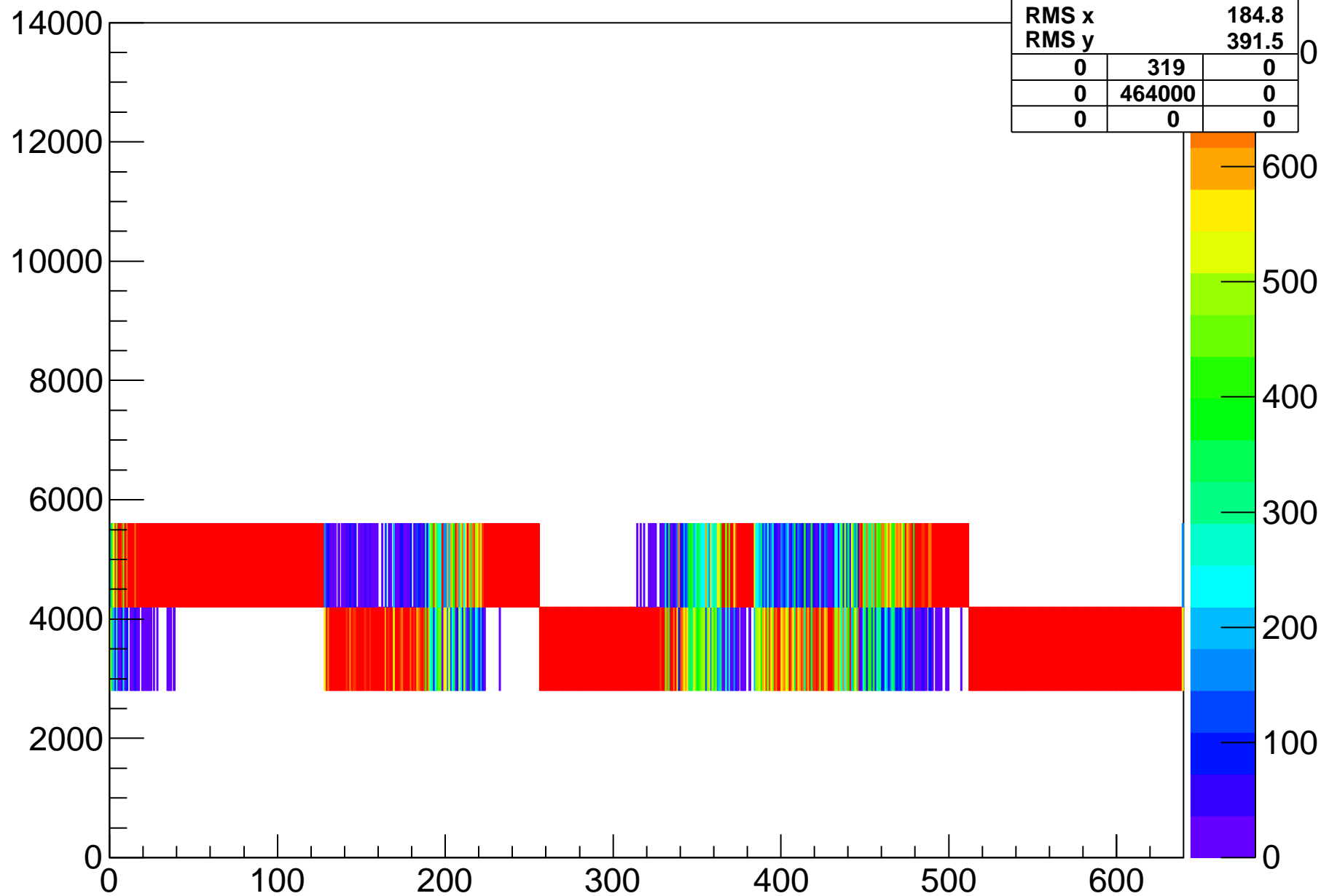
baselinesamples-fpga-8-hyb-1-sample-0

Entries	464319	
Mean x	319.5	
Mean y	4121	
RMS x	184.8	
RMS y	391.9	
0	319	0
0	464000	0
0	0	0



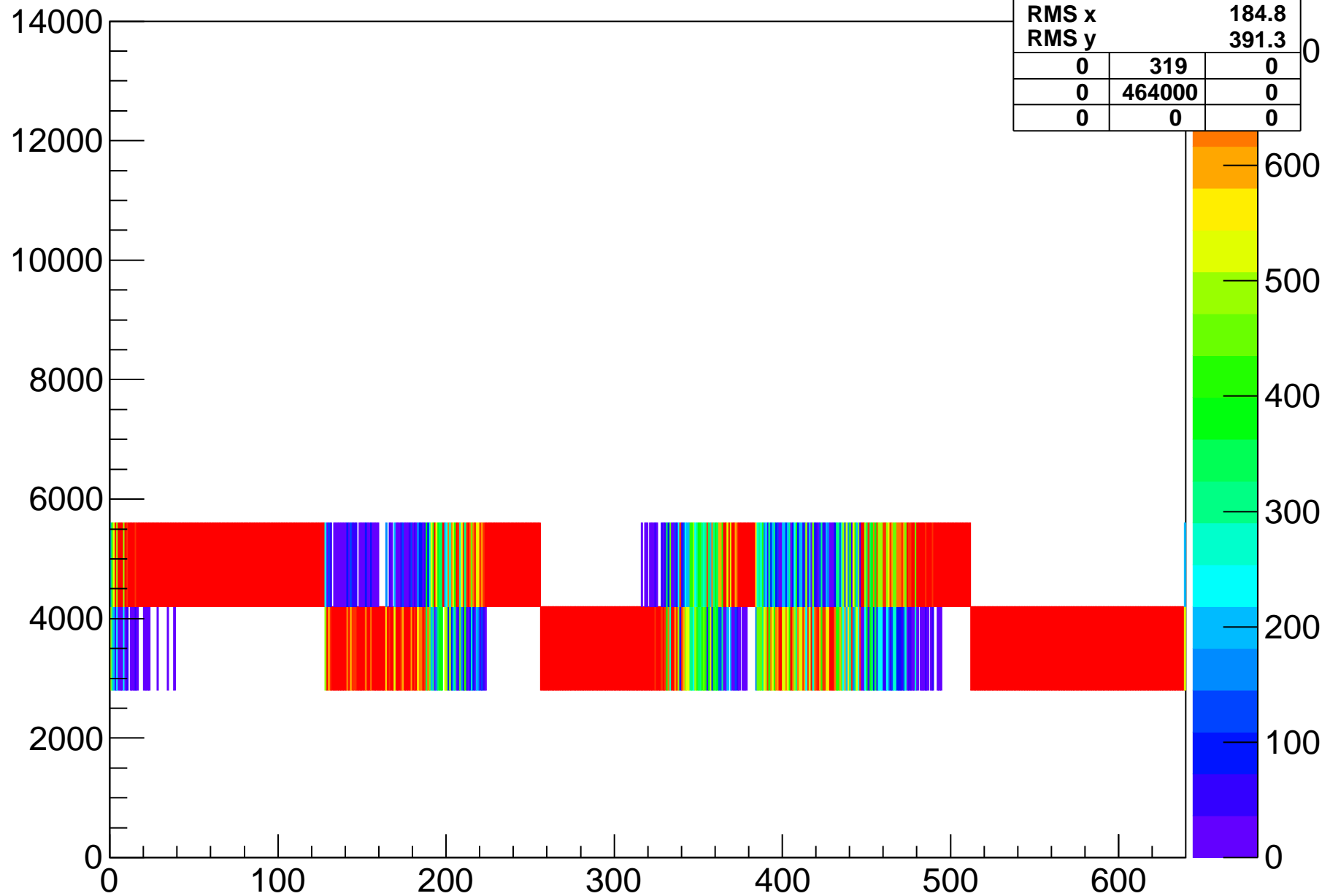
baselinesamples-fpga-8-hyb-1-sample-1

Entries	464319	
Mean x	319.5	
Mean y	4114	
RMS x	184.8	
RMS y	391.5	
0	319	0
0	464000	0
0	0	0



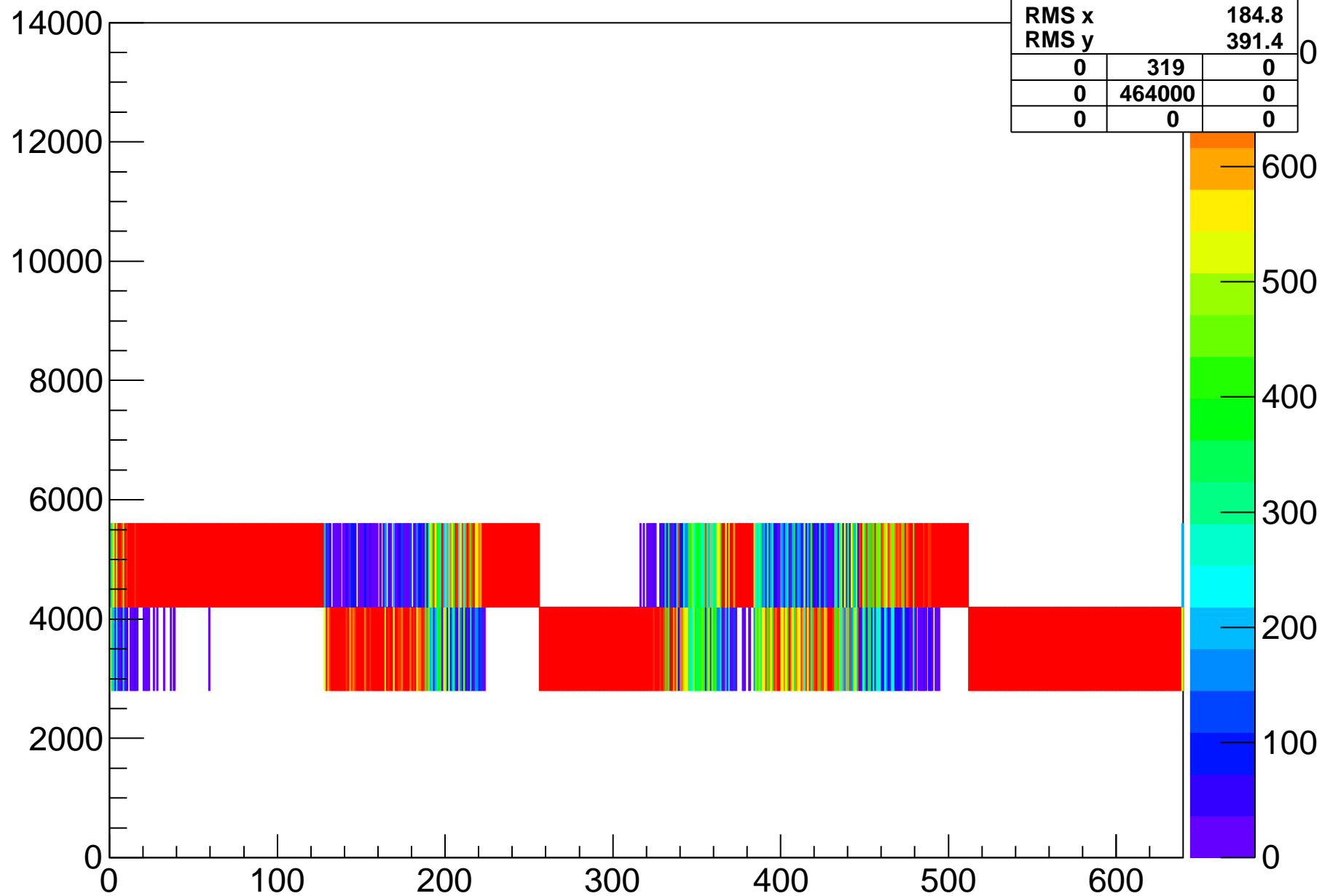
baselinesamples-fpga-8-hyb-1-sample-2

Entries	464319	
Mean x	319.5	
Mean y	4128	
RMS x	184.8	
RMS y	391.3	
0	319	0
0	464000	0
0	0	0



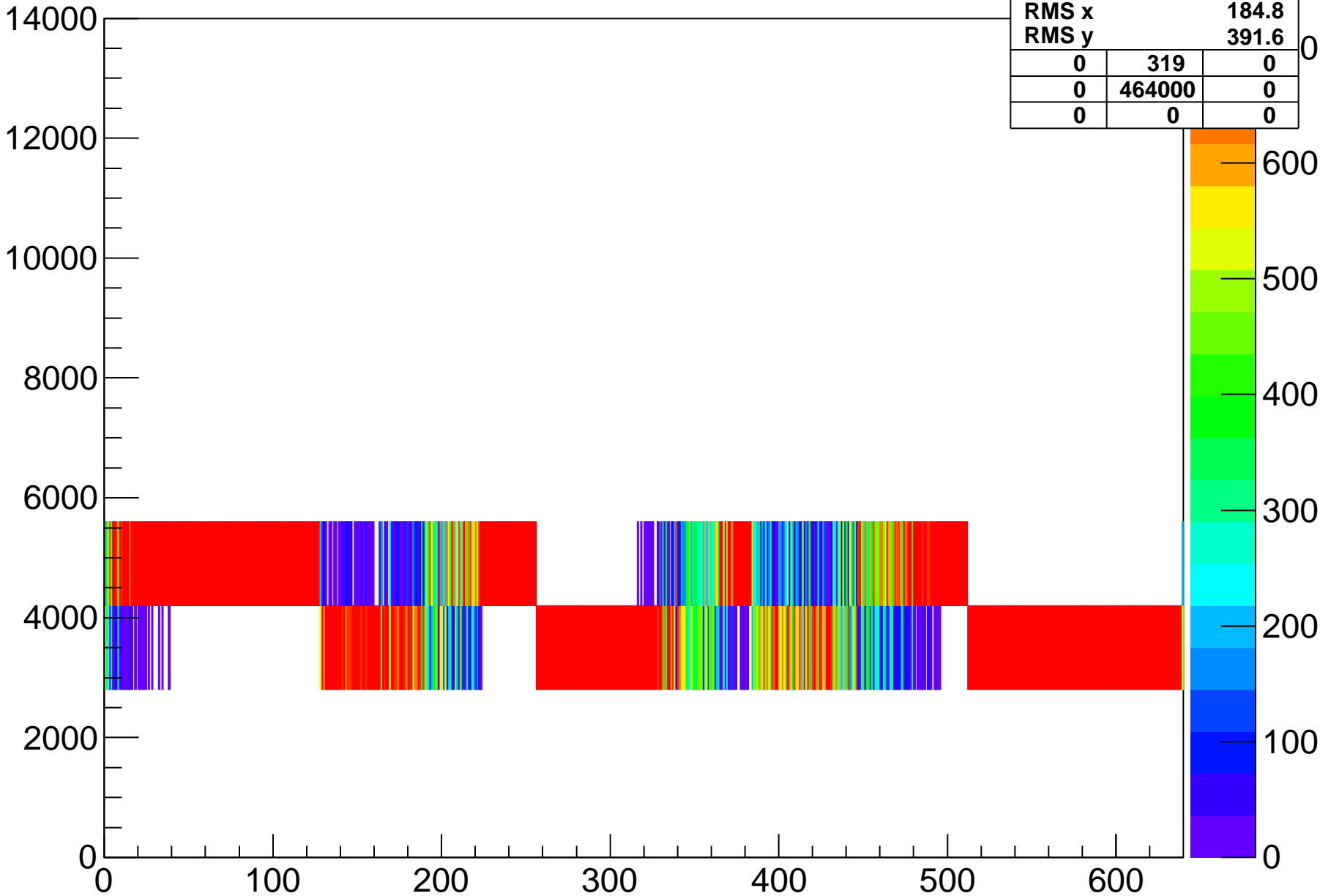
baselinesamples-fpga-8-hyb-1-sample-3

Entries	464319	
Mean x	319.5	
Mean y	4126	
RMS x	184.8	
RMS y	391.4	
0	319	0
0	464000	0
0	0	0



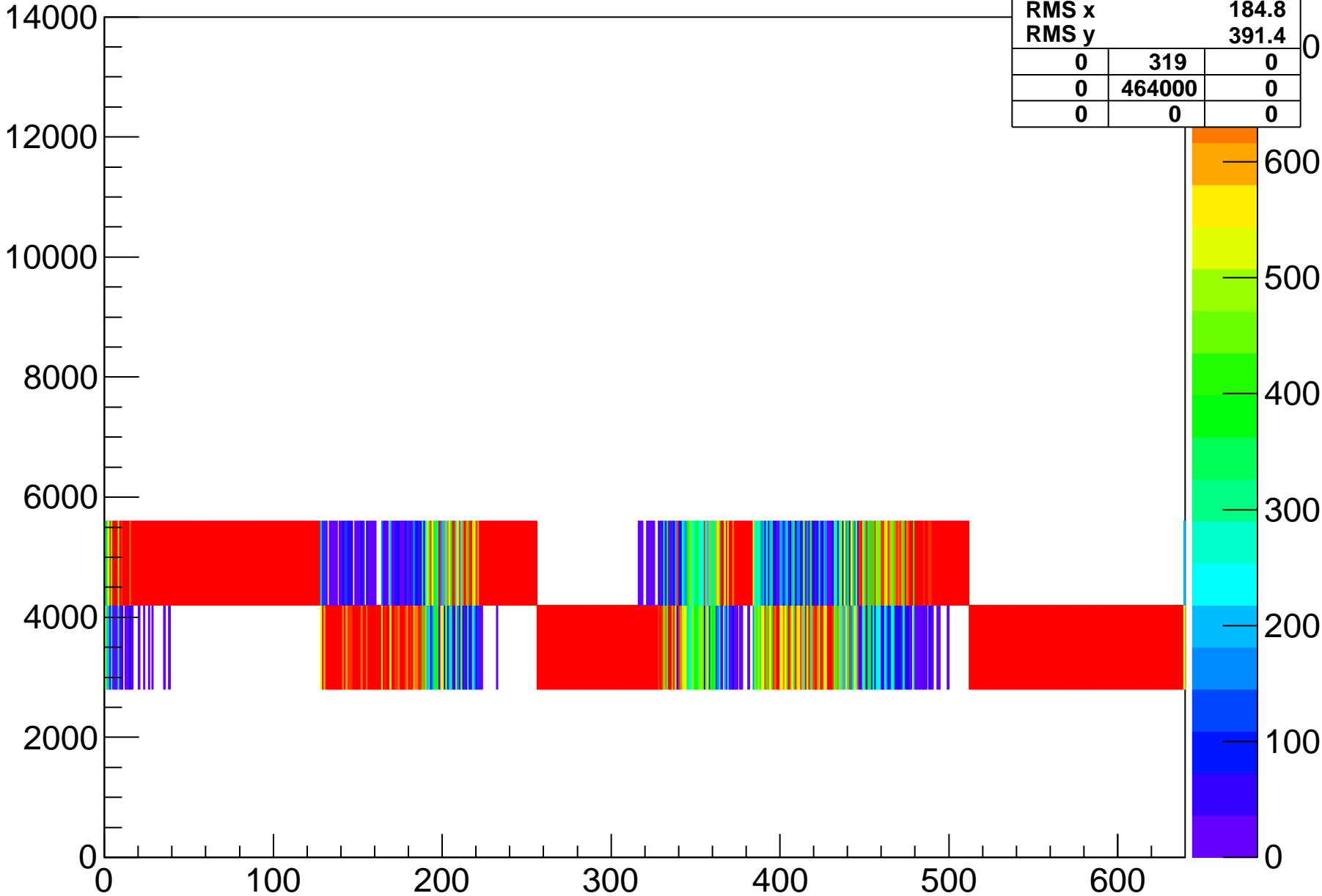
baselinesamples-fpga-8-hyb-1-sample-4

Entries	464319	
Mean x	319.5	
Mean y	4121	
RMS x	184.8	
RMS y	391.6	
0	319	0
0	464000	0
0	0	0



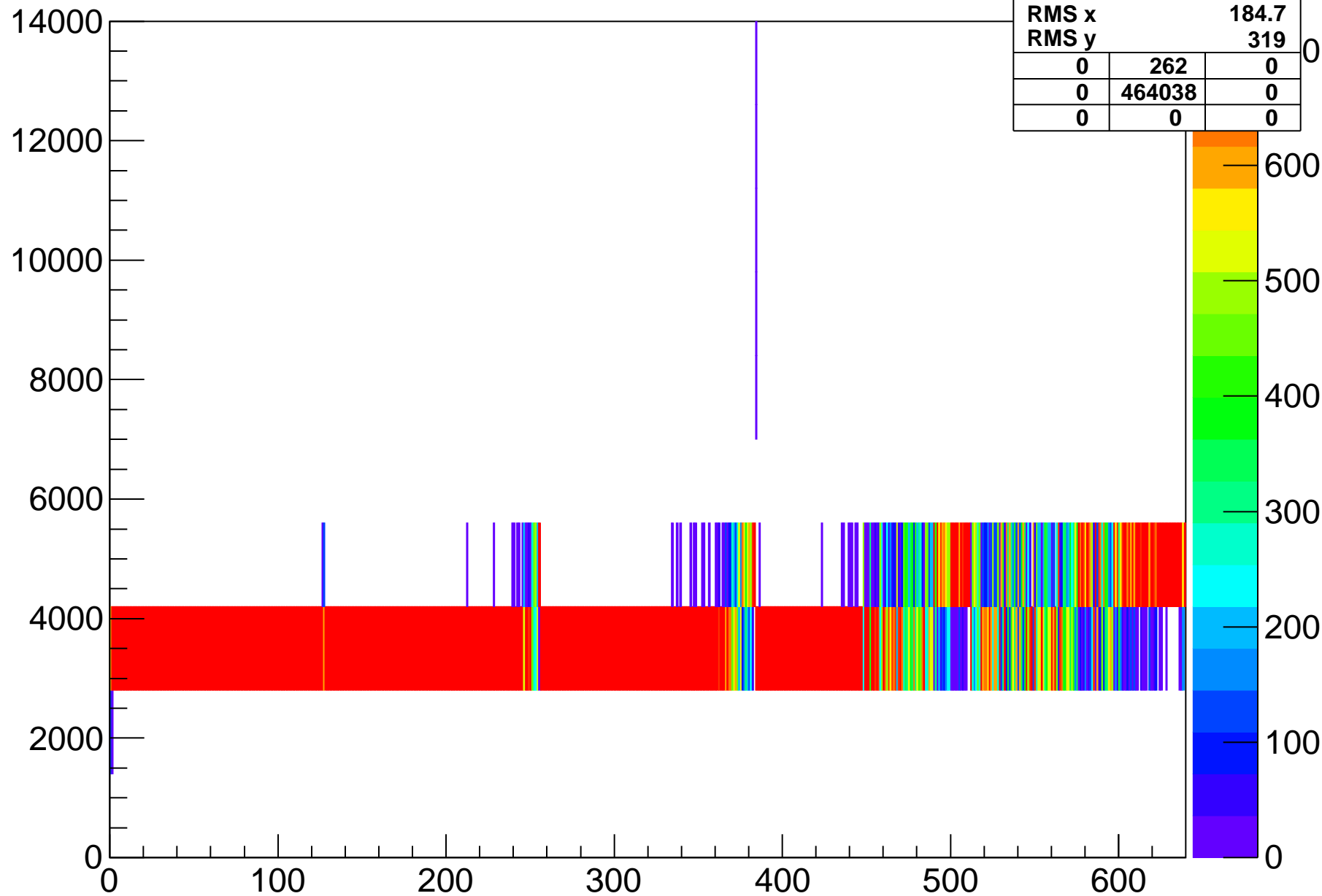
baselinesamples-fpga-8-hyb-1-sample-5

Entries	464319	
Mean x	319.5	
Mean y	4125	
RMS x	184.8	
RMS y	391.4	
0	319	0
0	464000	0
0	0	0



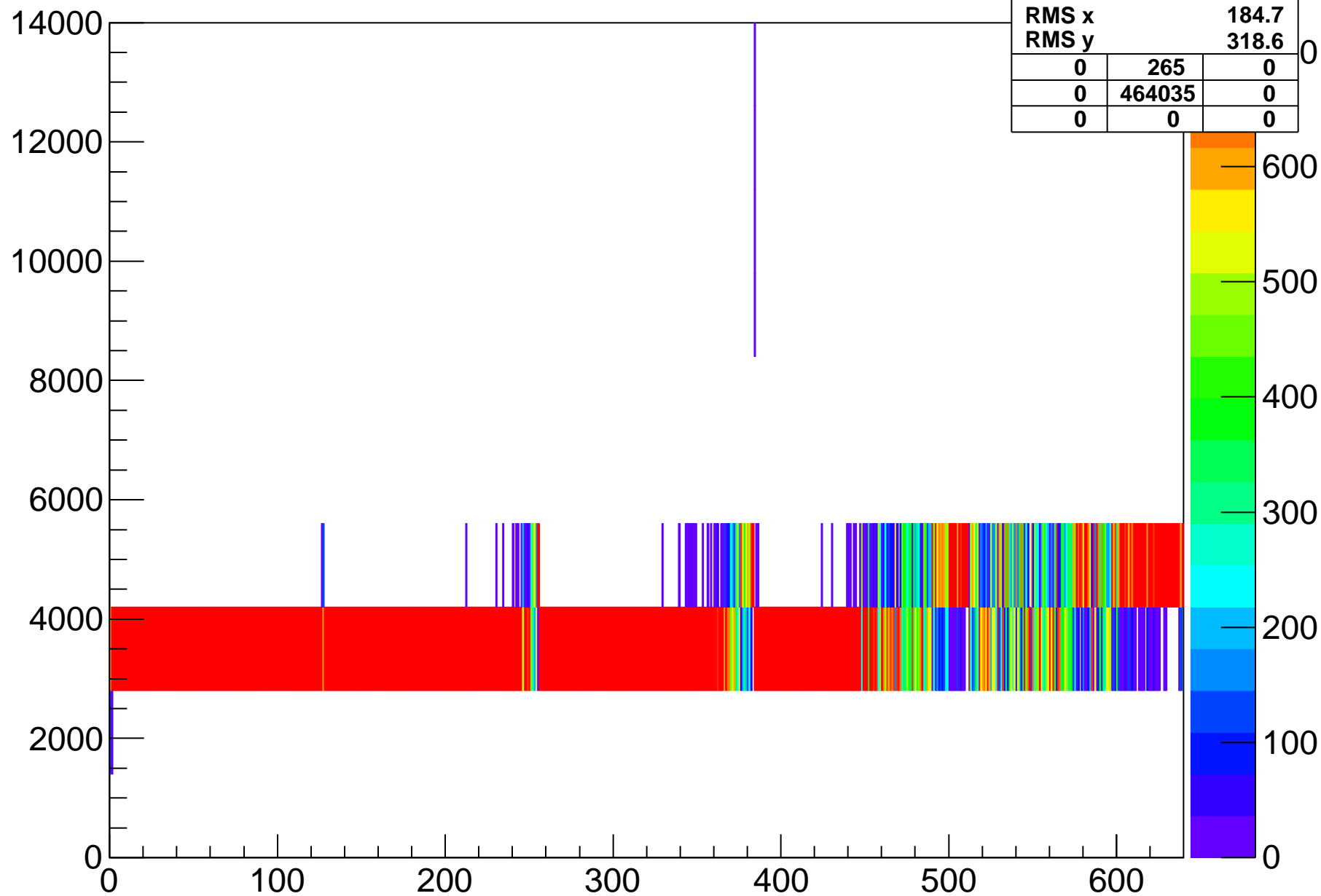
baselinesamples-fpga-8-hyb-2-sample-0

Entries	464300	
Mean x	319.5	
Mean y	3898	
RMS x	184.7	
RMS y	319	
0	262	0
0	464038	0
0	0	0



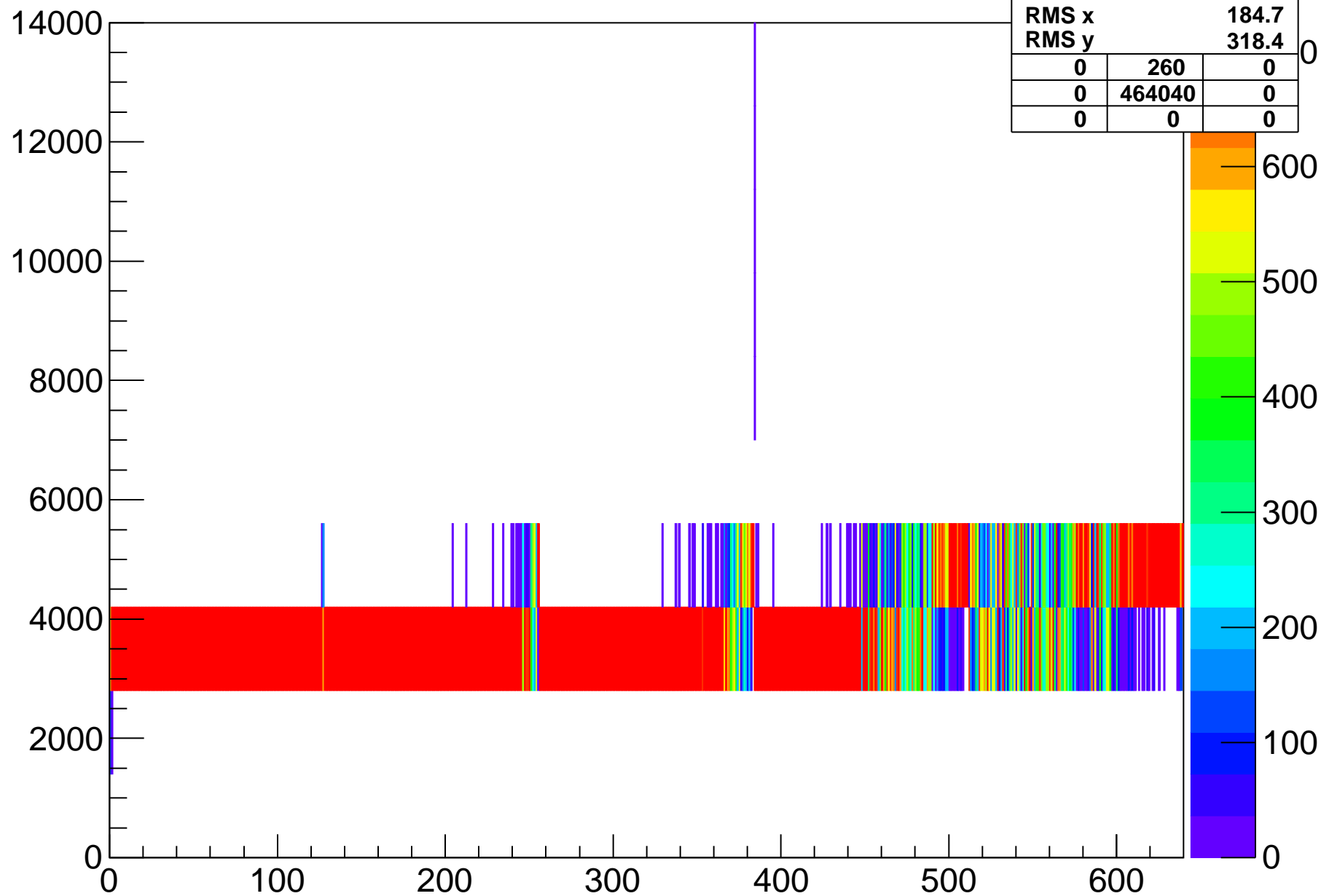
baselinesamples-fpga-8-hyb-2-sample-1

Entries	464300	
Mean x	319.5	
Mean y	3902	
RMS x	184.7	
RMS y	318.6	
0	265	0
0	464035	0
0	0	0



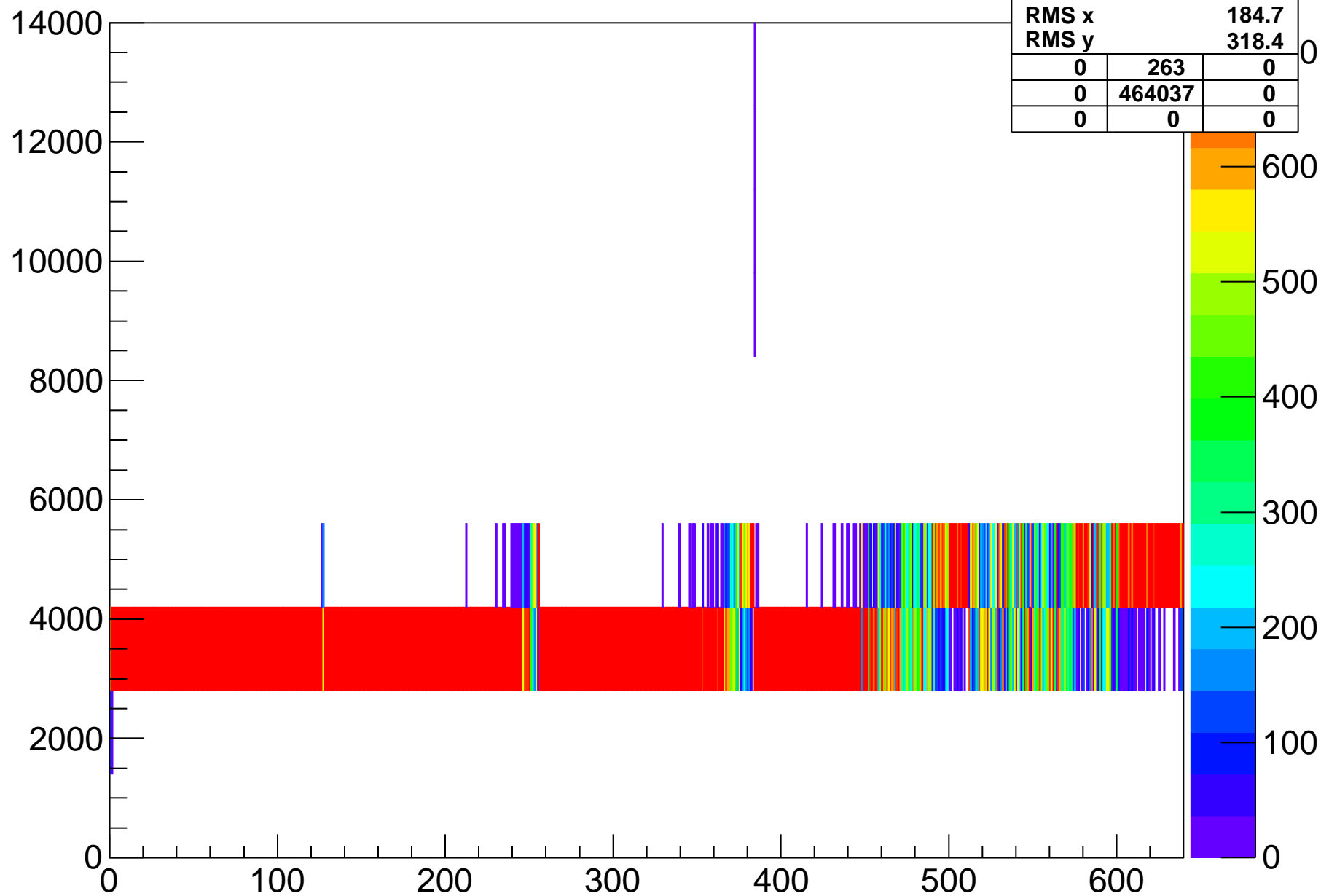
baselinesamples-fpga-8-hyb-2-sample-2

Entries	464300	
Mean x	319.5	
Mean y	3912	
RMS x	184.7	
RMS y	318.4	
0	260	0
0	464040	0
0	0	0



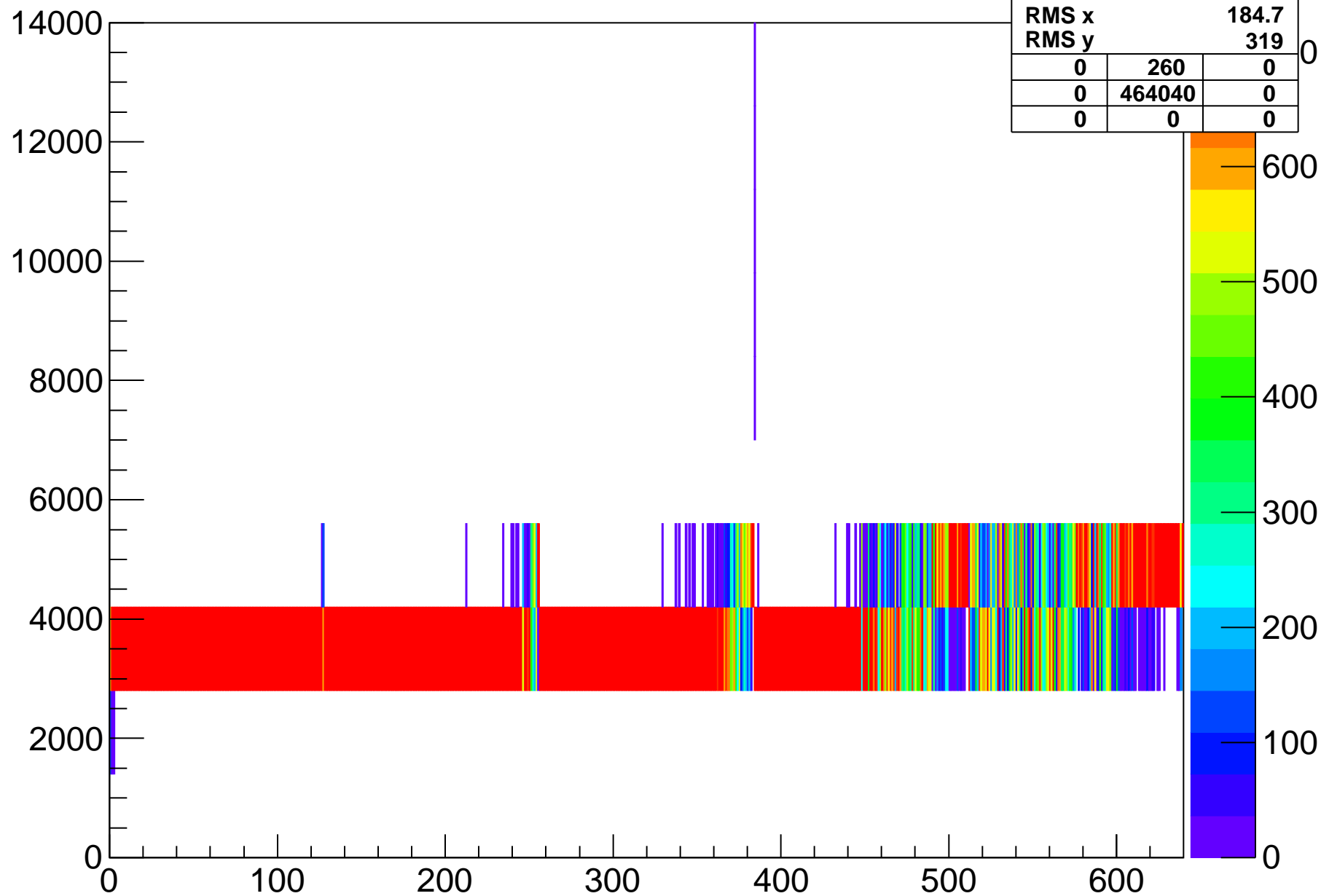
baselinesamples-fpga-8-hyb-2-sample-3

Entries	464300	
Mean x	319.5	
Mean y	3912	
RMS x	184.7	
RMS y	318.4	
0	263	0
0	464037	0
0	0	0



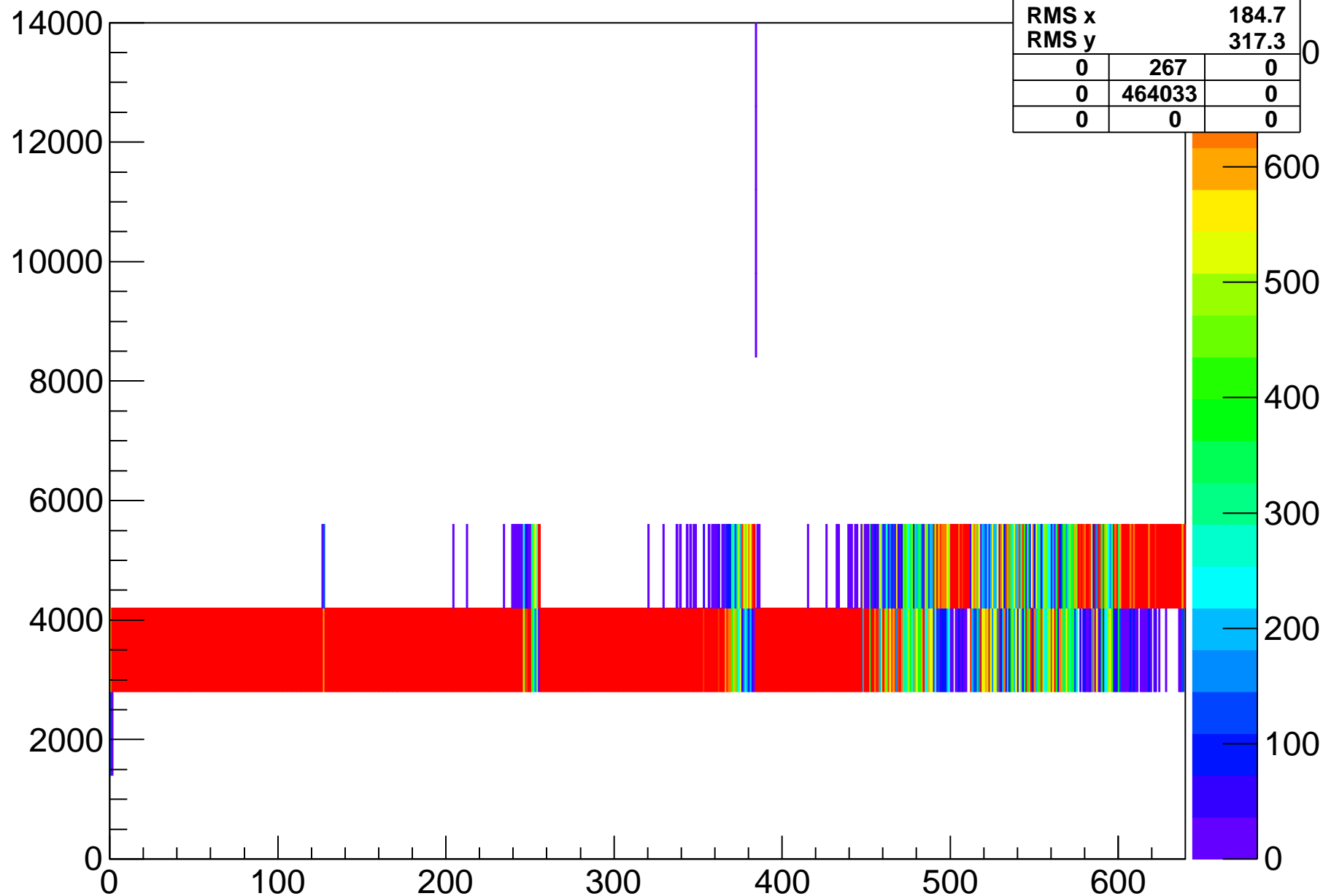
baselinesamples-fpga-8-hyb-2-sample-4

Entries	464300	
Mean x	319.5	
Mean y	3906	
RMS x	184.7	
RMS y	319	
0	260	0
0	464040	0
0	0	0



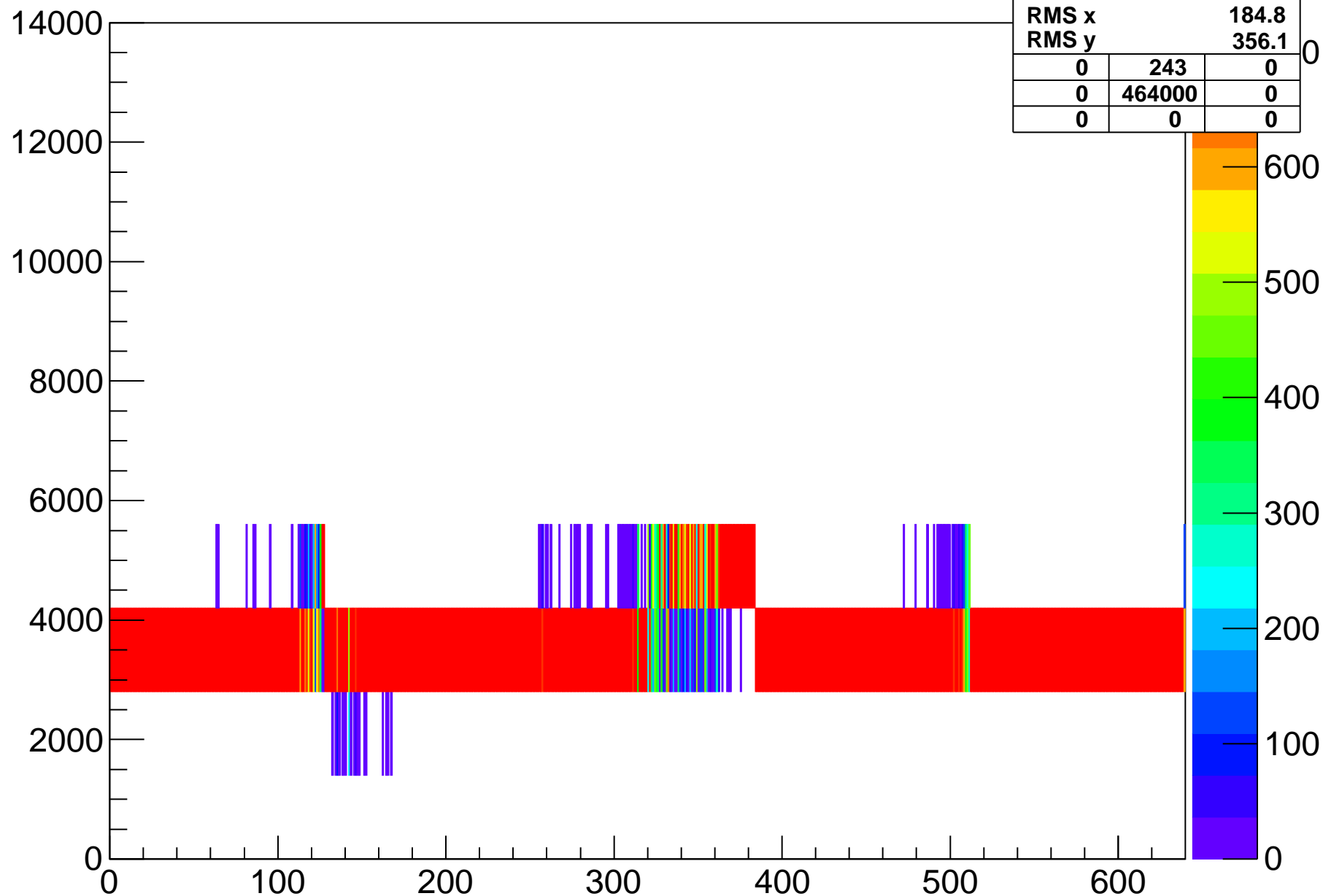
baselinesamples-fpga-8-hyb-2-sample-5

Entries	464300	
Mean x	319.5	
Mean y	3910	
RMS x	184.7	
RMS y	317.3	
0	267	0
0	464033	0
0	0	0



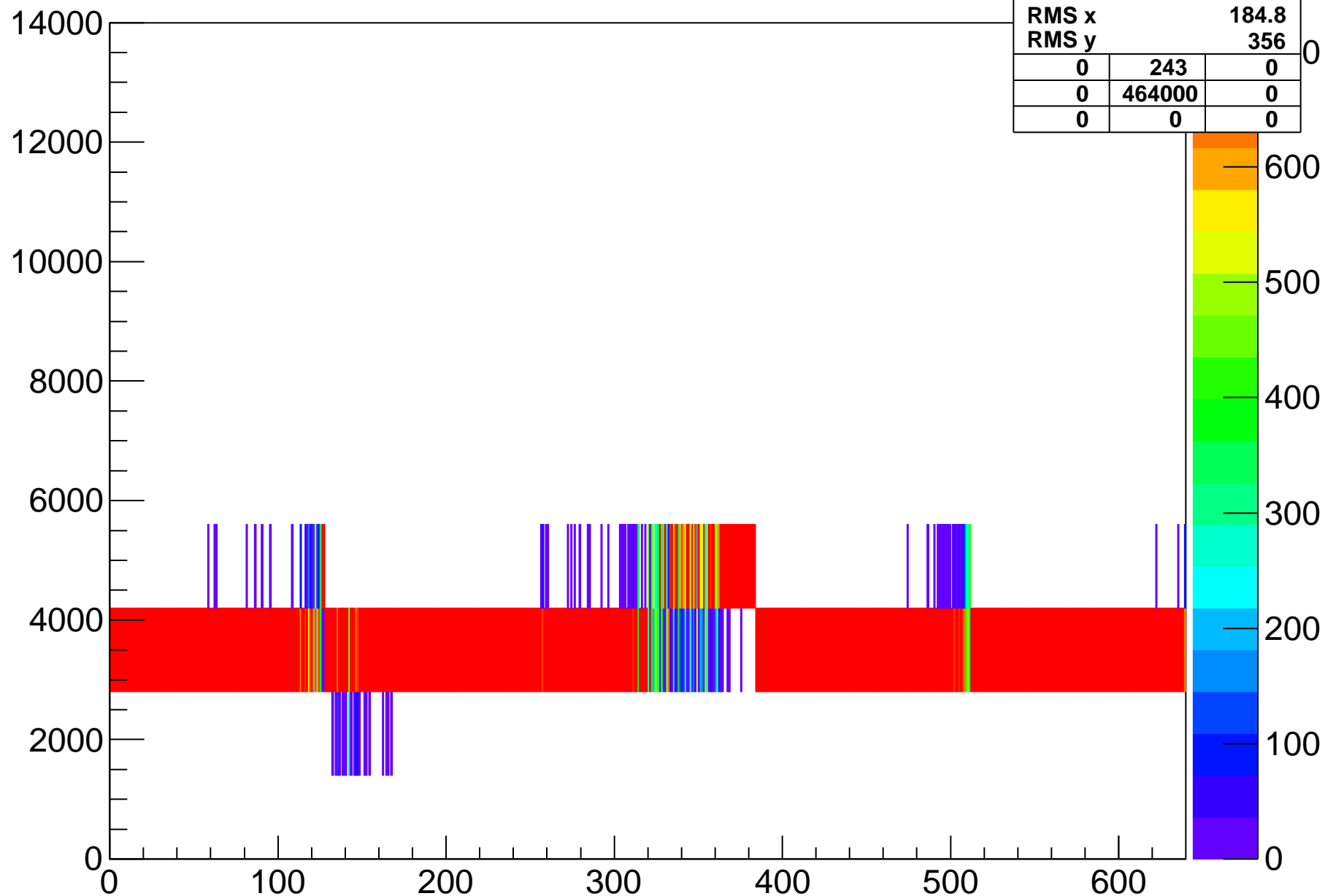
baselinesamples-fpga-8-hyb-3-sample-0

Entries	464243	
Mean x	319.5	
Mean y	3763	
RMS x	184.8	
RMS y	356.1	
0	243	0
0	464000	0
0	0	0



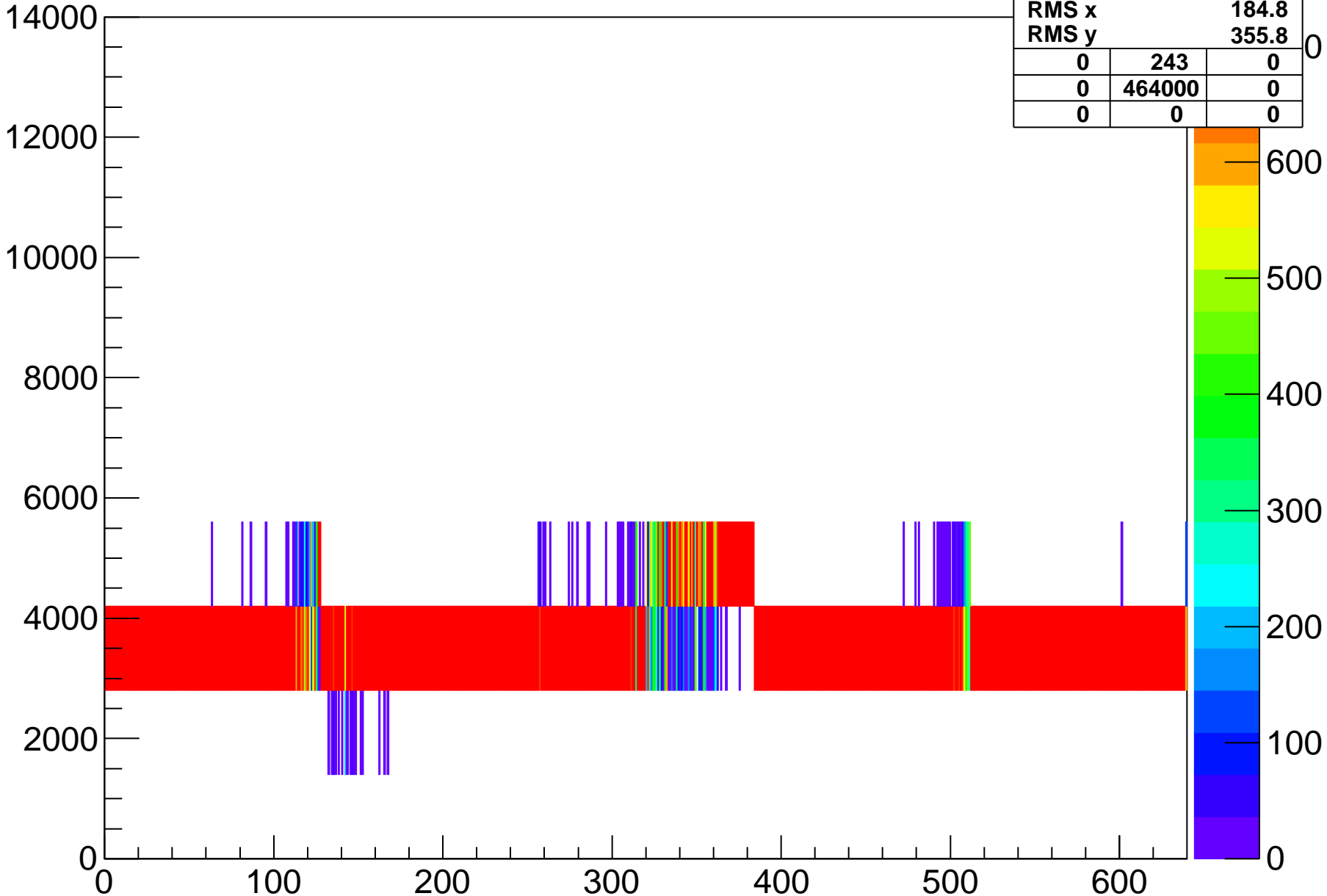
baselinesamples-fpga-8-hyb-3-sample-1

Entries	464243	
Mean x	319.5	
Mean y	3757	
RMS x	184.8	
RMS y	356	
0	243	0
0	464000	0
0	0	0



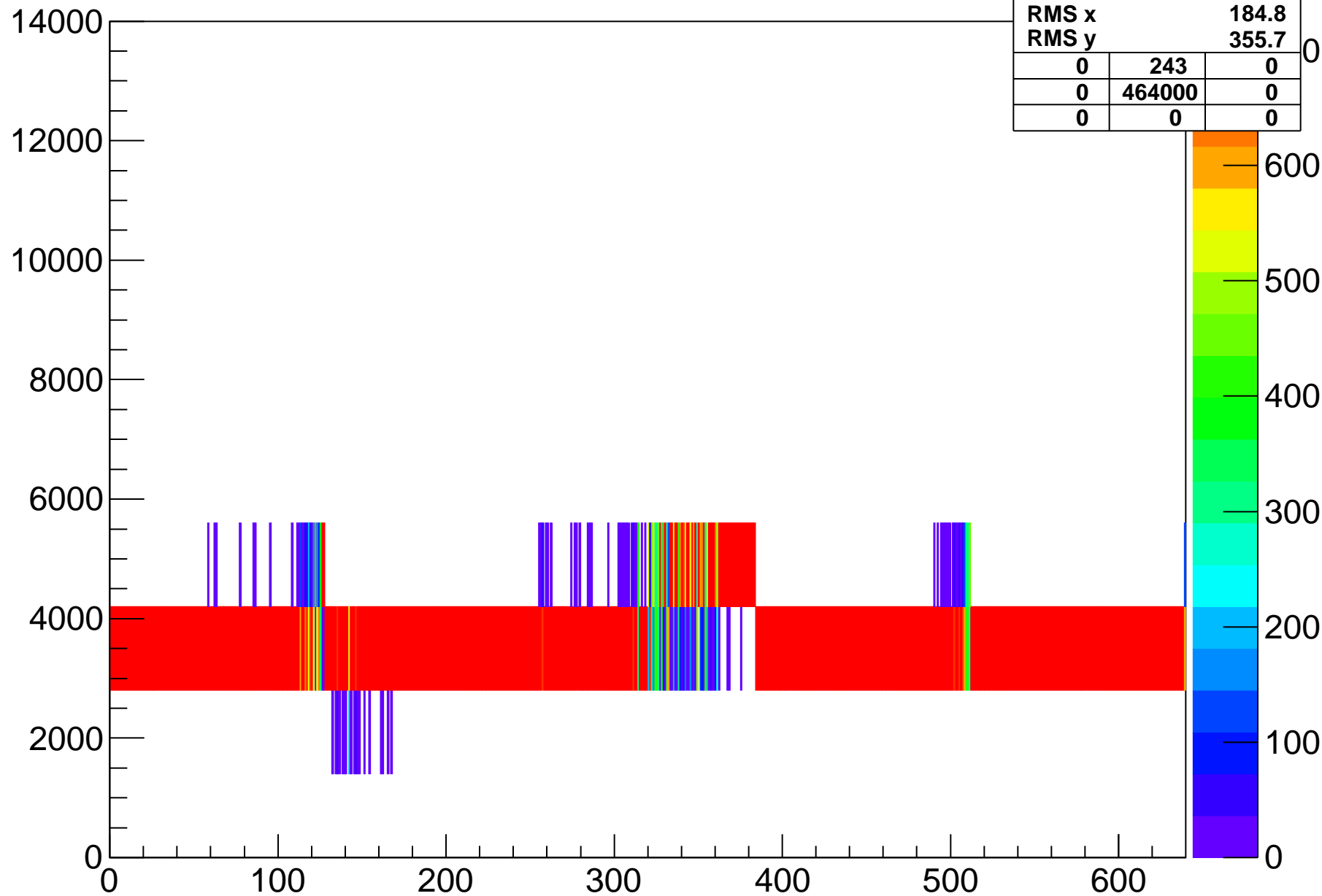
baselinesamples-fpga-8-hyb-3-sample-2

Entries	464243	
Mean x	319.5	
Mean y	3772	
RMS x	184.8	
RMS y	355.8	
0	243	0
0	464000	0
0	0	0



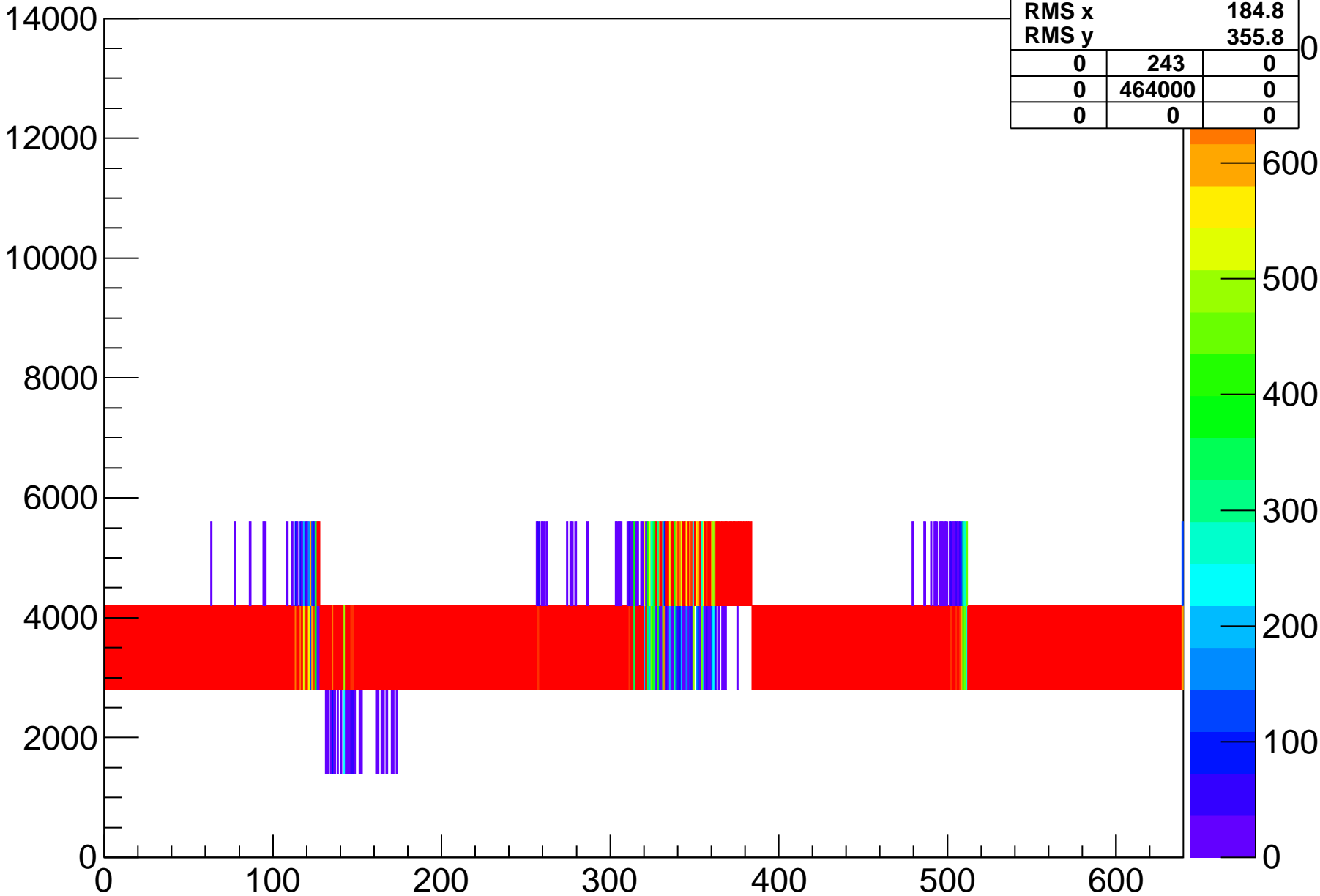
baselinesamples-fpga-8-hyb-3-sample-3

Entries	464243	
Mean x	319.5	
Mean y	3770	
RMS x	184.8	
RMS y	355.7	
0	243	0
0	464000	0
0	0	0



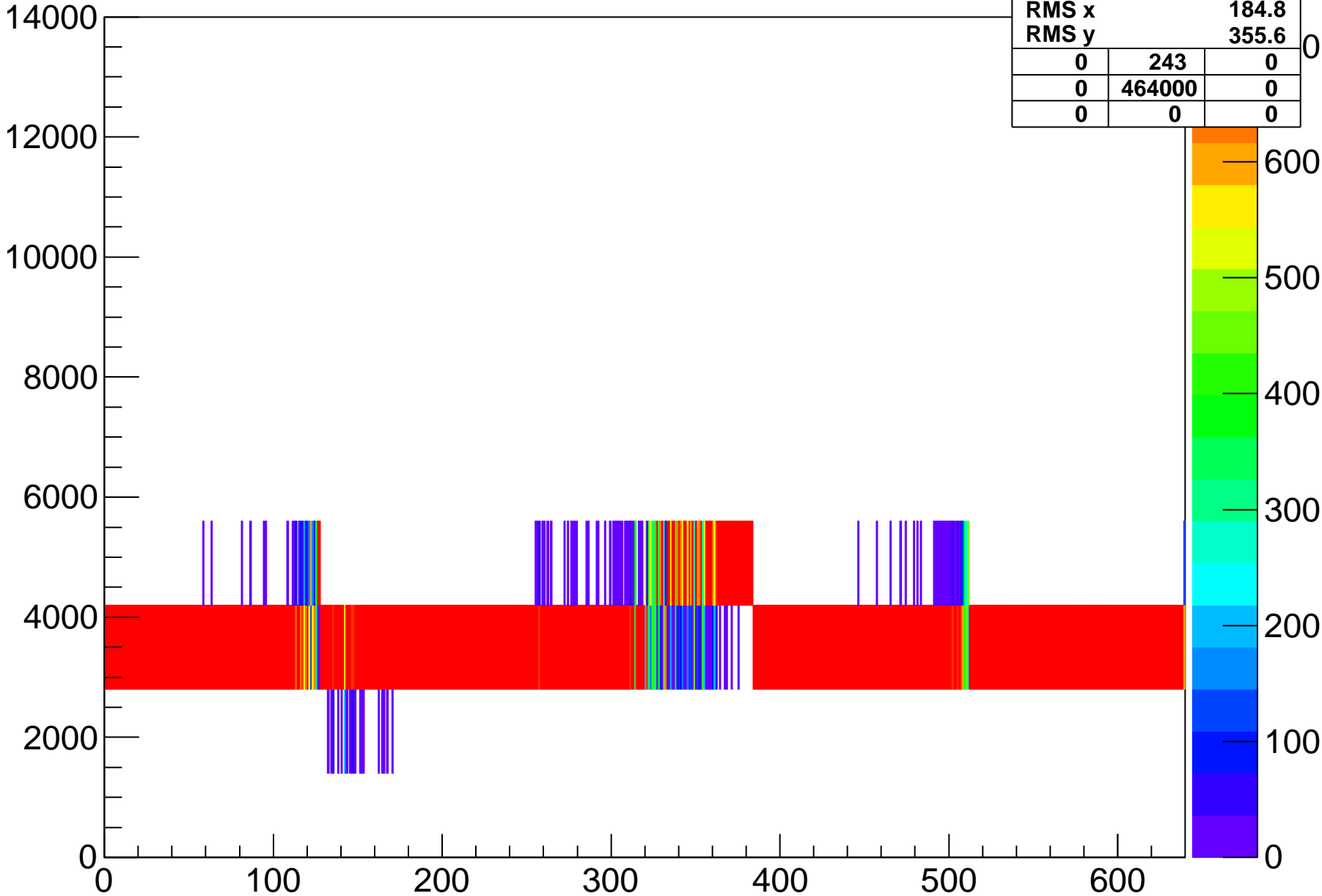
baselinesamples-fpga-8-hyb-3-sample-4
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Entries	464243	
Mean x	319.5	
Mean y	3764	
RMS x	184.8	
RMS y	355.8	
0	243	0
0	464000	0
0	0	0



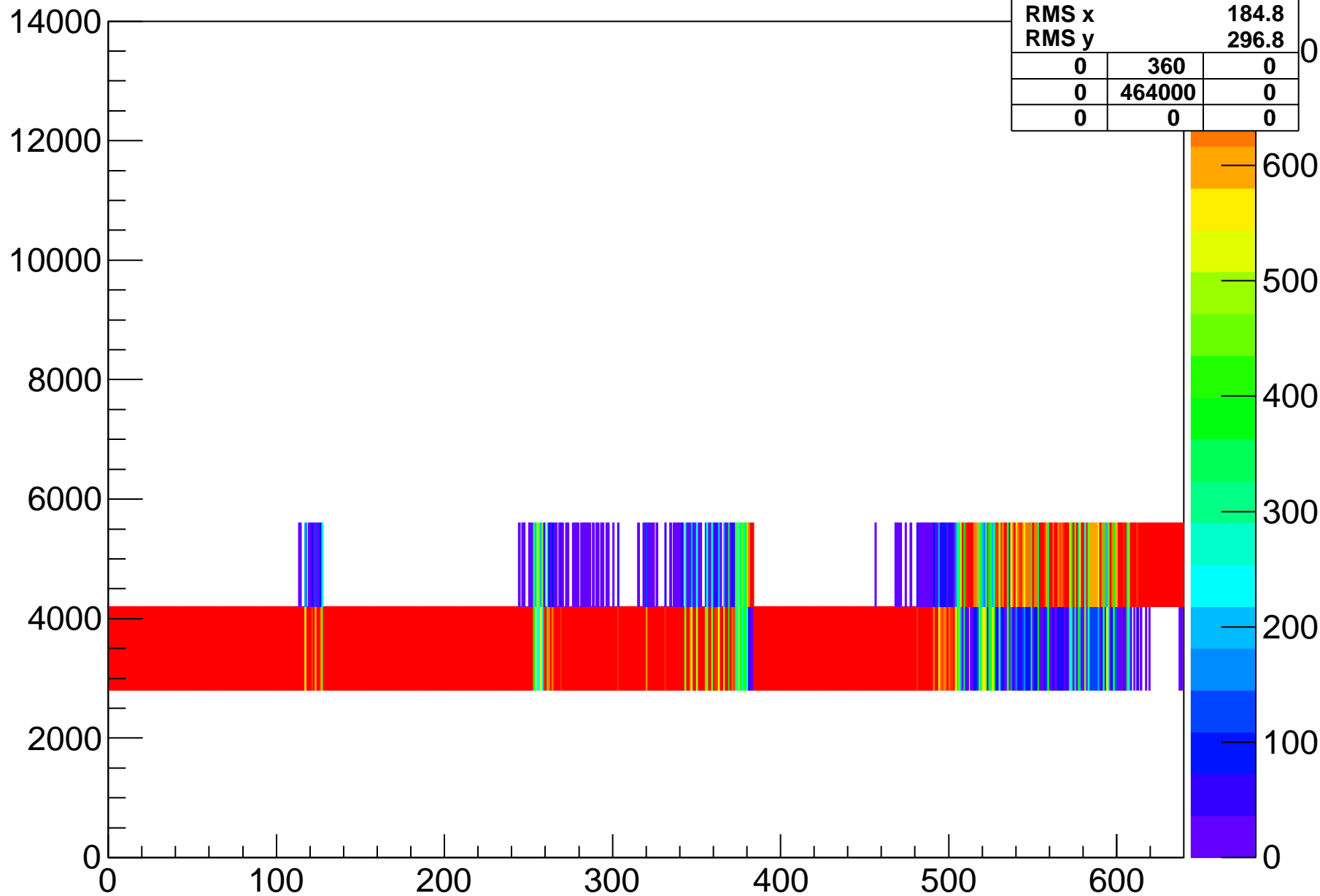
baselinesamples-fpga-8-hyb-3-sample-5

Entries	464243	
Mean x	319.5	
Mean y	3768	
RMS x	184.8	
RMS y	355.6	
0	243	0
0	464000	0
0	0	0



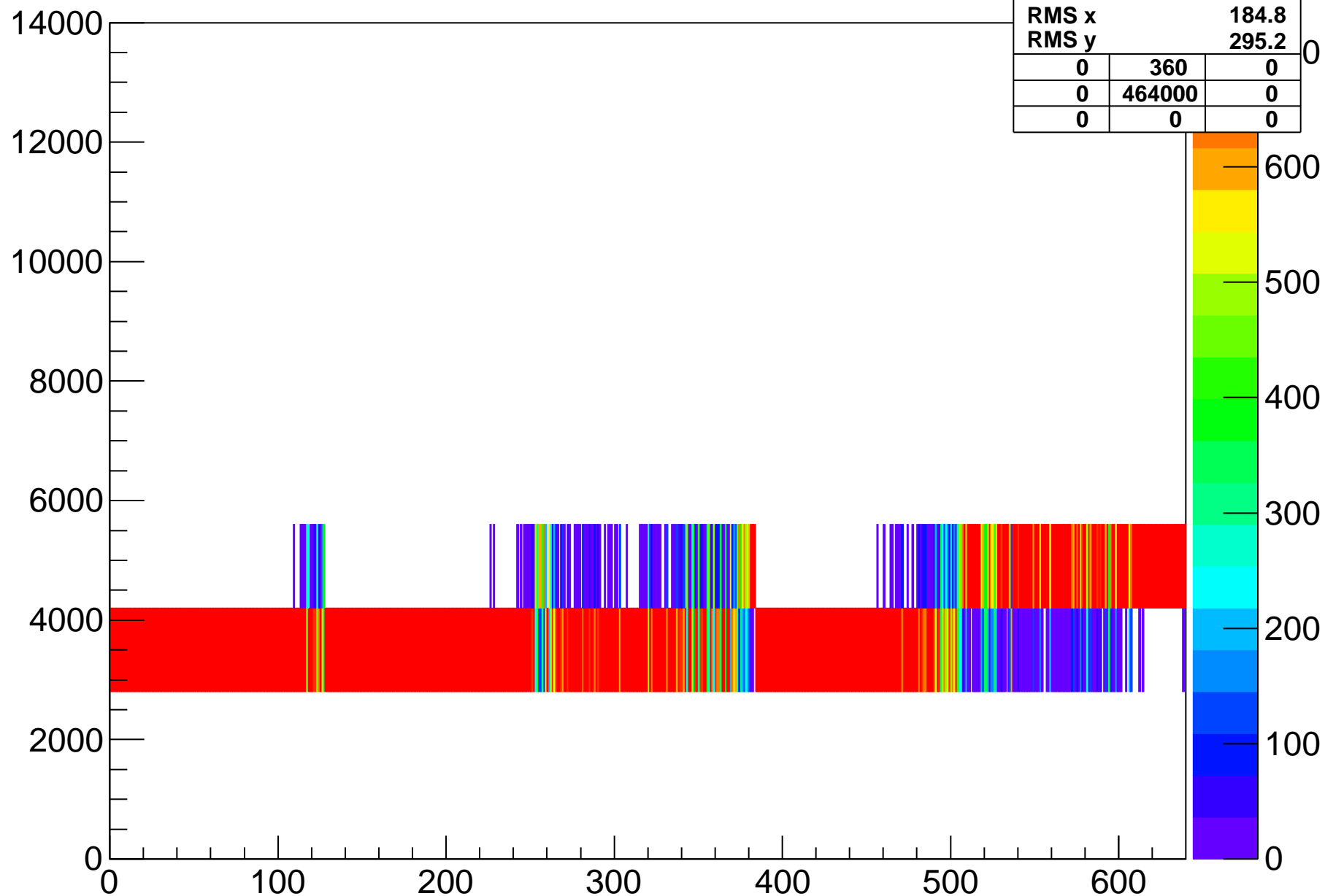
baselinesamples-fpga-9-hyb-0-sample-0

Entries	464360	
Mean x	319.5	
Mean y	3916	
RMS x	184.8	
RMS y	296.8	
0	360	0
0	464000	0
0	0	0



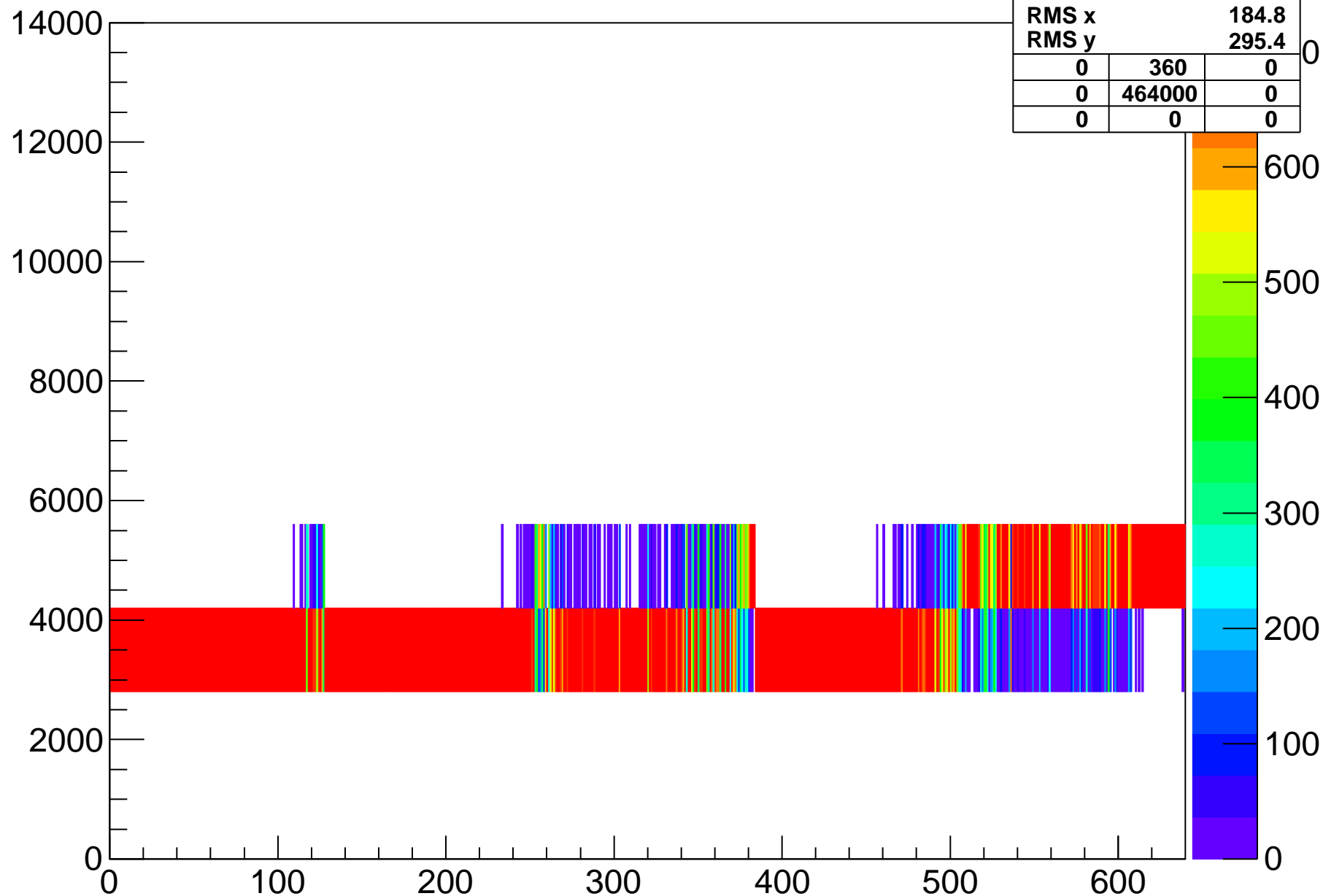
baselinesamples-fpga-9-hyb-0-sample-1

Entries	464360	
Mean x	319.5	
Mean y	3960	
RMS x	184.8	
RMS y	295.2	
0	360	0
0	464000	0
0	0	0



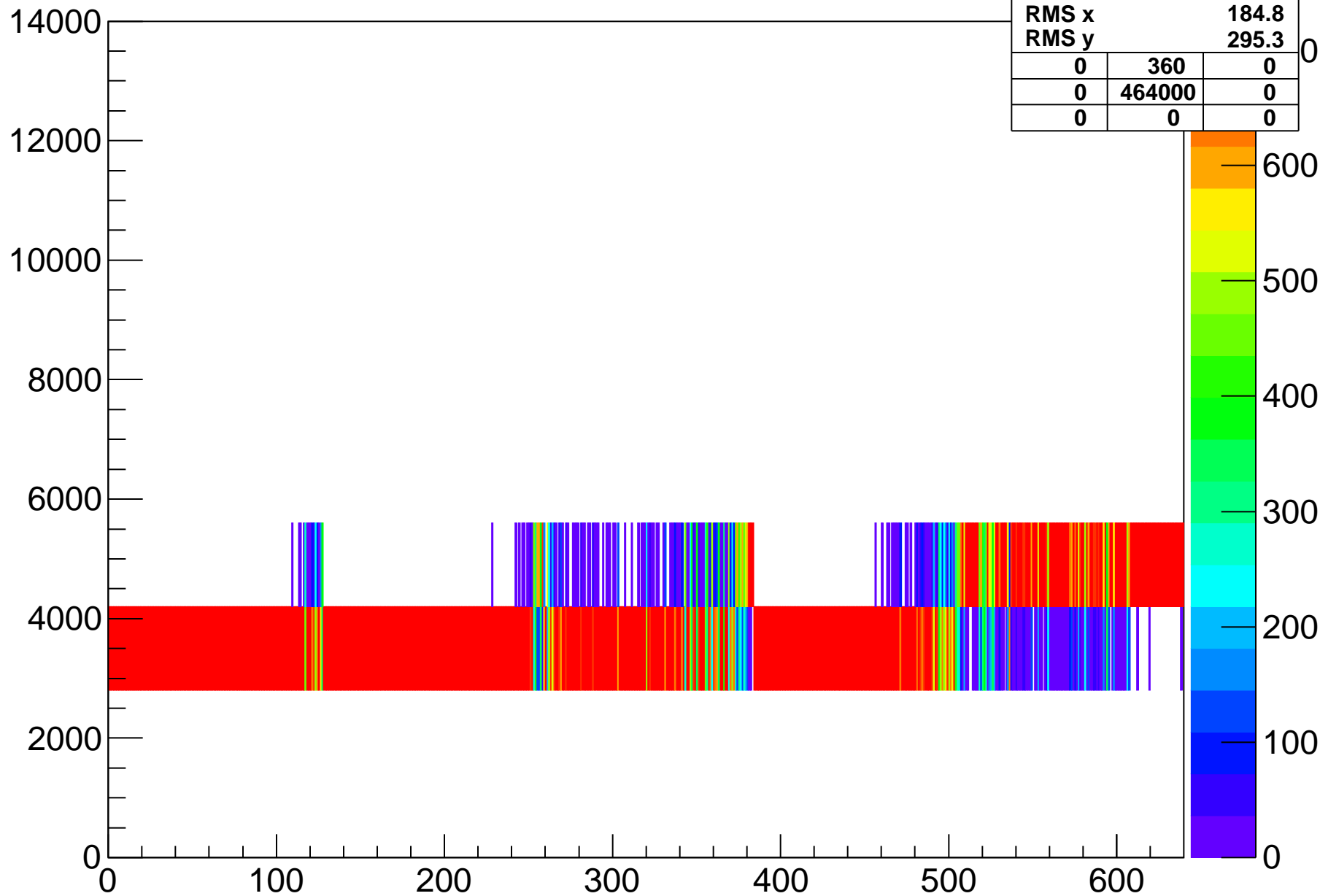
baselinesamples-fpga-9-hyb-0-sample-2

Entries	464360	
Mean x	319.5	
Mean y	3954	
RMS x	184.8	
RMS y	295.4	
0	360	0
0	464000	0
0	0	0



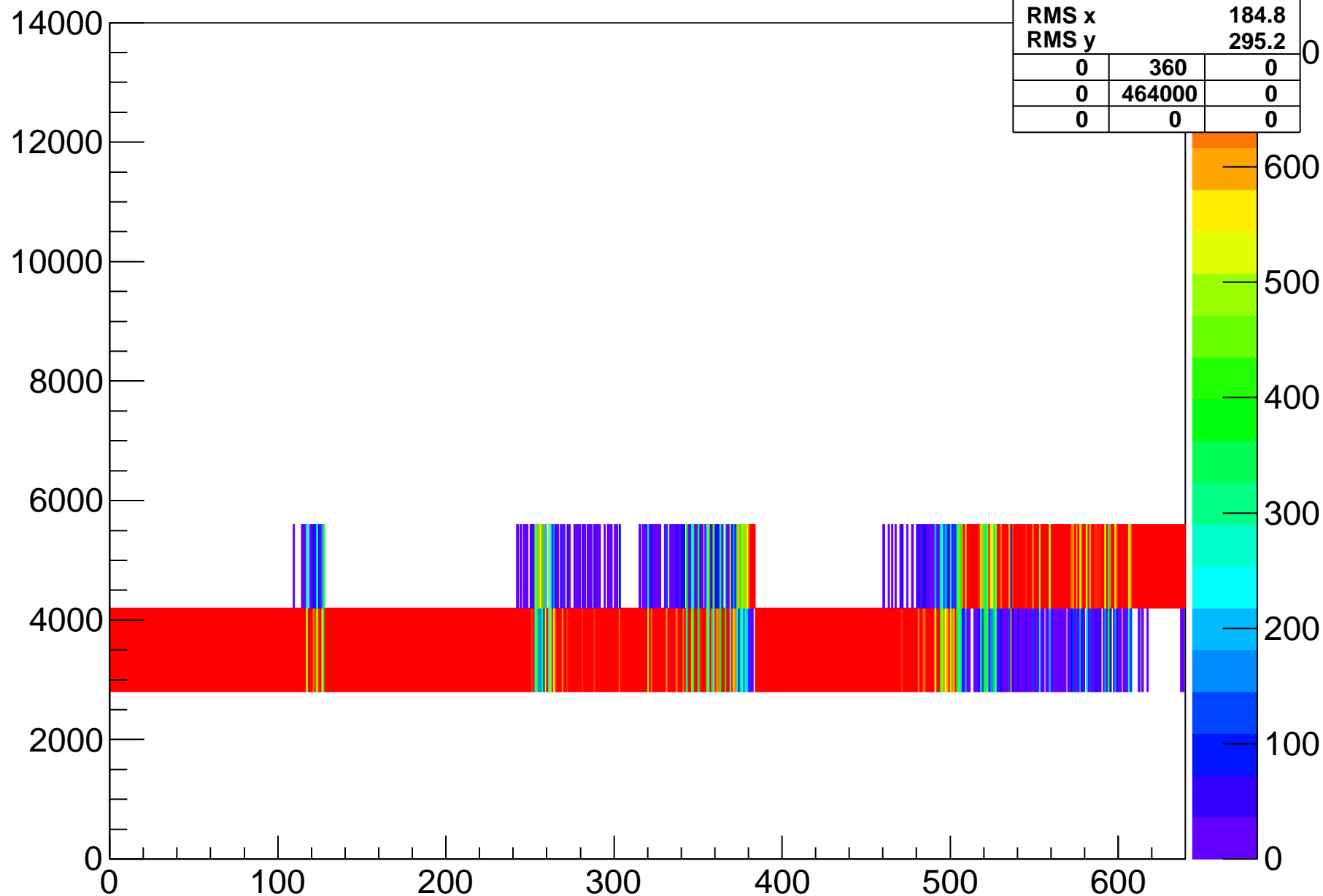
baselinesamples-fpga-9-hyb-0-sample-3

Entries	464360	
Mean x	319.5	
Mean y	3956	
RMS x	184.8	
RMS y	295.3	
0	360	0
0	464000	0
0	0	0



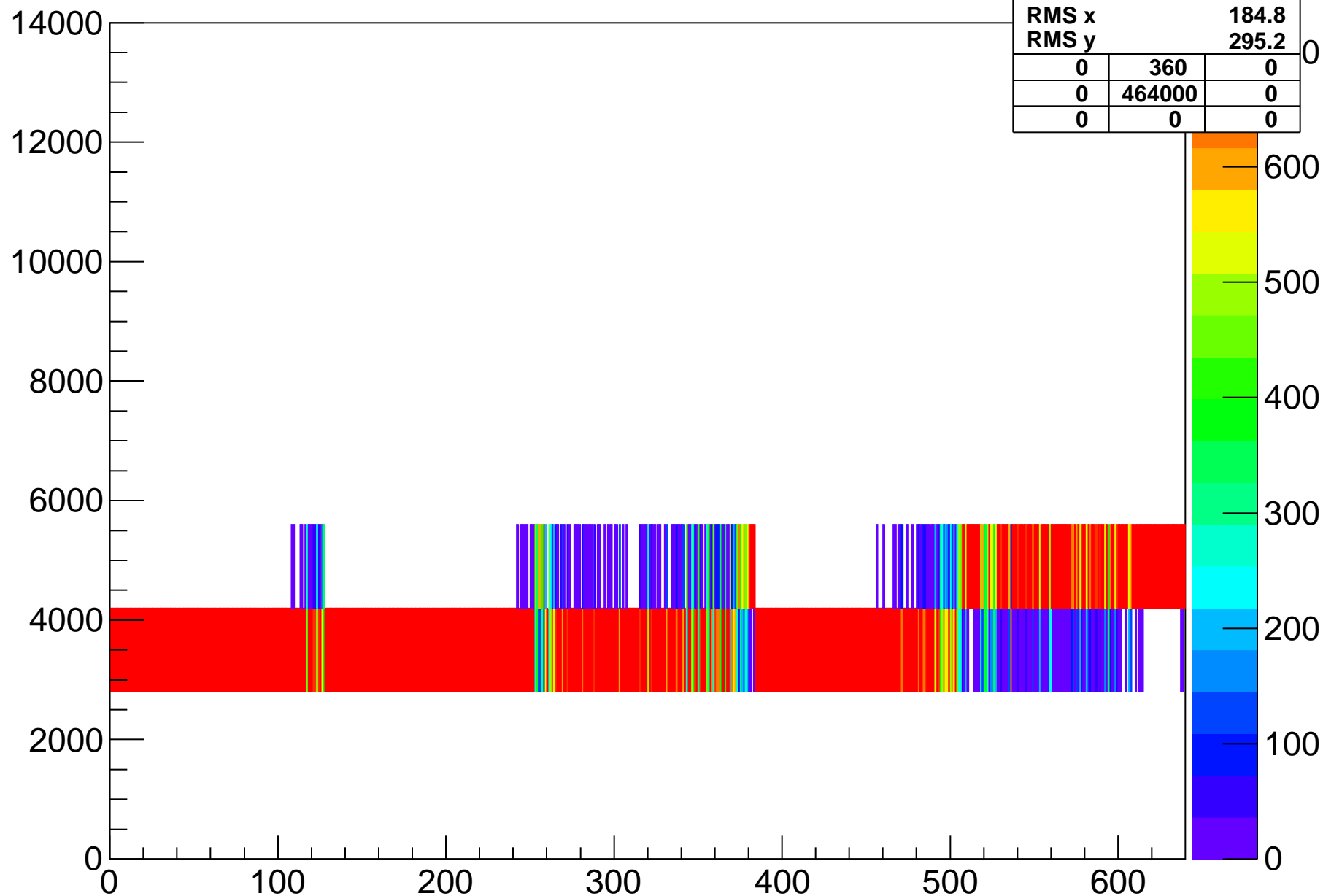
baselinesamples-fpga-9-hyb-0-sample-4

Entries	464360	
Mean x	319.5	
Mean y	3954	
RMS x	184.8	
RMS y	295.2	
0	360	0
0	464000	0
0	0	0



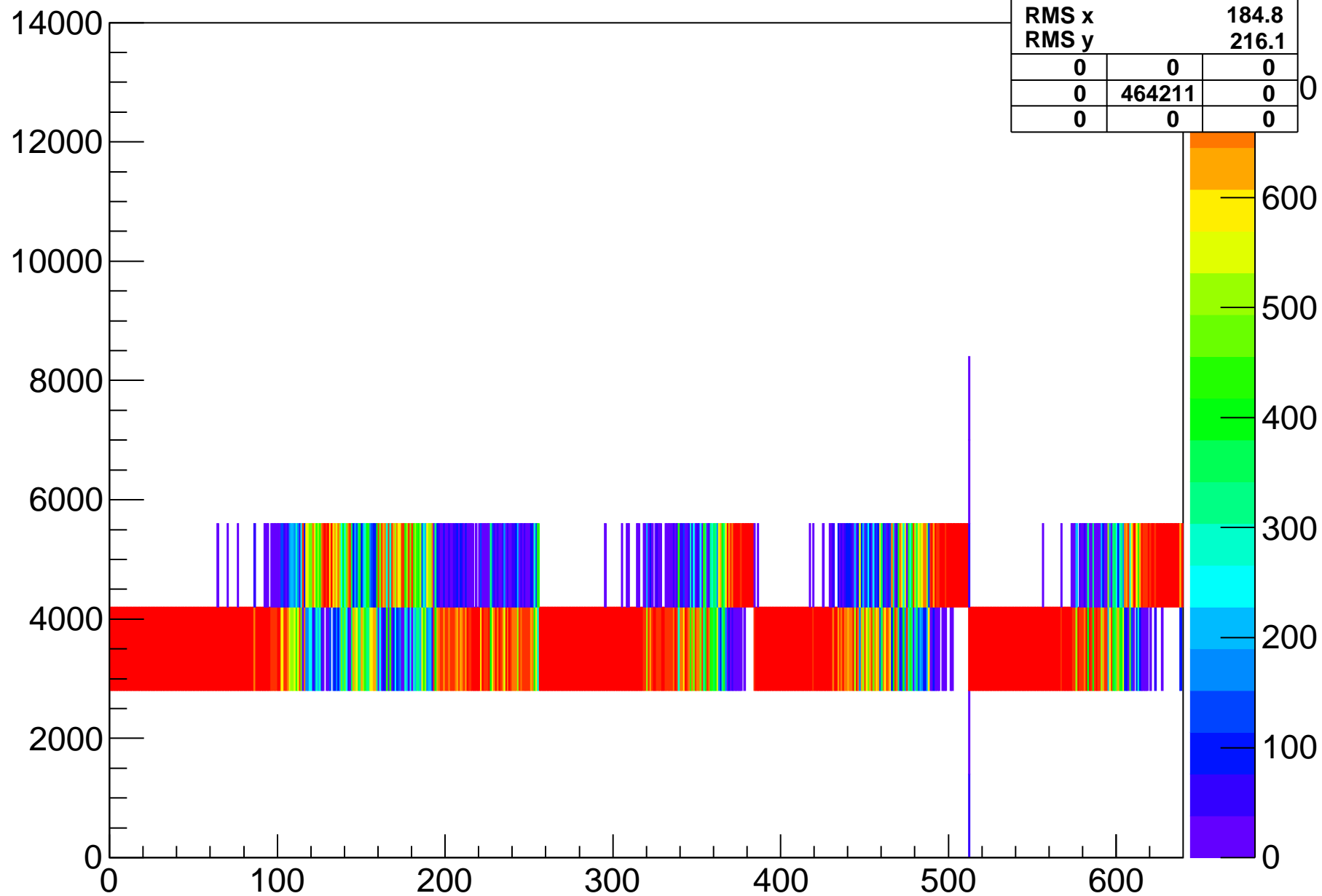
baselinesamples-fpga-9-hyb-0-sample-5

Entries	464360	
Mean x	319.5	
Mean y	3957	
RMS x	184.8	
RMS y	295.2	
0	360	0
0	464000	0
0	0	0



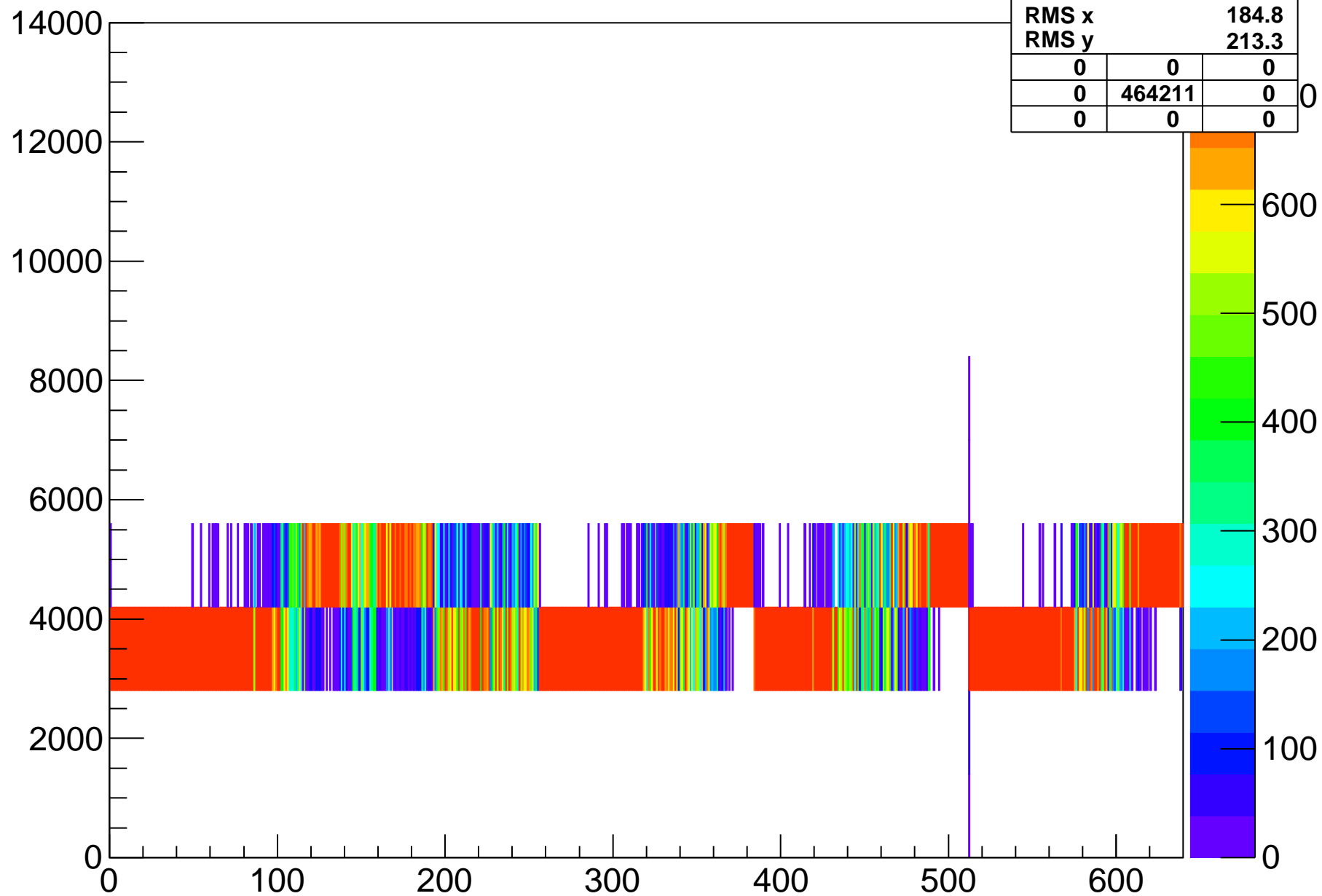
baselinesamples-fpga-9-hyb-1-sample-0

Entries	464211	
Mean x	319.6	
Mean y	4066	
RMS x	184.8	
RMS y	216.1	
0	0	0
0	464211	0
0	0	0



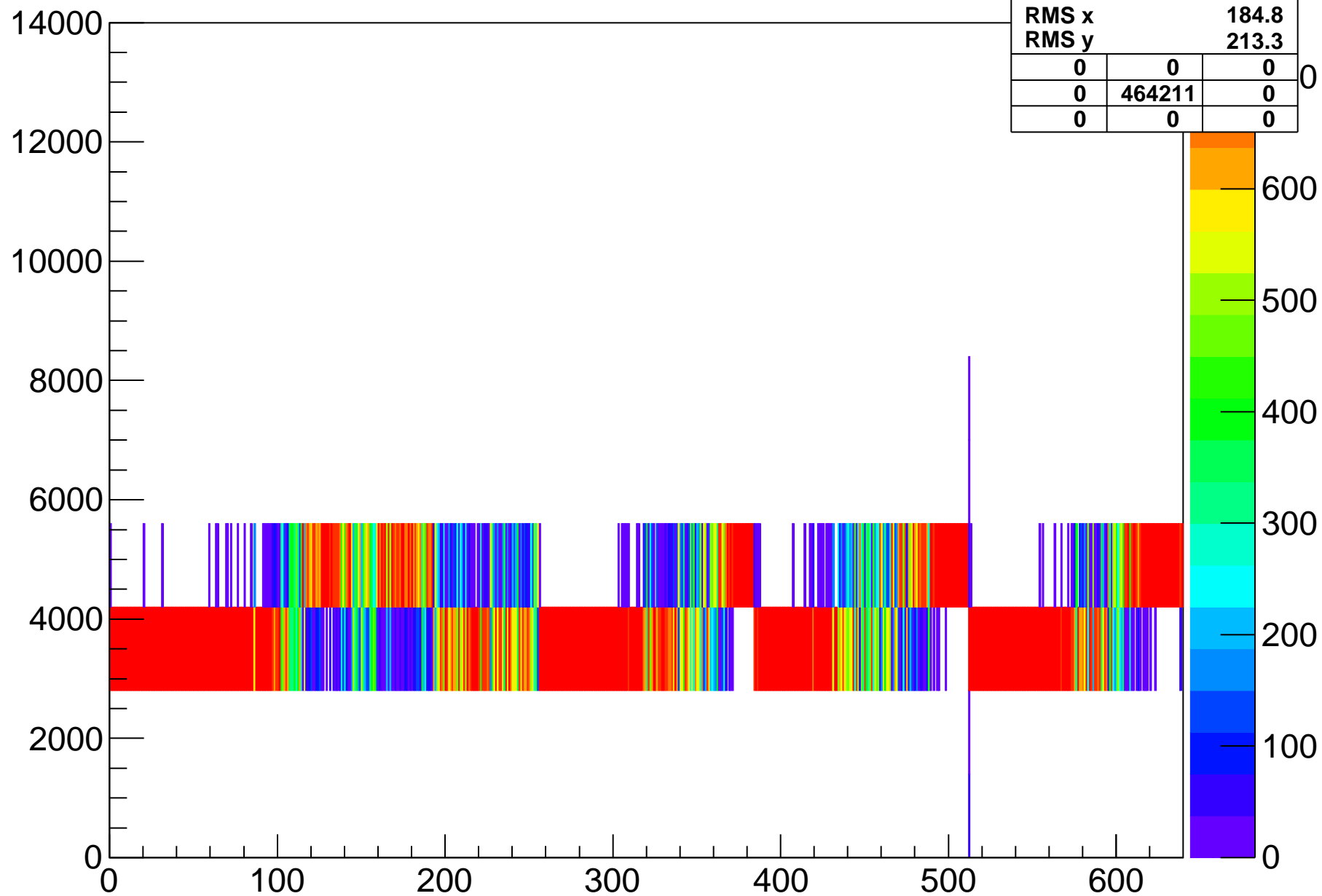
baselinesamples-fpga-9-hyb-1-sample-1

Entries	464211	
Mean x	319.6	
Mean y	4122	
RMS x	184.8	
RMS y	213.3	
0	0	0
0	464211	0
0	0	0



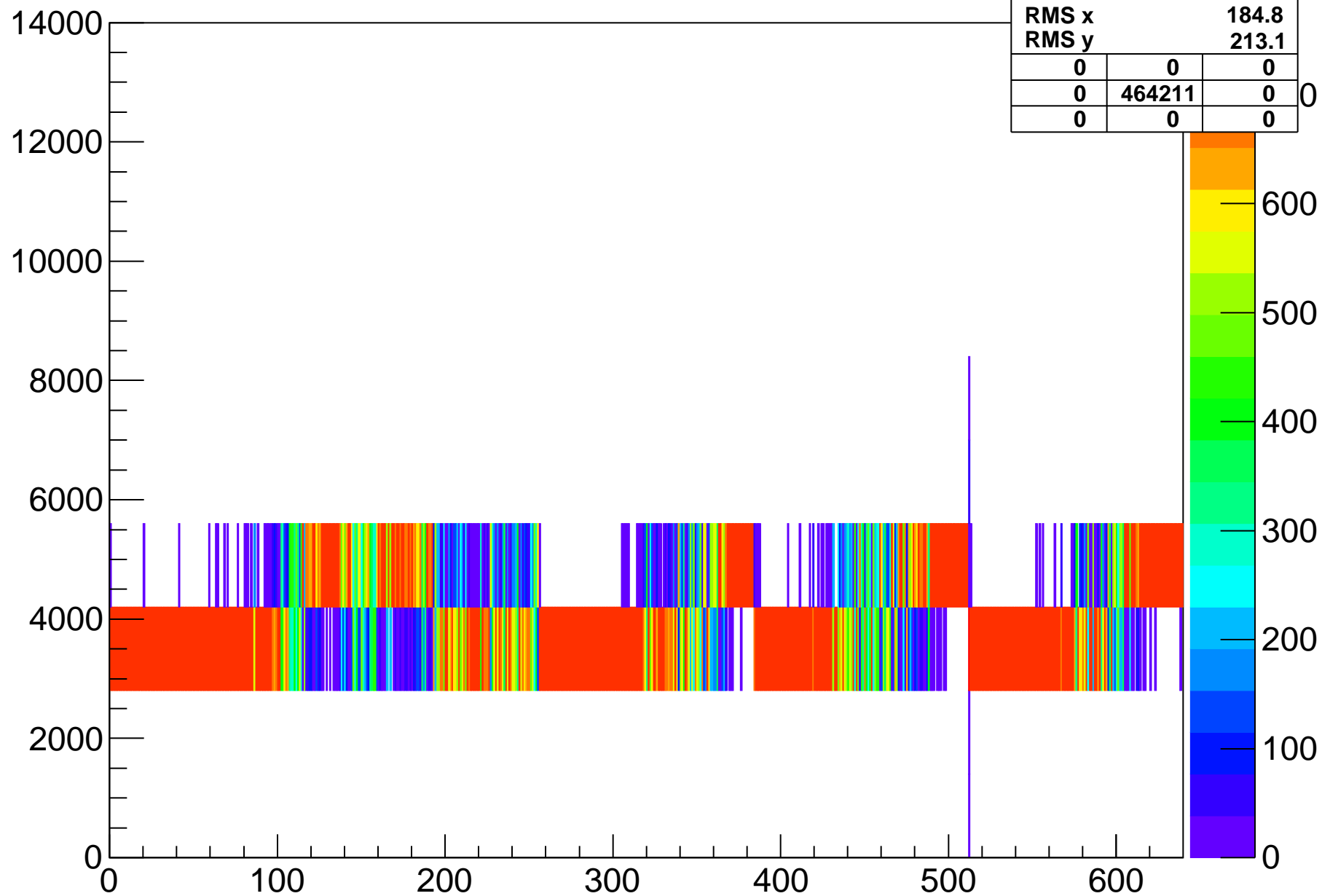
baselinesamples-fpga-9-hyb-1-sample-2

Entries	464211	
Mean x	319.6	
Mean y	4112	
RMS x	184.8	
RMS y	213.3	
0	0	0
0	464211	0
0	0	0



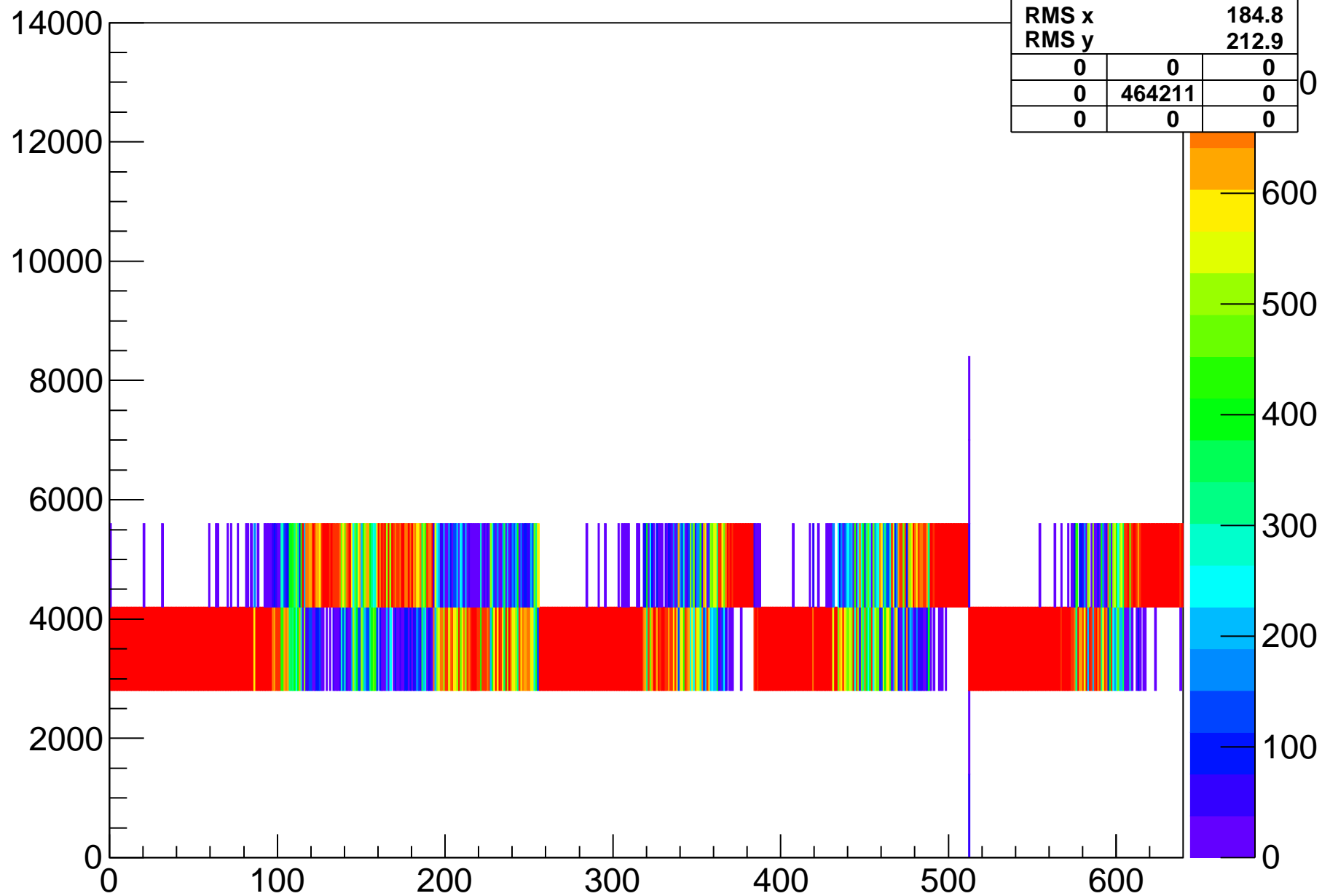
baselinesamples-fpga-9-hyb-1-sample-3

Entries	464211	
Mean x	319.6	
Mean y	4116	
RMS x	184.8	
RMS y	213.1	
0	0	0
0	464211	0
0	0	0



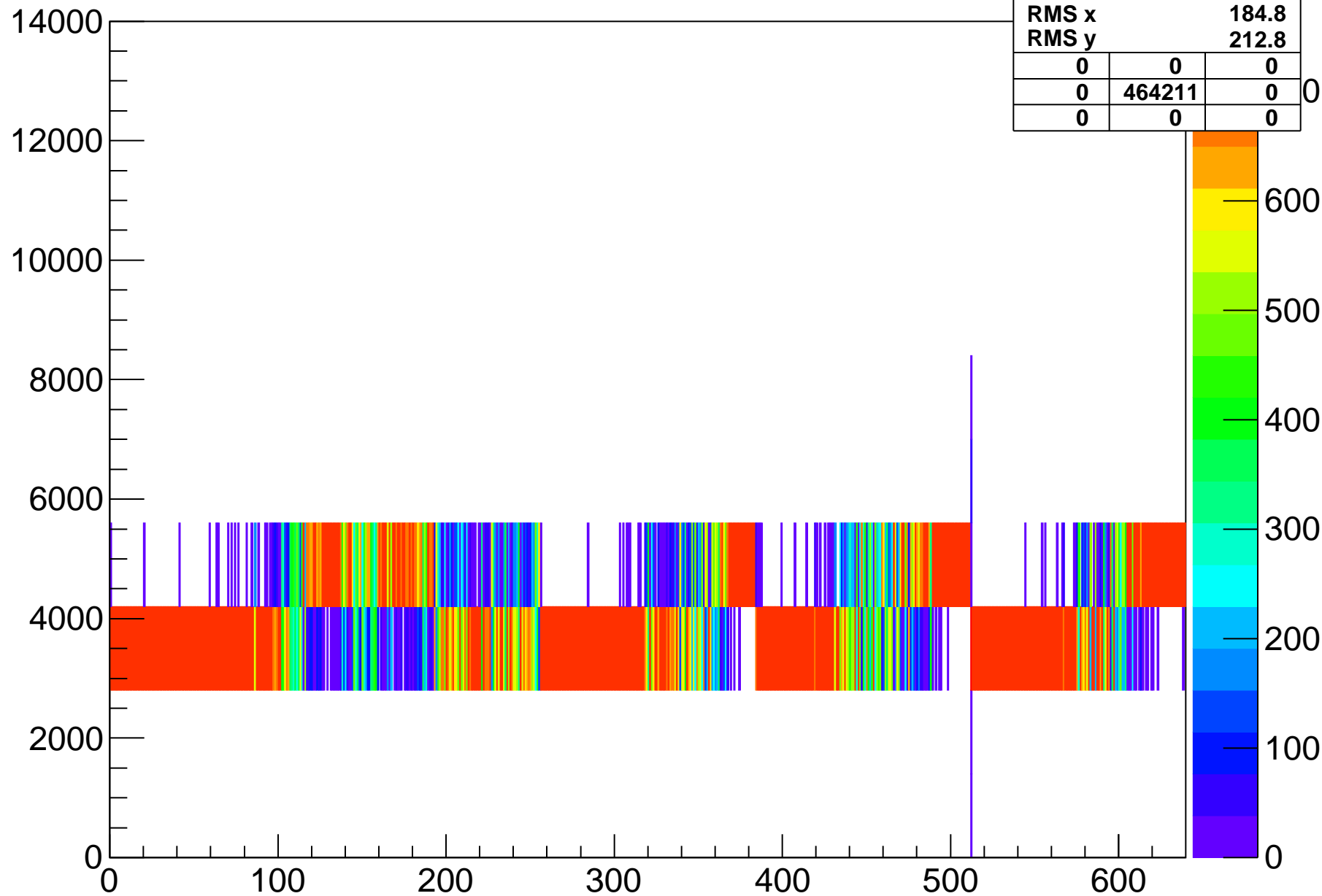
baselinesamples-fpga-9-hyb-1-sample-4

Entries	464211	
Mean x	319.6	
Mean y	4112	
RMS x	184.8	
RMS y	212.9	
0	0	0
0	464211	0
0	0	0

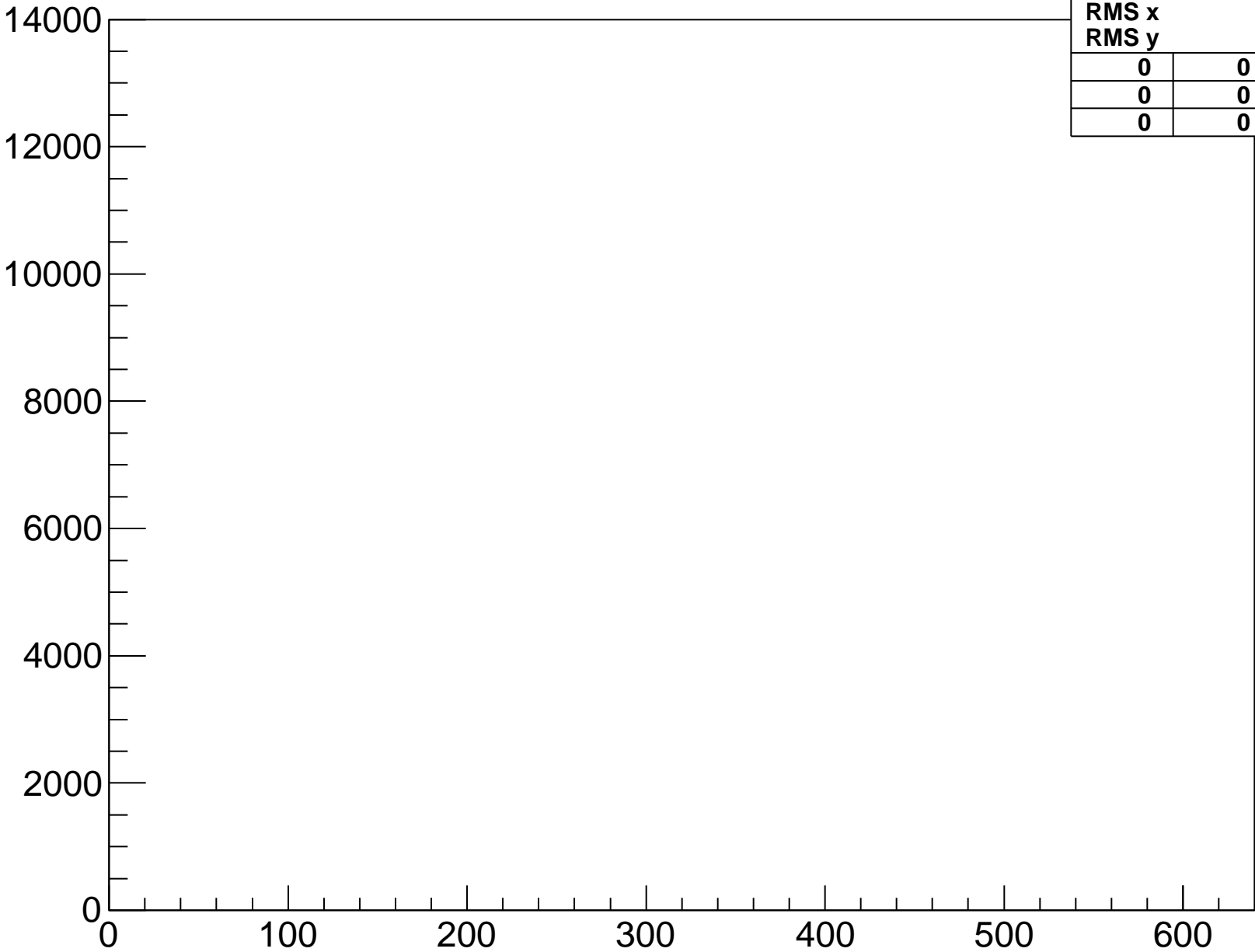


baselinesamples-fpga-9-hyb-1-sample-5

Entries	464211	
Mean x	319.6	
Mean y	4115	
RMS x	184.8	
RMS y	212.8	
0	0	0
0	464211	0
0	0	0



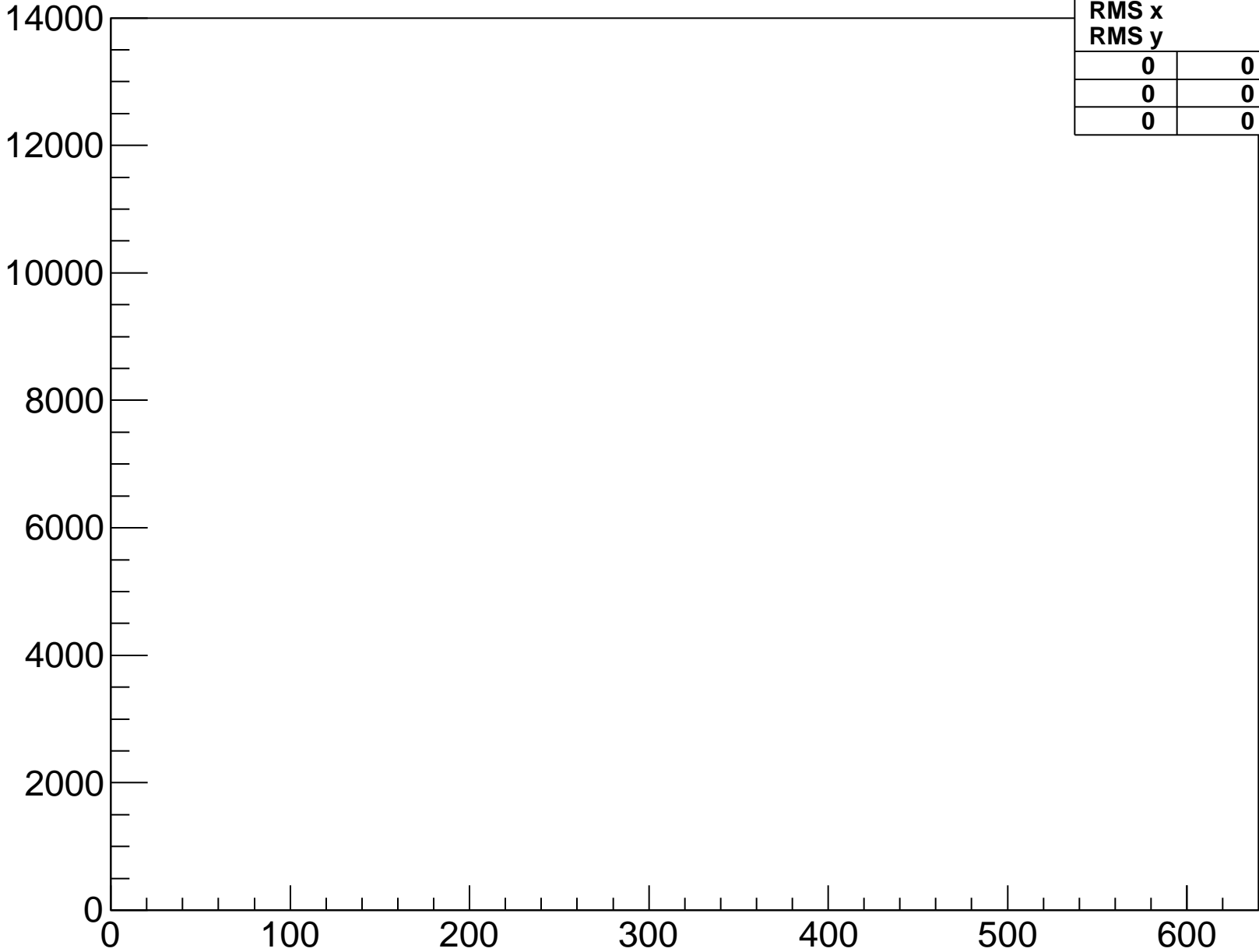
baselinesamples-fpga-9-hyb-2-sample-0



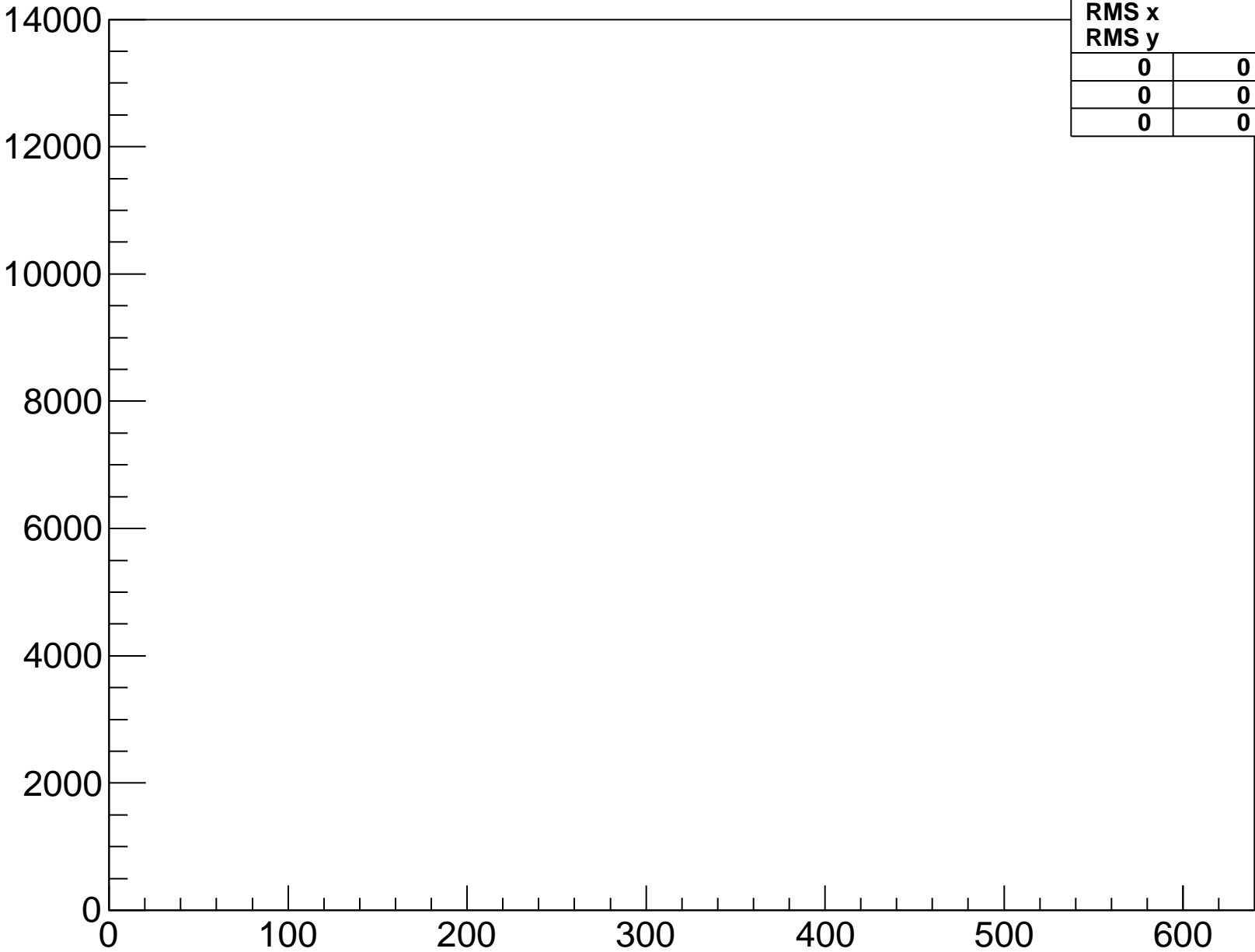
Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0

baselinesamples-fpga-9-hyb-2-sample-1

Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

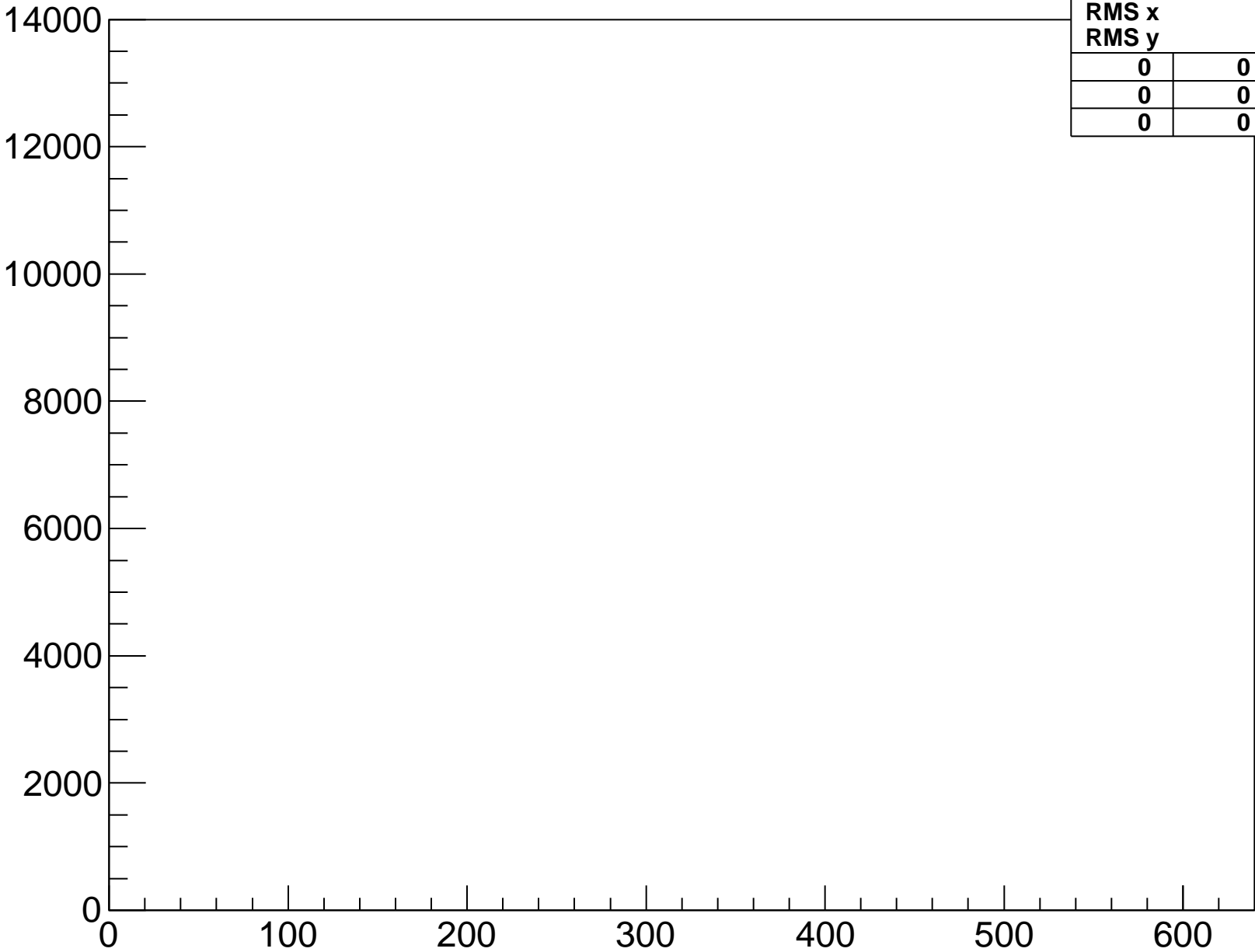


baselinesamples-fpga-9-hyb-2-sample-2



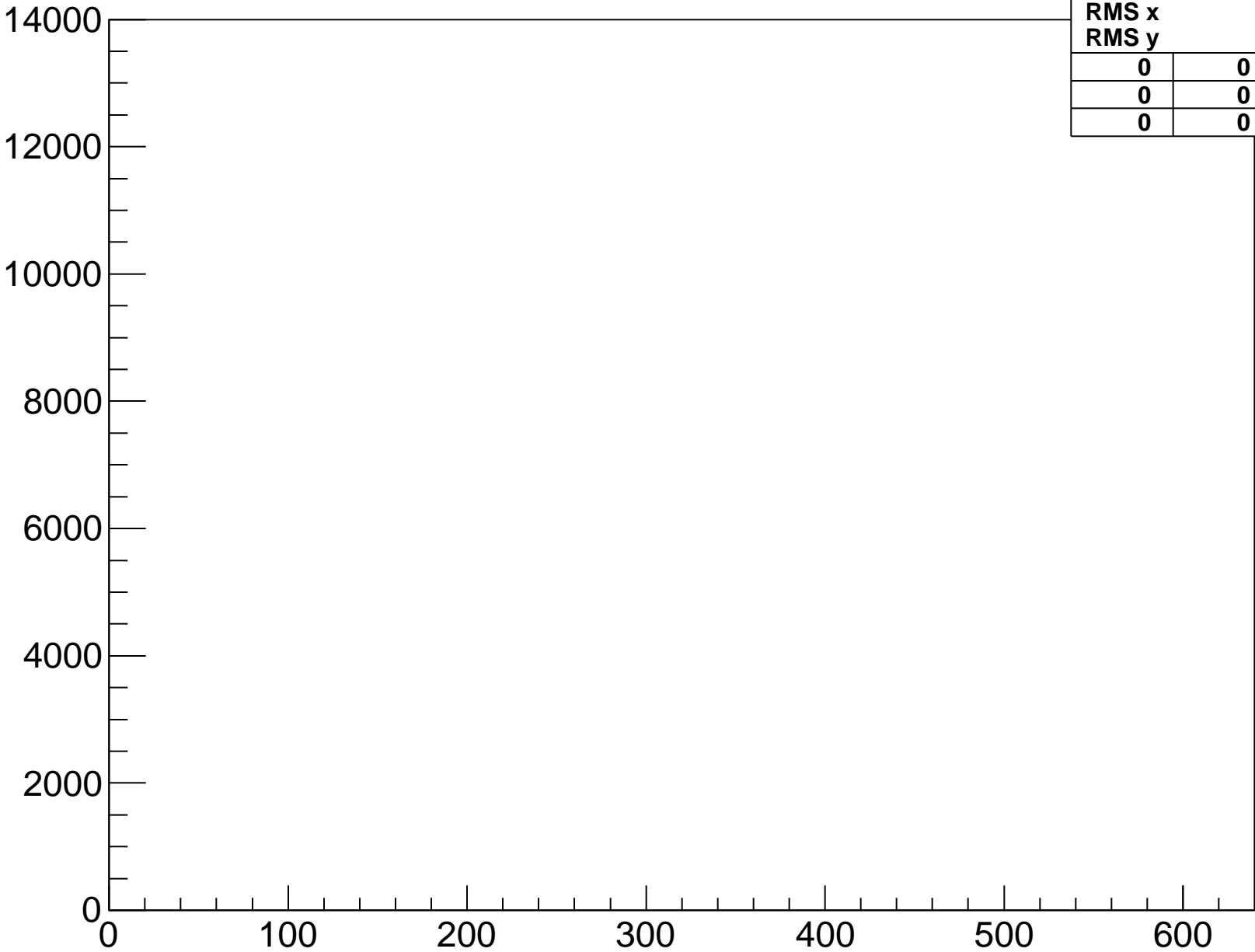
Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

baselinesamples-fpga-9-hyb-2-sample-3



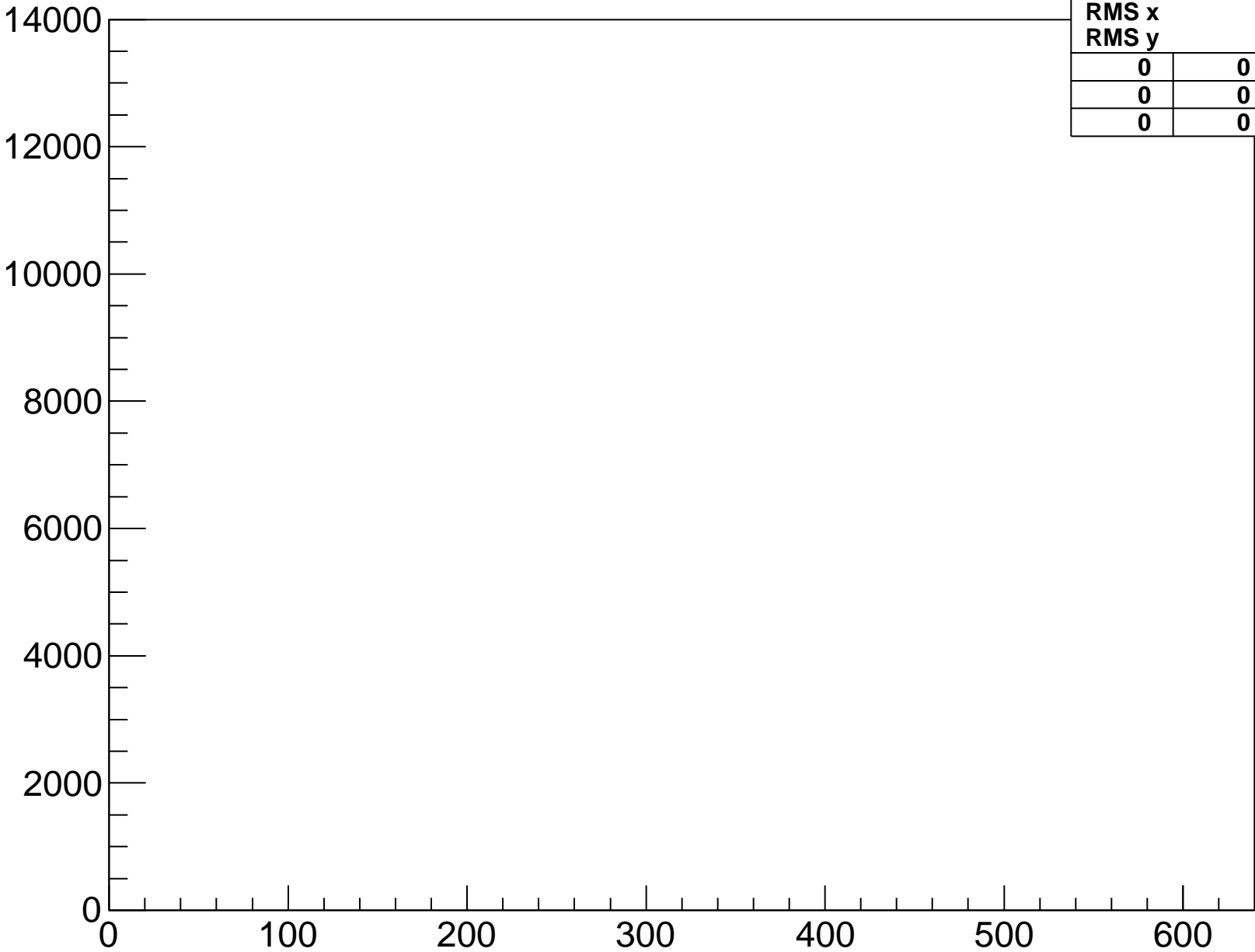
Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

baselinesamples-fpga-9-hyb-2-sample-4



Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

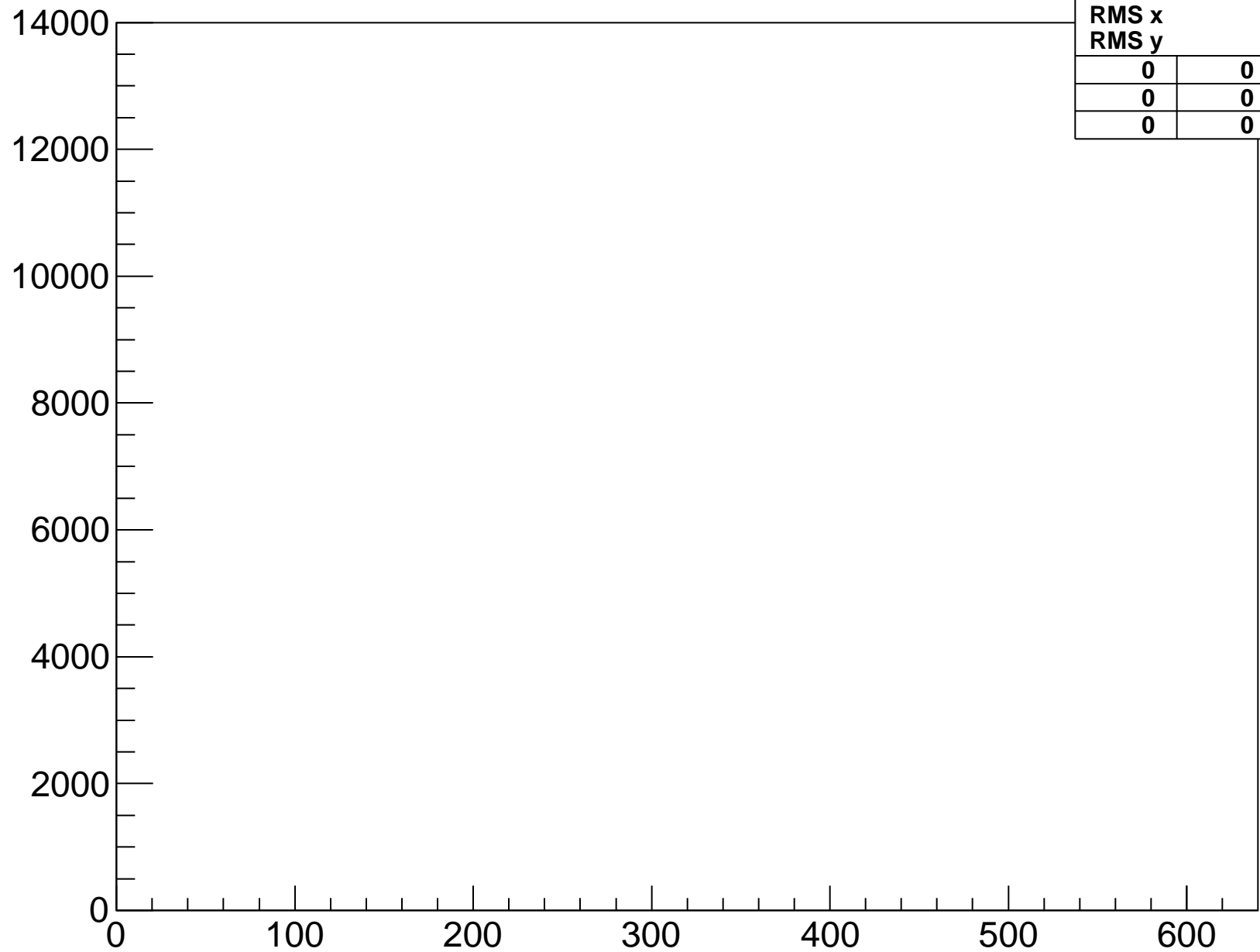
baselinesamples-fpga-9-hyb-2-sample-5



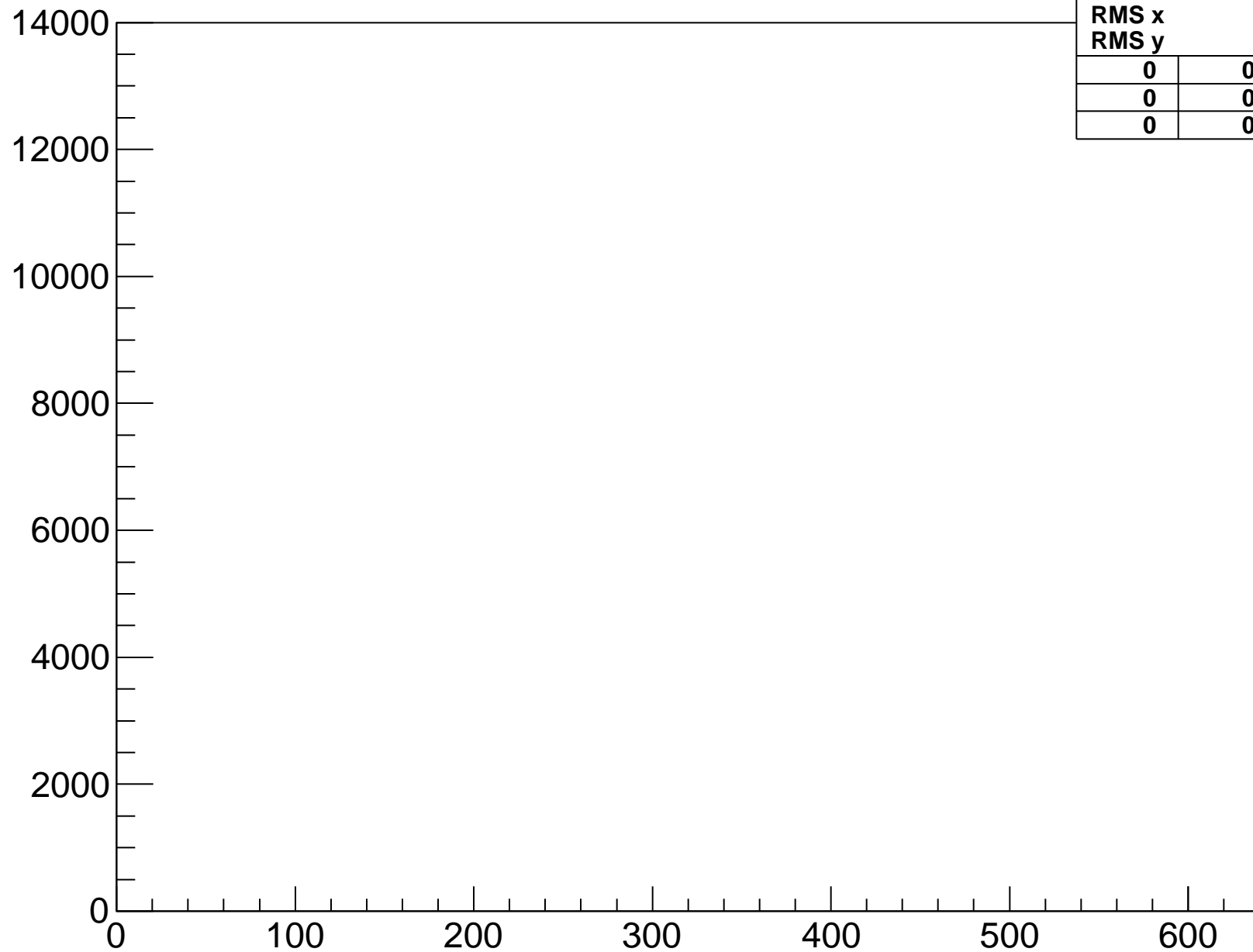
Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

baselinesamples-fpga-9-hyb-3-sample-0

Entries	0		
Mean x	0		
Mean y	0		
RMS x	0		
RMS y	0		
0	0	0	
0	0	0	
0	0	0	

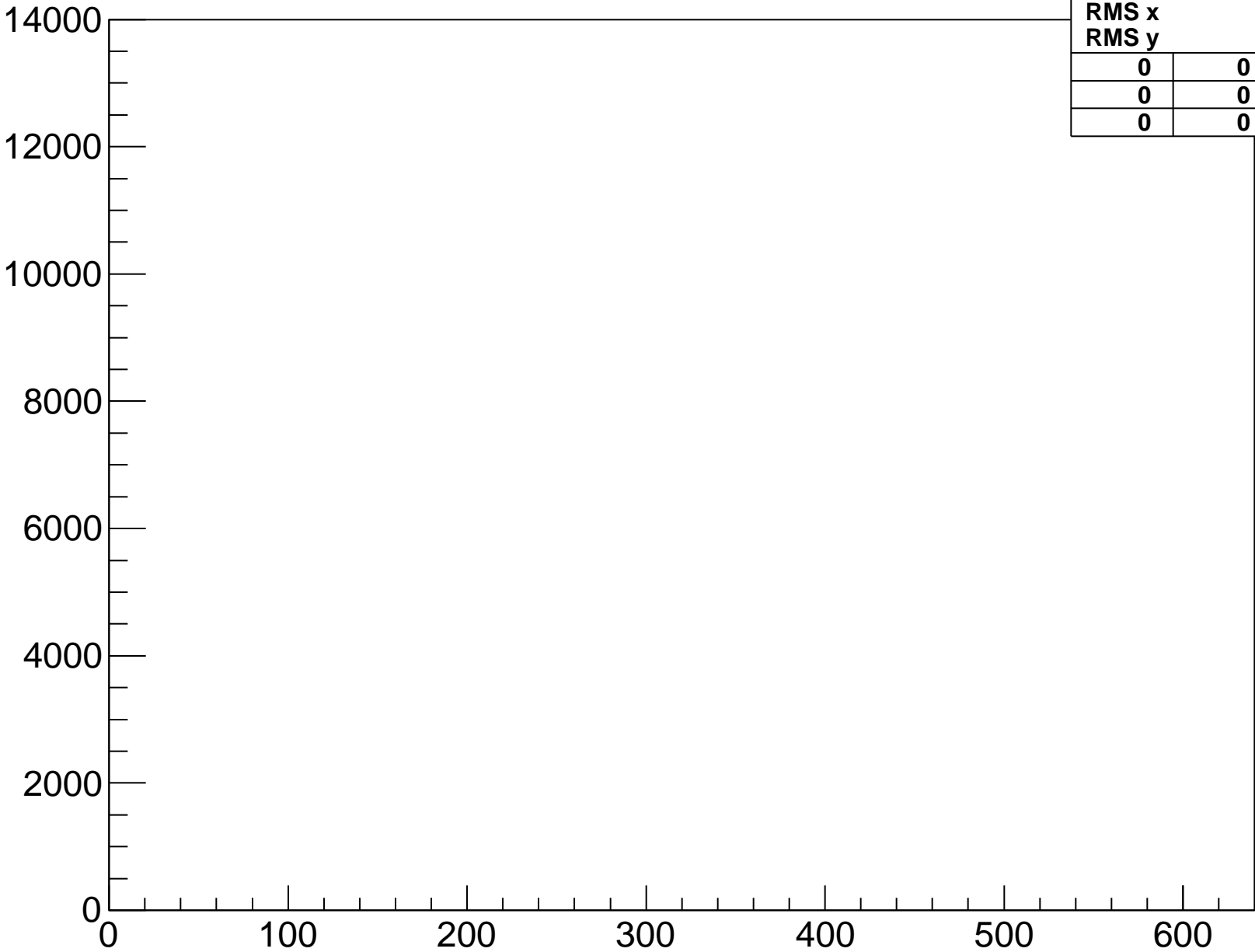


baselinesamples-fpga-9-hyb-3-sample-1



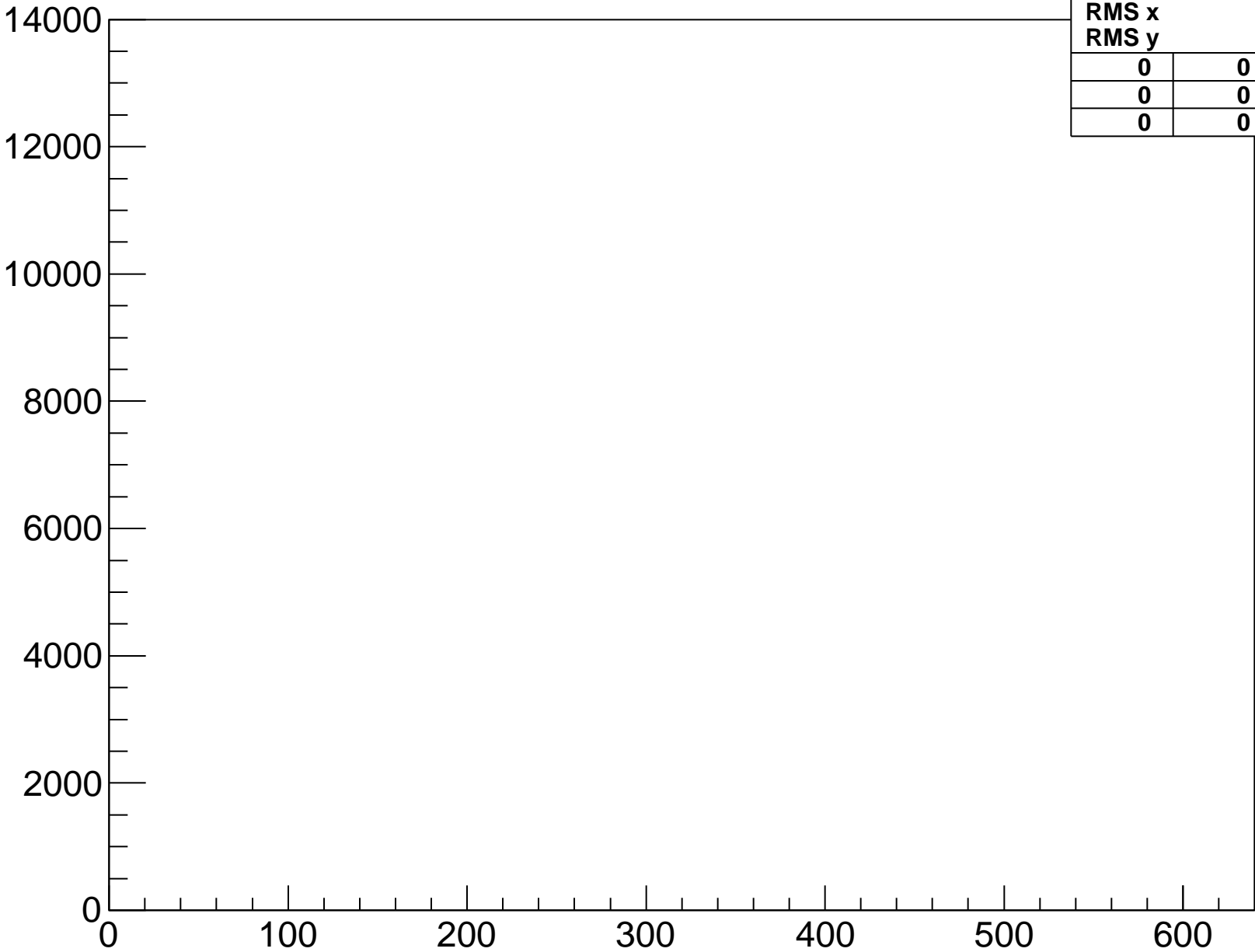
Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0

baselinesamples-fpga-9-hyb-3-sample-2



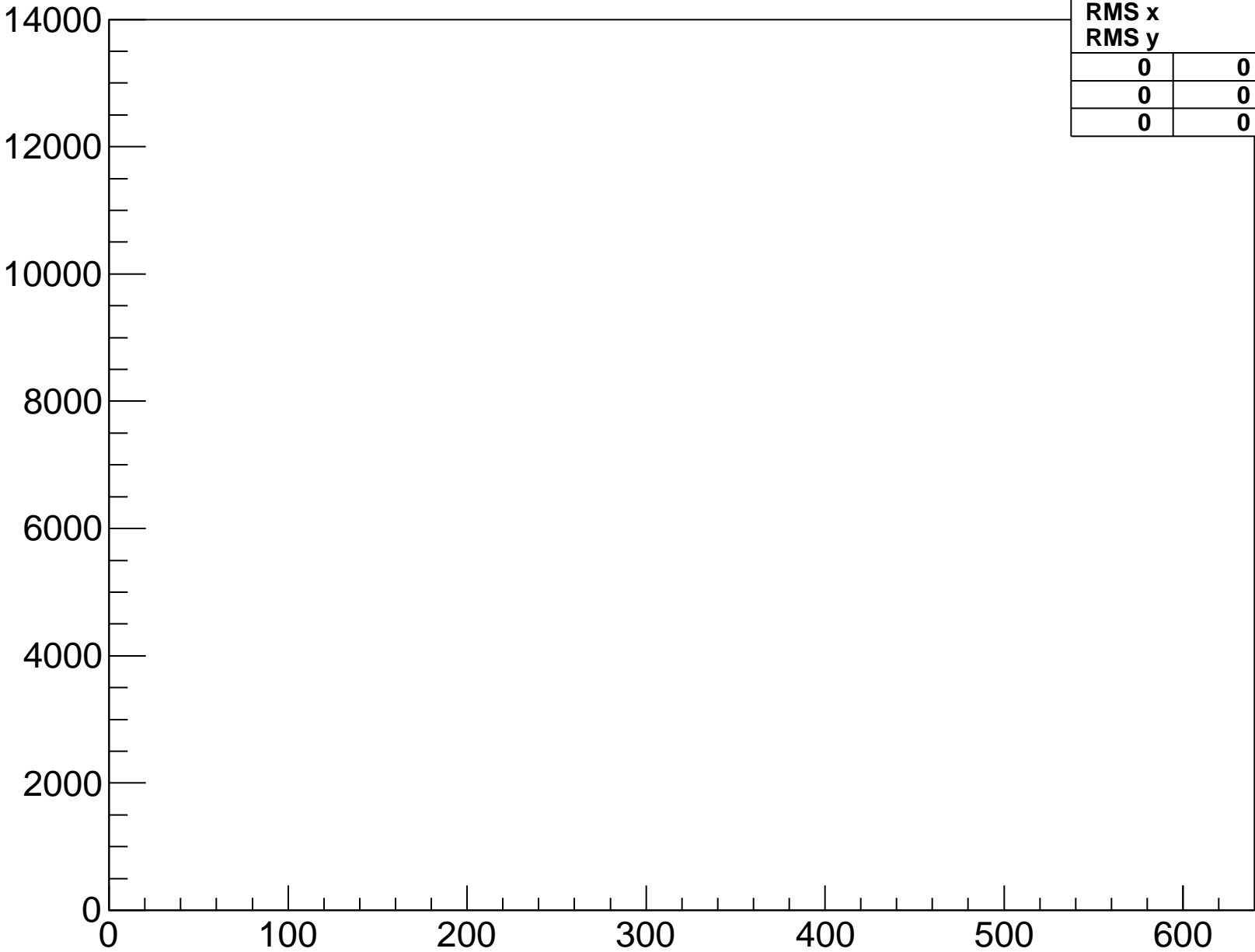
Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

baselinesamples-fpga-9-hyb-3-sample-3



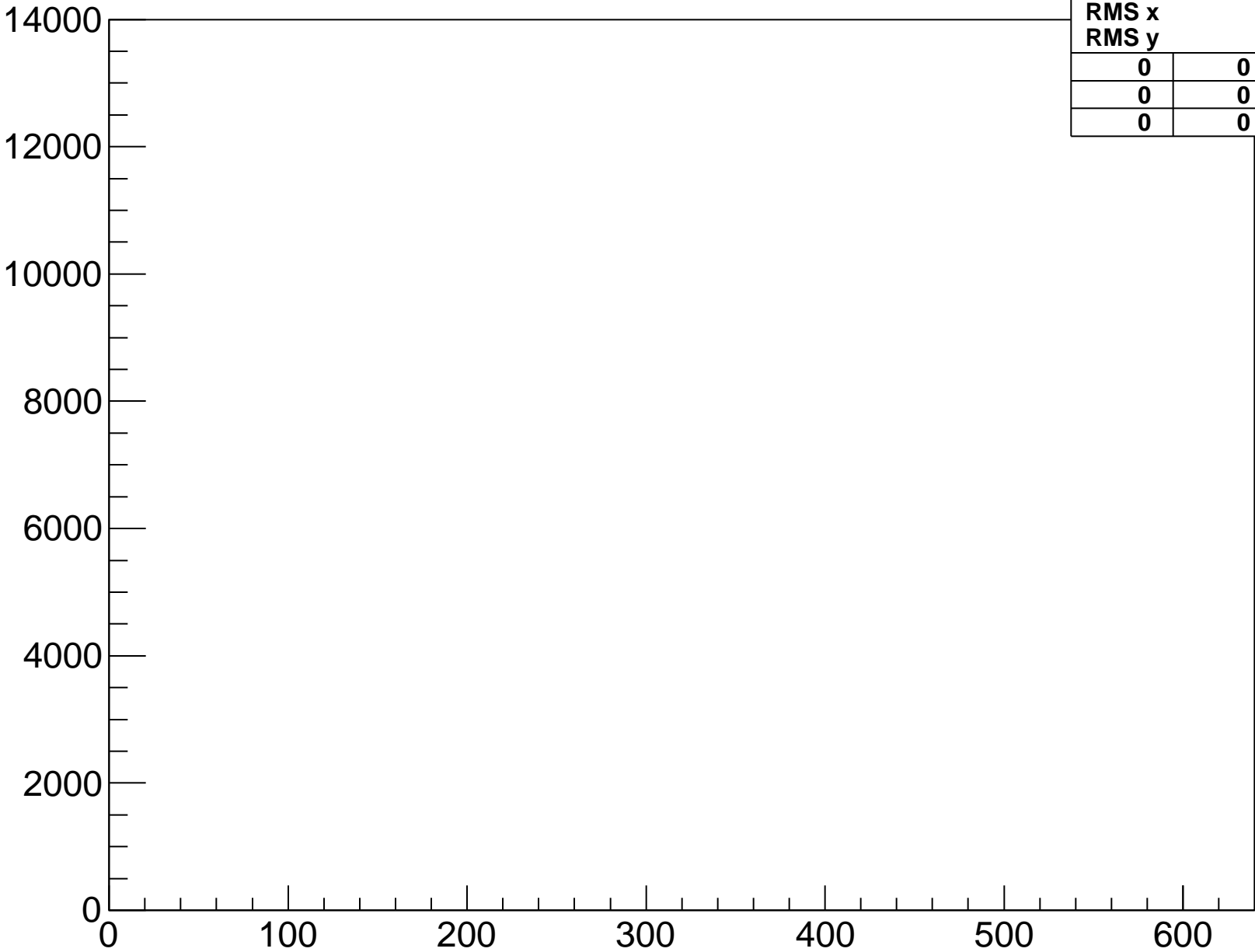
Entries	0	
Mean x	0	
Mean y	0	
RMS x	0	
RMS y	0	
0	0	0
0	0	0
0	0	0

baselinesamples-fpga-9-hyb-3-sample-4



Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0

baselinesamples-fpga-9-hyb-3-sample-5



Entries			0
Mean x			0
Mean y			0
RMS x			0
RMS y			0
0	0	0	0
0	0	0	0
0	0	0	0