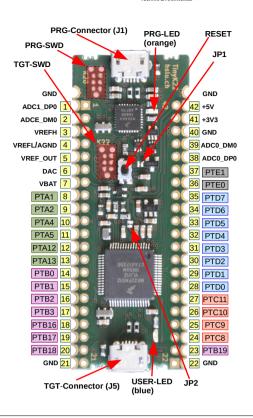
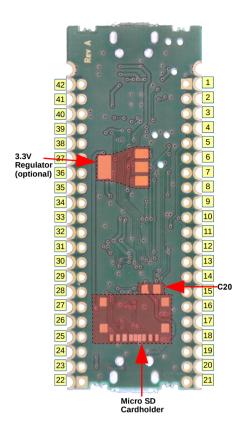
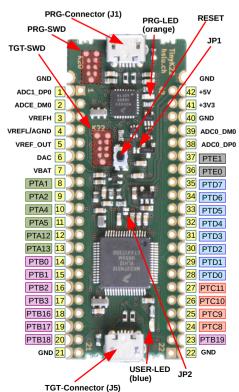
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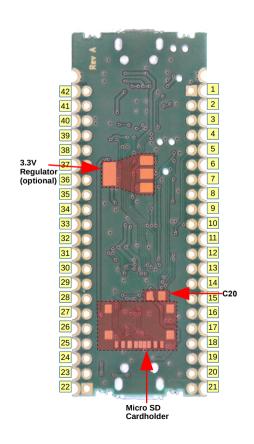




								_	_	_	_	_			
	LPUART0	UART0	UART1	UART2	FTM0	FTM1	FTM2	FTM3	12C0	12C1	SP10	SPI1	ADC0	ADC1	CMP0
PTA1		R			6										П
PTA2		Т			7										
PTA4					1										
PTA5					2										
PTA12						0			С						
PTA13						1			D						
PTB0						0			С				8	8	
PTB1						1			D				9	9	
PTB2					3								12		
PTB3					0								13		
PTB16		R										0			
PTB17		Т										1			
PTB18							0								
PTB19							1								
PTC8								4						4b	12
PTC9								5						5b	13
PTC10								6	С					6b	
PTC11								7	D					7b	
PTD0								0							
PTD1								1			С		5b		
PTD2	R			R				2	С		0				
PTD3	Т			Т				3	D		ı				
PTD4					4										
PTD5					5							С	6b		
PTD6		R			6							D	7b		
PTD7		Т			7							1			
PTE1			R								С	0		5a	
PTE0			Т								D			4a	







	LPUART0	UARTO	UART1	UART2	FTM0	FTM1	FTM2	FTM3	12C0	12C1	SPIO	SPI1	ADC0	ADC1	CMP0
PTA1		R			6										
PTA2		Т			7										
PTA4					1										
PTA5					2										
PTA12						0			С						
PTA13						1			D						
PTB0						0			С				8	8	
PTB1						1			D				9	9	
PTB2					3								12		
PTB3					0								13		
PTB16		R										0			
PTB17		Т										Ι			
PTB18							0								
PTB19							1								
PTC8								4						4b	12
PTC9								5						5b	13
PTC10								6	С					6b	
PTC11								7	D					7b	
PTD0								0							
PTD1								1			С		5b		
PTD2	R			R				2	С		0				
PTD3	Т			Т				3	D		ı				
PTD4					4										
PTD5					5							С	6b		
PTD6		R			6							D	7b		
PTD7		Т			7							1			
PTE1			R								С	0		5a	
PTE0			Т								D			4a	

