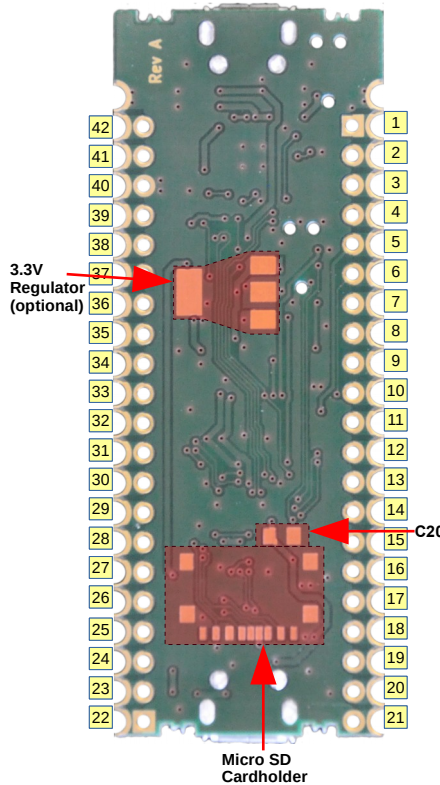
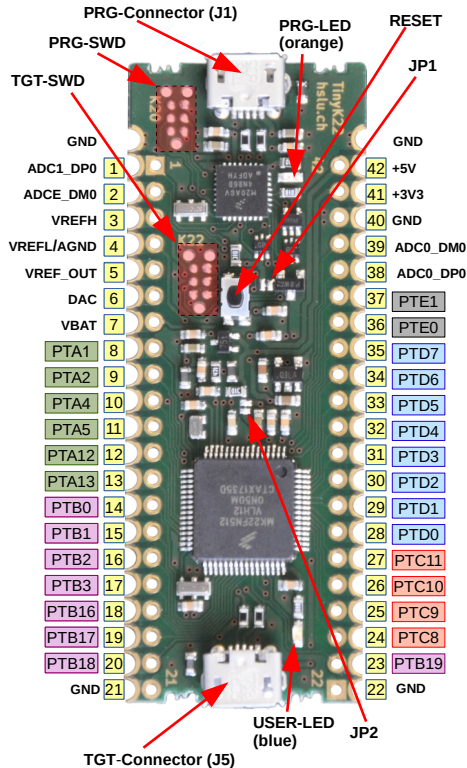


	LPUART0	UART0	UART1	UART2	FTM0	FTM1	FTM2	FTM3	I2C0	I2C1	SPI0	SPI1	ADC0	ADC1	CMPO
PTA1	R				6										
PTA2	T				7										
PTA4				1											
PTA5				2											
PTA12					0				C						
PTA13					1				D						
PTB0					0				C				8	8	
PTB1					1				D				9	9	
PTB2					3								12		
PTB3					0								13		
PTB16	R											O			
PTB17	T											I			
PTB18						0									
PTB19						1									
PTC8							4							4b	12
PTC9							5							5b	13
PTC10							6	C						6b	
PTC11							7	D						7b	
PTD0							0								
PTD1							1						5b		
PTD2	R		R				2	C	O						
PTD3	T		T				3	D	I						
PTD4					4										
PTD5					5								C	6b	
PTD6	R				6								D	7b	
PTD7	T				7								I		
PTE1		R								C	O			5a	
PTE0		T								D				4a	



	LPUART0	UART0	UART1	UART2	FTM0	FTM1	FTM2	FTM3	I2C0	I2C1	SPI0	SPI1	ADC0	ADC1	CMPO
PTA1	R				6										
PTA2	T				7										
PTA4				1											
PTA5				2											
PTA12					0				C						
PTA13					1				D						
PTB0					0				C				8	8	
PTB1					1				D				9	9	
PTB2					3								12		
PTB3					0								13		
PTB16	R											O			
PTB17	T											I			
PTB18						0									
PTB19						1									
PTC8							4							4b	12
PTC9							5							5b	13
PTC10							6	C						6b	
PTC11							7	D						7b	
PTD0							0								
PTD1							1						5b		
PTD2	R		R				2	C	O						
PTD3	T		T				3	D	I						
PTD4					4										
PTD5					5								C	6b	
PTD6	R				6								D	7b	
PTD7	T				7								I		
PTE1		R								C	O			5a	
PTE0		T								D				4a	

