

## 1. Description

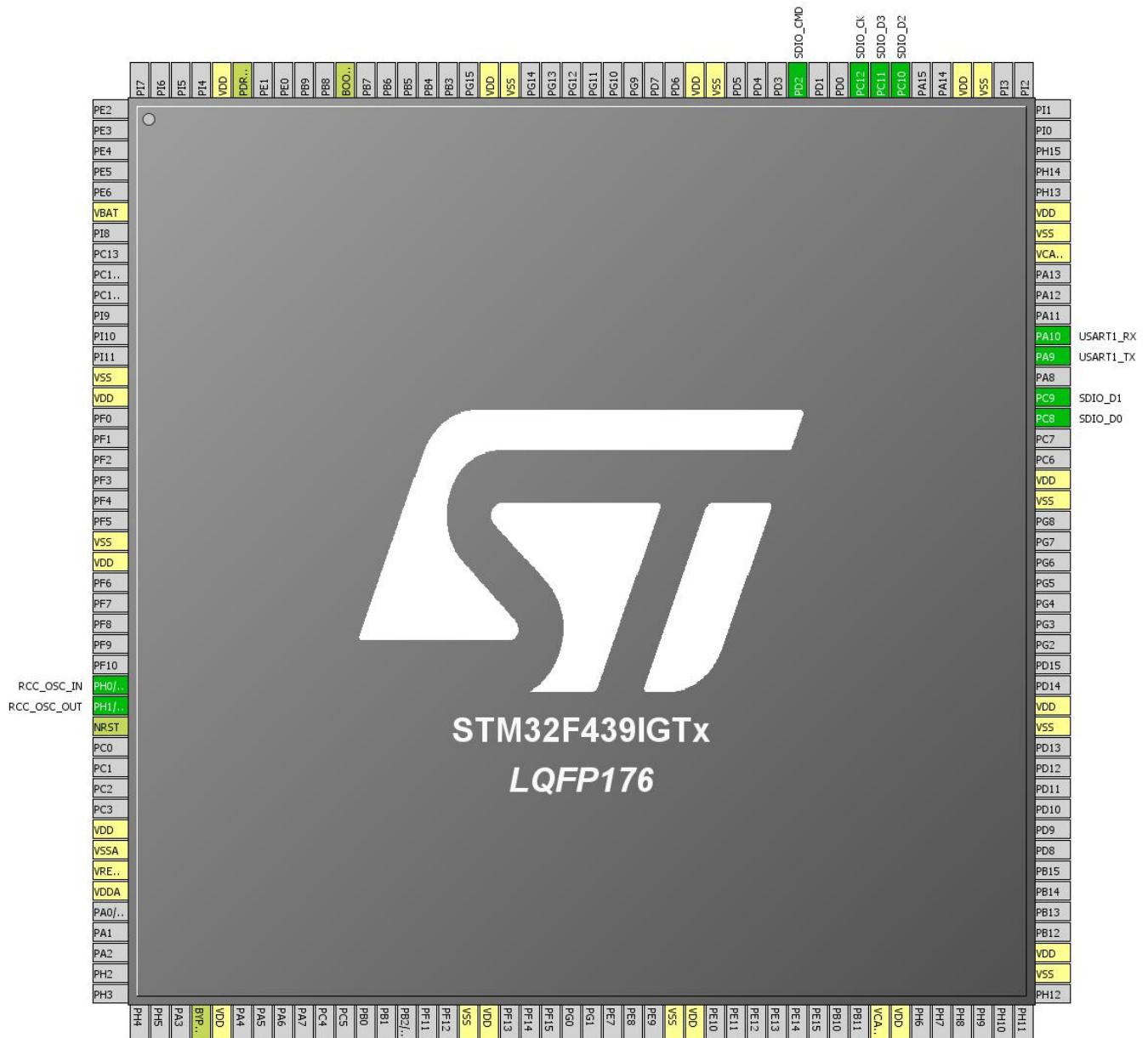
### 1.1. Project

Project Name	STM32F429I
Board Name	STM32F429I
Generated with:	STM32CubeMX 4.11.0
Date	11/30/2015

### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F429/439
MCU name	STM32F439IGTx
MCU Package	LQFP176
MCU Pin number	176

## 2. Pinout Configuration

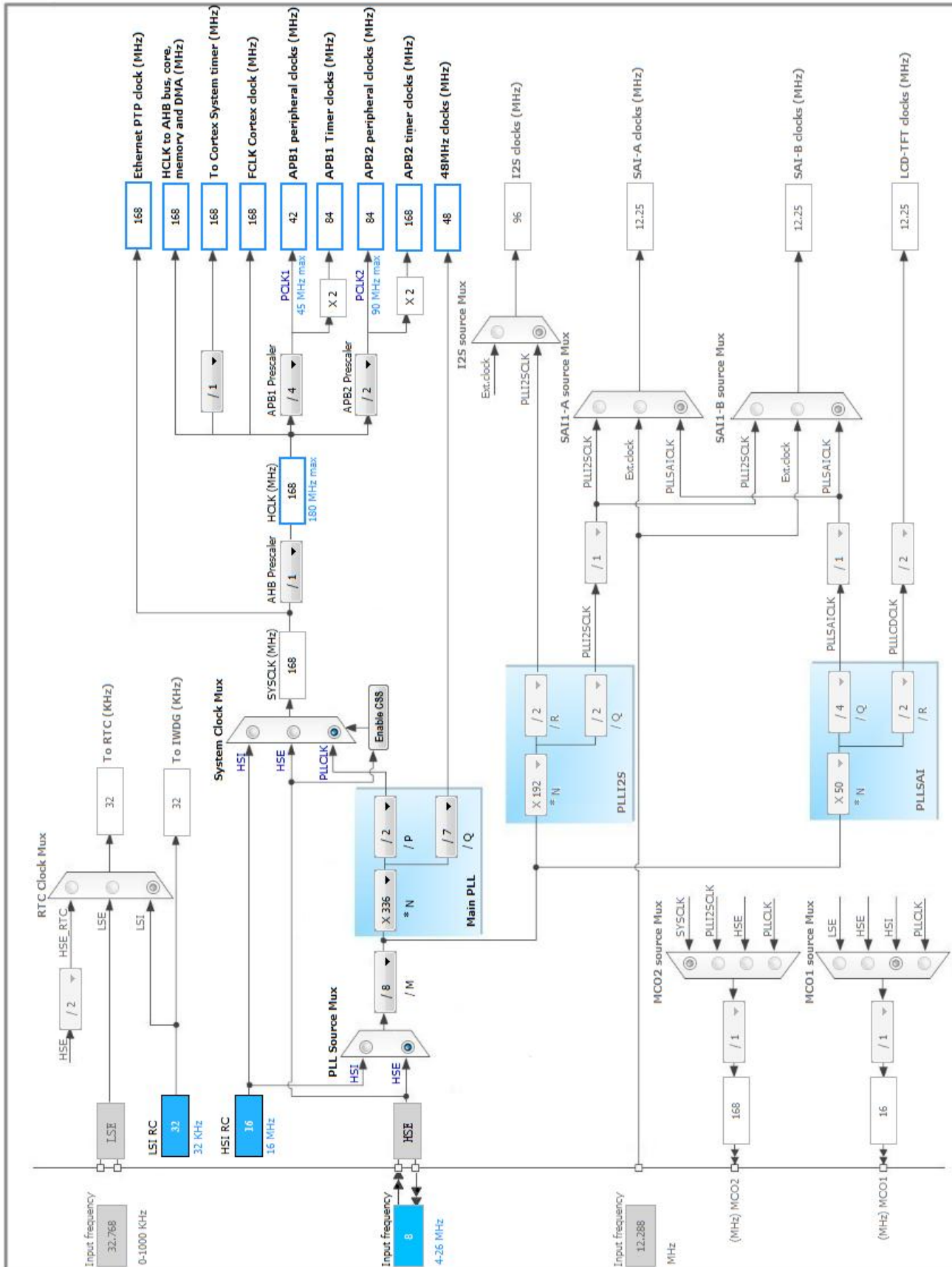


### 3. Pins Configuration

Pin Number LQFP176	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
6	VBAT	Power		
14	VSS	Power		
15	VDD	Power		
22	VSS	Power		
23	VDD	Power		
29	PH0/OSC_IN	I/O	RCC_OSC_IN	
30	PH1/OSC_OUT	I/O	RCC_OSC_OUT	
31	NRST	Reset		
36	VDD	Power		
37	VSSA	Power		
38	VREF+	Power		
39	VDDA	Power		
48	BYPASS_REG	Reset		
49	VDD	Power		
61	VSS	Power		
62	VDD	Power		
71	VSS	Power		
72	VDD	Power		
81	VCAP_1	Power		
82	VDD	Power		
90	VSS	Power		
91	VDD	Power		
102	VSS	Power		
103	VDD	Power		
113	VSS	Power		
114	VDD	Power		
117	PC8	I/O	SDIO_D0	
118	PC9	I/O	SDIO_D1	
120	PA9	I/O	USART1_TX	
121	PA10	I/O	USART1_RX	
125	VCAP_2	Power		
126	VSS	Power		
127	VDD	Power		
135	VSS	Power		
136	VDD	Power		
139	PC10	I/O	SDIO_D2	

Pin Number LQFP176	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
140	PC11	I/O	SDIO_D3	
141	PC12	I/O	SDIO_CK	
144	PD2	I/O	SDIO_CMD	
148	VSS	Power		
149	VDD	Power		
158	VSS	Power		
159	VDD	Power		
166	BOOT0	Boot		
171	PDR_ON	Reset		
172	VDD	Power		

## 4. Clock Tree Configuration



## 5. IPs and Middleware Configuration

### 5.1. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

##### 5.1.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	5 WS (6 CPU cycle)

###### RCC Parameters:

HSI Calibration Value	16
TIM Prescaler Selection	Disabled

###### Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 1
Power Over Drive	Disabled

### 5.2. SDIO

#### Mode: SD 4 bits Wide bus

##### 5.2.1. Parameter Settings:

###### SDIO parameters:

SDIOCLK clock divide factor	0
-----------------------------	---

### 5.3. USART1

#### Mode: Asynchronous

##### 5.3.1. Parameter Settings:

###### Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

**Advanced Parameters:**

Data Direction	Receive and Transmit
Over Sampling	16 Samples

\* User modified value

## 6. System Configuration

### 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PH0/OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1/OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SDIO	PC8	SDIO_D0	Alternate Function Push Pull	No pull-up and no pull-down	High	
	PC9	SDIO_D1	Alternate Function Push Pull	No pull-up and no pull-down	High	
	PC10	SDIO_D2	Alternate Function Push Pull	No pull-up and no pull-down	High	
	PC11	SDIO_D3	Alternate Function Push Pull	No pull-up and no pull-down	High	
	PC12	SDIO_CK	Alternate Function Push Pull	No pull-up and no pull-down	High	
	PD2	SDIO_CMD	Alternate Function Push Pull	No pull-up and no pull-down	High	
USART1	PA9	USART1_TX	Alternate Function Push Pull	Pull-up	<b>High *</b>	
	PA10	USART1_RX	Alternate Function Push Pull	Pull-up	<b>High *</b>	



## 6.2. DMA configuration

DMA request	Stream	Direction	Priority
SDIO_RX	DMA2_Stream3	Peripheral To Memory	<b>Very High *</b>
SDIO_TX	DMA2_Stream6	Memory To Peripheral	<b>Very High *</b>

### SDIO\_RX: DMA2\_Stream3 DMA request Settings:

Mode: **Peripheral Flow Control \***

Use fifo: **Enable \***

FIFO Threshold: Full

Peripheral Increment: Disable

Memory Increment: **Enable \***

Peripheral Data Width: **Word \***

Memory Data Width: Word

Peripheral Burst Size: **4 Increment \***

Memory Burst Size: 4 Increment

### SDIO\_TX: DMA2\_Stream6 DMA request Settings:

Mode: **Peripheral Flow Control \***

Use fifo: **Enable \***

FIFO Threshold: Full

Peripheral Increment: Disable

Memory Increment: **Enable \***

Peripheral Data Width: **Word \***

Memory Data Width: Word

Peripheral Burst Size: **4 Increment \***

Memory Burst Size: 4 Increment

### 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
System tick timer	true	0	0
SDIO global interrupt	true	5	0
DMA2 stream3 global interrupt	true	6	0
DMA2 stream6 global interrupt	true	6	0
Non maskable interrupt	unused		
Memory management fault	unused		
Pre-fetch fault, memory access fault	unused		
Undefined instruction or illegal state	unused		
Debug monitor	unused		
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
USART1 global interrupt	unused		

\* User modified value

## ***7. Power Plugin report***

### 7.1. Microcontroller Selection

Series	STM32F4
Line	STM32F429/439
MCU	STM32F439IGTx
Datasheet	024244_Rev6

### 7.2. Parameter Selection

Temperature	25
Vdd	null

## 8. Software Project

### 8.1. Project Settings

Name	Value
Project Name	STM32F429I
Project Folder	C:\Users\Administrator\Desktop\25.SDIO
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_F4 V1.9.0

### 8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No