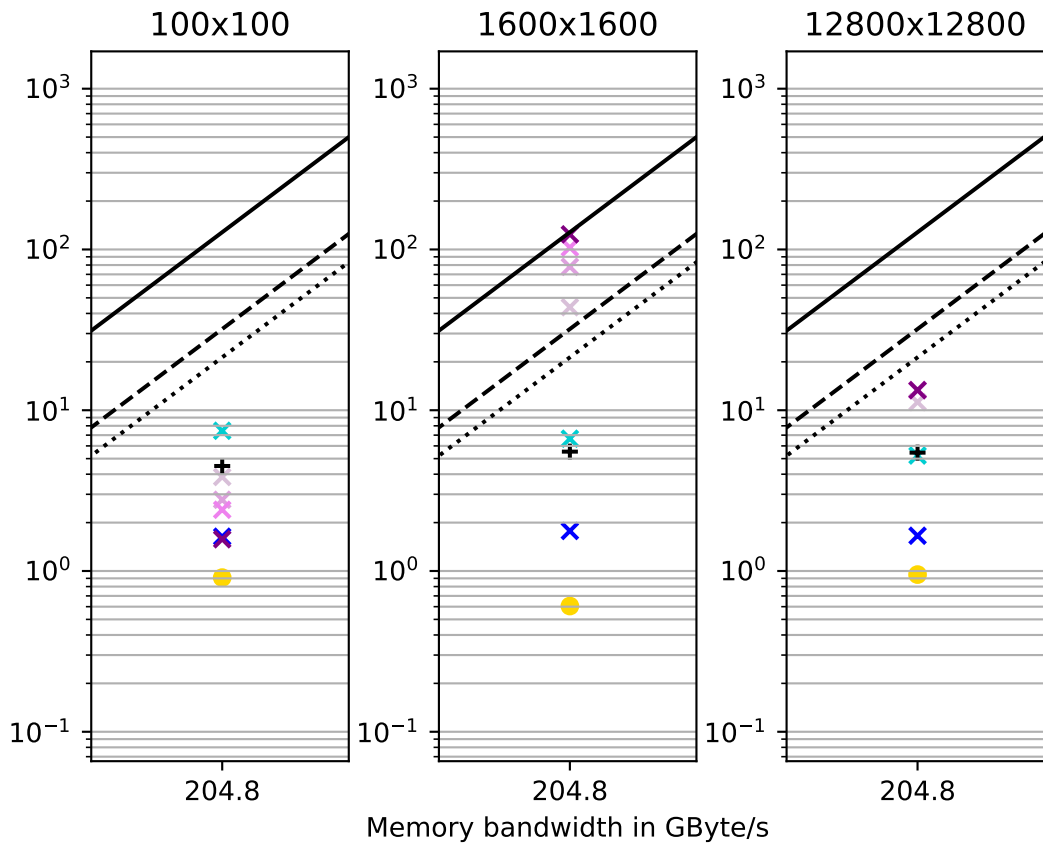


# Roofline analysis of CPU implementations



- |                                       |                                       |
|---------------------------------------|---------------------------------------|
| ..... Roofline no caching             | ✕ Julia sequential stencil code       |
| --- Roofline for caching 2/5 elements | ✕ Julia parallel stencil 8 threads    |
| — Roofline for caching all elements   | ✕ Julia parallel stencil 12 threads   |
| ✕ Julia parallel stencil 4 threads    | ✕ Julia parallel stencil 24 threads   |
| ✕ Julia array programming sequential  | + Naive sequential C++ implementation |
| ● Numpy array programming             |                                       |