net=GND:7
net=Vcc:14
documentation=http://www-s.ti.com/sc/ds/sn74hc00.pdf
description=4 NAND gates with 2 inputs
footprint=DIP14
slotdef=4:12,13,11
slotdef=3:9,10,8
slotdef=2:4,5,6
slotdef=1:1,2,3
numslots=4
slot=1
U?device=7400



1
1
A
in
Y
out
3
3
2
2
B
in
7400