PETER YEHL

OBJECTIVE

Seeking an internship that leverages embedded software development practices and offers opportunities for professional growth.

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EDUCATION

Iowa State University
Spring 2026

Bachelor of Science
Computer Engineering

GPA
3.58

SKILLS

- Java
- C
- React JS
- CSS
- Assembly
- MATLAB
- VHDL
- Object-Oriented Programming
- Database Management (MySQL)
- Embedded SW Development
- Backend Development
- GIT
- CI/CD
- Swagger
- LTspice
- Schematic Capture
- Circuit Design/Analysis
- Arduino Development
- PCC Level I Certified
- Forklift Certified

ACTIVITIES

Engineers for a Sustainable World
January 2024 - Present

Mountaineering and Climbing Club
January 2024 - Present

Associated General Contractors
March 2024 - Present

WORK EXPERIENCE

Engineering Intern — Ames, IA
2024
City of Ames

- Developed and maintained project records, “as-built” drawings, and progress payments
- Performed surveying and GPS marking using GNSS equipment
- Prepared CAD documents and GIS maps for street pavement and utility improvements
- Utilized Moasure software to measure and calculate quantities through a system of data collection and analysis

Engineering Intern — Bloomington, IL
2023
City of Bloomington

- Maintained continuous inspection protocols for risk mitigation and quality assurance in civil engineering projects
- Ensured compliance with city standards across all project phases
- Conducted thorough inspections on various types of pavement and installation

PROJECTS

Riff Radar | Java

- Developed a mobile application connecting music fans with bands, utilizing Android Studio, IntelliJ, and Spring Boot
- Integrated Ticketmaster, Google Maps, and Spotify APIs
- Utilized Maven for build automation and MySQL for database management
- Managed code with Git and GitLab; documented with Swagger

iRobot | C

- Simulated a hospital delivery robot using manual navigation through a websocket rooted GUI
- Utilized an ARM Cortex M4 microcontroller to conduct bit field manipulation through datasheet analysis
- Deployed UART, ADC, interrupt, memory mapping, and PWM techniques

Stereo Amplifier

- Designed and constructed a stereo amplifier leveraging operational amplifiers and bandpass filters
- Tested and analyzed frequencies in relation to phase and magnitude using oscilloscope traces