



# Graphics SIG Meeting

## Oct 28, 2021

10:05am PDT

<https://github.com/riscv-admin/graphics>

# Antitrust Policy Notice

RISC-V International meetings involve participation by industry competitors, and it is the intention of RISC-V International to conduct all its activities in accordance with applicable antitrust and competition laws. It is therefore extremely important that attendees adhere to meeting agendas, and be aware of, and not participate in, any activities that are prohibited under applicable US state, federal or foreign antitrust and competition laws.

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If you have questions about these matters, please contact your company counsel.

# Collaborative & Welcoming Community

RISC-V is a free and open ISA enabling a new era of processor innovation through open standard collaboration. Born in academia and research, RISC-V ISA delivers a new level of free, extensible software and hardware freedom on architecture, paving the way for the next 50 years of computing design and innovation.

**We are a transparent, collaborative community where all are welcomed**, and all members are encouraged to participate. We are a continuous improvement organization. If you see something that can be improved, please tell us. [help@riscv.org](mailto:help@riscv.org)

We as members, contributors, and leaders pledge to make participation in our community a **harassment-free experience for everyone**.

<https://riscv.org/risc-v-international-community-code-of-conduct/>

# Conventions



- Unless it is a scheduled agenda topic, we don't solve problems or detailed topics in most meetings unless specified in the agenda because we don't often have enough time to do so and it is more efficient to do so offline and/or in email. We identify items and send folks off to do the work and come back with solutions or proposals.
- If some policy, org, extension, etc. can be doing things in a better way, help us make it better. Do not change or not abide by the item unilaterally. Instead let's work together to make it better.
- Please conduct meetings that accommodates the virtual and broad geographical nature of our teams. This includes meeting times, repeating questions before you answer, at appropriate times polling attendees, guide people to interact in a way that has attendees taking turns speaking, ...
- Where appropriate and possible, meeting minutes will be added as speaker notes within the slides for the Agenda

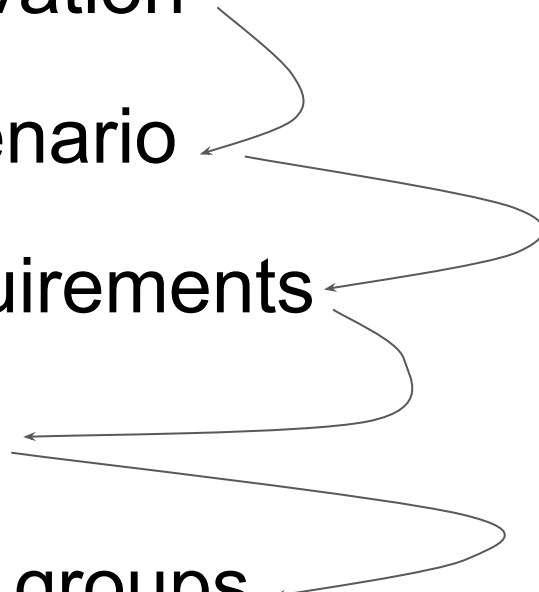
# Agenda



- Gap analysis index (20 min)
- Volunteering for product market research (20 min)

# Gap analysis index



- Motivation
  - Escenario
  - Requirements
  - Plan
  - Task groups
- 
- A diagram consisting of four curved arrows pointing from left to right, connecting the list items in sequence: from 'Motivation' to 'Escenario', 'Escenario' to 'Requirements', 'Requirements' to 'Plan', and 'Plan' to 'Task groups'.

# Motivation



Answer the following questions:

What is the size of the graphics market?

How much of that is embedded graphics?

Why do we want to do graphics with RISC-V?

What is the advantage for potential adopters?

**Market research volunteers needed!**



# Escenario



Pick a significant use case for RISC-V in graphics: OpenGL ES 2.0 (embedded!).

Description of ready-available components (like shader toolchain) and their limitations.



# Requirements



Limitation X is removed.

...

Limitation Y is removed.

Need for proposed extensions is justified for obvious cases.

Need for proposed extension is quantified for non-obvious cases.

# Plan



Tasks defined by:

Name

Dependencies

T-shirt size

Summary the whole picture as a Gantt chart

# Task groups



A task group owns a group of tasks from the plan.

Assigning tasks to task groups.

# Backup Slides