

Peter Dranishnikov

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Experienced Computer Engineer, with a strong background in informatics, seeking internships in embedded software development.

Education:

- **Florida Polytechnic University** (August 2017– Dec. 2020)
 - Major: Computer Engineering with concentration in Embedded Systems Design.
 - GPA: 3.70
 - **Projects:**
 - Implemented MIPS processor components in VHDL and Verilog.
 - Collaboratively programmed a parallel communication protocol on a microcontroller using AVR assembly.
 - Jointly developed a logic gate consisting of discrete transistors.
 - Leading a small team designing a solar-powered DC-DC power converter.
 - Interim Chair of the IEEE RF Committee.
- F. W. Buchholz High School (August 2013 – June 2017)
 - Worked closely with a large group to design a robot for the FIRST Robotics Competition.

Professional Experience:

Florida Polytechnic University, Health Informatics and Big Data Laboratory

Research Assistant, Programmer (July 2017 – October 2018)

- Processed, interpreted, and analyzed camper demographics for a park using R.
- Ported a neural network training program for predicting hospital stay lengths from R into Python.

University of Florida, Genetics Department; Research Computing

Research Assistant, Programmer (June 2016 – August 2016)

- Developed a Python program for computing correlation matrices from molecular data.
- Wrote a wrapper of a clustering library for the *Galaxy* user interface in Python and XML.
- Collaborated closely with IT in troubleshooting of a deployed *Galaxy* instance on a HPC.

Skills and Achievements:

- Language proficiency in Python, C, assembly (various), Java, MATLAB, Verilog, VHDL.
- Operating Systems: Linux, Microsoft Windows.
- Co-Author of a Journal Publication in PLOS ONE: *At Least 23 Genera Instead of One: The Case of Iris L. s.l. (Iridaceae)*, 2014
- Repair consumer electronics.
- Volunteer contributor of GIS data to the OpenStreetMap project.
- Fluent in English and Russian; Studied French for 2 years.