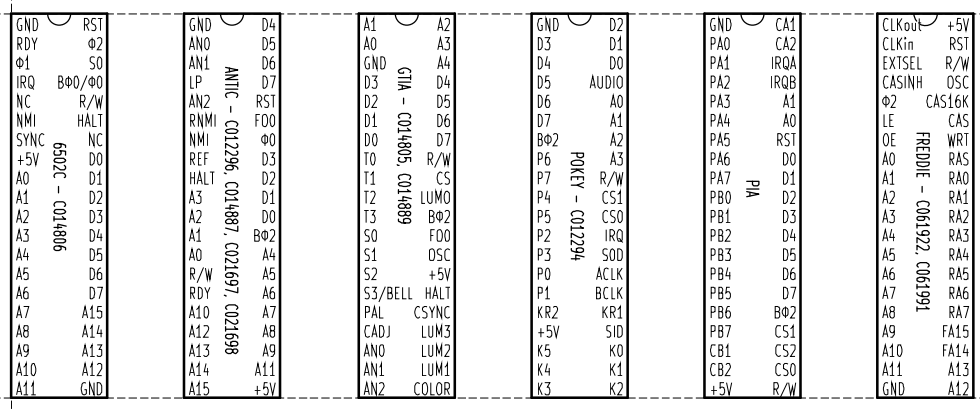

Supplementary Information on Φ0 and Φ2 Signals

1. Φ2 is the clock signal generated by the 6502C Sally.
   Most chips use the Buffered Φ2 signal (BΦ2), while Freddie is the only chip that directly uses Φ2.

2. The 65XE (without ECI) and XEGS have Φ0 directly connected from Antic to Sally.
   In contrast, the 130XE board has Sally clocked by the Buffered Φ0 (BΦ0) signal.