

GND	RST	GND	D4	A1	A2	GND	D2	GND	CA1	CLKout	+5V
RDY	Φ2	ANO	D5	A0	A3	D3	D1	PA0	CA2	CLKin	RST
Φ1	S0	AN1	D6	GND	A4	D4	D0	PA1	IRQA	EXTSEL	R/W
IRQ	BΦ0/Φ0	LP	D7	D3	D4	D5	AUDIO	PA2	IRQB	CASINH	OSC
NC	R/W	AN2	RST	D2	D5	D6	A0	PA3	A1	Φ2	CAS16K
NMI	HALT	RNMI	F00	D1	D6	D7	A1	PA4	A0	LE	CAS
SYNC	NC	NMI	Φ0	D0	D7	BΦ2	A2	PA5	RST	OE	WRT
+5V	D0	REF	D3	T0	R/W	P6	A3	PA6	D0	A0	RAS
6502C - C014806	D1	HALT	D2	T1	CS	P7	R/W	PA7	D1	A1	RA0
A0	D2	A3	D1	T2	LUM0	P4	CS1	PB0	D2	A2	RA1
A1	D3	A2	D0	T3	BΦ2	P5	CS0	PB1	D3	A3	RA2
A2	D4	A1	BΦ2	S0	F00	P2	IRQ	PB2	D4	A4	RA3
A3	D5	A0	A4	S1	OSC	P3	SOD	PB3	D5	A5	RA4
A4	D6	R/W	A5	S2	+5V	P0	ACLK	PB4	D6	A6	RA5
A5	D7	RDY	A6	S3/BELL	HALT	P1	BCLK	PB5	D7	A7	RA6
A6	A15	A10	A7	PAL	CSYNC	KR2	KR1	PB6	BΦ2	A8	RA7
A7	A14	A12	A8	CADJ	LUM3	+5V	SID	PB7	CS1	A9	FA15
A8	A13	A13	A9	ANO	LUM2	K5	K0	CB1	CS2	A10	FA14
A9	A12	A14	A11	AN1	LUM1	K4	K1	CB2	CS0	A11	A13
A10	GND	A15	+5V	AN2	COLOR	K3	K2	+5V	R/W	GND	A12

Supplementary Information on Φ0 and Φ2 Signals

1. Φ2 is the clock signal generated by the 6502C Sally.  
Most chips use the Buffered Φ2 signal (BΦ2), while Freddie is the only chip that directly uses Φ2.
2. The 65XE (without ECI) and XEGS have Φ0 directly connected from Antic to Sally.  
In contrast, the 130XE board has Sally clocked by the Buffered Φ0 (BΦ0) signal.