



PIN LIST

1.	VDD	35.	NPAIR_A_DRAIN		
2.	ENABLE	36.	NPAIR_A_GATE	J3	1
3.	CLK	37.	NPAIR_A_SOURCE		2
4.	DATA	38.	NPAIR_B_SOURCE		3
5.	PPAIR_4_BODY	39.	NPAIR_B_GATE		4
6.	PPAIR_4B_DRAIN	40.	NPAIR_B_DRAIN		5
7.	PPAIR_4B_GATE	41.	NPAIR_4A_DRAIN		6
8.	PPAIR_4B_SOURCE	42.	NPAIR_4A_GATE		7
9.	PPAIR_4A_SOURCE	43.	NPAIR_4A_SOURCE		8
10.	PPAIR_4A_GATE	44.	NPAIR_4B_SOURCE		9
11.	PPAIR_4A_DRAIN	45.	NPAIR_4B_GATE		10
12.	PPAIR_B_DRAIN	46.	NPAIR_4B_DRAIN		11
13.	PPAIR_B_GATE	47.	NPAIR_4_BODY		12
14.	PPAIR_B_SOURCE	48.	INV_TPLGY16_2INN		13
15.	PPAIR_A_SOURCE	49.	INV_TPLGY16_2OUT		14
16.	PPAIR_A_GATE	50.	INV_TPLGY16_2INP		15
17.	PPAIR_A_DRAIN	51.	INV_TPLGY16_1OUT		16
18.	VSS	52.	INV_TPLGY16_1INP		
19.	ISINK_REF	53.	INV_TPLGY16_1INN		
20.	ISINK_1	54.	CMFB_OTA2_OUT	J4	1
21.	ISINK_2	55.	CMFB_OTA2_INN		2
22.	ISINK_4	56.	CMFB_OTA2_INP		3
23.	ISINK_8	57.	PMOS_4B_DRAIN		4
24.	ISINK_16	58.	PMOS_4B_GATE		5
25.	ISINK_32	59.	PMOS_4A_DRAIN		6
26.	ISINK_REF2	60.	PMOS_4A_GATE		7
27.	ISINK_REF2_CPY	61.	L_SOURCE_REF2_CPY		8
28.	NMOS_4A_GATE	62.	L_SOURCE_REF2		9
29.	NMOS_4A_DRAIN	63.	L_SOURCE_16		10
30.	NMOS_4B_GATE	64.	L_SOURCE_8		11
31.	NMOS_4B_DRAIN	65.	L_SOURCE_4		12
32.	OTA_N_INN	66.	L_SOURCE_2		13
33.	OTA_N_INP	67.	L_SOURCE_1		14
34.	OTA_N_OUT	68.	L_SOURCE_REF		15

Changed Pin Assignment: P1 -> TopLeft -> (Clockwise)	
Simplified Breakout Board	
Columbia Integrated Systems Laboratory (CISL)	
Sheet: /	
File: MobiusPCB_v3_2024.kicad_sch	
Title: MobiusPCB_v3_2024	
Size: A3	Date: 2024-02-26
KiCad E.D.A. 8.0.1	