



STM32系列 产品选型手册



微控制器(MCU) 微处理器(MPU) 无线微控制器(Wireless)



149

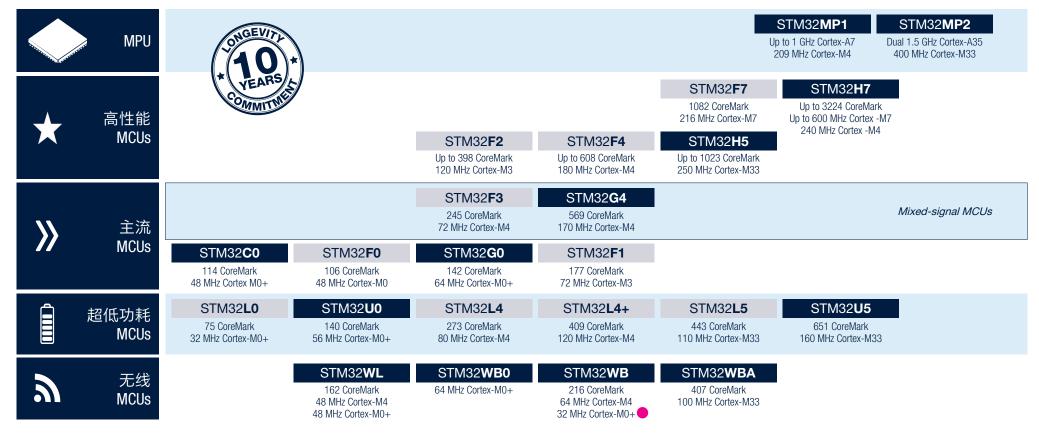
主流级MCU STM32 G4系列 - Arm® Cortex®-M4高性能模数混合型MCU 12 STM32 G0系列 - Arm® Cortex®-M0+入门级MCU 20 STM32 CO系列 - Arm Cortex-M0+ 超值入门型MCU STM32 F3系列 - Arm® Cortex®-M4模数混合型MCU 21 STM32 F1系列 - Arm® Cortex®-M3基础型MCU 26 STM32 F0系列 - Arm® Cortex®-M0入门级MCU 33 高性能MCU STM32 H7系列 - Arm® Cortex®-M7/Cortex®-M7+M4超高性能/Cortex®-M7 Boot Flash 高性能MCU 39 STM32 F7系列 - Arm® Cortex®-M7高性能MCU STM32 H5系列 - Arm® Cortex®-M33高性能MCU 72 STM32 F4系列 - Arm® Cortex®-M4高性能MCU 86 STM32 F2系列 - Arm® Cortex®-M3高性能MCU 超低功耗MCU 89 STM32 U0系列 - Arm® Cortex®-M0+ 新一代超低功耗入门级MCU STM32 U5系列 - Arm® Cortex®-M33新一代超低功耗旗舰系列MCU 99 STM32 L5系列 - Arm® Cortex®-M33超低功耗高性能高安全MCU STM32 L4+系列 - Arm® Cortex®-M4超低功耗高性能MCU 106 STM32 L4系列 - Arm® Cortex®-M4超低功耗MCU 116 STM32 L1系列 - Arm® Cortex®-M3超低功耗MCU 122 STM32 L0系列 - Arm® Cortex®-M0+超低功耗MCU 无线MCU STM32 WB0系列-Arm® Cortex®-M0+紧凑、节能设计的2.4GHz BLE 5.4 无线MCU 133 STM32 WB系列 - Arm® Cortex®-M4和Cortex®-M0+双核2.4G无线MCU STM32 WBA系列- Arm® Cortex®-M33超低功耗, 高性能安全2.4GHz BLE 5.4 无线MCU 136 138 STM32 WL系列 - Arm® Cortex®-M4/Arm® Cortex®-M4和Cortex®-M0+长距离无线SoC 射频收发器 140 SPIRIT系列 - SublG Hz 射频收发器 微处理器MPU 141 STM32 MP1系列 – Arm® Cortex®-A7/Cortex®-A7 + M4 高性价比工业级MPU STM32 MP2系列 -Arm® Cortex®-A35 + M33/Cortex®-A35 + M33 + NPU 64位工业级边缘AI MPU

STM32 产品矩阵

主推产品

射频协处理器





Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	128	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
										ST	M32	G43 ⁻	I_44°	1 Ac	cess	Line	;															
STM32G431K6Tx	Cortex-M4	170	32	32	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	4	4	3	3	2	0	3	2	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431K8Tx	Cortex-M4	170	64	32	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	4	4	3	3	2	0	3	2	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431KBTx	Cortex-M4	170	128	32	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	4	4	3	3	2	0	3	2	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431K6Ux	Cortex-M4	170	32	32	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	4	4	3	3	2	0	3	2	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431K8Ux	Cortex-M4	170	64	32	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	4	4	3	3	2	0	3	2	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431KBUx	Cortex-M4	170	128	32	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	4	4	3	3	2	0	3	2	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431C6Tx	Cortex-M4	170	32	32	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431C8Tx	Cortex-M4	170	64	32	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431CBTx	Cortex-M4	170	128	32	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431C6Ux	Cortex-M4	170	32	32	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431C8Ux	Cortex-M4	170	64	32	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431CBUx	Cortex-M4	170	128	32	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431CBYx	Cortex-M4	170	128	32	WLCSP49	41	1.71-3.6	9	1	2	1	0	2	18	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431R6Tx	Cortex-M4	170	32	32	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	128	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
STM32G431R8Tx	Cortex-M4	170	64	32	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431RBTx	Cortex-M4	170	128	32	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431R6lx	Cortex-M4	170	32	32	UFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431R8lx	Cortex-M4	170	64	32	UFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431RBlx	Cortex-M4	170	128	32	UFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431M6Tx	Cortex-M4	170	32	32	LQFP80	66	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431M8Tx	Cortex-M4	170	64	32	LQFP80	66	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431MBTx	Cortex-M4	170	128	32	LQFP80	66	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431V6Tx	Cortex-M4	170	32	32	LQFP100	86	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431V8Tx	Cortex-M4	170	64	32	LQFP100	86	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G431VBTx	Cortex-M4	170	128	32	LQFP100	86	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	N/A	125
STM32G441KBTx	Cortex-M4	170	128	32	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	4	4	3	3	2	0	3	2	1	1[FD]	0	1	1	1	YES	YES	YES	125
STM32G441KBUx	Cortex-M4	170	128	32	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	4	4	3	3	2	0	3	2	1	1[FD]	0	1	1	1	YES	YES	YES	125
STM32G441CBTx	Cortex-M4	170	128	32	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	YES	125
STM32G441CBUx	Cortex-M4	170	128	32	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	12S	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
STM32G441CBYx	Cortex-M4	170	128	32	WLCSP49	41	1.71-3.6	9	1	2	1	0	2	18	4	4	3	3	2	0	3	3	1	1[FD]	0	1	1	1	YES	YES	YES	125
STM32G441RBTx	Cortex-M4	170	128	32	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	YES	125
STM32G441RBlx	Cortex-M4	170	128	32	UFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	YES	125
STM32G441MBTx	Cortex-M4	170	128	32	LQFP80	66	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	YES	125
STM32G441VBTx	Cortex-M4	170	128	32	LQFP100	86	1.71-3.6	9	1	2	1	0	2	23	4	4	3	3	2	0	3	4	1	1[FD]	0	1	1	1	YES	YES	YES	125
									S	TM3	2G49	91_4	A1 P	erfo	rmaı	nce l	Line															
STM32G491KCUx	Cortex-M4	170	256	112	UFQFPN32	26	1.71-3.6	10	1	3	1	0	3	11	4	4	4	3	2	1[QUAD]	3	2	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491KEUx	Cortex-M4	170	512	112	UFQFPN32	26	1.71-3.6	10	1	3	1	0	3	11	4	4	4	3	2	1[QUAD]	3	2	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491CCTx	Cortex-M4	170	256	112	LQFP48	38	1.71-3.6	10	1	3	1	0	3	18	4	4	4	3	2	1[QUAD]	3	3	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491CETx	Cortex-M4	170	512	112	LQFP48	38	1.71-3.6	10	1	3	1	0	3	18	4	4	4	3	2	1[QUAD]	3	3	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491CCUx	Cortex-M4	170	256	112	UFQFPN48	42	1.71-3.6	10	1	3	1	0	3	19	4	4	4	3	2	1[QUAD]	3	3	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491CEUx	Cortex-M4	170	512	112	UFQFPN48	42	1.71-3.6	10	1	3	1	0	3	19	4	4	4	3	2	1[QUAD]	3	3	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491RCTx	Cortex-M4	170	256	112	LQFP64	52	1.71-3.6	10	1	3	1	0	3	24	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491RETx	Cortex-M4	170	512	112	LQFP64	52	1.71-3.6	10	1	3	1	0	3	24	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491REYx	Cortex-M4	170	512	112	WLCSP64	52	1.71-3.6	10	1	3	1	0	3	24	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	I2S	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
STM32G491REIx	Cortex-M4	170	512	112	UFBGA64	52	1.71-3.6	10	1	3	1	0	3	24	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491MCTx	Cortex-M4	170	256	112	LQFP80	66	1.71-3.6	10	1	3	1	0	3	32	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491METx	Cortex-M4	170	512	112	LQFP80	66	1.71-3.6	10	1	3	1	0	3	32	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491MCSx	Cortex-M4	170	256	112	LQFP80	66	1.71-3.6	10	1	3	1	0	3	32	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491MESx	Cortex-M4	170	512	112	LQFP80	66	1.71-3.6	10	1	3	1	0	3	32	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491VCTx	Cortex-M4	170	256	112	LQFP100	86	1.71-3.6	10	1	3	1	0	3	36	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G491VETx	Cortex-M4	170	512	112	LQFP100	86	1.71-3.6	10	1	3	1	0	3	36	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	N/A	125
STM32G4A1KEUx	Cortex-M4	170	512	112	UFQFPN32	26	1.71-3.6	10	1	3	1	0	3	11	4	4	4	3	2	1[QUAD]	3	2	1	2[FD]	0	1	1	1	YES	YES	YES	125
STM32G4A1CETx	Cortex-M4	170	512	112	LQFP48	38	1.71-3.6	10	1	3	1	0	3	18	4	4	4	3	2	1[QUAD]	3	3	1	2[FD]	0	1	1	1	YES	YES	YES	125
STM32G4A1CEUx	Cortex-M4	170	512	112	UFQFPN48	42	1.71-3.6	10	1	3	1	0	3	19	4	4	4	3	2	1[QUAD]	3	3	1	2[FD]	0	1	1	1	YES	YES	YES	125
STM32G4A1RETx	Cortex-M4	170	512	112	LQFP64	52	1.71-3.6	10	1	3	1	0	3	24	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	YES	125
STM32G4A1REIx	Cortex-M4	170	512	112	UFBGA64	52	1.71-3.6	10	1	3	1	0	3	24	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	YES	125
STM32G4A1REYx	Cortex-M4	170	512	112	WLCSP64	52	1.71-3.6	10	1	3	1	0	3	24	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	YES	125
STM32G4A1METx	Cortex-M4	170	512	112	LQFP80	66	1.71-3.6	10	1	3	1	0	3	32	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	YES	125
STM32G4A1MESx	Cortex-M4	170	512	112	LQFP80	66	1.71-3.6	10	1	3	1	0	3	32	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	12S	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
STM32G4A1VETx	Cortex-M4	170	512	112	LQFP100	86	1.71-3.6	10	1	3	1	0	3	36	4	4	4	3	2	1[QUAD]	3	5	1	2[FD]	0	1	1	1	YES	YES	YES	125
										STM	32G	473 _	_483	Adv	ance	d Li	ne															
STM32G473CBTx	Cortex-M4	170	128	120	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473CCTx	Cortex-M4	170	256	120	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473CETx	Cortex-M4	170	512	120	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473CBUx	Cortex-M4	170	128	120	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473CCUx	Cortex-M4	170	256	120	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473CEUx	Cortex-M4	170	512	120	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473RBTx	Cortex-M4	170	128	120	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	7	7	6	3	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473RCTx	Cortex-M4	170	256	120	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	7	7	6	3	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473RETx	Cortex-M4	170	512	120	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	7	7	6	3	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473MBTx	Cortex-M4	170	128	120	LQFP80	66	1.71-3.6	10	2	3	1	0	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473MCTx	Cortex-M4	170	256	120	LQFP80	66	1.71-3.6	10	2	3	1	0	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473METx	Cortex-M4	170	512	120	LQFP80	66	1.71-3.6	10	2	3	1	0	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G473MEYx	Cortex-M4	170	512	120	WLCSP81	67	1.71-3.6	10	2	3	1	0	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	I2S	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
STM32G473VBTx	Cortex-M4	170	128	120	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473VCTx	Cortex-M4	170	256	120	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473VETx	Cortex-M4	170	512	120	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473VBHx	Cortex-M4	170	128	120	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473VCHx	Cortex-M4	170	256	120	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473VEHx	Cortex-M4	170	512	120	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473PBIx	Cortex-M4	170	128	120	UFBGA121	102	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473PCIx	Cortex-M4	170	256	120	UFBGA121	102	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473PEIx	Cortex-M4	170	512	120	UFBGA121	102	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473QBTx	Cortex-M4	170	128	120	LQFP128	107	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473QCTx	Cortex-M4	170	256	120	LQFP128	107	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G473QETx	Cortex-M4	170	512	120	LQFP128	107	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G483CETx	Cortex-M4	170	512	120	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G483CEUx	Cortex-M4	170	512	120	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G483RETx	Cortex-M4	170	512	120	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	7	7	6	3	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	OPAMP	SPI	128	M-SPI	120	U(S)ART	LPUART	GAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
STM32G483METx	Cortex-M4	170	512	120	LQFP80	66	1.71-3.6	10	2	3	1	0	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G483MEYx	Cortex-M4	170	512	120	WLCSP81	67	1.71-3.6	10	2	3	1	0	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G483VETx	Cortex-M4	170	512	120	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	YES	125
STM32G483VEHx	Cortex-M4	170	512	120	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	YES	125
STM32G483PEIx	Cortex-M4	170	512	120	UFBGA121	102	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	YES	125
STM32G483QETx	Cortex-M4	170	512	120	LQFP128	107	1.71-3.6	10	2	3	1	0	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	YES	125
									STI	//320	473	_484	l Hig	h-re	solu	tion	Time	er														
STM32G474CBTx	Cortex-M4	170	128	128	LQFP48	38	1.71-3.6	10	2	3	1	1	5	20	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474CBUx	Cortex-M4	170	128	128	UFQFPN48	42	1.71-3.6	10	2	3	1	1	5	21	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474CCUx	Cortex-M4	170	256	128	UFQFPN48	42	1.71-3.6	10	2	3	1	1	5	21	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474CETx	Cortex-M4	170	512	128	LQFP48	38	1.71-3.6	10	2	3	1	1	5	20	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474CEUx	Cortex-M4	170	512	128	UFQFPN48	42	1.71-3.6	10	2	3	1	1	5	21	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474RBTx	Cortex-M4	170	128	128	LQFP64	52	1.71-3.6	10	2	3	1	1	5	26	7	7	6	3	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474RCTx	Cortex-M4	170	256	128	LQFP64	52	1.71-3.6	10	2	3	1	1	5	26	7	7	6	3	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474RETx	Cortex-M4	170	512	128	LQFP64	52	1.71-3.6	10	2	3	1	1	5	26	7	7	6	3	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	128	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
STM32G474MBTx	Cortex-M4	170	128	128	LQFP80	66	1.71-3.6	10	2	3	1	1	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474MCTx	Cortex-M4	170	256	128	LQFP80	66	1.71-3.6	10	2	3	1	1	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474METx	Cortex-M4	170	512	128	LQFP80	66	1.71-3.6	10	2	3	1	1	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474MEYx	Cortex-M4	170	512	128	WLCSP81	67	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	N/A	125
STM32G474VBTx	Cortex-M4	170	128	128	LQFP100	86	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474VBHx	Cortex-M4	170	128	128	TFBGA100	86	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474VCTx	Cortex-M4	170	256	128	LQFP100	86	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474VCHx	Cortex-M4	170	256	128	TFBGA100	86	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474VETx	Cortex-M4	170	512	128	LQFP100	86	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474VEHx	Cortex-M4	170	512	128	TFBGA100	86	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474PBIx	Cortex-M4	170	128	128	UFBGA121	102	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474PCIx	Cortex-M4	170	256	128	UFBGA121	102	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474PEIx	Cortex-M4	170	512	128	UFBGA121	102	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474QBTx	Cortex-M4	170	128	128	LQFP128	107	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G474QCTx	Cortex-M4	170	256	128	LQFP128	107	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	I0	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	ОРАМР	SPI	I2S	M-SPI	12C	U(S)ART	LPUART	CAN	F(S)MC	USB FS Device	SAI	UCPD	Math Accelerator	TRNG	AES/DES	T° Max (°C)
STM32G474QETx	Cortex-M4	170	512	128	LQFP128	107	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	N/A	125
STM32G484CETx	Cortex-M4	170	512	128	LQFP48	38	1.71-3.6	10	2	3	1	1	5	20	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G484CEUx	Cortex-M4	170	512	128	UFQFPN48	42	1.71-3.6	10	2	3	1	1	5	21	7	7	6	3	2	1[QUAD]	4	3	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G484RETx	Cortex-M4	170	512	128	LQFP64	52	1.71-3.6	10	2	3	1	1	5	26	7	7	6	3	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G484METx	Cortex-M4	170	512	128	LQFP80	66	1.71-3.6	10	2	3	1	1	5	41	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G484MEYx	Cortex-M4	170	512	128	WLCSP81	67	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	0	1	1	1	YES	YES	YES	125
STM32G484VETx	Cortex-M4	170	512	128	LQFP100	86	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	YES	125
STM32G484VEHx	Cortex-M4	170	512	128	TFBGA100	86	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	YES	125
STM32G484PEIx	Cortex-M4	170	512	128	UFBGA121	102	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	YES	125
STM32G484QETx	Cortex-M4	170	512	128	LQFP128	107	1.71-3.6	10	2	3	1	1	5	42	7	7	6	4	2	1[QUAD]	4	5	1	3[FD]	1	1	1	1	YES	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	I2S	12C	U(S)ART	LPUART	CAN	USB FS HOST/DEVICE	UCPD	TRNG	AES/DES	T° Max (°C)
								5	STM32	2G0x0	Value	Line														
STM32G030J6Mx	Cortex-M0+	64	32	8	S0-8	5	2-3.6	5	0	1	0	1	7	0	0	2	1	2	2	0	0	0	0	N/A	N/A	125
STM32G030F6Px	Cortex-M0+	64	32	8	TSS0P20	17	2-3.6	5	0	1	0	1	16	0	0	2	1	2	2	0	0	0	0	N/A	N/A	125
STM32G030K6Tx	Cortex-M0+	64	32	8	LQFP32	29	2-3.6	5	0	1	0	1	18	0	0	2	1	2	2	0	0	0	0	N/A	N/A	125
STM32G030K8Tx	Cortex-M0+	64	64	8	LQFP32	29	2-3.6	5	0	1	0	1	18	0	0	2	1	2	2	0	0	0	0	N/A	N/A	125
STM32G030C6Tx	Cortex-M0+	64	32	8	LQFP48	43	2-3.6	5	0	1	0	1	19	0	0	2	1	2	2	0	0	0	0	N/A	N/A	125
STM32G030C8Tx	Cortex-M0+	64	64	8	LQFP48	43	2-3.6	5	0	1	0	1	19	0	0	2	1	2	2	0	0	0	0	N/A	N/A	125
STM32G050F6Px	Cortex-M0+	64	32	18	TSS0P20	18	2-3.6	7	0	1	2	1	16	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G050K6Tx	Cortex-M0+	64	32	18	LQFP32	30	2-3.6	7	0	1	2	1	18	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G050K8Tx	Cortex-M0+	64	64	18	LQFP32	30	2-3.6	7	0	1	2	1	18	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G050C6Tx	Cortex-M0+	64	32	18	LQFP48	44	2-3.6	7	0	1	2	1	19	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G050C8Tx	Cortex-M0+	64	64	18	LQFP48	44	2-3.6	7	0	1	2	1	19	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G070KBTx	Cortex-M0+	64	128	36	LQFP32	29	2-3.6	8	0	1	0	1	13	0	0	2	1	2	4	0	0	0	0	N/A	N/A	125
STM32G070CBTx	Cortex-M0+	64	128	36	LQFP48	43	2-3.6	8	0	1	0	1	17	0	0	2	1	2	4	0	0	0	0	N/A	N/A	125
STM32G070RBTx	Cortex-M0+	64	128	36	LQFP64	59	2-3.6	8	0	1	0	1	19	0	0	2	1	2	4	0	0	0	0	N/A	N/A	125
STM32G0B0KETx	Cortex-M0+	64	512	144	LQFP32	29	2-3.6	9	0	1	0	1	13	0	0	3	2	3	6	0	0	0	0	N/A	N/A	125
STM32G0B0CETx	Cortex-M0+	64	512	144	LQFP48	43	2-3.6	9	0	1	0	1	17	0	0	3	2	3	6	0	0	1	0	N/A	N/A	125
STM32G0B0RETx	Cortex-M0+	64	512	144	LQFP64	59	2-3.6	9	0	1	0	1	19	0	0	3	2	3	6	0	0	1	0	N/A	N/A	125
STM32G0B0VETx	Cortex-M0+	64	512	144	LQFP100	93	2-3.6	9	0	1	0	1	19	0	0	3	2	3	6	0	0	1	0	N/A	N/A	125
								STIV	132GO	31_04	1 Acc	ess Li	ne													
STM32G031J4Mx	Cortex-M0+	64	16	8	S0-8	6	1.7-3.6	5	1	1	2	1	8	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	ממע	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	CAN	USB FS HOST/DEVICE	ИСРО	TRNG	AES/DES	T° Max (°C)
STM32G031J6Mx	Cortex-M0+	64	32	8	SO-8	6	1.7-3.6	5	1	1	2	1	8	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031Y8Yx	Cortex-M0+	64	64	8	WLCSP18	16	1.7-3.6	5	1	1	2	1	16	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031F4Px	Cortex-M0+	64	16	8	TSS0P20	18	1.7-3.6	5	1	1	2	1	16	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031F6Px	Cortex-M0+	64	32	8	TSS0P20	18	1.7-3.6	5	1	1	2	1	16	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031F8Px	Cortex-M0+	64	64	8	TSS0P20	18	1.7-3.6	5	1	1	2	1	16	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031G4Ux	Cortex-M0+	64	16	8	UFQFPN28	26	1.7-3.6	5	1	1	2	1	17	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031G6Ux	Cortex-M0+	64	32	8	UFQFPN28	26	1.7-3.6	5	1	1	2	1	17	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031G8Ux	Cortex-M0+	64	64	8	UFQFPN28	26	1.7-3.6	5	1	1	2	1	17	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031K4Tx	Cortex-M0+	64	16	8	LQFP32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031K6Tx	Cortex-M0+	64	32	8	LQFP32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031K8Tx	Cortex-M0+	64	64	8	LQFP32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031K4Ux	Cortex-M0+	64	16	8	UFQFPN32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031K6Ux	Cortex-M0+	64	32	8	UFQFPN32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031K8Ux	Cortex-M0+	64	64	8	UFQFPN32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031C4Tx	Cortex-M0+	64	16	8	LQFP48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031C6Tx	Cortex-M0+	64	32	8	LQFP48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031C8Tx	Cortex-M0+	64	64	8	LQFP48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031C4Ux	Cortex-M0+	64	16	8	UFQFPN48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031C6Ux	Cortex-M0+	64	32	8	UFQFPN48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G031C8Ux	Cortex-M0+	64	64	8	UFQFPN48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G041J6Mx	Cortex-M0+	64	32	8	S0-8	6	1.7-3.6	5	1	1	2	1	8	0	0	2	1	2	2	1	0	0	0	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	CAN	USB FS HOST/DEVICE	UCPD	TRNG	AES/DES	T° Max (°C)
STM32G041F6Px	Cortex-M0+	64	32	8	TSS0P20	18	1.7-3.6	5	1	1	2	1	16	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041F8Px	Cortex-M0+	64	64	8	TSS0P20	18	1.7-3.6	5	1	1	2	1	16	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041Y8Yx	Cortex-M0+	64	64	8	WLCSP18	16	1.7-3.6	5	1	1	2	1	16	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041G6Ux	Cortex-M0+	64	32	8	UFQFPN28	26	1.7-3.6	5	1	1	2	1	17	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041G8Ux	Cortex-M0+	64	64	8	UFQFPN28	26	1.7-3.6	5	1	1	2	1	17	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041K6Tx	Cortex-M0+	64	32	8	LQFP32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041K8Tx	Cortex-M0+	64	64	8	LQFP32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041K8Ux	Cortex-M0+	64	64	8	UFQFPN32	30	1.7-3.6	5	1	1	2	1	18	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041C6Tx	Cortex-M0+	64	32	8	LQFP48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041C8Tx	Cortex-M0+	64	64	8	LQFP48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	YES	YES	125
STM32G041C8Ux	Cortex-M0+	64	64	8	UFQFPN48	44	1.7-3.6	5	1	1	2	1	19	0	0	2	1	2	2	1	0	0	0	YES	YES	125
							STM32G	051_0	i061 [OAC, C	omp, l	Motor	Contr	ol Lin	е											
STM32G051F6Px	Cortex-M0+	64	32	18	TSS0P20	18	1.7-3.6	8	1	1	2	1	16	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051F8Px	Cortex-M0+	64	64	18	TSS0P20	18	1.7-3.6	8	1	1	2	1	16	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051F8Yx	Cortex-M0+	64	64	18	WLCSP20	18	1.7-3.6	8	1	1	2	1	16	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051G6Ux	Cortex-M0+	64	32	18	UFQFPN28	26	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051G8Ux	Cortex-M0+	64	64	18	UFQFPN28	26	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051K6Ux	Cortex-M0+	64	32	18	UFQFPN32	30	1.7-3.6	8	1	1	2	1	18	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051K8Ux	Cortex-M0+	64	64	18	UFQFPN32	30	1.7-3.6	8	1	1	2	1	18	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051K6Tx	Cortex-M0+	64	32	18	LQFP32	30	1.7-3.6	8	1	1	2	1	18	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051K8Tx	Cortex-M0+	64	64	18	LQFP32	30	1.7-3.6	8	1	1	2	1	18	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	I2S	120	U(S)ART	LPUART	CAN	USB FS HOST/DEVICE	ИСРО	TRNG	AES/DES	T° Max (°C)
STM32G051C6Ux	Cortex-M0+	64	32	18	UFQFPN48	44	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051C8Ux	Cortex-M0+	64	64	18	UFQFPN48	44	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051C6Tx	Cortex-M0+	64	32	18	LQFP48	44	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G051C8Tx	Cortex-M0+	64	64	18	LQFP48	44	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	2	1	0	0	0	N/A	N/A	125
STM32G061F6Px	Cortex-M0+	64	32	18	TSS0P20	18	1.7-3.6	8	1	1	2	1	16	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061F8Px	Cortex-M0+	64	64	18	TSS0P20	18	1.7-3.6	8	1	1	2	1	16	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061F8Yx	Cortex-M0+	64	64	18	WLCSP20	18	1.7-3.6	8	1	1	2	1	16	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061G6Ux	Cortex-M0+	64	32	18	UFQFPN28	26	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061G8Ux	Cortex-M0+	64	64	18	UFQFPN28	26	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061K6Ux	Cortex-M0+	64	32	18	UFQFPN32	30	1.7-3.6	8	1	1	2	1	18	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061K8Ux	Cortex-M0+	64	64	18	UFQFPN32	30	1.7-3.6	8	1	1	2	1	18	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061K6Tx	Cortex-M0+	64	32	18	LQFP32	30	1.7-3.6	8	1	1	2	1	18	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061K8Tx	Cortex-M0+	64	64	18	LQFP32	30	1.7-3.6	8	1	1	2	1	18	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061C6Ux	Cortex-M0+	64	32	18	UFQFPN48	44	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061C8Ux	Cortex-M0+	64	64	18	UFQFPN48	44	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061C6Tx	Cortex-M0+	64	32	18	LQFP48	44	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	2	1	0	0	0	YES	YES	125
STM32G061C8Tx	Cortex-M0+	64	64	18	LQFP48	44	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	2	1	0	0	0	YES	YES	125
								STM3	32G07	1_081	Adva	nced l	Line													
STM32G071EBYx	Cortex-M0+	64	128	36	WLCSP25	23	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	1	N/A	N/A	125
STM32G071G8Ux	Cortex-M0+	64	64	36	UFQFPN28	26	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	1	N/A	N/A	125
STM32G071GBUx	Cortex-M0+	64	128	36	UFQFPN28	26	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	1	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	CAN	USB FS HOST/DEVICE	ИСРО	TRNG	AES/DES	T° Max (°C)
STM32G071G8UxN	Cortex-M0+	64	64	36	UFQFPN28	26	1.7-3.6	8	1	1	2	1	11	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071GBUxN	Cortex-M0+	64	128	36	UFQFPN28	26	1.7-3.6	8	1	1	2	1	11	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071K8Tx	Cortex-M0+	64	64	36	LQFP32	30	1.7-3.6	8	1	1	2	1	13	2	2	2	1	2	4	1	0	0	1	N/A	N/A	125
STM32G071K8Ux	Cortex-M0+	64	64	36	UFQFPN32	30	1.7-3.6	8	1	1	2	1	13	2	2	2	1	2	4	1	0	0	1	N/A	N/A	125
STM32G071KBTx	Cortex-M0+	64	128	36	LQFP32	30	1.7-3.6	8	1	1	2	1	13	2	2	2	1	2	4	1	0	0	1	N/A	N/A	125
STM32G071KBUx	Cortex-M0+	64	128	36	UFQFPN32	30	1.7-3.6	8	1	1	2	1	13	2	2	2	1	2	4	1	0	0	1	N/A	N/A	125
STM32G071KBTxN	Cortex-M0+	64	128	36	LQFP32	30	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071KBUxN	Cortex-M0+	64	128	36	UFQFPN32	30	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071C8Tx	Cortex-M0+	64	64	36	LQFP48	44	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071C8Ux	Cortex-M0+	64	64	36	UFQFPN48	44	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071CBTx	Cortex-M0+	64	128	36	LQFP48	44	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071CBUx	Cortex-M0+	64	128	36	UFQFPN48	44	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071R8Tx	Cortex-M0+	64	64	36	LQFP64	60	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071RBTx	Cortex-M0+	64	128	36	LQFP64	60	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G071RBlx	Cortex-M0+	64	128	36	UFBGA64	60	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	4	1	0	0	2	N/A	N/A	125
STM32G081EBYx	Cortex-M0+	64	128	36	WLCSP25	23	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	1	YES	YES	125
STM32G081GBUx	Cortex-M0+	64	128	36	UFQFPN28	26	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	1	YES	YES	125
STM32G081GBUxN	Cortex-M0+	64	128	36	UFQFPN28	26	1.7-3.6	8	1	1	2	1	11	2	2	2	1	2	4	1	0	0	2	YES	YES	125
STM32G081KBTx	Cortex-M0+	64	128	36	LQFP32	30	1.7-3.6	8	1	1	2	1	13	2	2	2	1	2	4	1	0	0	1	YES	YES	125
STM32G081KBTxN	Cortex-M0+	64	128	36	LQFP32	30	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	2	YES	YES	125
STM32G081KBUx	Cortex-M0+	64	128	36	UFQFPN32	30	1.7-3.6	8	1	1	2	1	13	2	2	2	1	2	4	1	0	0	1	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	GAN	USB FS HOST/DEVICE	ИСРО	TRNG	AES/DES	T° Max (°C)
STM32G081KBUxN	Cortex-M0+	64	128	36	UFQFPN32	30	1.7-3.6	8	1	1	2	1	12	2	2	2	1	2	4	1	0	0	2	YES	YES	125
STM32G081CBTx	Cortex-M0+	64	128	36	LQFP48	44	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	4	1	0	0	2	YES	YES	125
STM32G081CBUx	Cortex-M0+	64	128	36	UFQFPN48	44	1.7-3.6	8	1	1	2	1	17	2	2	2	1	2	4	1	0	0	2	YES	YES	125
STM32G081RBTx	Cortex-M0+	64	128	36	LQFP64	60	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	4	1	0	0	2	YES	YES	125
STM32G081RBlx	Cortex-M0+	64	128	36	UFBGA64	60	1.7-3.6	8	1	1	2	1	19	2	2	2	1	2	4	1	0	0	2	YES	YES	125
							STM320	OB1_	0C1 U	SB, FI	CAN,	Perfo	rmanc	e Line	,											
STM32G0B1KBTx	Cortex-M0+	64	128	144	LQFP32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	0	1	N/A	N/A	125
STM32G0B1KCTx	Cortex-M0+	64	256	144	LQFP32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	0	1	N/A	N/A	125
STM32G0B1KETx	Cortex-M0+	64	512	144	LQFP32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	0	1	N/A	N/A	125
STM32G0B1KBTxN	Cortex-M0+	64	128	144	LQFP32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	0	2	N/A	N/A	125
STM32G0B1KCTxN	Cortex-M0+	64	256	144	LQFP32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	0	2	N/A	N/A	125
STM32G0B1KETxN	Cortex-M0+	64	512	144	LQFP32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	0	2	N/A	N/A	125
STM32G0B1KBUx	Cortex-M0+	64	128	144	UFQFPN32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	0	1	N/A	N/A	125
STM32G0B1KCUx	Cortex-M0+	64	256	144	UFQFPN32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	0	1	N/A	N/A	125
STM32G0B1KEUx	Cortex-M0+	64	512	144	UFQFPN32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	0	1	N/A	N/A	125
STM32G0B1KBUxN	Cortex-M0+	64	128	144	UFQFPN32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	0	2	N/A	N/A	125
STM32G0B1KCUxN	Cortex-M0+	64	256	144	UFQFPN32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	0	2	N/A	N/A	125
STM32G0B1KEUxN	Cortex-M0+	64	512	144	UFQFPN32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	0	2	N/A	N/A	125
STM32G0B1CBUx	Cortex-M0+	64	128	144	UFQFPN48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125
STM32G0B1CCUx	Cortex-M0+	64	256	144	UFQFPN48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125
STM32G0B1CEUx	Cortex-M0+	64	512	144	UFQFPN48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	128	12C	U(S)ART	LPUART	CAN	USB FS HOST/DEVICE	UCPD	TRNG	AES/DES	T° Max (°C)
STM32G0B1CBUxN	Cortex-M0+	64	128	144	UFQFPN48	42	1.7-3.6	9	1	1	2	1	15	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1CCUxN	Cortex-M0+	64	256	144	UFQFPN48	42	1.7-3.6	9	1	1	2	1	15	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1CEUxN	Cortex-M0+	64	512	144	UFQFPN48	42	1.7-3.6	9	1	1	2	1	15	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1CBTx	Cortex-M0+	64	128	144	LQFP48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125
STM32G0B1CCTx	Cortex-M0+	64	256	144	LQFP48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125
STM32G0B1CETx	Cortex-M0+	64	512	144	LQFP48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125
STM32G0B1CBTxN	Cortex-M0+	64	128	144	LQFP48	42	1.7-3.6	9	1	1	2	1	15	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1CETxN	Cortex-M0+	64	512	144	LQFP48	42	1.7-3.6	9	1	1	2	1	15	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1NEYx	Cortex-M0+	64	512	144	WLCSP52	46	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1RBTx	Cortex-M0+	64	128	144	LQFP64	60	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125
STM32G0B1RCTx	Cortex-M0+	64	256	144	LQFP64	60	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125
STM32G0B1RETx	Cortex-M0+	64	512	144	LQFP64	60	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	1	N/A	N/A	125
STM32G0B1RBTxN	Cortex-M0+	64	128	144	LQFP64	58	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1RCTxN	Cortex-M0+	64	256	144	LQFP64	58	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1RETxN	Cortex-M0+	64	512	144	LQFP64	58	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1RBIxN	Cortex-M0+	64	128	144	LQFP64	58	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1RClxN	Cortex-M0+	64	256	144	LQFP64	58	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1REIxN	Cortex-M0+	64	512	144	LQFP64	58	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1MBTx	Cortex-M0+	64	128	144	LQFP80	74	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1MCTx	Cortex-M0+	64	256	144	LQFP80	74	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1METx	Cortex-M0+	64	512	144	LQFP80	74	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	CAN	USB FS HOST/DEVICE	ИСРО	TRNG	AES/DES	T° Max (°C)
STM32G0B1VBTx	Cortex-M0+	64	128	144	LQFP100	94	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1VCTx	Cortex-M0+	64	256	144	LQFP100	94	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0B1VETx	Cortex-M0+	64	512	144	LQFP100	94	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	N/A	N/A	125
STM32G0C1KETx	Cortex-M0+	64	512	144	LQFP32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	1	1	YES	YES	125
STM32G0C1KCTxN	Cortex-M0+	64	256	144	LQFP32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125
STM32G0C1KETxN	Cortex-M0+	64	512	144	LQFP32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125
STM32G0C1KCUx	Cortex-M0+	64	256	144	UFQFPN32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	1	1	YES	YES	125
STM32G0C1KEUx	Cortex-M0+	64	512	144	UFQFPN32	30	1.7-3.6	9	1	1	2	1	13	2	3	3	2	3	6	2	2[FD]	1	1	YES	YES	125
STM32G0C1KCUxN	Cortex-M0+	64	256	144	UFQFPN32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125
STM32G0C1KEUxN	Cortex-M0+	64	512	144	UFQFPN32	29	1.7-3.6	9	1	1	2	1	12	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125
STM32G0C1CCUx	Cortex-M0+	64	256	144	UFQFPN48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	YES	YES	125
STM32G0C1CEUx	Cortex-M0+	64	512	144	UFQFPN48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	YES	YES	125
STM32G0C1CETx	Cortex-M0+	64	512	144	LQFP48	44	1.7-3.6	9	1	1	2	1	17	2	3	3	2	3	6	2	2[FD]	1	1	YES	YES	125
STM32G0C1RCTx	Cortex-M0+	64	256	144	LQFP64	60	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	1	YES	YES	125
STM32G0C1RETx	Cortex-M0+	64	512	144	LQFP64	60	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125
STM32G0C1MCTx	Cortex-M0+	64	256	144	LQFP80	74	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125
STM32G0C1METx	Cortex-M0+	64	512	144	LQFP80	74	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125
STM32G0C1VCTx	Cortex-M0+	64	256	144	LQFP100	94	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125
STM32G0C1VETx	Cortex-M0+	64	512	144	LQFP100	94	1.7-3.6	9	1	1	2	1	19	2	3	3	2	3	6	2	2[FD]	1	2	YES	YES	125

STM32 CO系列 – Arm Cortex-M0+超值入门型MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	l2S	120	U(S)ART	LPUART	GAN	USB FS HOST/DEVICE	ИСРО	TRNG	AES/DES	T° Max (°C)
STM32C011J4Mx	Cortex-M0+	48	16	6	SO-8	6	2.0-3.6	5	0	1	0	1	7	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C011J6Mx	Cortex-M0+	48	32	6	SO-8	6	2.0-3.6	5	0	1	0	1	7	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C011D6Yx	Cortex-M0+	48	32	6	WLCSP12	10	2.0-3.6	5	0	1	0	1	9	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C011F4Px	Cortex-M0+	48	16	6	TSS0P20	18	2.0-3.6	5	0	1	0	1	15	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C011F6Px	Cortex-M0+	48	32	6	TSS0P20	18	2.0-3.6	5	0	1	0	1	15	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C011F4Ux	Cortex-M0+	48	16	6	UFQFPN20	18	2.0-3.6	5	0	1	0	1	15	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C011F6Ux	Cortex-M0+	48	32	6	UFQFPN20	18	2.0-3.6	5	0	1	0	1	15	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031F4Px	Cortex-M0+	48	16	12	TSS0P20	18	2.0-3.6	5	0	1	0	1	15	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031F6Px	Cortex-M0+	48	32	12	TSS0P20	18	2.0-3.6	5	0	1	0	1	15	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031G4Ux	Cortex-M0+	48	16	12	UFQFPN28	26	2.0-3.6	5	0	1	0	1	17	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031G6Ux	Cortex-M0+	48	32	12	UFQFPN28	26	2.0-3.6	5	0	1	0	1	17	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031K4Tx	Cortex-M0+	48	16	12	LQFP32	30	2.0-3.6	5	0	1	0	1	18	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031K6Tx	Cortex-M0+	48	32	12	LQFP32	30	2.0-3.6	5	0	1	0	1	18	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031K4Ux	Cortex-M0+	48	16	12	UFQFPN32	30	2.0-3.6	5	0	1	0	1	18	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031K6Ux	Cortex-M0+	48	32	12	UFQFPN32	30	2.0-3.6	5	0	1	0	1	18	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031C4Tx	Cortex-M0+	48	16	12	LQFP48	45	2.0-3.6	5	0	1	0	1	21	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031C6Tx	Cortex-M0+	48	32	12	LQFP48	45	2.0-3.6	5	0	1	0	1	21	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031C4Ux	Cortex-M0+	48	16	12	UFQFPN48	45	2.0-3.6	5	0	1	0	1	21	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125
STM32C031C6Ux	Cortex-M0+	48	32	12	UFQFPN48	45	2.0-3.6	5	0	1	0	1	21	0	0	1	1	1	2	0	0	0	0	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (Bytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	12S	120	U(S)ART	CAN	F(S)MC	USB Device	T° Max (°C)
									STM32	F301	Acce	ss Lin	е														
STM32F301K6Tx	Cortex-M4	72	32	16	0	LQFPN32	25	2-3.6	5	1	1	0	1	8	0	0	1	2	1	2	2	3	2	0	0	0	105
STM32F301K6Ux	Cortex-M4	72	32	16	0	UFQFPN32	24	2-3.6	5	1	1	0	1	8	0	0	1	2	1	2	2	3	2	0	0	0	105
STM32F301K8Tx	Cortex-M4	72	64	16	0	LQFPN32	25	2-3.6	5	1	1	0	1	8	0	0	1	2	1	2	2	3	2	0	0	0	105
STM32F301K8Ux	Cortex-M4	72	64	16	0	UFQFPN32	24	2-3.6	5	1	1	0	1	8	0	0	1	2	1	2	2	3	2	0	0	0	105
STM32F301C6Tx	Cortex-M4	72	32	16	0	LQFP48	37	2-3.6	5	1	1	0	1	11	0	0	1	3	1	2	2	3	3	0	0	0	105
STM32F301C8Tx	Cortex-M4	72	64	16	0	LQFP48	37	2-3.6	5	1	1	0	1	11	0	0	1	3	1	2	2	3	3	0	0	0	105
STM32F301C8Yx	Cortex-M4	72	64	16	0	WLCSP49	37	2-3.6	5	1	1	0	1	11	0	0	1	3	1	2	2	3	3	0	0	0	105
STM32F301R6T6	Cortex-M4	72	32	16	0	LQFP64	51	2-3.6	5	1	1	0	1	15	0	0	1	3	1	2	2	3	3	0	0	0	85
STM32F301R8T6	Cortex-M4	72	64	16	0	LQFP64	51	2-3.6	5	1	1	0	1	15	0	0	1	3	1	2	2	3	3	0	0	0	85
								ST	M32F	302 U	SB & (CAN L	ine														
STM32F302K6U6	Cortex-M4	72	32	16	0	UFQFPN32	24	2-3.6	5	1	1	0	1	8	0	0	1	2	1	2	2	3	2	1	0	1	85
STM32F302K8Ux	Cortex-M4	72	64	16	0	UFQFPN32	24	2-3.6	5	1	1	0	1	8	0	0	1	2	1	2	2	3	2	1	0	1	105
STM32F302C6T6	Cortex-M4	72	32	16	0	LQFP48	37	2-3.6	5	1	1	0	1	11	0	0	1	3	1	2	2	3	3	1	0	1	85
STM32F302C8Tx	Cortex-M4	72	64	16	0	LQFP48	37	2-3.6	5	1	1	0	1	11	0	0	1	3	1	2	2	3	3	1	0	1	105
STM32F302C8Yx	Cortex-M4	72	64	16	0	WLCSP49	37	2-3.6	5	1	1	0	1	11	0	0	1	3	1	2	2	3	3	1	0	1	105
STM32F302R6T6	Cortex-M4	72	32	16	0	LQFP64	51	2-3.6	5	1	1	0	1	15	0	0	1	3	1	2	2	3	3	1	0	1	85
STM32F302R8Tx	Cortex-M4	72	64	16	0	LQFP64	51	2-3.6	5	1	1	0	1	15	0	0	1	3	1	2	2	3	3	1	0	1	105
STM32F302CBTx	Cortex-M4	72	128	32	0	LQFP48	37	2-3.6	7	1	1	0	2	9	0	0	1	4	2	3	2	2	3	1	0	1	105
STM32F302CCTx	Cortex-M4	72	256	40	0	LQFP48	37	2-3.6	7	1	1	0	2	9	0	0	1	4	2	3	2	2	3	1	0	1	105

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (Bytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	12S	120	U(S)ART	CAN	F(S)MC	USB Device	T° Max (°C)
STM32F302RBTx	Cortex-M4	72	128	32	0	LQFP64	52	2-3.6	7	1	1	0	2	16	0	0	1	4	2	3	2	2	5	1	0	1	105
STM32F302RCTx	Cortex-M4	72	256	40	0	LQFP64	52	2-3.6	7	1	1	0	2	16	0	0	1	4	2	3	2	2	5	1	0	1	105
STM32F302VBT6	Cortex-M4	72	128	32	0	LQFP100	87	2-3.6	7	1	1	0	2	17	0	0	1	4	2	3	2	2	5	1	0	1	85
STM32F302VCTx	Cortex-M4	72	256	40	0	LQFP100	87	2-3.6	7	1	1	0	2	17	0	0	1	4	2	3	2	2	5	1	0	1	105
STM32F302VCYx	Cortex-M4	72	256	40	0	WLCSP100	77	2-3.6	1	1	1	0	2	17	0	0	1	4	2	3	2	2	5	1	0	1	105
STM32F302RDTx	Cortex-M4	72	384	64	0	LQFP64	51	2-3.6	7	1	1	0	2	16	0	0	1	4	2	4	2	3	5	1	0	1	105
STM32F302RETx	Cortex-M4	72	512	64	0	LQFP64	51	2-3.6	7	1	1	0	2	16	0	0	1	4	2	4	2	3	5	1	0	1	105
STM32F302VDT6	Cortex-M4	72	384	64	0	LQFP100	82	2-3.6	7	1	1	0	2	17	0	0	1	4	2	4	2	3	5	1	1	1	85
STM32F302VDH6	Cortex-M4	72	384	64	0	UFBGA100	84	2-3.6	7	1	1	0	2	17	0	0	1	4	2	4	2	3	5	1	1	1	85
STM32F302VET6	Cortex-M4	72	512	64	0	LQFP100	82	2-3.6	7	1	1	0	2	17	0	0	1	4	2	4	2	3	5	1	1	1	85
STM32F302VEH6	Cortex-M4	72	512	64	0	UFBGA100	84	2-3.6	7	1	1	0	2	17	0	0	1	4	2	4	2	3	5	1	1	1	85
STM32F302ZDT6	Cortex-M4	72	384	64	0	LQFP144	115	2-3.6	7	1	1	0	2	18	0	0	1	4	2	4	2	3	5	1	1	1	85
STM32F302ZETx	Cortex-M4	72	512	64	0	LQFP144	115	2-3.6	7	1	1	0	2	18	0	0	1	4	2	4	2	3	5	1	1	1	105
								STI	M32F3	03 Pe	rfoma	ance l	ine				ı	l					l				
STM32F303K6T6	Cortex-M4	72	32	16	0	LQFP32	25	2-3.6	7	1	1	0	2	9	0	0	3	2	1	1	0	1	2	1	0	0	85
STM32F303K8T6	Cortex-M4	72	64	16	0	LQFP32	25	2-3.6	7	1	1	0	2	9	0	0	3	2	1	1	0	1	2	1	0	0	85
STM32F303C6T6	Cortex-M4	72	32	16	0	LQFP48	37	2-3.6	7	1	1	0	2	15	0	0	3	3	1	1	0	1	3	1	0	0	85
STM32F303C8T6	Cortex-M4	72	64	16	0	LQFP48	37	2-3.6	7	1	1	0	2	15	0	0	3	3	1	1	0	1	3	1	0	0	85
STM32F303C8Y6	Cortex-M4	72	64	16	0	WLCSP49	38	2-3.6	7	1	1	0	2	15	0	0	3	3	1	1	0	1	3	1	0	0	85
STM32F303R6T6	Cortex-M4	72	32	16	0	LQFP64	51	2-3.6	7	1	1	0	2	21	0	0	3	3	1	1	0	1	3	1	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (Bytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	12S	120	U(S)ART	CAN	F(S)MC	USB Device	T° Max (°C)
STM32F303R8Tx	Cortex-M4	72	64	16	0	LQFP64	51	2-3.6	7	1	1	0	2	21	0	0	3	3	1	1	0	1	3	1	0	0	105
STM32F303CBTx	Cortex-M4	72	128	40	0	LQFP48	37	2-3.6	9	1	2	0	4	15	0	0	2	7	4	3	2	2	3	1	0	1	105
STM32F303CCTx	Cortex-M4	72	256	48	0	LQFP48	37	2-3.6	9	1	2	0	4	15	0	0	2	7	4	3	2	2	3	1	0	1	105
STM32F303RBTx	Cortex-M4	72	128	40	0	LQFP64	52	2-3.6	9	1	2	0	4	22	0	0	2	7	4	3	2	2	5	1	0	1	105
STM32F303RCTx	Cortex-M4	72	256	48	0	LQFP64	52	2-3.6	9	1	2	0	4	22	0	0	2	7	4	3	2	2	5	1	0	1	105
STM32F303VBTx	Cortex-M4	72	128	40	0	LQFP100	87	2-3.6	9	1	2	0	4	39	0	0	2	7	4	3	2	2	5	1	0	1	105
STM32F303VCT6	Cortex-M4	72	256	48	0	LQFP100	87	2-3.6	9	1	2	0	4	39	0	0	2	7	4	3	2	2	5	1	0	1	85
STM32F303VCYx	Cortex-M4	72	256	48	0	WLCSP100	77	2-3.6	9	1	2	0	4	32	0	0	2	7	4	3	2	2	5	1	0	1	105
STM32F303RDTx	Cortex-M4	72	384	80	0	LQFP64	51	2-3.6	9	1	2	0	4	22	0	0	2	7	4	4	2	3	5	1	0	1	105
STM32F303RETx	Cortex-M4	72	512	80	0	LQFP64	51	2-3.6	9	1	2	0	4	22	0	0	2	7	4	4	2	3	5	1	0	1	105
STM32F303VDT6	Cortex-M4	72	384	80	0	LQFP100	82	2-3.6	10	1	3	0	4	39	0	0	2	7	4	4	2	3	5	1	1	1	85
STM32F303VDH6	Cortex-M4	72	384	80	0	UFBGA100	84	2-3.6	10	1	3	0	4	39	0	0	2	7	4	4	2	3	5	1	1	1	85
STM32F303VET6	Cortex-M4	72	512	80	0	LQFP100	82	2-3.6	10	1	3	0	4	39	0	0	2	7	4	4	2	3	5	1	1	1	85
STM32F303VEH6	Cortex-M4	72	512	80	0	UFBGA100	84	2-3.6	10	1	3	0	4	39	0	0	2	7	4	4	2	3	5	1	1	1	85
STM32F303VEYx	Cortex-M4	72	512	80	0	WLCSP100	77	2-3.6	10	1	3	0	4	33	0	0	2	7	4	4	2	3	5	1	1	1	105
STM32F303ZDTx	Cortex-M4	72	384	80	0	LQFP144	115	2-3.6	10	1	3	0	4	40	0	0	2	7	4	4	2	3	5	1	1	1	105
STM32F303ZETx	Cortex-M4	72	512	80	0	LQFP144	115	2-3.6	10	1	3	0	4	40	0	0	2	7	4	4	2	3	5	1	1	1	105
	STM	32F33	4 Digit	tal pov	ver Li	ne - With hig	h-reso	lution tim	er (21	7 pico	secoi	nds) a	nd co	mplex	wave	eform	build	er plu	s eve	nt har	ıdler (HRTII	VI)				
STM32F334K4T6	Cortex-M4	72	16	16	0	LQFP32	25	2-3.6	7	1	1	1	2	9	0	0	3	2	1	1	0	1	2	1	0	0	85
STM32F334K6Tx	Cortex-M4	72	32	16	0	LQFP32	25	2-3.6	7	1	1	1	2	9	0	0	3	2	1	1	0	1	2	1	0	0	105

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (Bytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	12S	120	U(S)ART	CAN	F(S)MC	USB Device	T° Max (°C)
STM32F334K8T6	Cortex-M4	72	64	16	0	LQFP32	25	2-3.6	7	1	1	1	2	9	0	0	3	2	1	1	0	1	2	1	0	0	85
STM32F334K8Ux	Cortex-M4	72	64	16	0	UFQFPN32	24	2-3.6	7	1	1	1	2	9	0	0	3	2	1	1	0	1	2	1	0	0	105
STM32F334C4T6	Cortex-M4	72	16	16	0	LQFP48	37	2-3.6	7	1	1	1	2	15	0	0	3	3	1	1	0	1	3	1	0	0	85
STM32F334C6Tx	Cortex-M4	72	32	16	0	LQFP48	37	2-3.6	7	1	1	1	2	15	0	0	3	3	1	1	0	1	3	1	0	0	105
STM32F334C8Yx	Cortex-M4	72	64	16	0	WLCSP49	38	2-3.6	7	1	1	1	2	15	0	0	3	3	1	1	0	1	3	1	0	0	105
STM32F334C8Tx	Cortex-M4	72	64	16	0	LQFP48	37	2-3.6	7	1	1	1	2	15	0	0	3	3	1	1	0	1	3	1	0	0	105
STM32F334R6T6	Cortex-M4	72	32	16	0	LQFP64	51	2-3.6	7	1	1	1	2	21	0	0	3	3	1	1	0	1	3	1	0	0	85
STM32F334R8Tx	Cortex-M4	72	64	16	0	LQFP64	51	2-3.6	7	1	1	1	2	21	0	0	3	3	1	1	0	1	3	1	0	0	105
				ST	M32F	373 high-pre	cision	measurer	nent L	ine - '	With 1	16-bit	sigma	a-delt	a ADC	and 7	7 buil	t-in ga	ains								
STM32F373C8T6	Cortex-M4	72	64	16	0	LQFP48	36	2-3.6	12	2	0	0	1	9	3	8	3	2	0	3	3	2	3	1	0	1	85
STM32F373CBTx	Cortex-M4	72	128	24	0	LQFP48	36	2-3.6	12	2	0	0	1	9	3	8	3	2	0	3	3	2	3	1	0	1	105
STM32F373CCTx	Cortex-M4	72	256	32	0	LQFP48	36	2-3.6	12	2	0	0	1	9	3	8	3	2	0	3	3	2	3	1	0	1	105
STM32F373R8T6	Cortex-M4	72	64	16	0	LQFP64	52	2-3.6	12	2	0	0	1	16	3	8	3	2	0	3	3	2	3	1	0	1	85
STM32F373RBTx	Cortex-M4	72	128	24	0	LQFP64	52	2-3.6	12	2	0	0	1	16	3	8	3	2	0	3	3	2	3	1	0	1	105
STM32F373RCT6	Cortex-M4	72	256	32	0	LQFP64	52	2-3.6	12	2	0	0	1	16	3	8	3	2	0	3	3	2	3	1	0	1	85
STM32F373V8T6	Cortex-M4	72	64	16	0	LQFP100	84	2-3.6	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	1	85
STM32F373V8H6	Cortex-M4	72	64	16	0	UFBGA100	84	2-3.6	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	1	85
STM32F373VBT6	Cortex-M4	72	128	24	0	LQFP100	84	2-3.6	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	1	85
STM32F373VBHx	Cortex-M4	72	128	24	0	UFBGA100	84	2-3.6	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	1	105
STM32F373VCT6	Cortex-M4	72	256	32	0	LQFP100	84	2-3.6	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	1	85
STM32F373VCHx	Cortex-M4	72	256	32	0	UFBGA100	84	2-3.6	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	1	105

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (Bytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	l2S	120	U(S)ART	CAN	F(S)MC	USB Device	T° Max (°C)
								STM32F3	k8 Lo	w-vol	tage L	ine -	1.8V :	⊧8 %													
STM32F318K8Ux	Cortex-M4	72	64	16	0	UFQFPN32	23	1.65-1.95	5	1	1	0	1	8	0	0	1	2	1	2	2	3	2	0	0	0	105
STM32F318C8Y6	Cortex-M4	72	64	16	0	WLCSP49	36	1.65-1.95	5	1	1	0	1	11	0	0	1	3	1	2	2	3	3	0	0	0	85
STM32F318C8T6	Cortex-M4	72	64	16	0	LQFP48	36	1.65-1.95	5	1	1	0	1	11	0	0	1	3	1	2	2	3	3	0	0	0	85
STM32F328C8T6	Cortex-M4	72	64	16	0	LQFP48	36	1.65-1.95	7	1	1	0	2	14	0	0	3	3	1	1	0	1	3	1	0	0	85
STM32F358CCT6	Cortex-M4	72	256	48	0	LQFP48	36	1.65-1.95	9	1	2	0	4	14	0	0	2	7	4	3	2	2	5	1	0	0	85
STM32F358RCT6	Cortex-M4	72	256	48	0	LQFP64	51	1.65-1.95	9	1	2	0	4	21	0	0	2	7	4	3	2	2	5	1	0	0	85
STM32F358VCT6	Cortex-M4	72	256	48	0	LQFP100	86	1.65-1.95	9	1	2	0	4	38	0	0	2	7	4	3	2	2	5	1	0	0	85
STM32F378CCT6	Cortex-M4	72	256	32	0	LQFP48	36	1.65-1.95	12	2	0	0	1	9	3	21	3	2	0	3	3	2	3	1	0	0	85
STM32F378RCT6	Cortex-M4	72	256	32	0	LQFP64	51	1.65-1.95	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	0	85
STM32F378RCY6	Cortex-M4	72	256	32	0	WLCSP66	51	1.65-1.95	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	0	85
STM32F378VCT6	Cortex-M4	72	256	32	0	LQFP100	83	1.65-1.95	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	0	85
STM32F378VCH6	Cortex-M4	72	256	32	0	UFBGA100	83	1.65-1.95	12	2	0	0	1	16	3	21	3	2	0	3	3	2	3	1	0	0	85
STM32F398VET6	Cortex-M4	72	512	80	0	LQFP100	85	1.65-1.95	10	1	3	0	4	38	0	0	2	7	4	4	2	3	5	1	1	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	12S	12C	U(S)ART	CAN	SDI0	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	Segment LCD	T° Max (°C)
					STM32F	00 Val	ue line – 2	4 MHz	CPU w	rith mo	tor cor	itrol a	nd CE	C func	tions									
STM32F100C4T6	Cortex-M3	24	16	4	LQFP48	37	2-3.6	5	1	1	10	2	1	0	1	2	0	0	0	0	0	0	0	85
STM32F100C6T6	Cortex-M3	24	32	4	LQFP48	37	2-3.6	5	1	1	10	2	1	0	1	2	0	0	0	0	0	0	0	85
STM32F100C8T6	Cortex-M3	24	64	8	LQFP48	37	2-3.6	6	1	1	10	2	2	0	2	3	0	0	0	0	0	0	0	85
STM32F100CBT6	Cortex-M3	24	128	8	LQFP48	37	2-3.6	6	1	1	10	2	2	0	2	3	0	0	0	0	0	0	0	85
STM32F100R4T6	Cortex-M3	24	16	4	LQFP64	51	2-3.6	5	1	1	16	2	1	0	1	2	0	0	0	0	0	0	0	85
STM32F100R4H6	Cortex-M3	24	16	4	TFBGA64	51	2-3.6	5	1	1	16	2	1	0	1	2	0	0	0	0	0	0	0	85
STM32F100R6T6	Cortex-M3	24	32	4	LQFP64	51	2-3.6	5	1	1	16	2	1	0	1	2	0	0	0	0	0	0	0	85
STM32F100R6H6	Cortex-M3	24	32	4	TFBGA64	51	2-3.6	5	1	1	16	2	1	0	1	2	0	0	0	0	0	0	0	85
STM32F100R8T6	Cortex-M3	24	64	8	LQFP64	51	2-3.6	6	1	1	16	2	2	0	2	3	0	0	0	0	0	0	0	85
STM32F100R8H6	Cortex-M3	24	64	8	TFBGA64	51	2-3.6	6	1	1	16	2	2	0	2	3	0	0	0	0	0	0	0	85
STM32F100RBT6	Cortex-M3	24	128	8	LQFP64	51	2-3.6	6	1	1	16	2	2	0	2	3	0	0	0	0	0	0	0	85
STM32F100RBH6	Cortex-M3	24	128	8	TFBGA64	51	2-3.6	6	1	1	16	2	2	0	2	3	0	0	0	0	0	0	0	85
STM32F100V8T6	Cortex-M3	24	64	8	LQFP100	80	2-3.6	6	1	1	16	2	2	0	2	3	0	0	0	0	0	0	0	85
STM32F100VBT6	Cortex-M3	24	128	8	LQFP100	80	2-3.6	6	1	1	16	2	2	0	2	3	0	0	0	0	0	0	0	85
STM32F100RCT6	Cortex-M3	24	256	24	LQFP64	51	2-3.6	11	1	1	16	2	3	0	2	5	0	0	0	0	0	0	0	85
STM32F100RDT6	Cortex-M3	24	384	32	LQFP64	51	2-3.6	11	1	1	16	2	3	0	2	5	0	0	0	0	0	0	0	85
STM32F100RET6	Cortex-M3	24	512	32	LQFP64	51	2-3.6	11	1	1	16	2	3	0	2	5	0	0	0	0	0	0	0	85
STM32F100VCT6	Cortex-M3	24	256	24	LQFP100	80	2-3.6	11	1	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F100VDT6	Cortex-M3	24	384	32	LQFP100	80	2-3.6	11	1	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F100VET6	Cortex-M3	24	512	32	LQFP100	80	2-3.6	11	1	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	120	U(S)ART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	Segment LCD	T° Max (°C)
STM32F100ZCT6	Cortex-M3	24	256	24	LQFP144	112	2-3.6	11	1	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F100ZDT6	Cortex-M3	24	384	32	LQFP144	112	2-3.6	11	1	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F100ZET6	Cortex-M3	24	512	32	LQFP144	112	2-3.6	11	1	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
						STI	/132F101 -	- 36 MI	iz CPU	, up to	1 Mby	te of F	lash											
STM32F101T4U6	Cortex-M3	36	16	4	VFQFPN36	26	2-3.6	2	0	1	10	0	1	0	1	2	0	0	0	0	0	0	0	85
STM32F101T6U6	Cortex-M3	36	32	6	VFQFPN36	26	2-3.6	2	0	1	10	0	1	0	1	2	0	0	0	0	0	0	0	85
STM32F101C4T6	Cortex-M4	36	16	4	LQFP48	37	2-3.6	2	0	1	10	0	1	0	1	2	0	0	0	0	0	0	0	85
STM32F101C6T6	Cortex-M3	36	32	6	LQFP48	37	2-3.6	2	0	1	10	0	1	0	1	2	0	0	0	0	0	0	0	85
STM32F101R4T6	Cortex-M3	36	16	4	LQFP64	51	2-3.6	2	0	1	10	0	1	0	1	2	0	0	0	0	0	0	0	85
STM32F101R6T6	Cortex-M3	36	32	6	LQFP64	51	2-3.6	2	0	1	10	0	1	0	1	2	0	0	0	0	0	0	0	85
STM32F101T8U6	Cortex-M3	36	64	10	VFQFPN36	26	2-3.6	3	0	1	10	0	1	0	1	2	0	0	0	0	0	0	0	85
STM32F101TBU6	Cortex-M3	36	128	16	VFQFPN36	26	2-3.6	3	0	1	10	0	1	0	1	2	0	0	0	0	0	0	0	85
STM32F101C8T6	Cortex-M3	36	64	10	LQFP48	37	2-3.6	3	0	1	10	0	2	0	2	3	0	0	0	0	0	0	0	85
STM32F101CBT6	Cortex-M3	36	128	16	LQFP48	37	2-3.6	3	0	1	10	0	2	0	2	3	0	0	0	0	0	0	0	85
STM32F101C8U6	Cortex-M3	36	64	10	UFQFPN48	37	2-3.6	3	0	1	10	0	2	0	2	3	0	0	0	0	0	0	0	85
STM32F101CBU6	Cortex-M3	36	128	16	UFQFPN48	37	2-3.6	3	0	1	10	0	2	0	2	3	0	0	0	0	0	0	0	85
STM32F101R8T6	Cortex-M3	36	64	10	LQFP64	51	2-3.6	3	0	1	16	0	2	0	2	3	0	0	0	0	0	0	0	85
STM32F101RBH6	Cortex-M4	36	128	16	TFBGA64	50	2-3.6	3	0	1	16	0	2	0	2	2	0	0	0	0	0	0	1	85
STM32F101RBT6	Cortex-M3	36	128	16	LQFP64	51	2-3.6	3	0	1	16	0	2	0	2	3	0	0	0	0	0	0	0	85
STM32F101V8T6	Cortex-M3	36	64	10	LQFP100	80	2-3.6	3	0	1	16	0	2	0	2	3	0	0	0	0	0	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	120	U(S)ART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	Segment LCD	T° Max (°C)
STM32F101VBT6	Cortex-M3	36	128	16	LQFP100	80	2-3.6	3	0	1	16	0	2	0	2	3	0	0	0	0	0	0	0	85
STM32F101RCT6	Cortex-M3	36	256	32	LQFP64	51	2-3.6	6	0	1	16	2	3	0	2	5	0	0	0	0	0	0	0	85
STM32F101RDT6	Cortex-M3	36	384	48	LQFP64	51	2-3.6	6	0	1	16	2	3	0	2	5	0	0	0	0	0	0	0	85
STM32F101RET6	Cortex-M3	36	512	48	LQFP64	51	2-3.6	6	0	1	16	2	3	0	2	5	0	0	0	0	0	0	0	85
STM32F101VCT6	Cortex-M3	36	256	32	LQFP100	80	2-3.6	6	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101VDT6	Cortex-M3	36	384	48	LQFP100	80	2-3.6	6	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101VET6	Cortex-M3	36	512	48	LQFP100	80	2-3.6	6	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101ZCT6	Cortex-M3	36	256	32	LQFP144	112	2-3.6	6	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101ZDT6	Cortex-M3	36	384	48	LQFP144	112	2-3.6	6	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101ZET6	Cortex-M3	36	512	48	LQFP144	112	2-3.6	6	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101RFT6	Cortex-M3	36	768	80	LQFP64	51	2-3.6	12	0	1	16	2	3	0	2	5	0	0	0	0	0	0	0	85
STM32F101RGT6	Cortex-M3	36	1024	80	LQFP64	51	2-3.6	12	0	1	16	2	3	0	2	5	0	0	0	0	0	0	0	85
STM32F101VFT6	Cortex-M3	36	768	80	LQFP100	80	2-3.6	12	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101VGT6	Cortex-M3	36	1024	80	LQFP100	80	2-3.6	12	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101ZFT6	Cortex-M3	36	768	80	LQFP144	112	2-3.6	12	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
STM32F101ZGT6	Cortex-M3	36	1024	80	LQFP144	112	2-3.6	12	0	1	16	2	3	0	2	5	0	0	1	0	0	0	0	85
							STM32F	102 –	48 MH:	z CPU	with US	B FS												
STM32F102C4T6	Cortex-M3	48	16	4	LQFP48	37	2-3.6	2	0	1	10	0	1	0	1	2	0	0	0	1	0	0	0	85
STM32F102C6T6	Cortex-M3	48	32	6	LQFP48	37	2-3.6	2	0	1	10	0	1	0	1	2	0	0	0	1	0	0	0	85
STM32F102R4T6	Cortex-M3	48	16	4	LQFP64	51	2-3.6	2	0	1	16	0	1	0	1	2	0	0	0	1	0	0	0	85
STM32F102R6T6	Cortex-M3	48	32	6	LQFP64	51	2-3.6	2	0	1	16	0	1	0	1	2	0	0	0	1	0	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	12S	120	U(S)ART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	Segment LCD	T° Max (°C)
STM32F102C8T6	Cortex-M3	48	64	10	LQFP48	37	2-3.6	3	0	1	10	0	2	0	2	3	0	0	0	1	0	0	0	85
STM32F102CBT6	Cortex-M3	48	128	16	LQFP48	37	2-3.6	3	0	1	10	0	2	0	2	3	0	0	0	1	0	0	0	85
STM32F102R8T6	Cortex-M3	48	64	10	LQFP64	51	2-3.6	3	0	1	16	0	2	0	2	3	0	0	0	1	0	0	0	85
STM32F102RBT6	Cortex-M3	48	128	16	LQFP64	51	2-3.6	3	0	1	16	0	2	0	2	3	0	0	0	1	0	0	0	85
					STM32F10	3 – 72 [MHz, up to	1 Mby	te of F	lash v	rith mo	tor co	ntrol,	USB ar	nd CAN	I								
STM32F103T4U6	Cortex-M3	72	16	6	VFQFPN36	26	2-3.6	3	1	2	10	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103T6U6	Cortex-M3	72	32	10	VFQFPN36	26	2-3.6	3	1	2	10	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103C4T6	Cortex-M3	72	16	6	LQFP48	37	2-3.6	3	1	2	10	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103C6U6	Cortex-M3	72	32	10	UFQFPN48	37	2-3.6	3	1	2	10	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103C6T6	Cortex-M3	72	32	10	LQFP48	37	2-3.6	3	1	2	10	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103R4H6	Cortex-M3	72	16	6	TFBGA64	51	2-3.6	3	1	2	16	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103R4T6	Cortex-M3	72	16	6	LQFP64	51	2-3.6	3	1	2	16	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103R6H6	Cortex-M3	72	32	10	TFBGA64	51	2-3.6	3	1	2	16	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103R6T6	Cortex-M3	72	32	10	LQFP64	51	2-3.6	3	1	2	16	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103T8U6	Cortex-M3	72	64	20	VFQFPN36	26	2-3.6	4	1	2	10	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103TBU6	Cortex-M3	72	128	20	VFQFPN36	26	2-3.6	4	1	2	10	0	1	0	1	2	1	0	0	1	0	0	0	85
STM32F103C8T6	Cortex-M3	72	64	20	LQFP48	37	2-3.6	4	1	2	10	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103CBT6	Cortex-M3	72	128	20	LQFP48	37	2-3.6	4	1	2	10	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103CBU6	Cortex-M3	72	128	20	UFQFPN48	37	2-3.6	4	1	2	10	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103R8T6	Cortex-M3	72	64	20	LQFP64	51	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103R8H6	Cortex-M3	72	64	20	TFBGA64	51	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	120	U(S)ART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	Segment LCD	T° Max (°C)
STM32F103RBT6	Cortex-M3	72	128	20	LQFP64	51	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103RBH6	Cortex-M3	72	128	20	TFBGA64	51	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103V8T6	Cortex-M3	72	64	20	LQFP100	80	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103V8H6	Cortex-M3	72	64	20	LFBGA100	80	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103VBT6	Cortex-M3	72	128	20	LQFP100	80	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103VBH6	Cortex-M3	72	128	20	LFBGA100	80	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103VBI6	Cortex-M3	72	128	20	UFBGA100	80	2-3.6	4	1	2	16	0	2	0	2	3	1	0	0	1	0	0	0	85
STM32F103RCT6	Cortex-M3	72	256	48	LQFP64	51	2-3.6	8	2	3	16	2	3	2	2	5	1	1	0	1	0	0	0	85
STM32F103RDT6	Cortex-M3	72	384	64	LQFP64	51	2-3.6	8	2	3	16	2	3	2	2	5	1	1	0	1	0	0	0	85
STM32F103RET6	Cortex-M3	72	512	64	LQFP64	51	2-3.6	8	2	3	16	2	3	2	2	5	1	1	0	1	0	0	0	85
STM32F103RCY6	Cortex-M3	72	256	48	WLCSP64	51	2-3.6	8	2	3	16	2	3	2	2	5	1	1	0	1	0	0	0	85
STM32F103RDY6	Cortex-M3	72	384	64	WLCSP64	51	2-3.6	8	2	3	16	2	3	2	2	5	1	1	0	1	0	0	0	85
STM32F103REY6	Cortex-M3	72	512	64	WLCSP64	51	2-3.6	8	2	3	16	2	3	2	2	5	1	1	0	1	0	0	0	85
STM32F103VCT6	Cortex-M3	72	256	48	LQFP100	80	2-3.6	8	2	3	16	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103VDT6	Cortex-M3	72	384	64	LQFP100	80	2-3.6	8	2	3	16	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103VET6	Cortex-M3	72	512	64	LQFP100	80	2-3.6	8	2	3	16	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103VCH6	Cortex-M3	72	256	48	LFBGA100	80	2-3.6	8	2	3	16	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103VDH6	Cortex-M3	72	384	64	LFBGA100	80	2-3.6	8	2	3	16	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103VEH6	Cortex-M3	72	512	64	LFBGA100	80	2-3.6	8	2	3	16	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZCT6	Cortex-M3	72	256	48	LQFP144	112	2-3.6	8	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZDT6	Cortex-M3	72	384	64	LQFP144	112	2-3.6	8	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	12S	120	U(S)ART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	Segment LCD	T° Max (°C)
STM32F103ZET6	Cortex-M3	72	512	64	LQFP144	112	2-3.6	8	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZCH6	Cortex-M3	72	256	48	LFBGA144	112	2-3.6	8	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZDH6	Cortex-M3	72	384	64	LFBGA144	112	2-3.6	8	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZEH6	Cortex-M3	72	512	64	LFBGA144	112	2-3.6	8	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103RFT6	Cortex-M3	72	768	96	LQFP64	51	2-3.6	14	2	3	16	2	3	2	2	5	1	1	0	1	0	0	0	85
STM32F103RGT6	Cortex-M3	72	1024	96	LQFP64	51	2-3.6	14	2	3	16	2	3	2	2	5	1	1	0	1	0	0	0	85
STM32F103VFT6	Cortex-M3	72	768	96	LQFP100	80	2-3.6	14	2	3	16	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103VGT6	Cortex-M3	72	1024	96	LQFP100	80	2-3.6	14	2	3	16	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZFT6	Cortex-M3	72	768	96	LQFP144	112	2-3.6	14	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZGT6	Cortex-M3	72	1024	96	LQFP144	112	2-3.6	14	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZFH6	Cortex-M3	72	768	96	LFBGA144	112	2-3.6	14	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
STM32F103ZGH6	Cortex-M3	72	1024	96	LFBGA144	112	2-3.6	14	2	3	21	2	3	2	2	5	1	1	1	1	0	0	0	85
					STM32F	105/10	7 – 72 MH	z CPU	with E	therne	t MAC,	CAN a	ınd US	B 2.0	OTG									
STM32F105R8T6	Cortex-M3	72	64	64	LQFP64	51	2-3.6	6	1	2	16	2	3	2	2	5	2	0	0	0	1	0	0	85
STM32F105RBT6	Cortex-M3	72	128	64	LQFP64	51	2-3.6	6	1	2	16	2	3	2	2	5	2	0	0	0	1	0	0	85
STM32F105RCT6	Cortex-M3	72	256	64	LQFP64	51	2-3.6	6	1	2	16	2	3	2	2	5	2	0	0	0	1	0	0	85
STM32F105V8H6	Cortex-M3	72	64	64	LFBGA100	80	2-3.6	6	1	2	16	2	3	2	2	5	2	0	0	0	1	0	1	85
STM32F105V8T6	Cortex-M3	72	64	64	LQFP100	80	2-3.6	6	1	2	16	2	3	2	2	5	2	0	0	0	1	0	0	85
STM32F105VBH6	Cortex-M3	72	128	64	LFBGA100	80	2-3.6	6	1	2	16	2	3	2	2	5	2	0	0	0	1	0	0	85
STM32F105VBT6	Cortex-M3	72	128	64	LQFP100	80	2-3.6	6	1	2	16	2	3	2	2	5	2	0	0	0	1	0	0	85
STM32F105VCT6	Cortex-M3	72	256	64	LQFP100	80	2-3.6	6	1	2	16	2	3	2	2	5	2	0	0	0	1	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	120	U(S)ART	CAN	SDI0	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	Segment LCD	T° Max (°C)
STM32F107RBT6	Cortex-M3	72	128	64	LQFP64	51	2-3.6	6	1	2	16	2	3	2	1	5	2	0	0	0	1	1	0	85
STM32F107RCT6	Cortex-M3	72	256	64	LQFP64	51	2-3.6	6	1	2	16	2	3	2	1	5	2	0	0	0	1	1	0	85
STM32F107VBT6	Cortex-M3	72	128	64	LQFP100	80	2-3.6	6	1	2	16	2	3	2	1	5	2	0	0	0	1	1	0	85
STM32F107VCT6	Cortex-M3	72	256	64	LQFP100	80	2-3.6	6	1	2	16	2	3	2	1	5	2	0	0	0	1	1	0	85
STM32F107VCH6	Cortex-M3	72	256	64	LFBGA100	80	2-3.6	6	1	2	16	2	3	2	1	5	2	0	0	0	1	1	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	SPI	128	120	U(S)ART	CAN	USB Device	T° Max (°C)
de										.											
							S	TM32F0	x0 Valu	e Line											
STM32F030F4P6	Cortex-M0	48	16	4	TSS0P20	15	2.4-3.6	5	0	1	1	11	0	0	1	0	1	1	0	0	85
STM32F030K6T6	Cortex-M0	48	32	4	LQFP32	26	2.4-3.6	5	0	1	1	12	0	0	1	0	1	1	0	0	85
STM32F030C6T6	Cortex-M0	48	32	4	LQFP48	39	2.4-3.6	5	0	1	1	12	0	0	1	0	1	1	0	0	85
STM32F030C8T6	Cortex-M0	48	64	8	LQFP48	39	2.4-3.6	7	0	1	1	12	0	0	2	0	2	2	0	0	85
STM32F030CCT6	Cortex-M0	48	256	32	LQFP48	37	2.4-3.6	8	0	1	1	12	0	0	2	0	2	6	0	0	85
STM32F030R8T6	Cortex-M0	48	64	8	LQFP64	55	2.4-3.6	7	0	1	1	18	0	0	2	0	2	2	0	0	85
STM32F030RCT6	Cortex-M0	48	256	32	LQFP64	51	2.4-3.6	8	0	1	1	18	0	0	2	0	2	6	0	0	85
STM32F070F6P6	Cortex-M0	48	32	6	TSS0P20	15	2.4-3.6	5	0	1	1	11	0	0	1	0	1	2	0	1	85
STM32F070C6T6	Cortex-M0	48	32	6	LQFP48	37	2.4-3.6	5	0	1	1	12	0	0	1	0	1	2	0	1	85
STM32F070CBT6	Cortex-M0	48	128	16	LQFP48	37	2.4-3.6	8	0	1	1	12	0	0	2	0	2	4	0	1	85
STM32F070RBT6	Cortex-M0	48	128	16	LQFP64	51	2.4-3.6	8	0	1	1	18	0	0	2	0	2	4	0	1	85
							ST	M32F0>	1 Acce	ss Line											
STM32F031F4P6	Cortex-M0	48	16	4	TSS0P20	15	2-3.6	5	1	1	1	12	0	0	1	1	1	1	0	0	85
STM32F031F6P6	Cortex-M0	48	32	4	TSS0P20	15	2-3.6	5	1	1	1	12	0	0	1	1	1	1	0	0	85
STM32F031E6Y6	Cortex-M0	48	32	4	WLCSP25	20	2-3.6	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F031G4U6	Cortex-M0	48	16	4	UFQFPN28	23	2-3.6	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F031G6U6	Cortex-M0	48	32	4	UFQFPN28	23	2-3.6	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F031K4U6	Cortex-M0	48	16	4	UFQFPN32	27	2-3.6	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F031K6U6	Cortex-M0	48	32	4	UFQFPN32	27	2-3.6	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F031K6T6	Cortex-M0	48	32	4	LQFP32	25	2-3.6	5	1	1	1	13	0	0	1	1	1	1	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	128	120	U(S)ART	CAN	USB Device	T° Max (°C)
STM32F031C4T6	Cortex-M0	48	16	4	LQFP48	39	2-3.6	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F031C6T6	Cortex-M0	48	32	4	LQFP48	39	2-3.6	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F051K4T6	Cortex-M0	48	16	8	LQFP32	25	2-3.6	7	1	1	1	13	1	2	1	1	1	1	0	0	85
STM32F051K4U6	Cortex-M0	48	16	8	UFQFPN32	27	2-3.6	7	1	1	1	13	1	2	1	1	1	1	0	0	85
STM32F051K6T6	Cortex-M0	48	32	8	LQFP32	25	2-3.6	7	1	1	1	13	1	2	1	1	1	2	0	0	85
STM32F051K6U6	Cortex-M0	48	32	8	UFQFPN32	27	2-3.6	7	1	1	1	13	1	2	1	1	1	2	0	0	85
STM32F051K8T6	Cortex-M0	48	64	8	LQFP32	25	2-3.6	7	1	1	1	13	1	2	1	1	1	2	0	0	85
STM32F051K8U6	Cortex-M0	48	64	8	UFQFPN32	27	2-3.6	7	1	1	1	13	1	2	1	1	1	2	0	0	85
STM32F051T8Y6	Cortex-M0	48	64	8	WLCSP36	29	2-3.6	7	1	1	1	13	1	2	1	1	1	2	0	0	85
STM32F051C4T6	Cortex-M0	48	16	8	LQFP48	39	2-3.6	7	1	1	1	13	1	2	1	1	1	1	0	0	85
STM32F051C4U6	Cortex-M0	48	16	8	UFQFPN48	39	2-3.6	7	1	1	1	13	1	2	1	1	1	1	0	0	85
STM32F051C6T6	Cortex-M0	48	32	8	LQFP48	39	2-3.6	7	1	1	1	13	1	2	1	1	1	2	0	0	85
STM32F051C6U6	Cortex-M0	48	32	8	UFQFPN48	39	2-3.6	7	1	1	1	13	1	2	1	1	1	2	0	0	85
STM32F051C8T6	Cortex-M0	48	64	8	LQFP48	39	2-3.6	7	1	1	1	13	1	2	2	1	2	2	0	0	85
STM32F051C8U6	Cortex-M0	48	64	8	UFQFPN48	39	2-3.6	7	1	1	1	13	1	2	2	1	2	2	0	0	85
STM32F051R4T6	Cortex-M0	48	16	8	LQFP64	55	2-3.6	7	1	1	1	19	1	2	2	1	1	1	0	0	85
STM32F051R6T6	Cortex-M0	48	32	8	LQFP64	55	2-3.6	7	1	1	1	19	1	2	2	1	1	2	0	0	85
STM32F051R8T6	Cortex-M0	48	64	8	LQFP64	55	2-3.6	7	1	1	1	19	1	2	2	1	2	1	0	0	85
STM32F051R8H6	Cortex-M0	48	64	8	UFBGA64	55	2-3.6	7	1	1	1	19	1	2	2	1	2	2	0	0	85
STM32F071C8T6	Cortex-M0	48	64	16	LQFP48	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	0	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	128	120	U(S)ART	CAN	USB Device	T° Max (°C)
STM32F071C8U6	Cortex-M0	48	64	16	UFQFPN48	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	0	0	85
STM32F071CBY6	Cortex-M0	48	128	16	WLCSP49	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	0	0	85
STM32F071CBT6	Cortex-M0	48	128	16	LQFP48	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	0	0	85
STM32F071CBU6	Cortex-M0	48	128	16	UFQFPN48	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	0	0	85
STM32F071RBT6	Cortex-M0	48	128	16	LQFP64	51	2-3.6	8	1	1	1	19	2	2	2	2	2	4	0	0	85
STM32F071V8H6	Cortex-M0	48	64	16	UFBGA100	87	2-3.6	8	1	1	1	19	2	2	2	2	2	4	0	0	85
STM32F071V8T6	Cortex-M0	48	64	16	LQFP100	87	2-3.6	8	1	1	1	19	2	2	2	2	2	4	0	0	85
STM32F071VBH6	Cortex-M0	48	128	16	UFBGA100	87	2-3.6	8	1	1	1	19	2	2	2	2	2	4	0	0	85
STM32F071VBT6	Cortex-M0	48	128	16	LQFP100	87	2-3.6	8	1	1	1	19	2	2	2	2	2	4	0	0	85
STM32F091CBT6	Cortex-M0	48	128	32	LQFP48	38	2-3.6	8	1	1	1	13	2	2	2	2	2	6	1	0	85
STM32F091CBU6	Cortex-M0	48	128	32	UFQFPN48	38	2-3.6	8	1	1	1	13	2	2	2	2	2	6	1	0	85
STM32F091CCT6	Cortex-M0	48	256	32	LQFP48	38	2-3.6	8	1	1	1	13	2	2	2	2	2	6	1	0	85
STM32F091CCU6	Cortex-M0	48	256	32	UFQFPN48	38	2-3.6	8	1	1	1	13	2	2	2	2	2	6	1	0	85
STM32F091RBT6	Cortex-M0	48	128	32	LQFP64	52	2-3.6	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F091RCT6	Cortex-M0	48	256	32	LQFP64	52	2-3.6	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F091RCH6	Cortex-M0	48	256	32	UFBGA64	52	2-3.6	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F091RCY6	Cortex-M0	48	256	32	WLCSP64	52	2-3.6	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F091VBT6	Cortex-M0	48	128	32	LQFP100	88	2-3.6	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F091VCT6	Cortex-M0	48	256	32	LQFP100	88	2-3.6	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F091VCH6	Cortex-M0	48	256	32	UFBGA100	88	2-3.6	8	1	1	1	19	2	2	2	2	2	8	1	0	85

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	128	120	U(S)ART	CAN	USB Device	T° Max (°C)
						STM3	2F0x2 USI	3 Line -	Crystal	-less US	SB 2.0 8	CAN									
STM32F042F4P6	Cortex-M0	48	16	6	TSS0P20	16	2-3.6	5	1	1	1	12	0	0	1	1	1	2	1	1	85
STM32F042F6P6	Cortex-M0	48	32	6	TSS0P20	16	2-3.6	5	1	1	1	12	0	0	1	1	1	2	1	1	85
STM32F042G4U6	Cortex-M0	48	16	6	UFQFPN28	24	2-3.6	5	1	1	1	13	0	0	1	1	1	2	1	1	85
STM32F042G6U6	Cortex-M0	48	32	6	UFQFPN28	24	2-3.6	5	1	1	1	13	0	0	1	1	1	2	1	1	85
STM32F042K4T6	Cortex-M0	48	16	6	LQFP32	26	2-3.6	5	1	1	1	13	0	0	1	1	1	2	1	1	85
STM32F042K4U6	Cortex-M0	48	16	6	UFQFPN32	28	2-3.6	5	1	1	1	13	0	0	1	1	1	2	1	1	85
STM32F042K6T6	Cortex-M0	48	32	6	LQFP32	26	2-3.6	5	1	1	1	13	0	0	1	1	1	2	1	1	85
STM32F042K6U6	Cortex-M0	48	32	6	UFQFPN32	28	2-3.6	5	1	1	1	13	0	0	1	1	1	2	1	1	85
STM32F042T6Y6	Cortex-M0	48	32	6	WLCSP36	30	2-3.6	5	1	1	1	13	0	0	1	1	1	2	1	1	85
STM32F042C4T6	Cortex-M0	48	16	6	LQFP48	38	2-3.6	5	1	1	1	13	0	0	2	1	1	2	1	1	85
STM32F042C4U6	Cortex-M0	48	16	6	UFQFPN48	38	2-3.6	5	1	1	1	13	0	0	2	1	1	2	1	1	85
STM32F042C6T6	Cortex-M0	48	32	6	LQFP48	38	2-3.6	5	1	1	1	13	0	0	2	1	1	2	1	1	85
STM32F042C6U6	Cortex-M0	48	32	6	UFQFPN48	38	2-3.6	5	1	1	1	13	0	0	2	1	1	2	1	1	85
STM32F072C8T6	Cortex-M0	48	64	16	LQFP48	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	1	1	85
STM32F072C8U6	Cortex-M0	48	64	16	UFQFPN48	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	1	1	85
STM32F072CBY6	Cortex-M0	48	128	16	WLCSP49	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	1	1	85
STM32F072CBT6	Cortex-M0	48	128	16	LQFP48	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	1	1	85
STM32F072CBU6	Cortex-M0	48	128	16	UFQFPN48	37	2-3.6	8	1	1	1	13	2	2	2	2	2	4	1	1	85
STM32F072R8T6	Cortex-M0	48	64	16	LQFP64	51	2-3.6	8	1	1	1	19	2	2	2	2	2	4	1	1	85
STM32F072RBH6	Cortex-M0	48	128	16	UFBGA64	51	2-3.6	8	1	1	1	19	2	2	2	2	2	4	1	1	85

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	128	120	U(S)ART	CAN	USB Device	T° Max (°C)
STM32F072RBT6	Cortex-M0	48	128	16	LQFP64	51	2-3.6	8	1	1	1	19	2	2	2	2	2	4	1	1	85
STM32F072V8H6	Cortex-M0	48	64	16	UFBGA100	87	2-3.6	8	1	1	1	19	2	2	2	2	2	4	1	1	85
STM32F072V8T6	Cortex-M0	48	64	16	LQFP100	87	2-3.6	8	1	1	1	19	2	2	2	2	2	4	1	1	85
STM32F072VBH6	Cortex-M0	48	128	16	UFBGA100	87	2-3.6	8	1	1	1	19	2	2	2	2	2	4	1	1	85
STM32F072VBT6	Cortex-M0	48	128	16	LQFP100	87	2-3.6	8	1	1	1	19	2	2	2	2	2	4	1	1	85
						;	STM32F0x8	3 Low-v	oltage I	_ine - 1.	.8 V ± 8%	Ď									
STM32F038F6P6	Cortex-M0	48	32	4	TSS0P20	14	1.65-1.95	5	1	1	1	11	0	0	1	1	1	1	0	0	85
STM32F038E6Y6	Cortex-M0	48	32	4	WLCSP25	19	1.65-1.95	5	1	1	1	12	0	0	1	1	1	1	0	0	85
STM32F038G6U6	Cortex-M0	48	32	4	UFQFPN28	22	1.65-1.95	5	1	1	1	12	0	0	1	1	1	1	0	0	85
STM32F038K6U6	Cortex-M0	48	32	4	UFQFPN32	26	1.65-1.95	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F038C6T6	Cortex-M0	48	32	4	LQFP48	38	1.65-1.95	5	1	1	1	13	0	0	1	1	1	1	0	0	85
STM32F048G6U6	Cortex-M0	48	32	6	UFQFPN28	23	1.65-1.95	5	1	1	1	13	0	0	1	1	1	2	0	1	85
STM32F048T6Y6	Cortex-M0	48	32	6	WLCSP36	29	1.65-1.95	5	1	1	1	13	0	0	1	1	1	2	0	1	85
STM32F048C6U6	Cortex-M0	48	32	6	UFQFPN48	37	1.65-1.95	5	1	1	1	13	0	0	2	1	1	2	0	1	85
STM32F058T8Y6	Cortex-M0	48	64	8	WLCSP36	28	1.65-1.95	7	1	1	1	13	1	2	1	1	2	2	0	0	85
STM32F058C8U6	Cortex-M0	48	64	8	UFQFPN48	38	1.65-1.95	7	1	1	1	13	1	2	2	1	2	2	0	0	85
STM32F058R8H6	Cortex-M0	48	64	8	UFBGA64	54	1.65-1.95	7	1	1	1	19	1	2	2	1	2	2	0	0	85
STM32F058R8T6	Cortex-M0	48	64	8	LQFP64	54	1.65-1.95	7	1	1	1	19	1	2	2	1	2	2	0	0	85
STM32F078CBT6	Cortex-M0	48	128	16	LQFP48	36	1.65-1.95	8	1	1	1	13	2	2	2	2	2	4	0	1	85
STM32F078CBU6	Cortex-M0	48	128	16	UFQFPN48	36	1.65-1.95	8	1	1	1	13	2	2	2	2	2	4	0	1	85
STM32F078CBY6	Cortex-M0	48	128	16	WLCSP49	36	1.65-1.95	8	1	1	1	13	2	2	2	2	2	4	0	1	85

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	SPI	128	120	U(S)ART	CAN	USB Device	T° Max (°C)
STM32F078RBH6	Cortex-M0	48	128	16	UFBGA64	50	1.65-1.95	8	1	1	1	19	2	2	2	2	2	4	0	1	85
STM32F078RBT6	Cortex-M0	48	128	16	LQFP64	50	1.65-1.95	8	1	1	1	19	2	2	2	2	2	4	0	1	85
STM32F078VBH6	Cortex-M0	48	128	16	UFBGA100	86	1.65-1.95	8	1	1	1	19	2	2	2	2	2	4	0	1	85
STM32F078VBT6	Cortex-M0	48	128	16	LQFP100	86	1.65-1.95	8	1	1	1	19	2	2	2	2	2	4	0	1	85
STM32F098CCT6	Cortex-M0	48	256	32	LQFP48	37	1.65-1.95	8	1	1	1	13	2	2	2	2	2	6	1	0	85
STM32F098CCU6	Cortex-M0	48	256	32	UFQFPN48	37	1.65-1.95	8	1	1	1	13	2	2	2	2	2	6	1	0	85
STM32F098RCH6	Cortex-M0	48	256	32	UFBGA64	51	1.65-1.95	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F098RCT6	Cortex-M0	48	256	32	LQFP64	51	1.65-1.95	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F098RCY6	Cortex-M0	48	256	32	WLCSP64	51	1.65-1.95	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F098VCT6	Cortex-M0	48	256	32	LQFP100	87	1.65-1.95	8	1	1	1	19	2	2	2	2	2	8	1	0	85
STM32F098VCH6	Cortex-M0	48	256	32	UFBGA100	87	1.65-1.95	8	1	1	1	19	2	2	2	2	2	8	1	0	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Times (16-bit)	nigh Resolution Timer	2-bi	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	COMP	OPAMP	SPI	I2S	M-SPI	120	U(S)ART	CAN LPUART	SDIO	E/C/MC	USB HS UTG	Ethernet	MDIO	JPEG Codec	GPU GPU	3D COLL	MIPI USI	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	TRNG	PNA	AES/DES	SHA/HMAC	T° Max (°C)
																STN	32	l7x	O Va	alu	e Line																							
STM32H750VBT6	Cortex -M7	480	128	1060	0	LQFP100	82	1.71- 3.6	12	2	2 5	5 1	0	0	3	16	2 2	2	6	3	1[QUAD]	4	8	1 2 [FD]	2	1	1 1	1	1	Υ	Y	N 1	1 0	4	1	1	1	1	N	Y I	N N	I Y	Υ	85
STM32H750ZBT6	Cortex -M7	480	128	1060	0	LQFP144	114	1.62- 3.6	12	2	2 5	5 1	0	0	3	28	2 2	2	6	3	1[QUAD]	4	8	1 2 [FD]	2	1	1 1	1	1	Υ	ΥI	N 1	1 0	4	1	1	1	1	N	Y I	N N	I Y	Υ	85
STM32H750IBK6	Cortex -M7	480	128	1060	0	UFBGA176	140	1.62- 3.6	12	2	2 5	5 1	0	0	3	34	2 2	2	6	3	1[QUAD]	4	8	1 2 [FD]	2	1	1 1	1	1	Υ	ΥI	N 1	1 0	4	1	1	1	1	N	Y I	N N	I Y	Υ	85
STM32H750IBT6	Cortex -M7	480	128	1060	0	UFBGA176	140	1.62- 3.6	12	2	2 5	5 1	0	0	3	34	2 2	2	6	3	1[QUAD]	4	8	1 2 [FD]	2	1	1 1	1	1	Υ	ΥI	N 1	1 0	4	1	1	1	1	N	ΥI	N N	I Y	Υ	85
STM32H750XBH6	Cortex -M7	480	128	1060	0	TFBGA240	168	1.62- 3.6	12	2	2 5	5 1	0	0	3	36	2 2	2	6	3	1[QUAD]	4	8	1 2 [FD]	2	1	1 1	1	1	Υ	ΥI	N 1	1 0	4	1	1	1	1	N	ΥI	N N	I Y	Υ	85
STM32H7B0RBT6	Cortex -M7	280	128	1380	0	LQFP64	49	1.62- 3.6	12	2	2 3	3 (0	0	2	16	3 1	1	4	4	2[0CT0]	3	7	1 2 [FD]	2	0	1 1	0	1	Υ	Υ	N 1	1 0	1	1	2	1	1	N	Y,	Y	I Y	Υ	85
STM32H7B0VBT6	Cortex -M7	280	128	1380	0	LQFP100	80	1.71- 3.6	12	2	2 3	3 (0	0	2	16	3 2	2	5	4	2[0CT0]	4	10	1 2 [FD]	2	1	1 1	0	1	Υ	ΥI	N 1	1 0	2	1	2	1	1	N	Ϋ́	Y	I Y	Υ	85
STM32H7B0ZBT6	Cortex -M7	280	128	1380	0	LQFP144	112	1.62- 3.6	12	2	2 3	3 0	0	0	2	20	3 2	2	6	4	2[0CT0]	4	10	1 2 [FD]	2	1	1 1	0	1	Υ	Y	N 1	1 0	2	1	2	1	1	N	Ϋ́	Y	I Y	Υ	85
STM32H7B0ABI6Q	Cortex -M7	280	128	1380	0	UFBGA169	121	1.62- 3.6	12	2	2 3	3 (0	0	2	24	3 2	2	6	4	2[0CT0]	4	10	1 2 [FD]	2	1	1 1	0	1	Υ	ΥI	N 1	1 0	2	1	2	1	1	N	Y,	Y	I Y	Υ	85
STM32H7B0IBK6Q	Cortex -M7	280	128	1380	0	UFBGA176	128	1.62- 3.6	12	2	2 3	3 (0	0	2	24	3 2	2	6	4	2[0CT0]	4	10	1 2 [FD]	2	1	1 1	0	1	Υ	ΥI	N 1	1 0	2	1	2	1	1	N	Y,	Y	I Y	Υ	85
STM32H7B0IBT6	Cortex -M7	280	128	1380	0	LQFP176	138	1.62- 3.6	12	2	2 3	3 0	0	0	2	20	3 2	2	6	4	2[0CT0]	4	10	1 2 [FD]	2	1	1 1	0	1	Υ	Y	N 1	1 0	2	1	2	1	1	N	γ ,	Y	I Y	Υ	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	LF limer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels		OPAMP	SPI	2S	M-SPI	120	U(S)ART	LPIJART	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	MUIO	JPEG Codec	GPU	3D GPU	TFT LCD	MIDI DEI	SPUIHKX	DFSDM	DCMI	SWPMI	Math Accelerator	TRNG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H730VBT6	Cortex -M7	550	128	564	0	LQFP100	80	1.71- 3.6	12	4 :	2 5	0	1	4	2	26 2	2	2	5	4	2[0CT0]	5	10	1 3 [FI	2	1	1	1	1 1	N	Υ	N	1	0 2	2 1	1	1	1	Υ	Y	ſΝ	ΙY	Υ	85
STM32H730VBH6	Cortex -M7	550	128	564	0	TFBGA100	80	1.62- 3.6	12	4	2 5	0	1	17	2	26 2	2	2	5	4	2[0CT0]	5	10	1 [FI	2	1	1	1	1 1	N	Υ	N	1	0 2	2 1	1	1	1	Υ	Y	ſΝ	ΙY	Υ	85
STM32H730ZBT6	Cortex -M7	550	128	564	0	LQFP144	112	1.62- 3.6	12	4	2 5	0	1	12	2	30 2	2	2	6	4	2[0CT0]	5	10	1 3 [FI	2	1	1	1	1 1	N	Υ	N	1	0 2	2 1	1	1	1	Υ	Y	Y N	ΙY	Υ	85
STM32H730ZBI6	Cortex -M7	550	128	564	0	UFBGA144	114	1.62- 3.6	12	4	2 5	0	1	17	2	34 2	2	2	6	4	2[0CT0]	5	10	1 3 [FI	2	1	1	1	1 1	N	Υ	N	1	0 2	2 1	1	1	1	Υ	Y	Y N	ΙY	Υ	85
STM32H730ABI6Q	Cortex -M7	550	128	564	0	UFBGA169	121	1.62- 3.6	12	4	2 5	0	1	17	2	38 2	2	2	4	4	2[0CT0]	5	10	1 3 [FI	2	1	1	1	1 1	N	Υ	N	1	0 2	2 1	1	1	1	Υ	Y	Y N	I Y	Υ	85
STM32H730IBK6Q	Cortex -M7	550	128	564	0	UFBGA176	128	1.62- 3.6	12	4	2 5	0	1	17	2	38 2	2	2	6	4	2[0CT0]	5	10	1 3 [FI	2	1	1	1	1 1	N	Υ	N	1	0 2	2 1	1	1	1	Υ	Y	Y N	I Y	Υ	85
STM32H730IBT6Q	Cortex -M7	550	128	564	0	UFBGA176	119	1.62- 3.6	12	4	2 5	6 0	1	12	2	30 2	2	2	6	4	2[0CT0]	5	10	1 3 [FI	2	1	1	1	1 1	N	Υ	N	1	0 2	2 1	1	1	1	Υ	ΥY	/ N	ΙY	Υ	85
											S	TM:	32H	742	_74	3_7	53	Sin	gle	Coi	re Perfor	man	ice	Line																				
STM32H742VGT6	Cortex -M7	480	1024	692	0	LQFP100	82	1.71- 3.6	12	2 2	2 5	1	0	0	3	36 2	2	2	6	3	1[QUAD]	4	8	1 2 [FI	2	1	1	1	1 1	N	Υ	N	0	0 4	1 1	1	1	1	N	Y	1 N	I N	N	85
STM32H742VGH6	Cortex -M7	480	1024	692	0	TFBGA100	82	1.62- 3.6	12	2 :	2 5	1	0	0	3	36 2	2	2	6	3	1[QUAD]	4	8	1 2 [FI	2	1	1	1	1 1	N	Υ	N	0	0 4	1 1	1	1	1	N	Y	N N	I N	N	85
STM32H742ZGT6	Cortex -M7	480	1024	692	0	LQFP144	114	1.62- 3.6	12	2	2 5	1	0	0	3	36 2	2	2	6	3	1[QUAD]	4	8	1 2 [FI	2	1	1	1	1 1	N	Υ	N	0	0 4	1 1	1	1	1	N	Y	N N	I N	N	85
STM32H742AGI6	Cortex -M7	480	1024	692	0	UFBGA169	131	1.62- 3.6	12	2	2 5	1	0	0	3	36 2	2	2	6	3	1[QUAD]	4	8	1 2 [FI	2	1	1	1	1 1	N	Υ	N	0	0 4	1 1	1	1	1	N	Y	N N	I N	N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SDI IZS	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS HOST/OTG	USB HS OTG	MUIO*	JPEG Codec	GPU	3D GPU	TET I CD	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	TRNIC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H742IGT6	Cortex -M7	480	1024	692	0	LQFP176	140	1.62- 3.6	12 2	2 2	5	1	0	0 3	36	2	2	2	6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0	4	1	1	1	1	N Y	Y	I N	N	N	85
STM32H742IGI6	Cortex -M7	480	1024	692	0	UFBGA176	140	1.62- 3.6	12 2	2 2	5	1	0 (0 3	36	2	2	2 (6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0) 4	1	1	1	1	N Y	Y N	I N	N	N	85
STM32H742BGT6	Cortex -M7	480	1024	692	0	LQFP208	168	1.62- 3.6	12	2 2	5	1	0	0 3	36	2	2	2 (6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0) 4	1	1	1	1	N Y	Y	I N	N	N	85
STM32H742XGH6	Cortex -M7	480	1024	692	0	TFBGA240	168	1.62- 3.6	12	2 2	5	1	0	0 3	36	2	2	2 (6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0) 4	1	1	1	1	N Y	Y	I N	N	N	85
STM32H742VIT6	Cortex -M7	480	2048	692	0	LQFP100	82	1.71- 3.6	12	2 2	5	1	0	0 3	36	2	2	2 (6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0) 4	1	1	1	1	N Y	Y	I N	I N	N	85
STM32H742VIH6	Cortex -M7	480	2048	692	0	TFBGA100	82	1.62- 3.6	12	2 2	5	1	0	0 3	36	2	2	2	6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0) 4	1	1	1	1	N Y	Y N	I N	I N	N	85
STM32H742ZIT6	Cortex -M7	480	2048	692	0	LQFP144	114	1.62- 3.6	12	2 2	5	1	0	0 3	36	2	2	2	6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0	4	1	1	1	1	N Y	Y N	I N	N	N	85
STM32H742All6	Cortex -M7	480	2048	692	0	UFBGA169	131	1.62- 3.6	12 2	2 2	5	1	0 (0 3	36	2	2	2 (6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0	4	1	1	1	1	N Y	Y	I N	N	N	85
STM32H742IIT6	Cortex -M7	480	2048	692	0	LQFP176	140	1.62- 3.6	12 2	2 2	5	1	0 (0 3	36	2	2	2	6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0) 4	1	1	1	1	N Y	Y	I N	N	N	85
STM32H742III6	Cortex -M7	480	2048	692	0	UFBGA176	140	1.62- 3.6	12	2 2	5	1	0	0 3	36	2	2	2	6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0) 4	1	1	1	1	N Y	Y N	I N	N	N	85
STM32H742BIT6	Cortex -M7	480	2048	692	0	LQFP208	168	1.62- 3.6	12	2 2	5	1	0 (0 3	36	2	2	2 (6 3	1[QUAD]	4	8	1	2 [FD] 2	1	1	1	1 1	N	Υ	N	0 0) 4	1	1	1	1	N Y	Y	I N	I N	N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	LT TIMER	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	DAC 12-bit Channels ADC 16-bit Channels	COMP	OPAMP	SPI	128	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS HOST/OTG		Ethernet	MDIO	IDEG Codec	SD GPU	IFI LGD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DOMI	SWPMI SWPMI	Moth Applicator	OIFUEG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H742XIH6	Cortex -M7	480	2048	692	0	TFBGA240	168	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 -D] 2	2 1	1	1	1	1	N ,	Y	1 0	0	4	1	1	1	1 N	۱	Y N	N	N	N	85
STM32H743VGT6	Cortex -M7	480	1024	1060	0	LQFP100	82	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 -D] 2	2 1	1	1	1	1	Ϋ́	Y	J 1	0	4	1	1	1	1 N	۱V	Y N	N	N	N	85
STM32H743VGH6	Cortex -M7	480	1024	1060	0	TFBGA100	82	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 -D] 2	2 1	1	1	1	1	Ϋ́	Y	J 1	0	4	1	1	1	1 N	۱ ۱	Y N	N	N	N	85
STM32H743ZGT6	Cortex -M7	480	1024	1060	0	LQFP144	114	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 -D] 2	2 1	1	1	1	1	Ϋ́	Y	J 1	0	4	1	1	1	1 N	۱ ۱	Y N	l N	N	N	85
STM32H743AGI6	Cortex -M7	480	1024	1060	0	UFBGA169	131	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 -D] 2	2 1	1	1	1	1	γ,	YN	J 1	0	4	1	1	1	1 N	۱ ۱	Y N	N	N	N	85
STM32H743IGT6	Cortex -M7	480	1024	1060	0	LQFP176	140	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 -D] 2	2 1	1	1	1	1	γ,	Y N	J 1	0	4	1	1	1	1 1	۱ ،	Y N	N	N	N	85
STM32H743IGI6	Cortex -M7	480	1024	1060	0	UFBGA176	140	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 D] 2	2 1	1	1	1	1	Ϋ́	Y	J 1	0	4	1	1	1	1 N	N Y	Y N	N	N	N	85
STM32H743BGT6	Cortex -M7	480	1024	1060	0	LQFP208	168	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 D] 2	2 1	1	1	1	1	γ,	Y N	J 1	0	4	1	1	1	1 N	N Y	Y N	N	N	N	85
STM32H743XGH6	Cortex -M7	480	1024	1060	0	TFBGA240	168	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 D] 2	2 1	1	1	1	1	γ ,	YN	J 1	0	4	1	1	1	1 N	u v	Y N	N	N	N	85
STM32H743VIT6	Cortex -M7	480	2048	1060	0	LQFP100	82	1.71- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 D] 2	2 1	1	1	1	1	γ,	YN	J 1	0	4	1	1	1	1 N	۷	Y N	N	N	N	85
STM32H743VIH6	Cortex -M7	480	2048	1060	0	TFBGA100	82	1.62- 3.6	12	2	2 5	5 1	0	0	3 3	36 2	2	2	6	3	1[QUAD]	4	8	1 [[2 -D] 2	2 1	1	1	1	1	γ,	Y	J 1	0	4	1	1	1	1 N	۱ ۱	Y N	N	N	N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced (Imer (16-bit) Timer (32-bit)	LPTimer	High Resolution Timer	oit Un	ADC 12-hit Channels	ADC 16-bit Units	DAC 12-bit Channels	COMP	OPAMP	Spi 12	126	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	JPEG Codec	GPU :	3D GPU	TFT LCD	MIPI DSI	SAI	CRDIEDA	DCMI	SWPMI	Math Accelerator	TRNG	OTFDEC	PKA	SHA/HMAC	T° Max (°C)
STM32H743ZIT6	Cortex -M7	480	2048	1060	0	LQFP144	114	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N 1	N N	85
STM32H743All6	Cortex -M7	480	2048	1060	0	UFBGA169	131	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N I	N N	85
STM32H743IIT6	Cortex -M7	480	2048	1060	0	LQFP176	140	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N I	N N	85
STM32H743III6	Cortex -M7	480	2048	1060	0	UFBGA176	140	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N I	N N	85
STM32H743BIT6	Cortex -M7	480	2048	1060	0	LQFP208	168	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N I	N N	85
STM32H743XIH6	Cortex -M7	480	2048	1060	0	TFBGA240	168	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N I	N N	85
STM32H753VIT6	Cortex -M7	480	2048	1060	0	LQFP100	82	1.71- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N '	ΥY	85
STM32H753VIH6	Cortex -M7	480	2048	1060	0	TFBGA100	82	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N,	ΥY	85
STM32H753ZIT6	Cortex -M7	480	2048	1060	0	LQFP144	114	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N '	ΥY	85
STM32H753All6	Cortex -M7	480	2048	1060	0	UFBGA169	131	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N '	ΥY	85
STM32H753IIT6	Cortex -M7	480	2048	1060	0	LQFP176	140	1.62- 3.6	12	2 2	5	1	0	0	3 3	6 2	2	2	6	3	1[QUAD]	4	8	1 [2 [FD] 2	2 1	1	1	1 1	Υ	Υ	N	1	0	4	1 1	1	1	N	Υ	N	N '	ΥΥ	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	OPAMP	SPI	I2S	M-SPI	120	U(S)ART	I PIIART	SUID	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethornot	JPEG Codec	GPU	3D GPU	MIPI DSI	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H753III6	Cortex -M7	480	2048	1060	0	UFBGA176	140	1.62 3.6	12	2	2 5	5 1	0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	Υ	Υ	85
STM32H753BIT6	Cortex -M7	480	2048	1060	0 0	LQFP208	168	1.62 3.6	12	2	2 5	5 1	0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	Υ	Υ	85
STM32H753XIH6	Cortex -M7	480	2048	1060	0 0	TFBGA240	168	1.62 3.6	12	2	2 5	5 1	0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	! 1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	Υ	Υ	85
													STI	M32	2H7	45_	755	Du	al C	ore	Industri	al Li	ine																					
STM32H745ZGTx	M7 & M4	480 & 240	1024	1060	0 0	LQFP144	97	1.62 3.6	12	2	2 5	5 1	0	0	3	23	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	N	N	125
STM32H745IGTx	M7 & M4	480	1024	1060	0 0	LQFP176	119	1.62 3.6	12	2	2 5	5 1	0	0	3	28	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	N	N	125
STM32H745IGKx	M7 & M4	480 & 240	1024	1060	0 0	UFBGA176	128	1.62 3.6	12	2	2 5	5 1	0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	! 1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	N	N	125
STM32H745BGTx	M7 & M4	480	1024	1060	0 0	LQFP208	148	1.62 3.6	12	2	2 5	5 1	0	0	3	32	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	! 1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	N	N	125
STM32H745XGHx	M7 & M4	480	1024	1060	0 0	TFBGA240	168	1.62 3.6	12	2	2 5	5 1	0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	N	N	125
STM32H745ZITx	M7 & M4	480	2048	1060	0 0	LQFP144	97	1.62 3.6	12	2	2 5	5 1	0	0	3	23	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	N	N	125
STM32H745IITx	M7 & M4	480 & 240	2048	1060	0 0	LQFP176	119	1.62 3.6	12	2	2 5	5 1	0	0	3	28	2 2	2 2	6	3	1[QUAD]	4	8	1 (F	2 D] 2	! 1	1	1	1 1	Υ	Υ	N ·	1 0	4	1	1	1	1 1	N Y	/ N	N	N	N	125
STM32H745IIKx	M7 & M4	480 & 240	2048	1060	0 0	UFBGA176	128	1.62 3.6	12	2	2 5	5 1	0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	1 [F	2 D] 2	! 1	1	1	1 1	Υ	Υ	N	1 0	4	1	1	1	1	N Y	/ N	N	N	N	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units High Resolution Timer	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 19-hit Channels	OPAMIP	SPI	I2S	M-SPI	120	U(S)ART	I PIIART	CAN	F(S)MC	USB FS HOST/OTG	USB HS OTG	MDIO Ethernet	JPEG Codec	GPU	3D GPU	WIFT US	SAI	SPDIFRX	DFSDM	DGMI	Math Accelerator SWPMI	TRNG	OTFDEC	PKA	SHA/HMAC AFS/DES	T° Max (°C)
STM32H745BITx	M7 & M4	480 & 240	2048	1060	0	LQFP208	148	1.62- 3.6	12	2 2	5	1 0	0	3	32	2 2	2 2	6	3	1[QUAD]	4	8	1 [2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 () 4	1	1	1	1 N	ΙΥ	N	N	N N	125
STM32H745XIHx	M7 & M4		2048	1060	0	TFBGA240	168	1.62- 3.6	12	2 2	5	1 0	0	3	36	2 2	2 2	2 6	3	1[QUAD]	4	8		2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 () 4	1	1	1	1 N	ΙY	N	N	N N	125
STM32H755ZITx	M7 & M4	480 & 240	2048	1060	0	LQFP144	97	1.62- 3.6	12	2 2	5	1 0	0	3	23	2 2	2 2	6	3	1[QUAD]	4	8		2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 () 4	1	1	1	1 N	ΙΥ	N	N	YY	125
STM32H755IITx	M7 & M4	480 & 240	2048	1060	0	LQFP176	119	1.62- 3.6	12	2 2	5	1 0	0	3	28	2 2	2 2	2 6	3	1[QUAD]	4	8		2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 () 4	1	1	1	1 N	ΙY	N	N	Y	125
STM32H755llKx	M7 & M4	480 & 240	2048	1060	0	UFBGA176	128	1.62- 3.6	12	2 2	5	1 0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	1 [2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 () 4	1	1	1	1 N	ΙY	N	N	Y	125
STM32H755BITx	M7 & M4	240	2048	1060	0	LQFP208	148	1.62- 3.6	12	2 2	5	1 0	0	3	32	2 2	2 2	2 6	3	1[QUAD]	4	8		2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 () 4	1 1	1	1	1 N	ΙY	N	N	Y	125
STM32H755XIHx	M7 & M4	480 & 240	2048	1060	0	TFBGA240	168	1.62- 3.6	12	2 2	5	1 0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	1 [2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 () 4	1	1	1	1 N	ΙY	N	N,	Y	125
											S	ГМ32	2H74	47 _	757	Dua	ıl C	ore l	MIP	I_DSI Gr	aph	ic Li	ine																			
STM32H747AGI6	M7 & M4	480 & 240	1024	1060	0	UFBGA169	112	1.62- 3.6	12	2 2	5	1 0	0	3	28	2 2	2 2	6	3	1[QUAD]	4	8		2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 1	1 4	1	1	1	1 N	ΙY	N	N I	N N	85
STM32H747IGT6	M7 & M4	480	1024	1060	0	LQFP176	119	1.62- 3.6	12	2 2	5	1 0	0	3	28	2 2	2 2	2 6	3	1[QUAD]	4	8	1 [2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 1	1 4	1	1	1	1 N	ΙY	N	N I	N N	85
STM32H747BGT6	M7 & M4	240	1024	1060	0	LQFP208	148	1.62- 3.6	12	2 2	5	1 0	0	3	32	2 2	2 2	6	3	1[QUAD]	4	8	1 [2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 1	1 4	1	1	1	1 N	ΙY	N	N	N N	l 85
STM32H747XGH6	M7 & M4	480 & 240	1024	1060	0	TFBGA240	168	1.62- 3.6	12	2 2	5	1 0	0	3	36	2 2	2 2	6	3	1[QUAD]	4	8	7	2 FD]	2 1	1	1	1 1	Υ	Υ	N	1 1	1 4	1	1	1	1 N	ΙΥ	N	N	N N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	LPlimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	COMP	OPAMP	SPI	I2S	M-SPI	12C	U(S)ART	LPUART	CAN	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	MDIO MDIO	GPU	3D GPU	TFT LCD	MIPI DSI	SAI	CEDIEBY	DCMI	SWPMI	Math Accelerator	TRNG	OTFDEC	AES/DES	SHA/HMAC	T° Max (°C)
STM32H747ZIY6	M7 & M4	480 & 240	2048	1060	0	WLCSP156	99	1.62- 3.6	12	2 :	2 5	1	0	0	3	23 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	/ Y	N	1	1	4	1 1	1	1	N	Υ	N I	N N	N	85
STM32H747All6	M7 & M4	480 & 240	2048	1060	0	UFBGA169	112	1.62- 3.6	12	2	2 5	1	0	0	3	28 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	/ Y	N	1	1	4	1 1	1	1	N	Υ	N N	N N	N	85
STM32H747IIT6	M7 & M4	480 & 240	2048	1060	0	LQFP176	119	1.62- 3.6	12	2	2 5	1	0	0	3	28 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	Υ	N	1	1	4	1 1	1	1	N	Υ	N N	N N	N	85
STM32H747BIT6	M7 & M4	480 & 240	2048	1060	0 0	LQFP208	148	1.62- 3.6	12	2	2 5	1	0	0	3	32 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	Υ	N	1	1	4	1 1	1	1	N	Υ	N I	N N	N	85
STM32H747XIH6	M7 & M4	480 & 240	2048	1060	0 0	TFBGA240	168	1.62- 3.6	12	2	2 5	1	0	0	3	36 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	Υ	N	1	1	4	1 1	1	1	N	Υ	N I	N N	N	85
STM32H757ZIY6	M7 & M4	480 & 240	2048	1060	0	WLCSP156	99	1.62- 3.6	12	2	2 5	1	0	0	3	23 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	Υ	N	1	1	4	1 1	1	1	N	Υ	N N	V Y	Υ	85
STM32H757All6	M7 & M4	480 & 240	2048	1060	0 0	UFBGA169	112	1.62- 3.6	12	2	2 5	1	0	0	3	28 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	Υ	N	1	1	4	1 1	1	1	N	Υ	N N	N Y	Υ	85
STM32H757IIT6	M7 & M4	480 & 240	2048	1060	0	LQFP176	119	1.62- 3.6	12	2	2 5	1	0	0	3	28 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	Υ	N	1	1	4	1 1	1	1	N	Υ	N I	1 Y	Υ	85
STM32H757BIT6	M7 & M4	480 & 240	2048	1060	0	LQFP208	148	1.62- 3.6	12	2	2 5	1	0	0	3	32 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 D]	2 1	1	1	1	1 \	/ Y	N	1	1	4	1 1	1	1	N	Υ	N N	V Y	Υ	85
STM32H757XIH6	M7 & M4	480 & 240	2048	1060	0	TFBGA240	168	1.62- 3.6	12	2	2 5	1	0	0	3	36 2	2	2	6	3	1[QUAD]	4	8	1 [F	2 -D]	2 1	1	1	1	1 \	/ Y	N	1	1	4	1 1	1	1	N	Υ	N N	N Y	Υ	85
													STN	132	H7/	3_7	В3	Sin	gle	Co	re Graph	ic Li	ine																					
STM32H7A3RIT6	Cortex -M7	280	2048	1380	0	LQFP64	49	1.62- 3.6	12	2	2 3	0	0	0	2	16 3	1	1	4	4	2[0CT0]	3	7	1 [F	2 D]	2 0	1	1	0	1 \	′ Y	N	1	0	1 1	1 2	1	1	N	Υ	N N	N N	N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 15-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	COMP	OPAMP	IZS	M-SPI	120	U(S)ART	CAN	SDIO SDIO	E/G/MC	USB HS OTG	Ethernet	MDIO	JPEG Codec	3D GPU	TFT LCD	MIPI DSI	SAI	SPOIFRX	DGMI	SWPMI	Math Accelerator	TRNG	OTFDEC	AES/DES	SHA/HMAC	T° Max (°C)
STM32H7A3RGT6	Cortex -M7	280	1024	1380	0	LQFP64	49	1.62- 3.6	12 2	2 2	3	0	0 (0 2	16	3	1	1 4	1 4	2[0CT0]	3	7	1 2 [FD]	2	0 -	1 1	0	1	Y	/ N	1	0	1	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3VIH6	Cortex -M7	280	2048	1380	0	TFBGA100	80	1.62- 3.6	12 2	2 2	3	0	0 (0 2	16	3	2	2 !	5 4	2[0CT0]	4	10	1 2 [FD]	2	0 -	1 1	0	1	Y	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	N	85
STM32H7A3VGH6	Cortex -M7	280	1024	1380	0	TFBGA100	80	1.62- 3.6	12 2	2 2	3	0	0 (0 2	16	3	2	2	5 4	2[0CT0]	4	10	1 2 [FD]	2	0 -	1 1	0	1	ΥY	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3VIT6	Cortex -M7	280	2048	1380	0	LQFP100	80	1.71- 3.6	12 2	2 2	3	0	0 (0 2	16	3	2	2	5 4	2[0CT0]	4	10	1 2 [FD]	2	0 -	1 1	0	1	ΥY	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3VGT6	Cortex -M7	280	1024	1380	0	LQFP100	80	1.71- 3.6	12 2	2 2	3	0	0 (0 2	16	3	2	2 !	5 4	2[0CT0]	4	10	1 2 [FD]	2	0 -	1 1	0	1	ΥY	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3VIH6Q	Cortex -M7	280	2048	1380	0	TFBGA100	75	1.62- 3.6	12 2	2 2	3	0	0 (0 2	16	3	2	2	5 4	2[0CT0]	4	10	1 2 [FD]	2	0 -	1 1	0	1	ΥY	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3VGH6Q	Cortex -M7	280	1024	1380	0	TFBGA100	75	1.62- 3.6	12 2	2 2	3	0	0 (0 2	16	3	2	2	5 4	2[0CT0]	4	10	1 2 [FD]	2 (0 -	1 1	0	1	Y	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3VIT6Q	Cortex -M7	280	2048	1380	0	LQFP100	68	1.71- 3.6	12 2	2 2	3	0	0 (0 2	16	3	2	2 !	5 4	2[0CT0]	4	9	1 2 [FD]	2	0 -	1 1	0	1	ΥY	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3VGT6Q	Cortex -M7	280	1024	1380	0	LQFP100	68	1.71- 3.6	12 2	2 2	3	0	0 (0 2	16	3	2	2	5 4	2[0CT0]	4	9	1 2 [FD]	2	0 -	1 1	0	1	Y	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3QIY6Q	Cortex -M7	280	2048	1380	0	WLCSP132	87	1.62- 3.6	12 2	2 2	3	0	0 (0 2	17	3	2	2	5 4	2[0CT0]	4	10	1 2 [FD]	2	0 -	1 1	0	1	Y	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85
STM32H7A3ZIT6	Cortex -M7	280	2048	1380	0	LQFP144	112	1.62- 3.6	12 2	2 2	3	0	0 (0 2	20	3	2	2 (6 4	2[0CT0]	4	10	1 2 [FD]	2	0 -	1 1	0	1	Υ	/ N	1	0	2	1 2	2 1	1	N	Υ	N	N N	I N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD		Timer (32-bit)		High Resolution Timer	2-bit Uni	ADC 12-hit Channels	ADC 16-bit linits	ADC 16 hit Channels	COMP	OPAMP	SPI	128	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	JPEG Codec	GPU	3D GPU	TET LCD	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	TRNG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H7A3ZGT6	Cortex -M7	280	1024	1380	0	LQFP144	112	1.62- 3.6	12	2 2	3	0	0	0	2 2	0 3	2	2	6	4	2[OCTO]	4	10	1	2 [FD]	2 0	1	1	0 1	Υ	Υ	N	1 () 2	2 1	2	1	1	N	Y	N N	I N	N	85
STM32H7A3ZIT6Q	Cortex -M7	280	2048	1380	0	LQFP144	97	1.62- 3.6	12	2 2	2 3	0	0	0	2 1	8 3	2	2	6	4	2[0CT0]	4	10	1	2 [FD]	2 0	1	1	0 1	Υ	Υ	N	1 () 2	2 1	2	1	1	N '	Y	N N	I N	N	85
STM32H7A3ZGT6Q	Cortex -M7	280	1024	1380	0	LQFP144	97	1.62- 3.6	12	2 2	3	0	0	0	2 1	8 3	2	2	6	4	2[0CT0]	4	10	1	2 [FD]	2 0	1	1	0 1	Υ	Υ	N	1 () 2	2 1	2	1	1	N '	Y	N N	I N	N	85
STM32H7A3AII6Q	Cortex -M7	280	2048	1380	0	UFBGA169	121	1.62- 3.6	12	2 2	2 3	0	0	0	2 2	4 3	2	2	6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 () 2	2 1	2	1	1	N '	Y	N N	I N	N	85
STM32H7A3AGI6Q	Cortex -M7	280	1024	1380	0	UFBGA169	121	1.62- 3.6	12	2 2	2 3	0	0	0	2 2	4 3	2	2	6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 () 2	2 1	2	1	1	N,	Y	N N	I N	N	85
STM32H7A3IIK6	Cortex -M7	280	2048	1380	0	UFBGA176	138	1.62- 3.6	12	2 2	2 3	0	0	0	2 2	0 2	2	2	6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1) 2	2 1	2	1	1	N '	Y	N N	I N	N	85
STM32H7A3IGK6	Cortex -M7	280	1024	1380	0	UFBGA176	138	1.62- 3.6	12	2 2	2 3	0	0	0	2 2	0 3	2	2	6	4	2[OCTO]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 () 2	2 1	2	1	1	N '	Y	N N	I N	N	85
STM32H7A3IIT6	Cortex -M7	280	2048	1380	0	LQFP176	138	1.62- 3.6	12	2 2	2 3	0	0	0	2 2	0 3	2	2	6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 () 2	2 1	2	1	1	N '	Y	N N	I N	N	85
STM32H7A3IGT6	Cortex -M7	280	1024	1380	0	LQFP176	138	1.62- 3.6	12	2 2	2 3	0	0	0	2 2	0 3	2	2	6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1) 2	2 1	2	1	1	N '	Y	N N	I N	N	85
STM32H7A3IIK6Q	Cortex -M7	280	2048	1380	0	UFBGA176	128	1.62- 3.6	12	2 2	2 3	0	0	0	2 2	4 3	2	2	6	4	2[OCTO]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1) 2	2 1	2	1	1	N,	Y	N N	I N	N	85
STM32H7A3IGK6Q	Cortex -M7	280	1024	1380	0	UFBGA176	128	1.62- 3.6	12	2 2	2 3	0	0	0	2 2	4 3	2	2	6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1) 2	2 1	2	1	1	N '	Y	N N	I N	N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	High Resolution Timer		ADC 12-bit Channels	ADC 16-bit Units		DAC 12-bit Channels	COMP	ODAMD	128	M-SPI	12C	U(S)ART	LPUART	CAN	F(S)MC SDIO	USB FS HOST/OTG	USB HS OTG	Ethernet	MDIO MDIO	GPU	3D GPU	TFT LGD	MIPI DSI	SAI	SPOIERY	DCMI	SWPMI	Math Accelerator	TRNG	OTFDEC	PKA	VEC/DEC	T° Max (°C)
STM32H7A3IIT6Q	Cortex -M7	280	2048	138	0 0	LQFP176	119	1.62- 3.6	12	2	2 3	3 0	0	0	2	20	3	2	2 6	6 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	1	1	N	Υ	N	N N	N N	N 85
STM32H7A3IGT6Q	Cortex -M7	280	1024	138	0 0	LQFP176	119	1.62- 3.6	12	2	2 3	3 0	0	0	2	20	3	2	2 6	6 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	1	1	N	Υ	N	N N	N N	N 85
STM32H7A3NIH6	Cortex -M7	280	2048	138	0 0	TFBGA216	166	1.62- 3.6	12	2	2 3	3 0	0	0	2	20	3	2	2 6	6 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	1	1	N	Υ	N	N N	N N	N 85
STM32H7A3NGH6	Cortex -M7	280	1024	138	0 0	TFBGA216	166	1.62- 3.6	12	2	2 3	3 0	0	0	2	20	3	2	2 6	6 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	! 1	1	N	Υ	N	N N	N N	N 85
STM32H7A3LIH6Q	Cortex -M7	280	2048	138	0 0	TFBGA225	168	1.62- 3.6	12	2	2 3	3 0	0	0	2	24	3	2	2 6	6 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	! 1	1	N	Υ	N	N N	N N	N 85
STM32H7A3LGH6Q	Cortex -M7	280	1024	138	0 0	TFBGA225	168	1.62- 3.6	12	2	2 3	3 0	0 0	0	2	24	3	2	2 6	6 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	! 1	1	N	Υ	N	N N	N N	N 85
STM32H7B3RIT6	Cortex -M7	280	2048	138	0 0	LQFP64	49	1.62- 3.6	12	2	2 3	3 0	0 0	0	2	16	3	1	1 4	1 4	2[0CT0]	3	7	1 [2 [FD]	2 0	1	1	0	1 \	′ Y	N	1	0	1	1 2	1	1	N	Υ	Υ	N Y	Y	/ 85
STM32H7B3VIH6	Cortex -M7	280	2048	138	0 0	TFBGA100	80	1.62- 3.6	12	2	2 3	3 0	0	0	2	16	3	2	2 5	5 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	1	1	N	Υ	Υ	N Y	ΥY	/ 85
STM32H7B3VIT6	Cortex -M7	280	2048	138	0 0	LQFP100	80	1.71- 3.6	12	2	2 3	3 0	0 0	0	2	16	3	2	2 5	5 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	1	1	N	Υ	Υ	N Y	Y	/ 85
STM32H7B3VIH6Q	Cortex -M7	280	2048	138	0 0	TFBGA100	75	1.62- 3.6	12	2	2 3	3 0	0	0	2	16	3	2	2 5	5 4	2[0CT0]	4	10	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	1	1	N	Υ	Υ	N Y	ΥY	/ 85
STM32H7B3VIT6Q	Cortex -M7	280	2048	138	0 0	LQFP100	68	1.71- 3.6	12	2	2 3	3 0	0 0	0	2	16	3	2	2 5	5 4	2[0CT0]	4	9	1 [2 [FD]	2 1	1	1	0	1 \	′ Y	N	1	0	2	1 2	1	1	N	Υ	Υ	N Y	Υ	/ 85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit) Timer (32-bit)	LPTimer	High Resolution Timer	ADC 12-bit Channels	Units	13	DAC 12-bit Channels	COMP	SPI	128	M-SPI	120	U(S)ART	LPUART	CAN	F(S)MC	USB FS HOST/OTG	USB HS OTG	MUIO	JPEG Codec	GPU	3D GPU	TET I CD	SAI	SPDIFRX	DFSDM	SWPMI	Math Accelerator	TRNG	OTFDEC	PKA	SHA/HMAC	T° Max (°C)
STM32H7B3QIY6Q	Cortex -M7	280	2048	1380	0	WLCSP132	87	1.62- 3.6	12	2 2	3	0	0 0	2	17	3	2 2	2 5	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (2	1	2 1	1	N	Υ	Υ	N '	Y	85
STM32H7B3ZIT6	Cortex -M7	280	2048	1380	0	LQFP144	112	1.62- 3.6	12	2 2	3	0	0 0	2	20	3	2 2	2 6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (0 2	1	2 1	1	N	Υ	Υ	N '	Y	85
STM32H7B3ZIT6Q	Cortex -M7	280	2048	1380	0	LQFP144	97	1.62- 3.6	12	2 2	3	0	0 0	2	18	3	2 2	2 6	4	2[OCTO]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (0 2	1	2 1	1	N	Υ	Υ	N '	Y	85
STM32H7B3All6Q	Cortex -M7	280	2048	1380	0	UFBGA169	121	1.62- 3.6	12	2 2	3	0	0 0	2	24	3	2 2	2 6	4	2[OCTO]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (0 2	1	2 1	1	N	Υ	Υ	N '	ΥY	85
STM32H7B3IIK6	Cortex -M7	280	2048	1380	0	UFBGA176	138	1.62- 3.6	12	2 2	3	0	0 0	2	20	2	2 2	2 6	4	2[OCTO]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (0 2	1	2 1	1	N	Υ	Υ	N '	Y	85
STM32H7B3IIT6	Cortex -M7	280	2048	1380	0	LQFP176	138	1.62- 3.6	12	2 2	3	0	0 0	2	20	3	2 2	2 6	4	2[OCTO]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (0 2	1	2 1	1	N	Υ	Υ	N '	YY	85
STM32H7B3llK6Q	Cortex -M7	280	2048	1380	0	UFBGA176	128	1.62- 3.6	12	2 2	3	0	0 0	2	24	3	2 2	2 6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (2	1	2 1	1	N	Υ	Υ	N '	ΥY	85
STM32H7B3IIT6Q	Cortex -M7	280	2048	1380	0	LQFP176	119	1.62- 3.6	12	2 2	3	0	0 0	2	20	3	2 2	2 6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (0 2	1	2 1	1	N	Υ	Υ	N '	YY	85
STM32H7B3NIH6	Cortex -M7	280	2048	1380	0	TFBGA216	166	1.62- 3.6	12	2 2	3	0	0 0	2	20	3	2 2	2 6	4	2[0CT0]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (0 2	1	2 1	1	N	Υ	Υ	N '	Y	85
STM32H7B3LIH6Q	Cortex -M7	280	2048	1380	0	TFBGA225	168	1.62- 3.6	12	2 2	3	0	0 0	2	24	3	2 2	2 6	4	2[OCTO]	4	10	1	2 [FD]	2 1	1	1	0 1	Υ	Υ	N	1 (0 2	1	2 1	1	N	Υ	Υ	N '	ΥY	85
													STN	132H	172 3	_ 7 3	33_	725	_73	5 Access	Lin	e																				
STM32H723VET6	Cortex -M7	550	512	564	0	LQFP100	80	1.71- 3.6	12	4 2	5	0	1 4	2	26	2	2 2	2 5	4	2[OCTO]	5	10	1	3 [FD]	2 1	1	1	1 1	N	Υ	N	1 (0 2	1	1 1	1	Υ	Υ	N	N N	N N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced Ilmer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	261	M-SPI	12C	U(S)ART	CAN	SDI0	F(S)MC	USB FS HOST/OTG	LISB HS OTG	MDIO	JPEG Codec	GPU C	אם הפון ורו בניט	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	SWDMI SWDMI	Math Accolorator	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H723VEH6	Cortex -M7	550	512	564	0	TFBGA100	80	1.62- 3.6	12	4 2	5	0	1	17	2 26	6 2	2	2	5 4	4	2[0CT0]	5	10	1 [FD]	2	1	1	1 1	1	N	Y	N 1	0	2	1	1	1	1 '	Y	N	N	N	N	85
STM32H723VGT6	Cortex -M7	550	1024	564	0	LQFP100	80	1.71- 3.6	12	4 2	5	0	1	4	2 26	6 2	2	2	5 4	4	2[0CT0]	5	10	1 (FD)	2	1	1	1 1	1	N	ΥI	N -	0	2	1	1	1	1 ,	YY	' N	N	N	N	85
STM32H723VGH6	Cortex -M7	550	1024	564	0	TFBGA100	80	1.62- 3.6	12	4 2	5	0	1	17	2 26	6 2	2	2	5 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N -	1 0	2	1	1	1	1 '	YY	' N	N	N	N	85
STM32H723ZET6	Cortex -M7	550	512	564	0	LQFP144	112	1.62- 3.6	12	4 2	5	0	1	12	2 30	2	2	2	6 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N -	1 0	2	1	1	1	1 '	YY	' N	N	N	N	85
STM32H723ZEI6	Cortex -M7	550	512	564	0	UFBGA144	114	1.62- 3.6	12	4 2	5	0	1	17	2 34	1 2	2	2	6 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N -	0	2	1	1	1	1 '	Y	' N	N	N	N	85
STM32H723ZGT6	Cortex -M7	550	1024	564	0	LQFP144	112	1.62- 3.6	12	4 2	5	0	1	12	2 30	2	2	2	6 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	Υ	N -	1 0	2	1	1	1	1 '	Y	' N	N	N	N	85
STM32H723ZGI6	Cortex -M7	550	1024	564	0	UFBGA144	114	1.62- 3.6	12	4 2	5	0	1	17	2 34	1 2	2	2	6 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N 1	0	2	1	1	1	1 '	Y	'N	N	N	N	85
STM32H733VGT6	Cortex -M7	550	1024	564	0	LQFP100	80	1.71- 3.6	12	4 2	5	0	1	4	2 26	5 2	2	2	5 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	١ -	1 0	2	1	1	1	1 '	Y	Υ	N	Υ	Υ	85
STM32H733VGH6	Cortex -M7	550	1024	564	0	TFBGA100	80	1.62- 3.6	12	4 2	5	0	1	17	2 26	6 2	2	2	5 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	۱ -	0	2	1	1	1	1 '	YY	′ Y	N	Υ	Υ	85
STM32H733ZGT6	Cortex -M7	550	1024	564	0	LQFP144	112	1.62- 3.6	12	4 2	5	0	1	12	2 30	2	2	2	6 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	۱ -	0	2	1	1	1	1 '	YY	′ Y	N	Υ	Υ	85
STM32H733ZGI6	Cortex -M7	550	1024	564	0	UFBGA144	114	1.62- 3.6	12	4 2	5	0	1	17	2 34	1 2	2	2	6 4	4	2[0CT0]	5	10	1 (FD)	2	1	1	1 1	1	N	ΥI	N -	0	2	1	1	1	1 '	YY	′ Y	N	Υ	Υ	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	96	M-SPI	120	U(S)ART	CAN	SDI0	F/S)MC	USB ES HOST/OTG	Ethernet	MDIO	JPEG Codec	GPU C	3D GPII	MIPI USI	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	TRNC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H725REVx	Cortex -M7	550	512	564	0	VFQFPN68	46	1.71- 3.6	12	4 2	2 5	0	1	2	2 20	5 2	2	2	4	4	1[QUAD]	4	7	1 2 [FD]	2	1	1 (0 1	1	N	Υ	N	1 0	2	1	1	1	1 '	YY	/ N	l N	N	N	125
STM32H725RGVx	Cortex -M7	550	1024	564	0	VFQFPN68	46	1.71- 3.6	12	4 2	2 5	0	1	2	2 20	5 2	2	2	4	4	1[QUAD]	4	7	1 2 [FD]	2	1	1 (0 1	1	N	Υ	N	1 0	2	1	1	1	1	ΥY	/ N	I N	N	N	125
STM32H725VETx	Cortex -M7	550	512	564	0	LQFP100	67	1.71- 3.6	12	4 2	2 5	0	1	4	2 20	6 2	2	2	5 4	4	2[QUAD]	5	8	1 3 [FD]	2	1	1	1 1	1	N	Υ	N	1 0	2	1	1	1	1	ΥY	/ N	l N	N	N	125
STM32H725VGTx	Cortex -M7	550	1024	564	0	LQFP100	67	1.71- 3.6	12	4 2	2 5	0	1	4	2 20	6 2	2	2	5	4	2[QUAD]	5	8	1 3 [FD]	2	1	1	1 1	1	N	Υ	N	1 0	2	1	1	1	1	ΥY	/ N	I N	N	N	125
STM32H725VEHx	Cortex -M7	550	512	564	0	TFBGA100	74	1.62- 3.6	12	4 2	2 5	0	1	17	2 20	6 2	2	2	5 4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	Υ	N	1 0	2	1	1	1	1	ΥY	/ N	I N	N	N	125
STM32H725VGHx	Cortex -M7	550	1024	564	0	TFBGA100	74	1.62- 3.6	12	4 2	2 5	0	1	17	2 20	6 2	2	2	5	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	Υ	N	1 0	2	1	1	1	1	ΥY	/ N	I N	N	N	125
STM32H725VGY6	Cortex -M7	550	1024	564	0	WLCSP115	67	1.62- 3.6	12	4 2	2 5	0	1	17	2 2	6 2	2	2	6	4	2[QUAD]	5	8	1 3 [FD]	2	1	1	0 1	1	N	Υ	N	1 0	2	1	1	1	1 '	ΥY	/ N	I N	N	N	85
STM32H725ZETx	Cortex -M7	550	512	564	0	LQFP144	97	1.62- 3.6	12	4 2	2 5	0	1	9 :	2 28	3 2	2	2	6	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	Υ	N	1 0	2	1	1	1	1	ΥY	/ N	I N	N	N	125
STM32H725ZGTx	Cortex -M7	550	1024	564	0	LQFP144	97	1.62- 3.6	12	4 2	2 5	0	1	9	2 28	3 2	2	2	6	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	Υ	N	1 0	2	1	1	1	1	ΥY	/ N	I N	N	N	125
STM32H725AEIx	Cortex -M7	550	512	564	0	UFBGA169	121	1.62- 3.6	12	4 2	2 5	0	1	17	2 38	3 2	2	2	4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	Υ	N	1 0	2	1	1	1	1	YY	/ N	I N	N	N	125
STM32H725AGIx	Cortex -M7	550	1024	564	0	UFBGA169	121	1.62- 3.6	12	4 2	2 5	0	1	17	2 3	3 2	2	2	4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	Υ	N	1 0	2	1	1	1	1	YY	/ N	I N	N	N	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced Timer (15-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	COMP	OPAMP	SPI	I2S	M-SPI	120	U(S)ART	CAN	SDI0	F(S)MC	USB FS HOST/OTG	Ethernet	MDIO	JPEG Codec	GPU G	3D GBII	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	TRNC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H725IEKx	Cortex -M7	550	512	564	0	UFBGA176	128	1.62- 3.6	12	4 2	2 5	0	1	17	2 3	88 2	2	2	6	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	Y	N 1	0	2	1	1	1	1 '	YY	/ N	N	N	N -	125
STM32H725IGKx	Cortex -M7	550	1024	564	0	UFBGA176	128	1.62- 3.6	12	4 2	2 5	0	1	17	2 3	88 2	2	2	6	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N 1	1 0	2	1	1	1	1	ΥY	/ N	N	N	N -	125
STM32H725IETx	Cortex -M7	550	512	564	0	LQFP176	119	1.62- 3.6	12	4 2	2 5	0	1	12	2 3	80 2	2	2	6	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N 1	1 0	2	1	1	1	1	ΥY	/ N	N	N	N -	125
STM32H725IGTx	Cortex -M7	550	1024	564	0	LQFP176	119	1.62- 3.6	12	4 2	2 5	0	1	12	2 3	30 2	2	2	6	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N 1	0	2	1	1	1	1	ΥY	/ N	N	N	N -	125
STM32H735RGVx	Cortex -M7	550	1024	564	0	VFQFPN68	46	1.71- 3.6	12	4 2	2 5	0	1	2	2 2	26 2	2	2	4	4	1[QUAD]	4	7	1 2 [FD]	2	1	1	0 1	1	N	ΥΙ	N 1	1 0	2	1	1	1	1	ΥY	/ Y	N	Υ	γ.	125
STM32H735VGTx	Cortex -M7	550	1024	564	0	LQFP100	67	1.71- 3.6	12	4 2	2 5	0	1	4	2 2	26 2	2	2	5	4	2[QUAD]	5	8	1 3 [FD]	2	1	1	1 1	1	N	Υ	N 1	1 0	2	1	1	1	1	ΥY	/ Y	N	Υ	γ.	125
STM32H735VGHx	Cortex -M7	550	1024	564	0	TFBGA100	74	1.62- 3.6	12	4 2	2 5	0	1	17	2 2	26 2	2	2	5	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N 1	0	2	1	1	1	1 '	ΥY	/ Y	N	Υ	γ -	125
STM32H735VGY6	Cortex -M7	550	1024	564	0	WLCSP115	67	1.62- 3.6	12	4 2	2 5	0	1	17	2 2	26 2	2	2	6	4	2[QUAD]	5	8	1 3 [FD]	2	1	1	0 1	1	N	ΥI	N 1	0	2	1	1	1	1	ΥY	/ Y	N	Υ	Υ	85
STM32H735ZGTx	Cortex -M7	550	1024	564	0	LQFP144	97	1.62- 3.6	12	4 2	2 5	0	1	9	2 2	28 2	2	2	6	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N 1	0	2	1	1	1	1	ΥY	/ Y	N	Υ	Υ -	125
STM32H735AGlx	Cortex -M7	550	1024	564	0	UFBGA169	121	1.62- 3.6	12	4 2	2 5	0	1	17	2 3	88 2	2	2	4	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N 1	0	2	1	1	1	1	ΥY	/ Y	N	Υ	Υ -	125
STM32H735IGKx	Cortex -M7	550	1024	564	0	UFBGA176	128	1.62- 3.6	12	4 2	2 5	0	1	17	2 3	88 2	2	2	6	4	2[0CT0]	5	10	1 3 [FD]	2	1	1	1 1	1	N	ΥI	N 1	0	2	1	1	1	1	ΥY	/ Y	N	Υ	γ .	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	LPTimer /16	High Resolution Timer	2-bit U	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels		OPAMP	SPI	I2S	M-SPI	12C	U(S)ART	LPHART	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	MDIO	JPEG Codec	GPU	3D GPU	TET I CD	MIDLING	SPUIFKX	DFSDM	DCMI	SWPMI	Math Accelerator	TRNG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H735IGTx	Cortex	550	1024	564	0	LQFP176	119	1.62- 3.6		4 2						30 2		2	6	4	2[0CT0]	5	10	1 (F	3 2	1		1	1 1	N	Y	N	1 (0 2) 1	1		1		Y	Y	J Y	v	125
OTHIOLITY COIGTX	-M7	000	1021	001		2011110	110	3.6													Boot Fla			· [F	D] -	,	•	•			·		· `		•	,				•				120
STM32H7R3A8I6	Cortex -M7	600	64	620	0	UFBGA169	116	1.71- 3.6	7	4	1 5	Г					T				1[0CT0]			1 [F	2 D] 2	1	1	1	1 1	Υ	N	N	0 0	0 2	2 1	0	1	1	Υ	ΥI	N N	1 N	N	85
STM32H7R3I8K6	Cortex -M7	600	64	620	0	UFBGA176 +25	122	1.71- 3.6	7	4	1 5	0	2	18	0	0 0	0	0	6	4	1[0CT0]	3/1	7	1 [F	2 D] 2	1	1	1	1 1	Υ	N	N	0 (0 2	2 1	0	1	1	Υ	ΥI	N N	1 N	N	85
STM32H7R3I8T6	Cortex -M7	600	64	620	0	LQFP176	119	1.71- 3.6	7	4	1 5	0	2	17	0	0 0	0	0	6	4	1[0CT0]	3/1	7	1 [F	2 D] 2	1	1	1	1 1	Υ	N	N	0 (0 2	2 1	0	1	1	Υ	ΥI	N N	I N	N	85
STM32H7R3L8H6	Cortex -M7	600	64	620	0	TFBGA225	152	1.71- 3.6	7	4	1 5	0	2	20	0	0 0	0	0	6	4	2[0CT0]	3/1	7	1 [F	2 D] 2	1	1	1	1 1	Υ	N	N	0 (0 2	2 1	0	1	1	Υ	ΥI	N N	1 N	N	85
STM32H7R3L8H6H	Cortex -M7	600	64	620	0	TFBGA225	150	1.71- 3.6	7	4	1 5	0	2	20	0	0 0	0	0	6	4	1[0CT0] +1[HEXA]	3/1	7	1 [F	2 D] 2	1	1	1	1 1	Υ	N	N	0 (0 2	2 1	0	1	1	Υ	ΥI	N N	1 N	N	85
STM32H7R3R8V6	Cortex -M7	600	64	620	0	VQFN68	45	1.71- 3.6	6	2	1 3	0	2	10	0	0 0	0	0	4	3	1[0CT0]	3/1	6	1 [F	2 D] 0	1	1	0 () 1	Υ	N	N	0 () 1	0	0	0	1	Υ	ΥI	N N	I N	N	85
STM32H7R3V8H6	Cortex -M7	600	64	620	0	TFBGA100	63	1.71- 3.6	7	1 1	1 3	0	2	11	0	0 0	0	0	5	4	1[0CT0]	3/1	5	1 [F	<u>?</u> D] 1	1	1	1 () 1	Υ	N	N	0 () 1	0	0	0	1	Υ	ΥI	N N	1 N	N	85
STM32H7R3V8T6	Cortex -M7	600	64	620	0	LQFP100	67	1.71- 3.6	7	2	3	0	2	12	0	0 0	0	0	5	4	1[0CT0]	3/1	6	1 [F	2 D] 1	1	1	1 () 1	Υ	N	N	0 (0 1	0	0	0	1	Υ	ΥI	N N	1 N	N	85
STM32H7R3V8Y6	Cortex -M7	600	64	620	0	WLCSP101	65	1.71- 3.6	5	1	1 3	0	2	11	0	0 0	0	0	5	4	1[0CT0]	3/1	5	1 [F	2 D] 1	1	1	1 () 1	Υ	N	N	0 (0 1	1	0	0	1	Υ	ΥI	N N	1 N	N	85
STM32H7R3Z8J6	Cortex -M7	600	64	620	0	UFBGA144	94	1.71- 3.6	7	4	1 5	0	2	17	0	0 0	0	0	6	4	1[0CT0]	3/1	7	1 [F	2 D] 2	1	1	1	1 1	Υ	N	N	0	0 2	2 1	0	1	1	Υ	ΥI	N N	1 N	N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Advanced limer (16-bit) Timer (32-bit)	LPTimer	High Resolution Timer	ADC 12-bit Units		ADC 16-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	261	M-SPI	120	U(S)ART	CAN	SDI0	F(S)MC	USB ES HOST/OTG	IISB HS OTG	MDIO	JPEG Codec	GPII	3D COLL	MIPI DSI	SAI	SPDIFRX	DFSDM	DOMI	Wath Accelerator	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H7R3Z8T6	Cortex -M7	600	64	620	0	LQFP144	98	1.71- 3.6	7	4 1	5	0	2 1	6	0 0	0	0	0	6	4 1	1[0CT0]	3/1	7	1 2 [FD	2	1	1	1 1	1	Υ	N I	N O	0	2	1	0	1 1	1 Y	′ Y	N	N	N	N	85
STM32H7S3A8I6	Cortex -M7	600	64	620	0	UFBGA169	116	1.71- 3.6	7	4 1	5	0	2 1	6	0 0	0	0	0	6	4 1	1[0CT0]	3/1	7	1 2 [FD	2	1	1	1 1	1	Υ	N I	N O	0	2	1	0	1 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3I8K6	Cortex -M7	600	64	620	0	UFBGA176 +25	122	1.71- 3.6	7	4 1	5	0	2 1	8	0 0	0	0	0	6	4 1	1[OCTO]	3/1	7	1 2 [FD	2	1	1	1 1	1	Υ	N I	N O	0	2	1	0	1 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3I8T6	Cortex -M7	600	64	620	0	LQFP176	119	1.71- 3.6	7	4 1	5	0	2 1	7	0 0	0	0	0	6	4 1	1[OCTO]	3/1	7	1 2 [FD	2	1	1	1 1	1	Υ	N I	N O	0	2	1	0	1 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3L8H6	Cortex -M7	600	64	620	0	TFBGA225	152	1.71- 3.6	7	4 1	5	0	2 2	20	0 0	0	0	0	6	4 2	2[0CT0]	3/1	7	1 2 [FD	2	1	1	1 1	1	Υ	N I	N O	0	2	1	0	1 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3L8H6H	Cortex -M7	600	64	620	0	TFBGA225	150	1.71- 3.6	7	4 1	5	0	2 2	20	0 0	0	0	0	6	4 +	1[0CT0] -1[HEXA]	3/1	7	1 2 [FD	2	1	1	1 1	1	Υ	N	N O	0	2	1	0	1 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3R8V6	Cortex -M7	600	64	620	0	VQFN68	45	1.71- 3.6	6	2 1	3	0	2 1	0	0 0	0	0	0	4 :	3 1	1[OCTO]	3/1	6	1 2 [FD	0	1	1	0 () 1	Υ	N I	N O	0	1	0	0	0 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3V8H6	Cortex -M7	600	64	620	0	TFBGA100	63	1.71- 3.6	7	1 1	3	0	2 1	1	0 0	0	0	0	5 4	4 1	1[OCTO]	3/1	5	1 2 [FD	1	1	1	1 () 1	Υ	N I	N O	0	1	0	0	0 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3V8T6	Cortex -M7	600	64	620	0	LQFP100	67	1.71- 3.6	7	2 1	3	0	2 1	2	0 0	0	0	0	5	4 1	1[OCTO]	3/1	6	1 2 [FD	1	1	1	1 () 1	Υ	N I	N O	0	1	0	0	0 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3V8Y6	Cortex -M7	600	64	620	0	WLCSP101	65	1.71- 3.6	5	1 1	3	0	2 1	1	0 0	0	0	0	5	4 1	1[0CT0]	3/1	5	1 2 [FD	1	1	1	1 () 1	Υ	N I	N O	0	1	1	0	0 1	1 Y	′ Y	Υ	Υ	Υ	Υ	85
STM32H7S3Z8J6	Cortex -M7	600	64	620	0	UFBGA144	94	1.71- 3.6	7	4 1	5	0	2 1	7	0 0	0	0	0	6	4 1	1[OCTO]	3/1	7	1 2 [FD	2	1	1	1 1	1	Υ	N I	N 0	0	2	1	0	1 1	1 Y	/ Y	Υ	Υ	Υ	Υ	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	ADC 16-bit Units	DAC 12-bit Channels	COMP	OPAMP	SPI	12S	M-SPI	l2C	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	Ethernet	MDIO	JPEG Codec	GPU	3D GPII	MINI USI	SAI	SPDIFRX	DFSDM	DGMI	SWPMI SWPMI	IKNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H7S3Z8T6	Cortex -M7	600	64	620	0	LQFP144	98	1.71- 3.6	7	4	1 5	5 0	2	16	0	0 0	0	0	6	4	1[0CT0]	3/1	7	1 2 [FC	2	1	1 1	1	1	Υ	N	N	0 0) 2	1	0	1	1	/ Y	′ Y	Υ	Υ	Υ	85
STM32H723VET6	Cortex -M7	550	512	564	0	LQFP100	80	1.71- 3.6	12	4	2 5	5 0	1	4	2 2	:6 2	2	2	5	4	2[0CT0]	5	10	1 3 [FC	2	1	1 1	1	1	N	Υ	N	1 () 2	1	1	1	1 \	/ Y	/ N	N	N	N	85
STM32H7R7A8I6	Cortex -M7	600	64	620	0	UFBGA169	117	1.71- 3.6	7	4	1 4	1 0	2	17	0	0 0	0	0	6	4	2[0CT0]	3/1	7	1 2 [FC	2	1	1 1	1 1	1	Υ	Υ	N	1 0) 2	1	0	1	1 \	/ Y	/ N	N	N	N	85
STM32H7R7I8K6	Cortex -M7	600	64	620	0	UFBGA176 +25	122	1.71- 3.6	7	4	1 4	1 0	2	18	0	0 0	0	0	6	4	2[0CT0]	3/1	7	1 2 [FC	2	1	1 1	1 1	1	Υ	Υ	N	1 0) 2	1	0	1	1 \	/ Y	/ N	N	N	N	85
STM32H7R7I8T6	Cortex -M7	600	64	620	0	LQFP176	118	1.71- 3.6	7	4	1 4	1 0	2	17	0	0 0	0	0	6	4	2[0CT0]	3/1	7	1 2 [FE	2	1	1 1	1 1	1	Υ	Υ	N	1 () 2	1	0	1	1 \	γY	/ N	N	N	N	85
STM32H7R7L8H6	Cortex -M7	600	64	620	0	TFBGA225	152	1.71- 3.6	7	4	1 5	5 0	2	20	0	0 0	0	0	6	4	2[0CT0]	3/1	7	1 2 [FE		1	1 -	1 1	1	Υ	Υ	N	1 () 2	1	0	1	1 \	γY	/ N	N	N	N	85
STM32H7R7L8H6H	Cortex -M7	600	64	620	0	TFBGA225	150	1.71- 3.6	7	4	1 5	5 0	2	20	0	0 0	0	0	6	4	1[0CT0] +1[HEXA]	3/1	7	1 2 [FC	2	1	1 1	1	1	Υ	Υ	N	1 0) 2	1	0	1	1 \	/ Y	/ N	N	N	N	85
STM32H7R7Z8J6	Cortex -M7	600	64	620	0	UFBGA144	93	1.71- 3.6	7	3	1 4	1 0	2	16	0	0 0	0	0	6	4	2[0CT0]	3/1	6	1 2 [FC	1	1	1 1	1	1	Υ	Υ	N	1 0) 2	1	0	1	1 \	/ Y	/ N	N	N	N	85
STM32H7S7A8I6	Cortex -M7	600	64	620	0	UFBGA169	117	1.71- 3.6	7	4	1 4	1 0	2	17	0	0 0	0	0	6	4	2[0CT0]	3/1	7	1 2 [FE	2	1	1 1	1 1	1	Υ	Υ	N	1 () 2	1	0	1	1 \	/ Y	/ Y	Υ	Υ	Υ	85
STM32H7S7l8K6	Cortex -M7	600	64	620	0	UFBGA176 +25	122	1.71- 3.6	7	4	1 4	1 0	2	18	0	0 0	0	0	6	4	2[OCTO]	3/1	7	1 2 [FC	2	1	1 1	1 1	1	Υ	Υ	N	1 () 2	1	0	1	1 \	/ Y	/ Y	Υ	Υ	Υ	85
STM32H7S7l8T6	Cortex -M7	600	64	620	0	LQFP176	118	1.71- 3.6	7	4	1 4	1 0	2	17	0	0 0	0	0	6	4	2[0CT0]	3/1	7	1 2 [FE		1	1 1	1 1	1	Υ	Υ	N	1 () 2	1	0	1	1 \	ΥY	/ Y	Υ	Υ	Υ	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	E2PROM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	High Resolution Timer	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	COMP	OPAMP	128	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	USB FS HUSI/UIG	USB HS OTG	Ethernet	MDIO	JPEG Codec	SD GPU	TET LCD	MIPI DSI	SAI	SPDIFRX	DFSDM	DGMI	SWPMI	Math Accolorator	OIFUEG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H7S7L8H6	Cortex -M7	600	64	620	0	TFBGA225	152	1.71- 3.6	7	4	1	5	0 2	2 20	0	0	0	0	0 6	6 4	2[0CT0]	3/1	7	1	2 [FD]	2	1 1	1	1	1	Υ	Y	l 1	0	2	1	0	1	1 '	YY	′ Y	Y	Υ	Υ	85
STM32H7S7L8H6H	Cortex -M7	600	64	620	0	TFBGA225	150	1.71- 3.6	7	4	1	5	0 2	2 20	0	0	0	0	0 6	6 4	1[0CT0] +1[HEXA]	3/1	7	1	2 [FD]	2	1 1	1	1	1	Υ	Y	l 1	0	2	1	0	1	1	ΥY	/ Y	Υ Υ	Υ	Υ	85
STM32H7S7Z8J6	Cortex -M7	600	64	620	0	UFBGA144	93	1.71- 3.6	7	3	1	4	0 2	2 16	0	0	0	0	0 6	6 4	2[0CT0]	3/1	6	1	2 [FD]	1	1 1	1	1	1	Υ	Y	l 1	0	2	1	0	1	1	ΥY	/ Y	′ Y	Υ	Υ	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	I2S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	MDIO	JPEG Codec	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
												ST	M32	F7x) Va	lue	Line																					
STM32F730R8T6	Cortex-M7	216	64	256	LQFP64	50	1.7-3.6	11	2	2	0	3	16	2	3	3	1[QUAD]	3	6	1	1	0	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F730V8T6	Cortex-M7	216	64	256	LQFP100	82	1.7-3.6	11	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F730Z8T6	Cortex-M7	216	64	256	LQFP144	112	1.7-3.6	11	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F730l8K6	Cortex-M7	216	64	256	UFBGA176	138	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F750V8T6	Cortex-M7	216	64	320	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F750Z8T6	Cortex-M7	216	64	320	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F750N8H6	Cortex-M7	216	64	320	TFBGA216	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
											ST	M32	2F72	2x_7	3x /	\cc	ess Line																					
STM32F722RCT6	Cortex-M7	216	256	256	LQFP64	50	1.7-3.6	12	2	2	0	3	16	2	3	3	1[QUAD]	3	6	1	1	0	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F722RET6	Cortex-M7	216	512	256	LQFP64	50	1.7-3.6	12	2	2	0	3	16	2	3	3	1[QUAD]	3	6	1	1	0	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F722VCT6	Cortex-M7	216	256	256	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F722VET6	Cortex-M7	216	512	256	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F722ZCT6	Cortex-M7	216	256	256	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F722ZET6	Cortex-M7	216	512	256	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F722ICT6	Cortex-M7	216	256	256	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	12S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	MDIO	JPEG Codec	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F722ICK6	Cortex-M7	216	256	256	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F722IET6	Cortex-M7	216	512	256	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F722IEK6	Cortex-M7	216	512	256	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F732RET6	Cortex-M7	216	512	256	LQFP64	50	1.7-3.6	12	2	2	0	3	16	2	3	3	1[QUAD]	3	6	1	1	0	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F732VET6	Cortex-M7	216	512	256	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F732ZET6	Cortex-M7	216	512	256	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F732IET6	Cortex-M7	216	512	256	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F732IEK6	Cortex-M7	216	512	256	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	2	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F723VCT6	Cortex-M7	216	256	256	LQFP100	79	1.7-3.6	11	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723VET6	Cortex-M7	216	512	256	LQFP100	79	1.7-3.6	11	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723VEY6	Cortex-M7	216	512	256	WLCSP100	79	1.7-3.6	11	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723ZCT6	Cortex-M7	216	256	256	LQFP144	112	1.7-3.6	11	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723ZCI6	Cortex-M7	216	256	256	UFBGA144	112	1.7-3.6	11	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723ZET6	Cortex-M7	216	512	256	LQFP144	112	1.7-3.6	11	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723ZEI6	Cortex-M7	216	512	256	UFBGA144	112	1.7-3.6	11	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A	N/A	0	0	2	0	0	0	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	I2S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	MDIO	JPEG Codec	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F723ICT6	Cortex-M7	216	256	256	LQFP176	138	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0 1	N/A I	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723ICK6	Cortex-M7	216	256	256	UFBGA176	138	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0 1	N/A I	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723IET6	Cortex-M7	216	512	256	LQFP176	138	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0 1	N/A I	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F723IEK6	Cortex-M7	216	512	256	UFBGA176	138	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A I	N/A	0	0	2	0	0	0	YES	N/A	N/A	85
STM32F733VET6	Cortex-M7	216	512	256	LQFP100	79	1.7-3.6	11	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	1	1	1	1	0	0 1	N/A I	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F733VEY6	Cortex-M7	216	512	256	WLCSP100	79	1.7-3.6	11	2	2	1	3	16	2	4	3	1[QUAD]	3	8	1	1	1	1	1	0	0 1	N/A I	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F733ZET6	Cortex-M7	216	512	256	LQFP144	112	1.7-3.6	11	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A I	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F733ZEI6	Cortex-M7	216	512	256	UFBGA144	112	1.7-3.6	11	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A I	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F733IET6	Cortex-M7	216	512	256	LQFP176	138	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0	N/A I	N/A	0	0	2	0	0	0	YES	YES	N/A	85
STM32F733IEK6	Cortex-M7	216	512	256	UFBGA176	138	1.7-3.6	12	2	2	1	3	24	2	5	3	1[QUAD]	3	8	1	1	1	1	1	0	0 1	N/A I	N/A	0	0	2	0	0	0	YES	YES	N/A	85
										S	тмз	2F7	′4x_	75x	Per	forr	nance Li	ne																				
STM32F745VET6	Cortex-M7	216	512	320	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	N/A \	/ES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F745VGT6	Cortex-M7	216	1024	320	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	N/A Y	/ES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F745VEH6	Cortex-M7	216	512	320	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	N/A	YES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F745VGH6	Cortex-M7	216	1024	320	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A Y	YES	0	0	2	1	0	1	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	12S	M-SPI	12C	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	MDIO	JPEG Codec	GPII	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F745ZET6	Cortex-M7	216	512	320	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	N/A Y	ES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F745ZGT6	Cortex-M7	216	1024	320	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A Y	ES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F745IET6	Cortex-M7	216	512	320	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	N/A Y	ES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F745IGT6	Cortex-M7	216	1024	320	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A Y	ES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F745IEK6	Cortex-M7	216	512	320	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	V/A Y	ES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F745IGK6	Cortex-M7	216	1024	320	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	V/A Y	ES	0	0	2	1	0	1	YES	N/A	N/A	85
STM32F746VET6	Cortex-M7	216	512	320	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A Y	ΈS	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746VGT6	Cortex-M7	216	1024	320	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A Y	ΈS	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746VEH6	Cortex-M7	216	512	320	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A Y	ΈS	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746VGH6	Cortex-M7	216	1024	320	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	V/A Y	ES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746ZET6	Cortex-M7	216	512	320	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	V/A Y	ES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746ZGT6	Cortex-M7	216	1024	320	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	V/A Y	ES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746ZEY6	Cortex-M7	216	512	320	WLCSP143	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0 1	V/A Y	ES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746ZGY6	Cortex-M7	216	1024	320	WLCSP143	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A Y	ES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746IET6	Cortex-M7	216	512	320	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	V/A Y	ES	1	0	2	1	0	1	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	12S	M-SPI	120	U(S)ART	CAN	SDI0	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	MDIO	JPEG Codec	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F746IGT6	Cortex-M7	216	1024	320	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746IEK6	Cortex-M7	216	512	320	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746IGK6	Cortex-M7	216	1024	320	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746BET6	Cortex-M7	216	512	320	LQFP208	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746BGT6	Cortex-M7	216	1024	320	LQFP208	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746NEH6	Cortex-M7	216	512	320	TFBGA216	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F746NGH6	Cortex-M7	216	1024	320	TFBGA216	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	N/A	N/A	85
STM32F756VGT6	Cortex-M7	216	1024	320	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F756VGH6	Cortex-M7	216	1024	320	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F756ZGT6	Cortex-M7	216	1024	320	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F756ZGY6	Cortex-M7	216	1024	320	WLCSP143	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F756IGT6	Cortex-M7	216	1024	320	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F756IGK6	Cortex-M7	216	1024	320	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F756BGT6	Cortex-M7	216	1024	320	LQFP208	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85
STM32F756NGH6	Cortex-M7	216	1024	320	TFBGA216	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	2	1	1	1	1	1	0	N/A	YES	1	0	2	1	0	1	YES	YES	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	I2S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	טר במ סטמפט	GPU	IFI LGD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
										ST	M32	2F76	35 <u>_</u> 7	67_	777	Ad	vanced L	ine																			
STM32F765VGT6	Cortex-M7	216	1024	512	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	1	1	1	1	1	N	/A YE	S O	0	2	1	1	1	YES	N/A	N/A	85
STM32F765VIT6	Cortex-M7	216	2048	512	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	1	1	1	1	1	N	/A YE	S O	0	2	1	1	1	YES	N/A	N/A	85
STM32F765VGH6	Cortex-M7	216	1024	512	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	1	1	1	1	1 .	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85
STM32F765VIH6	Cortex-M7	216	2048	512	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	1	1	1	1	1 -	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85
STM32F765ZGT6	Cortex-M7	216	1024	512	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1 .	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85
STM32F765ZIT6	Cortex-M7	216	2048	512	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85
STM32F765IGT6	Cortex-M7	216	1024	512	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1 .	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85
STM32F765IIT6	Cortex-M7	216	2048	512	LQFP176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1 .	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85
STM32F765IGK6	Cortex-M7	216	1024	512	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85
STM32F765IIK6	Cortex-M7	216	2048	512	UFBGA176	140	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1 -	N	/A YE	S O	0	2	1	1	1	YES	N/A	N/A	85
STM32F765BGT6	Cortex-M7	216	1024	512	LQFP208	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1 -	N	/A YE	S O	0	2	1	1	1	YES	N/A	N/A	85
STM32F765BIT6	Cortex-M7	216	2048	512	LQFP208	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1 .	N	/A YE	S O	0	2	1	1	1	YES	N/A	N/A	85
STM32F765NGH6	Cortex-M7	216	1024	512	TFBGA216	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85
STM32F765NIH6	Cortex-M7	216	2048	512	TFBGA216	168	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	1	1	1	1	1	N	/A YE	S C	0	2	1	1	1	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	T+hornot	MDIO	JPFG Codec	ודו נטט	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F767VGT6	Cortex-M7	216	1024	512	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767VIT6	Cortex-M7	216	2048	512	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767VGH6	Cortex-M7	216	1024	512	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767VIH6	Cortex-M7	216	2048	512	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767ZGT6	Cortex-M7	216	1024	512	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767ZIT6	Cortex-M7	216	2048	512	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767IGT6	Cortex-M7	216	1024	512	LQFP176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767IIT6	Cortex-M7	216	2048	512	LQFP176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767IGK6	Cortex-M7	216	1024	512	UFBGA176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767IIK6	Cortex-M7	216	2048	512	UFBGA176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767BGT6	Cortex-M7	216	1024	512	LQFP208	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767BIT6	Cortex-M7	216	2048	512	LQFP208	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767NGH6	Cortex-M7	216	1024	512	TFBGA216	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F767NIH6	Cortex-M7	216	2048	512	TFBGA216	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	N/A	N/A	85
STM32F777VIT6	Cortex-M7	216	2048	512	LQFP100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	2	1	1	1	1	1 Y	ES YE	S 1	0	2	1	1	1	YES	YES	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	I2S	M-SPI	I2C	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	MUIO	JPEG Codec	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F777VIH6	Cortex-M7	216	2048	512	TFBGA100	82	1.7-3.6	12	2	2	1	3	16	2	4	3	1[QUAD]	4	8	3	2	1	1	1 1	1	YE	S YES	3 1	0	2	1	1	1	YES	YES	YES	85
STM32F777ZIT6	Cortex-M7	216	2048	512	LQFP144	114	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	YE	S YES	3 1	0	2	1	1	1	YES	YES	YES	85
STM32F777IIT6	Cortex-M7	216	2048	512	LQFP176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 1	1 1	YE	S YES	3 1	0	2	1	1	1	YES	YES	YES	85
STM32F777IIK6	Cortex-M7	216	2048	512	UFBGA176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1 1	YE	S YES	3 1	0	2	1	1	1	YES	YES	YES	85
STM32F777BIT6	Cortex-M7	216	2048	512	LQFP208	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 -	1 1	YE	S YES	3 1	0	2	1	1	1	YES	YES	YES	85
STM32F777NIH6	Cortex-M7	216	2048	512	TFBGA216	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 -	1 1	YE	S YES	3 1	0	2	1	1	1	YES	YES	YES	85
										STN	/132F	769	9_77	'9 M	IIPI-	DSI	Graphic	Lin	е																		
STM32F769AIY6	Cortex-M7	216	2048	512	WLCSP180	129	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 () 1	YE	S YES	3 1	1	2	1	1	1	YES	N/A	N/A	85
STM32F769BGT6	Cortex-M7	216	1024	512	LQFP208	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 1	1	YE	S YES	3 1	1	2	1	1	1	YES	N/A	N/A	85
STM32F769BIT6	Cortex-M7	216	2048	512	LQFP208	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 1	1 1	YE	S YES	3 1	1	2	1	1	1	YES	N/A	N/A	85
STM32F769IGT6	Cortex-M7	216	1024	512	LQFP176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 1	1	YE	S YES	3 1	1	2	1	1	1	YES	N/A	N/A	85
STM32F769IIT6	Cortex-M7	216	2048	512	LQFP176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 1	1	YE	S YES	3 1	1	2	1	1	1	YES	N/A	N/A	85
STM32F769NGH6	Cortex-M7	216	1024	512	TFBGA216	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 1	1	YE	S YES	3 1	1	2	1	1	1	YES	N/A	N/A	85
STM32F769NIH6	Cortex-M7	216	2048	512	TFBGA216	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	YE	S YES	3 1	1	2	1	1	1	YES	N/A	N/A	85
STM32F778AIY6	Cortex-M7	216	2048	512	WLCSP180	129	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1 () 1	YE	S YES	3 1	1	2	1	1	1	YES	YES	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	I2S	M-SPI	I2C	U(S)ART	CAN	SDIO SDIO	F(S)MC		IISB HS OTG	Ethernot	JPEG Codec	GPU	TFT LCD	MIPI_DSI		SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F779AIY6	Cortex-M7	216	2048	512	WLCSP180	129	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	0 -	YE	S YE	3 1	1	2	1	1	1	YES	YES	YES	85
STM32F779BIT6	Cortex-M7	216	2048	512	LQFP208	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	YE	S YE	3 1	1	2	1	1	1	YES	YES	YES	85
STM32F779IIT6	Cortex-M7	216	2048	512	LQFP176	132	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	YE	S YE	S 1	1	2	1	1	1	YES	YES	YES	85
STM32F779NIH6	Cortex-M7	216	2048	512	TFBGA216	159	1.7-3.6	12	2	2	1	3	24	2	6	3	1[QUAD]	4	8	3	2	1	1	1	1	YE	S YE	S 1	1	2	1	1	1	YES	YES	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	<u>8</u>	961	M-SPI	120	U(S)ART	I DIIAPT	CAN	SDIO	F(S)MC	USB Device	FUICE ES MOST/OTS	SAI	DCMI	Math Accelerator	Trust'Zone	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
											S	ГМЗ	32H5	٥3 ١	/alue	e Liı	ne																			
STM32H503CBT6	Cortex-M33	250	128	32	LQFP48	35	1.71-3.6	6	1	1	2	1	10	2	1	1	3	3	0	2	3	1 1	[FD]	0	0	1	1 0	0	0	N/A	N/A	N/A	N/A	N/A	YES	85
STM32H503CBU6	Cortex-M33	250	128	32	UFQFPN48	35	1.71-3.6	6	1	1	2	1	10	2	1	1	3	3	0	2	3	1 1	[FD]	0	0	1	1 (0	0	N/A	N/A	N/A	N/A	N/A	YES	85
STM32H503EBY6	Cortex-M33	250	128	32	WLCSP25	19	1.71-3.6	6	1	1	2	1	10	2	1	1	3	3	0	2	3	1 1	[FD]	0	0	1	1 (0	0	N/A	N/A	N/A	N/A	N/A	YES	85
STM32H503KBU6	Cortex-M33	250	128	32	UFQFPN32	24	1.71-3.6	6	1	1	2	1	10	2	1	1	3	3	0	2	3	1 1	[FD]	0	0	1	1 (0	0	N/A	N/A	N/A	N/A	N/A	YES	85
STM32H503RBT6	Cortex-M33	250	128	32	LQFP64	49	1.71-3.6	6	1	1	2	1	16	2	1	1	3	3	0	2	3	1 1	[FD]	0	0	1	1 (0	0	N/A	N/A	N/A	N/A	N/A	YES	85
									S	MT	32H	562	? Tru	st Z	one,	Acc	ess	Line	;																	
STM32H562All6	Cortex-M33	250	2048	640	UFBGA169	136	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3 1	[OCTO]	4	6	1 1	[FD]	2	1	1	1 () 2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H562IIK6	Cortex-M33	250	2048	640	UFBGA176	140	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3 1	[OCTO]	4	6	1 1	[FD]	2	1	1	1 0) 2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H562IIT6	Cortex-M33	250	2048	640	LQFP176	140	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3 1	[OCTO]	4	6	1 1	[FD]	2	1	1	1 0) 2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H562RIT6	Cortex-M33	250	2048	640	LQFP64	53	1.71-3.6	16	2	2	6	2	16	2	0	0	4	3 1	[OCTO]	4	5	1 1	[FD]	2	0	1	1 () 2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H562RIV6	Cortex-M33	250	2048	640	VFQFPN68	53	1.71-3.6	16	2	2	6	2	16	2	0	0	4 3	3 1	[OCTO]	4	5	1 1	[FD]	2	0	1	1 () 2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H562VIT6	Cortex-M33	250	2048	640	LQFP100	80	1.71-3.6	16	2	2	6	2	16	2	0	0	5 3	3 1	[OCTO]	4	5	1 1	[FD]	2	1	1	1 () 2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H562ZIT6	Cortex-M33	250	2048	640	LQFP144	112	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3 1	[OCTO]	4		1 1	[FD]	2	1	1	1 () 2	1	YES	YES	N/A	N/A	N/A	YES	85
								STI	VI32	H56	3 E	thei	rnet,	Trus	stZor	ne, <i>l</i>	Adva	ance	d Line																	
STM32H563All3Q	Cortex-M33	250	2048	640	UFBGA169	134	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3 1	[OCTO]	4	6	1 2	[FD]	2	1	1	1 1	2	1	YES	YES	N/A	N/A	N/A	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-hit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	OPAMP	SPI	12S	M-SPI	12C	U(S)ART	LPUART	CAN	SDI0	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	SAI	DGMI	Math Accelerator	Trust'Zone	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H563All6	Cortex-M33	250	2048	640	UFBGA169	136	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H563IIK3Q	Cortex-M33	250	2048	640	UFBGA176	140	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32H563IIK6	Cortex-M33	250	2048	640	UFBGA176	140	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H563IIT3Q	Cortex-M33	250	2048	640	LQFP176	140	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32H563IIT6	Cortex-M33	250	2048	640	LQFP176	136	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H563MIY3QTR	Cortex-M33	250	2048	640	WLCSP80	57	1.71-3.6	16	2	2	6	2	16	2	0	0	4	3	1[0CT0]	4	5	1	2[FD]	2	1	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32H563RIT6	Cortex-M33	250	2048	640	LQFP64	53	1.71-3.6	16	2	2	6	2	16	2	0	0	4	3	1[0CT0]	4	5	1	2[FD]	2	0	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H563RIV6	Cortex-M33	250	2048	640	VFQFPN68	53	1.71-3.6	16	2	2	6	2	16	2	0	0	4	3	1[0CT0]	4	5	1	2[FD]	2	0	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H563VIT3Q	Cortex-M33	250	2048	640	LQFP100	78	1.71-3.6	16	2	2	6	2	14	2	0	0	5	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	0	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32H563VIT6	Cortex-M33	250	2048	640	LQFP100	80	1.71-3.6	16	2	2	6	2	16	2	0	0	5	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	85
STM32H563ZIT3Q	Cortex-M33	250	2048	640	LQFP144	110	1.71-3.6	16	2	2	6	2	18	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	0	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32H563ZIT6	Cortex-M33	250	2048	640	LQFP144	112	1.71-3.6	16	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	85
							STM32H5	73 E	ther	net	Trus	ŧΖα	one, <i>l</i>	AES	6/0 T	FDE	EC,H	igh	Perform	nan	ce L	ine															
STM32H573AII3Q	Cortex-M33	250	2048	640	UFBGA169	134	1.71-3.6	18	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32H573All6	Cortex-M33	250	2048	640	UFBGA169	136	1.71-3.6	18	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	YES	YES	YES	YES	85
STM32H573IIK3Q	Cortex-M33	250	2048	640	UFBGA176	139	1.71-3.6	18	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	OPAMP	SPI	2S	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	SAL	DCMI	Math Accelerator	Trust'Zone	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32H573IIK6	Cortex-M33	250	2048	640	UFBGA176	140	1.71-3.6	18	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	85
STM32H573IIK6TR	Cortex-M33	250	2048	640	UFBGA176	140	1.71-3.6	18	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	85
STM32H573IIT3Q	Cortex-M33	250	2048	640	LQFP176	139	1.71-3.6	18	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	125
STM32H573IIT6	Cortex-M33	250	2048	640	LQFP176	136	1.71-3.6	18	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	85
STM32H573MIY3QTR	Cortex-M33	250	2048	640	WLCSP80	57	1.71-3.6	18	2	2	6	2	16	2	0	0	4	3	1[0CT0]	4	5	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	125
STM32H573RIT6	Cortex-M33	250	2048	640	LQFP64	53	1.71-3.6	18	2	2	6	2	16	2	0	0	4	3	1[0CT0]	4	5	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	85
STM32H573RIV6	Cortex-M33	250	2048	640	VFQFPN68	53	1.71-3.6	18	2	2	6	2	16	2	0	0	4	3	1[0CT0]	4	5	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	85
STM32H573VIT3Q	Cortex-M33	250	2048	640	LQFP100	78	1.71-3.6	18	2	2	6	2	14	2	0	0	5	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	0 2	2 1	YES	YES	YES	YES	YES	YES	125
STM32H573VIT6	Cortex-M33	250	2048	640	LQFP100	80	1.71-3.6	18	2	2	6	2	16	2	0	0	5	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	85
STM32H573ZIT3Q	Cortex-M33	250	2048	640	LQFP144	110	1.71-3.6	18	2	2	6	2	18	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1) 2	! 1	YES	YES	YES	YES	YES	YES	125
STM32H573ZIT6	Cortex-M33	250	2048	640	LQFP144	112	1.71-3.6	18	2	2	6	2	20	2	0	0	6	3	1[0CT0]	4	6	1	2[FD]	2	1	1	1	1 2	2 1	YES	YES	YES	YES	YES	YES	85
											ST	M3	2H52	23 V	/alu	e Li	ne																			
STM32H523HEY6TR	Cortex-M33	250	512	272	WLCSP39	26	1.71-3.6	10	2	2	6	2	10	2	0	0	3	2	1[0CT0]	2	2	1	2[FD]	1	0	1	1) () 1	YES	YES	N/A	YES	N/A	YES	85
STM32H523CCT6	Cortex-M33	250	256	272	LQFP48	35	1.71-3.6	10	2	2	6	2	10	2	0	0	4	3	1[0CT0]	3	3	1	2[FD]	1	0	1	1) () 1	YES	YES	N/A	YES	N/A	YES	85
STM32H523CCU6	Cortex-M33	250	256	272	UFQFPN48	35	1.71-3.6	10	2	2	6	2	10	2	0	0	4	3	1[0CT0]	3	3	1	2[FD]	1	0	1	1) () 1	YES	YES	N/A	YES	N/A	YES	85
STM32H523CET6	Cortex-M33	250	512	272	LQFP48	35	1.71-3.6	10	2	2	6	2	10	2	0	0	4	3	1[0CT0]	3	3	1	2[FD]	1	0	1	1) () 1	YES	YES	N/A	YES	N/A	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	I2S	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	_	USB FS HOST/OTG	Ethornot	SAL	Matil Acceletator	Irust 20ne	TenetiTene	OTFDEC	РКА	AES/DES	SHA/HMAC	T° Max (°C)
STM32H523CEU6	Cortex-M33	250	512	272	UFQFPN48	35	1.71-3.6	10	2	2	6	2	10	2	0	0	4	3	1[0CT0]	3	3	1	2[FD]	1	0	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523RCT6	Cortex-M33	250	256	272	LQFP64	49	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	0	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523RCT7	Cortex-M33	250	256	272	LQFP64	49	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	0	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	105
STM32H523RCT7R	Cortex-M33	250	256	272	LQFP64	49	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	0	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	105
STM32H523RET6	Cortex-M33	250	512	272	LQFP64	49	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	0	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523VCT6	Cortex-M33	250	256	272	LQFP100	80	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523VCl6	Cortex-M33	250	256	272	UFBGA100	80	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523VET6	Cortex-M33	250	512	272	LQFP100	80	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523VEI6	Cortex-M33	250	512	272	UFBGA100	80	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523ZCT6	Cortex-M33	250	256	272	LQFP144	112	1.71-3.6	10	2	2	6	2	20	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523ZCJ6	Cortex-M33	250	256	272	UFBGA144	112	1.71-3.6	10	2	2	6	2	20	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523ZET6	Cortex-M33	250	512	272	LQFP144	112	1.71-3.6	10	2	2	6	2	20	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
STM32H523ZEJ6	Cortex-M33	250	512	272	UFBGA144	112	1.71-3.6	10	2	2	6	2	20	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1	0	0	1 YE	S YE	ES N	V/A	YES	N/A	YES	85
									S	TM3	32H5	33	Valu	ıe Li	ine	ΑE	S/0	ΓFD	EC																		
STM32H533HEY6TR	Cortex-M33	250	512	272	WLCSP39	26	1.71-3.6	10	2	2	6	2	10	2	0	0	3	2	1[0CT0]	2	2	1	2[FD]	1	0	1	1	0	0	1 YE	S YE	∃S Y	/ES	YES	YES	YES	85
STM32H533CET6	Cortex-M33	250	512	272	LQFP48	35	1.71-3.6	10	2	2	6	2	10	2	0	0	4	3	1[0CT0]	3	3	1	2[FD]	1	0	1	1	0	0	1 YE	S	∃S Y	/ES	YES	YES	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP OPAMP	SDI	128	M-SPI	I2C	U(S)ART	LPUART	CAN	SDIO	F(S)MC		USB FS HOST/OTG	T-thouset	DCIVIL	Math Accelerator	Irust.70ue	T	OTFDEC	РКА	AES/DES	SHA/HMAC	T° Max (°C)
STM32H533CEU6	Cortex-M33	250	512	272	UFQFPN48	35	1.71-3.6	10	2	2	6	2	10	2	0	0	4	3	1[0CT0]	3	3	1	2[FD]	1	0	1	1 () () 1	YE	S YE	S Y	/ES	YES	YES	YES	85
STM32H533RET6	Cortex-M33	250	512	272	LQFP64	49	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	0	1	1 () () 1	YE	S YE	S Y	/ES \	YES	YES	YES	85
STM32H533RET6U	Cortex-M33	250	512	272	LQFP64	49	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	0	1	1 () () 1	YE	S YE	S Y	/ES	YES	YES	YES	85
STM32H533VET6	Cortex-M33	250	512	272	LQFP100	80	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1 () () 1	YE	S YE	S Y	ES '	YES	YES	YES	85
STM32H533VEI6	Cortex-M33	250	512	272	UFNGA100	80	1.71-3.6	10	2	2	6	2	16	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1 () (0 1	YE	S YE	S Y	/ES \	YES	YES	YES	85
STM32H533ZET6	Cortex-M33	250	512	272	LQFP144	112	1.71-3.6	10	2	2	6	2	20	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1 () (0 1	YE	SYE	S Y	/ES	YES	YES	YES	85
STM32H533ZEJ6	Cortex-M33	250	512	272	UFBGA144	112	1.71-3.6	10	2	2	6	2	20	2	0	0	4	3	1[0CT0]	3	4	1	2[FD]	1	1	1	1 () (0 -	YE	S YE	SY	/ES	YES	YES	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	I2S	M-SPI	120	U(S)ART	CAN	SDIO	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
											STM	132F	400_	402	Val	ue L	ine																		
STM32F400CBT6	Cortex-M4	100	128	32	LQFP48	36	1.7-3.6	4	1	1	1	1	10	1	3	3	0	3	3	0	0 0	0	0	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F400RBT6	Cortex-M4	100	128	32	LQFP64	50	1.8-3.6	4	1	1	1	1	16	1	3	3	0	3	3	0	0 0	0	0	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F402RCT6	Cortex-M4	84	256	64	LQFP64	50	1.7-3.6	6	2	1	0	1	16	0	3	2	0	3	3	0	1 (1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F402VCT6	Cortex-M4	84	256	64	LQFP100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1 () 1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
								STI	/132	F40 1	_41	0_4	11_4	12_	413	42	3 Acces	s Liı	1e																
STM32F401CBU6	Cortex-M4	84	128	64	UFQFPN48	36	1.7-3.6	6	2	1	0	1	10	0	3	2	0	3	3	0	0 0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401CBY6	Cortex-M4	84		0.4	WII 00D 40	200										_	0	3	3	0	0 0	1	0	0	N/A	0	0	_				NI/A	N/A	N/A	85
		04	128	64	WLCSP49	36	1.7-3.6	6	2	1	0	1	10	0	3	2	U	٦								ľ	U	0	0	0	0	N/A	IV/A	IN/A	00
STM32F401CCU6	Cortex-M4	84	128 256	64	UFQFPN48	36	1.7-3.6	6	2	1	0	1	10	0	3	2	0		3		0 0) 1	0	0	N/A	0	0	0	0	0	0	N/A		N/A	85
STM32F401CCU6 STM32F401CCY6	Cortex-M4									-										0	0 0		0		N/A									N/A	
		84	256	64	UFQFPN48	36	1.7-3.6	6	2	1	0	1	10	0	3	2	0	3	3	0) 1		0		0	0	0	0	0	0	N/A	N/A N/A	N/A	85
STM32F401CCY6	Cortex-M4	84	256 256	64 64	UFQFPN48 WLCSP49	36 36	1.7-3.6 1.7-3.6	6	2	1	0	1	10	0	3	2	0	3	3	0 0 0	0 0) 1	0	0	N/A	0	0	0	0	0	0	N/A N/A	N/A N/A N/A	N/A N/A	85 85
STM32F401CCY6 STM32F401CDU6	Cortex-M4	84 84 84	256 256 384	64 64 96	UFQFPN48 WLCSP49 UFQFPN48	36 36 36	1.7-3.6 1.7-3.6 1.7-3.6	6 6	2 2	1 1 1	0 0 0	1 1 1	10 10 10	0 0 0	3 3	2 2 2	0 0 0	3 3 3	3 3	0 0 0	0 0) 1	0	0 0	N/A N/A	0 0	0 0	0 0	0 0	0 0	0 0	N/A N/A N/A	N/A N/A N/A	N/A N/A N/A	85 85 85
STM32F401CCY6 STM32F401CDU6 STM32F401CDY6	Cortex-M4 Cortex-M4	84 84 84	256 256 384 384	64 64 96 96	UFQFPN48 WLCSP49 UFQFPN48 WLCSP49	36 36 36	1.7-3.6 1.7-3.6 1.7-3.6 1.7-3.6	6 6 6	2 2 2	1 1 1 1	0 0 0 0	1 1 1	10 10 10 10	0 0 0 0	3 3 3	2 2 2 2	0 0 0 0	3 3 3 3	3 3 3	0 0 0 0	0 () 1	0 0	0 0 0	N/A N/A N/A	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	N/A N/A N/A	N/A N/A N/A	N/A N/A N/A N/A	85 85 85 85
STM32F401CCY6 STM32F401CDU6 STM32F401CDY6 STM32F401CEU6	Cortex-M4 Cortex-M4 Cortex-M4	84 84 84 84	256 256 384 384 512	64 64 96 96	UFQFPN48 WLCSP49 UFQFPN48 WLCSP49 UFQFPN48	36 36 36 36	1.7-3.6 1.7-3.6 1.7-3.6 1.7-3.6 1.7-3.6	6 6 6 6	2 2 2 2	1 1 1 1	0 0 0 0	1 1 1 1	10 10 10 10	0 0 0 0	3 3 3 3	2 2 2 2 2	0 0 0 0	3 3 3 3	3 3 3 3	0 0 0 0 0	0 (0 0 (0 0 (0) 1 1 1 1 1	0 0 0	0 0 0 0	N/A N/A N/A	0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	0 0 0 0	N/A N/A N/A N/A	N/A N/A N/A N/A N/A	N/A N/A N/A N/A	85 85 85 85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F401RDT6	Cortex-M4	84	384	96	LQFP64	50	1.7-3.6	6	2	1	0	1	16	0	3	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401RET6	Cortex-M4	84	512	96	LQFP64	50	1.7-3.6	6	2	1	0	1	16	0	3	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401VBT6	Cortex-M4	84	128	64	LQFP100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401VBH6	Cortex-M4	84	128	64	UFBGA100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401VCT6	Cortex-M4	84	256	64	LQFP100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401VCH6	Cortex-M4	84	256	64	UFBGA100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401VDT6	Cortex-M4	84	384	96	LQFP100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401VDH6	Cortex-M4	84	384	96	UFBGA100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401VET6	Cortex-M4	84	512	96	LQFP100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F401VEH6	Cortex-M4	84	512	96	UFBGA100	81	1.7-3.6	6	2	1	0	1	16	0	4	2	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F410T8Y6	Cortex-M4	100	64	32	WLCSP36	23	1.7-3.6	4	1	1	1	1	4	1	1	1	0	2	2	0	0	0	0	0	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F410TBY6	Cortex-M4	100	128	32	WLCSP36	23	1.7-3.6	4	1	1	1	1	4	1	1	1	0	2	2	0	0	0	0	0	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F410C8U6	Cortex-M4	100	64	32	UFQFPN48	36	1.7-3.6	4	1	1	1	1	10	1	3	3	0	3	3	0	0	0	0	0	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F410CBU6	Cortex-M4	100	128	32	UFQFPN48	36	1.7-3.6	4	1	1	1	1	10	1	3	3	0	3	3	0	0	0	0	0	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F410R8T6	Cortex-M4	100	64	32	LQFP64	50	1.7-3.6	4	1	1	1	1	16	1	3	3	0	3	3	0	0	0	0	0	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F410RBT6	Cortex-M4	100	128	32	LQFP64	50	1.7-3.6	4	1	1	1	1	16	1	3	3	0	3	3	0	0	0	0	0	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F411CCU6	Cortex-M4	100	256	128	UFQFPN48	36	1.7-3.6	6	2	1	0	1	10	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411CCY6	Cortex-M4	100	256	128	WLCSP49	36	1.7-3.6	6	2	1	0	1	10	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411CEU6	Cortex-M4	100	512	128	UFQFPN48	36	1.7-3.6	6	2	1	0	1	10	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411CEY6	Cortex-M4	100	512	128	WLCSP49	36	1.7-3.6	6	2	1	0	1	10	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411RCT6	Cortex-M4	100	256	128	LQFP64	50	1.7-3.6	6	2	1	0	1	16	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411RET6	Cortex-M4	100	512	128	LQFP64	50	1.7-3.6	6	2	1	0	1	16	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411VCT6	Cortex-M4	100	256	128	LQFP100	81	1.7-3.6	6	2	1	0	1	16	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411VCH6	Cortex-M4	100	256	128	UFBGA100	81	1.7-3.6	6	2	1	0	1	16	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411VET6	Cortex-M4	100	512	128	LQFP100	81	1.7-3.6	6	2	1	0	1	16	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F411VEH6	Cortex-M4	100	512	128	UFBGA100	81	1.7-3.6	6	2	1	0	1	16	0	5	5	0	3	3	0	1	0	1	0	0	N/A	0	0	0	0	0	0	N/A	N/A	N/A	85
STM32F412CEU6	Cortex-M4	100	512	256	UFQFPN48	36	1.7-3.6	12	2	2	0	1	10	0	5	5	0	4	3	2	1	0	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412CGU6	Cortex-M4	100	1024	256	UFQFPN48	36	1.7-3.6	12	2	2	0	1	10	0	5	5	0	4	3	2	1	0	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412RET6	Cortex-M4	100	512	256	LQFP64	50	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	0	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412REY6	Cortex-M4	100	512	256	WLCSP64	50	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	0	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F412RGT6	Cortex-M4	100	1024	256	LQFP64	50	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	0	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412RGY6	Cortex-M4	100	1024	256	WLCSP64	50	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	0	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412RGY6P	Cortex-M4	100	1024	256	WLCSP64	50	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	0	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412VET6	Cortex-M4	100	512	256	LQFP100	81	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	1	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412VEH6	Cortex-M4	100	512	256	UFBGA100	81	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	1	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412VGT6	Cortex-M4	100	1024	256	LQFP100	81	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	1	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412VGH6	Cortex-M4	100	1024	256	UFBGA100	81	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	1	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412ZET6	Cortex-M4	100	512	256	LQFP144	114	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	1	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412ZEH6	Cortex-M4	100	512	256	UFBGA144	114	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	1	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412ZGT6	Cortex-M4	100	1024	256	LQFP144	114	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	1	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F412ZGH6	Cortex-M4	100	1024	256	UFBGA144	114	1.7-3.6	12	2	2	0	1	16	0	5	5	1[QUAD]	4	4	2	1	1	1	0	0	N/A	0	0	0	0	1	0	YES	N/A	N/A	85
STM32F413CGU6	Cortex-M4	100	1024	320	UFQFPN48	36	1.7-3.6	12	2	2	1	1	10	2	5	5	0	4	6	3	1	0	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413CHU6	Cortex-M4	100	1536	320	UFQFPN48	36	1.7-3.6	12	2	2	1	1	10	2	5	5	0	4	6	3	1	0	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413RGT6	Cortex-M4	100	1024	320	LQFP64	50	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	7	3	1	0	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413RHT6	Cortex-M4	100	1536	320	LQFP64	50	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	7	3	1	0	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	I2S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F413MGY6	Cortex-M4	100	1024	320	WLCSP81	60	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	7	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413MHY6	Cortex-M4	100	1024	320	WLCSP81	60	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	7	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413VGT6	Cortex-M4	100	1024	320	LQFP100	81	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413VGJ6	Cortex-M4	100	1024	320	UFBGA100	81	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413VHT6	Cortex-M4	100	1536	320	LQFP100	81	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413VHJ6	Cortex-M4	100	1536	320	UFBGA100	81	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413ZGT6	Cortex-M4	100	1024	320	LQFP144	114	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413ZGJ6	Cortex-M4	100	1024	320	UFBGA144	114	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413ZHT6	Cortex-M4	100	1536	320	LQFP144	114	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F413ZHJ6	Cortex-M4	100	1536	320	UFBGA144	114	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	N/A	N/A	85
STM32F423CHU6	Cortex-M4	100	1536	320	UFQFPN48	36	1.7-3.6	12	2	2	1	1	10	2	5	5	0	4	6	3	1	0	1	0	0	N/A	0	0	1	0	2	0	YES	YES	N/A	85
STM32F423RHT6	Cortex-M4	100	1536	320	LQFP64	50	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	7	3	1	0	1	0	0	N/A	0	0	1	0	2	0	YES	YES	N/A	85
STM32F423MHY6	Cortex-M4	100	1024	320	WLCSP81	60	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	7	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	YES	N/A	85
STM32F423VHT6	Cortex-M4	100	1536	320	WLCSP81	60	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	7	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	YES	N/A	85
STM32F423VHT6	Cortex-M4	100	1536	320	LQFP100	81	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	YES	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDI0	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F423VHJ6	Cortex-M4	100	1536	320	UFBGA100	81	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	YES	N/A	85
STM32F423ZHT6	Cortex-M4	100	1536	320	LQFP144	114	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	YES	N/A	85
STM32F423ZHJ6	Cortex-M4	100	1536	320	UFBGA144	114	1.7-3.6	12	2	2	1	1	16	2	5	5	1[QUAD]	4	10	3	1	1	1	0	0	N/A	0	0	1	0	2	0	YES	YES	N/A	85
								S	TM	32F4	405	415	5_40	7_4 1	17 P	erfo	rmance I	Line)																	
STM32F405RGT6	Cortex-M4	168	1024	192	LQFP64	51	1.8-3.6	12	2	2	0	3	16	2	3	2	0	3	6	2	1	0	1	1	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F4050GY6	Cortex-M4	168	1024	192	WLCSP90	72	1.8-3.6	12	2	2	0	3	13	2	3	2	0	3	6	2	1	1	1	1	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F4050EY6	Cortex-M4	168	512	192	WLCSP90	72	1.8-3.6	12	2	2	0	3	13	2	3	2	0	3	6	2	1	1	1	1	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F405VGT6	Cortex-M4	168	1024	192	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	3	2	0	3	6	2	1	1	1	1	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F405ZGT6	Cortex-M4	168	1024	192	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	0	N/A	0	0	0	0	0	0	YES	N/A	N/A	85
STM32F415RGT6	Cortex-M4	168	1024	192	LQFP64	51	1.8-3.6	12	2	2	0	3	16	2	3	2	0	3	6	2	1	0	1	1	0	N/A	0	0	0	0	0	0	YES	YES	YES	85
STM32F4150GY6	Cortex-M4	168	1024	192	WLCSP90	72	1.8-3.6	12	2	2	0	3	13	2	3	2	0	3	6	2	1	1	1	1	0	N/A	0	0	0	0	0	0	YES	YES	YES	85
STM32F415VGT6	Cortex-M4	168	1024	192	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	3	2	0	3	6	2	1	1	1	1	0	N/A	0	0	0	0	0	0	YES	YES	YES	85
STM32F415ZGT6	Cortex-M4	168	1024	192	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	0	N/A	0	0	0	0	0	0	YES	YES	YES	85
STM32F407VET6	Cortex-M4	168	512	192	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	3	2	0	3	6	2	1	1	1	1	1	N/A	0	0	0	0	0	1	YES	N/A	N/A	85
STM32F407VGT6	Cortex-M4	168	1024	192	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	3	2	0	3	6	2	1	1	1	1	1	N/A	0	0	0	0	0	1	YES	N/A	N/A	85
STM32F407ZET6	Cortex-M4	168	512	192	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	1	N/A	0	0	0	0	0	1	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	12C	U(S)ART	CAN	SDIO SDIO	F(S)MC	USB FS HOST/OTG	LISB HS OTG	GPO	IFI LGD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F407ZGT6	Cortex-M4	168	1024	192	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	l N/	A 0	0	0	0	0	1	YES	N/A	N/A	85
STM32F407IET6	Cortex-M4	168	512	192	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	l N/	A 0	0	0	0	0	1	YES	N/A	N/A	85
STM32F407IEH6	Cortex-M4	168	512	192	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1 -	l N/	A 0	0	0	0	0	1	YES	N/A	N/A	85
STM32F407IGT6	Cortex-M4	168	1024	192	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	I N/	A 0	0	0	0	0	1	YES	N/A	N/A	85
STM32F407IGH6	Cortex-M4	168	1024	192	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	l N/	A 0	0	0	0	0	1	YES	N/A	N/A	85
STM32F417VET6	Cortex-M4	168	512	192	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	3	2	0	3	6	2	1	1	1	1	l N/	A 0	0	0	0	0	1	YES	YES	YES	85
STM32F417VGT6	Cortex-M4	168	1024	192	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	3	2	0	3	6	2	1	1	1	1	I N/	A 0	0	0	0	0	1	YES	YES	YES	85
STM32F417ZET6	Cortex-M4	168	512	192	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	I N/	A 0	0	0	0	0	1	YES	YES	YES	85
STM32F417ZGT6	Cortex-M4	168	1024	192	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	l N/	A 0	0	0	0	0	1	YES	YES	YES	85
STM32F417IET6	Cortex-M4	168	512	192	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1	l N/	A 0	0	0	0	0	1	YES	YES	YES	85
STM32F417IEH6	Cortex-M4	168	512	192	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1 .	l N/	A 0	0	0	0	0	1	YES	YES	YES	85
STM32F417IGT6	Cortex-M4	168	1024	192	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1 .	l N/	A 0	0	0	0	0	1	YES	YES	YES	85
STM32F417IGH6	Cortex-M4	168	1024	192	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	3	2	0	3	6	2	1	1	1	1 '	l N/	A 0	0	0	0	0	1	YES	YES	YES	85
									STI	//32	427	7_43	37 <u>4</u> 2	29_4	139	Adva	anced Lii	ne																	
STM32F427VGT6	Cortex-M4	180	1024	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	4	2	0	3	8	2	1	1	1	1	l N/	A 0	0	1	0	0	1	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F427VIT6	Cortex-M4	180	2048	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	4	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F427ZGT6	Cortex-M4	180	1024	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F427ZIT6	Cortex-M4	180	2048	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F427AGH6	Cortex-M4	180	1024	256	UFBGA169	130	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F427AIH6	Cortex-M4	180	2048	256	UFBGA169	130	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F427IGT6	Cortex-M4	180	1024	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F427IGH6	Cortex-M4	180	1024	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F427IIT6	Cortex-M4	180	2048	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F427IIH6	Cortex-M4	180	2048	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	N/A	N/A	85
STM32F437VGT6	Cortex-M4	180	1024	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	4	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85
STM32F437VIT6	Cortex-M4	180	2048	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	4	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85
STM32F437ZGT6	Cortex-M4	180	1024	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85
STM32F437ZIT6	Cortex-M4	180	2048	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85
STM32F437AlH6	Cortex-M4	180	2048	256	UFBGA169	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85
STM32F437IGT6	Cortex-M4	180	1024	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F437IGH6	Cortex-M4	180	1024	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85
STM32F437IIT6	Cortex-M4	180	2048	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85
STM32F437IIH6	Cortex-M4	180	2048	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	N/A	0	0	1	0	0	1	YES	YES	YES	85
STM32F429VET6	Cortex-M4	180	512	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	4	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429VGT6	Cortex-M4	180	1024	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	4	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429VIT6	Cortex-M4	180	2048	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429ZET6	Cortex-M4	180	512	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429ZGT6	Cortex-M4	180	1024	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429ZIT6	Cortex-M4	180	2048	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429ZEY6	Cortex-M4	180	512	256	WLCSP143	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429ZGY6	Cortex-M4	180	1024	256	WLCSP143	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429ZIY6	Cortex-M4	180	2048	256	WLCSP143	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429AGH6	Cortex-M4	180	1024	256	UFBGA169	130	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429AlH6	Cortex-M4	180	2048	256	UFBGA169	130	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429IEH6	Cortex-M4	180	512	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F429IET6	Cortex-M4	180	512	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429IGH6	Cortex-M4	180	1024	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429IGT6	Cortex-M4	180	1024	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429IIH6	Cortex-M4	180	2048	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429IIT6	Cortex-M4	180	2048	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429BET6	Cortex-M4	180	512	256	LQFP208	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429BGT6	Cortex-M4	180	1024	256	LQFP208	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429BIT6	Cortex-M4	180	2048	256	LQFP208	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429NEH6	Cortex-M4	180	512	256	TFBGA216	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429NGH6	Cortex-M4	180	1024	256	TFBGA216	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F429NIH6	Cortex-M4	180	2048	256	TFBGA216	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	N/A	N/A	85
STM32F439VGT6	Cortex-M4	180	1024	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	4	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439VIT6	Cortex-M4	180	2048	256	LQFP100	82	1.8-3.6	12	2	2	0	3	16	2	4	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439AlH6	Cortex-M4	180	2048	256	UFBGA169	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439ZGT6	Cortex-M4	180	1024	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	12S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F439ZIT6	Cortex-M4	180	2048	256	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439ZIY6	Cortex-M4	180	2048	256	WLCSP143	114	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439IGT6	Cortex-M4	180	1024	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439IGH6	Cortex-M4	180	1024	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439IIT6	Cortex-M4	180	2048	256	LQFP176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439IIH6	Cortex-M4	180	2048	256	UFBGA176	140	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439BGT6	Cortex-M4	180	1024	256	LQFP208	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439BIT6	Cortex-M4	180	2048	256	LQFP208	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439NGH6	Cortex-M4	180	1024	256	TFBGA216	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
STM32F439NIH6	Cortex-M4	180	2048	256	TFBGA216	168	1.8-3.6	12	2	2	0	3	24	2	6	2	0	3	8	2	1	1	1	1	1	YES	1	0	1	0	0	1	YES	YES	YES	85
										ST	M3	2F44	46 Cc	ost-l	Effe	ctive	e Line																			
STM32F446MCY6	Cortex-M4	180	256	128	WLCSP81	63	1.8-3.6	12	2	2	0	3	14	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0	N/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446MEY6	Cortex-M4	180	512	128	WLCSP81	63	1.8-3.6	12	2	2	0	3	14	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0	N/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446RCT6	Cortex-M4	180	256	128	LQFP64	50	1.8-3.6	12	2	2	0	3	16	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0	N/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446RET6	Cortex-M4	180	512	128	LQFP64	50	1.8-3.6	12	2	2	0	3	16	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0	N/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446VCT6	Cortex-M4	180	256	128	LQFP100	81	1.8-3.6	12	2	2	0	3	16	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0	N/A	0	0	2	1	0	1	N/A	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethornot	CPII	TFT LCD	MIPI DSI	SAI	SPINERY	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F446VET6	Cortex-M4	180	512	128	LQFP100	81	1.8-3.6	12	2	2	0	3	16	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0 N	/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446ZCT6	Cortex-M4	180	256	128	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0 N	/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446ZCH6	Cortex-M4	180	256	128	UFBGA144	114	1.8-3.6	12	2	2	0	3	24	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0 N	/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446ZCJ6	Cortex-M4	180	256	128	UFBGA144	114	1.8-3.6	12	2	2	0	3	24	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0 N	/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446ZET6	Cortex-M4	180	512	128	LQFP144	114	1.8-3.6	12	2	2	0	3	24	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0 N	/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446ZEH6	Cortex-M4	180	512	128	UFBGA144	114	1.8-3.6	12	2	2	0	3	24	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0 N	/A	0	0	2	1	0	1	N/A	N/A	N/A	85
STM32F446ZEJ6	Cortex-M4	180	512	128	UFBGA144	114	1.8-3.6	12	2	2	0	3	24	2	4	3	1[QUAD]	4	6	2	1	0	1	1	0 N	/A	0	0	2	1	0	1	N/A	N/A	N/A	85
									ST	M32	2F46	9_4	79 N	IIPI_	DS	l Gra	aphic Lin	е																		
STM32F469VET6	Cortex-M4	180	1024	384	LQFP100	71	1.7-3.6	12	2	2	0	3	14	2	4	2	1[QUAD]	3	7	2	1	1	1	1	0 Y	ES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469VGT6	Cortex-M4	180	1024	384	LQFP100	71	1.7-3.6	12	2	2	0	3	14	2	4	2	1[QUAD]	3	7	2	1	1	1	1	0 Y	ES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469VIT6	Cortex-M4	180	1024	384	LQFP100	71	1.7-3.6	12	2	2	0	3	14	2	4	2	1[QUAD]	3	7	2	1	1	1	1	0 Y	ES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469ZET6	Cortex-M4	180	1024	384	LQFP144	106	1.7-3.6	12	2	2	0	3	20	2	4	2	1[QUAD]	3	7	2	1	1	1	1	0 Y	ES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469ZGT6	Cortex-M4	180	1024	384	LQFP144	106	1.7-3.6	12	2	2	0	3	20	2	4	2	1[QUAD]	3	7	2	1	1	1	1	0 Y	ES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469ZIT6	Cortex-M4	180	1024	384	LQFP144	106	1.7-3.6	12	2	2	0	3	20	2	4	2	1[QUAD]	3	7	2	1	1	1	1	0 Y	ES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469AEH6	Cortex-M4	180	512	384	UFBGA169	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0 Y	ES	1	1	1	0	0	1	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F469AGH6	Cortex-M4	180	1024	384	UFBGA169	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469AIH6	Cortex-M4	180	2048	384	UFBGA169	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469AEY6	Cortex-M4	180	512	384	WLCSP168	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469AGY6	Cortex-M4	180	1024	384	WLCSP168	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469AIY6	Cortex-M4	180	2048	384	WLCSP168	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469IET6	Cortex-M4	180	512	384	LQFP176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469IEH6	Cortex-M4	180	512	384	UFBGA176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469IGT6	Cortex-M4	180	1024	384	LQFP176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469IGH6	Cortex-M4	180	1024	384	UFBGA176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469IIT6	Cortex-M4	180	2048	384	LQFP176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469IIH6	Cortex-M4	180	2048	384	UFBGA176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469BET6	Cortex-M4	180	512	384	LQFP208	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469BGT6	Cortex-M4	180	1024	384	LQFP208	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469BIT6	Cortex-M4	180	2048	384	LQFP208	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469NEH6	Cortex-M4	180	512	384	TFBGA216	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	I0	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F469NGH6	Cortex-M4	180	1024	384	TFBGA216	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F469NIH6	Cortex-M4	180	2048	384	TFBGA216	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	N/A	N/A	85
STM32F479AGH6	Cortex-M4	180	1024	384	UFBGA169	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479AlH6	Cortex-M4	180	2048	384	UFBGA169	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479AGY6	Cortex-M4	180	1024	384	WLCSP168	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479AIY6	Cortex-M4	180	2048	384	WLCSP168	114	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	0	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479IGT6	Cortex-M4	180	1024	384	LQFP176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479IGH6	Cortex-M4	180	1024	384	UFBGA176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479IIT6	Cortex-M4	180	2048	384	LQFP176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479IIH6	Cortex-M4	180	2048	384	UFBGA176	131	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479BGT6	Cortex-M4	180	1024	384	LQFP208	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479BIT6	Cortex-M4	180	2048	384	LQFP208	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479NGH6	Cortex-M4	180	1024	384	TFBGA216	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	YES	YES	85
STM32F479NIH6	Cortex-M4	180	2048	384	TFBGA216	161	1.7-3.6	12	2	2	0	3	24	2	6	2	1[QUAD]	3	8	2	1	1	1	1	1	YES	1	1	1	0	0	1	YES	YES	YES	85

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	12C	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	ДСМІ	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
Code										bit)			s	S															
								S	TM32	F205	_215	Perfo	rman	ce Li	ne														
STM32F205RBT6	Cortex-M3	120	128	64	LQFP64	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	N/A	N/A	85	85
STM32F205RCT6	Cortex-M3	120	256	96	LQFP64	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	N/A	N/A	85	85
STM32F205RET6	Cortex-M3	120	512	128	LQFP64	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	N/A	N/A	85	85
STM32F205REY6	Cortex-M3	120	512	128	WLCSP66	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	N/A	N/A	85	85
STM32F205RFT6	Cortex-M3	120	768	128	LQFP64	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	N/A	N/A	85	85
STM32F205RGT6	Cortex-M3	120	1024	128	LQFP64	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	N/A	N/A	85	85
STM32F205RGY6	Cortex-M3	120	1024	128	WLCSP66	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	N/A	N/A	85	85
STM32F205VBT6	Cortex-M3	120	128	64	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F205VCT6	Cortex-M3	120	256	96	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F205VET6	Cortex-M3	120	512	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F205VFT6	Cortex-M3	120	768	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F205VGT6	Cortex-M3	120	1024	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F205ZCT6	Cortex-M3	120	256	96	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F205ZET6	Cortex-M3	120	512	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F205ZFT6	Cortex-M3	120	768	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F205ZGT6	Cortex-M3	120	1024	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	0	0	YES	N/A	N/A	85	85
STM32F215RET6	Cortex-M3	120	512	128	LQFP64	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	YES	YES	85	85
STM32F215RGT6	Cortex-M3	120	1024	128	LQFP64	51	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	0	1	1	0	0	YES	YES	YES	85	85
STM32F215VET6	Cortex-M3	120	512	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	0	0	YES	YES	YES	85	85

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	I2S	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
													S	S															
STM32F215VGT6	Cortex-M3	120	1024	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	0	0	YES	YES	YES	85	85
STM32F215ZET6	Cortex-M3	120	512	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	0	0	YES	YES	YES	85	85
STM32F215ZGT6	Cortex-M3	120	1024	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	0	0	YES	YES	YES	85	85
												17 Eth																	
STM32F207VCT6	Cortex-M3	120	256	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207VET6	Cortex-M3	120	512	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207VFT6	Cortex-M3	120	768	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207VGT6	Cortex-M3	120	1024	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207ZCT6	Cortex-M3	120	256	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207ZET6	Cortex-M3	120	512	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207ZFT6	Cortex-M3	120	768	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207ZGT6	Cortex-M3	120	1024	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207ICT6	Cortex-M3	120	256	128	LQFP176	140	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207IET6	Cortex-M3	120	512	128	LQFP176	140	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207IFT6	Cortex-M3	120	768	128	LQFP176	140	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207IGT6	Cortex-M3	120	1024	128	LQFP176	140	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207ICH6	Cortex-M3	120	256	128	UFBGA176	140	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207IEH6	Cortex-M3	120	512	128	UFBGA176	140	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207IFH6	Cortex-M3	120	768	128	UFBGA176	140	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85
STM32F207IGH6	Cortex-M3	120	1024	128	UFBGA176	140	1.8-3.6	12	2	2	3	24	2	3	2	3	6	2	2	1	1	1	1	1	YES	N/A	N/A	85	85

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	SPI	128	120	U(S)ART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	USB HS OTG	Ethernet	DCMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32F217VET6	Cortex-M3	120	512	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	2	6	2	2	1	1	1	1	1	YES	YES	YES	85	85
STM32F217VGT6	Cortex-M3	120	1024	128	LQFP100	82	1.8-3.6	12	2	2	3	16	2	3	2	2	6	2	2	1	1	1	1	1	YES	YES	YES	85	85
STM32F217ZET6	Cortex-M3	120	512	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	2	6	2	2	1	1	1	1	1	YES	YES	YES	85	85
STM32F217ZGT6	Cortex-M3	120	1024	128	LQFP144	114	1.8-3.6	12	2	2	3	24	2	3	2	2	6	2	2	1	1	1	1	1	YES	YES	YES	85	85
STM32F217IET6	Cortex-M3	120	512	128	LQFP176	140	1.8-3.6	12	2	2	3	24	2	3	2	2	6	2	2	1	1	1	1	1	YES	YES	YES	85	85
STM32F217IGT6	Cortex-M3	120	1024	128	LQFP176	140	1.8-3.6	12	2	2	3	24	2	3	2	2	6	2	2	1	1	1	1	1	YES	YES	YES	85	85
STM32F217IEH6	Cortex-M3	120	512	128	UFBGA176	140	1.8-3.6	12	2	2	3	24	2	3	2	2	6	2	2	1	1	1	1	1	YES	YES	YES	85	85
STM32F217IGH6	Cortex-M3	120	1024	128	UFBGA176	140	1.8-3.6	12	2	2	3	24	2	3	2	2	6	2	2	1	1	1	1	1	YES	YES	YES	85	85

STM32 U0系列 - Arm® Cortex®-M0+ 新一代超低功耗入门级MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	Ī0	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	T° Max (°C)
STM32U031F4	Cortex-M0+	56	16	12	TSSOP-20	17	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031F6	Cortex-M0+	56	32	12	TSSOP-20	17	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031F8	Cortex-M0+	56	64	12	TSS0P-20	17	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031G6	Cortex-M0+	56	32	12	WLCSP 27	24	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031G8	Cortex-M0+	56	64	12	WLCSP 27	24	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031K4	Cortex-M0+	56	16	12	UFQFPN 32	27	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031K6	Cortex-M0+	56	32	12	UFQFPN 32	27	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031K8	Cortex-M0+	56	64	12	UFQFPN 32	27	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031C6	Cortex-M0+	56	32	12	LQFP 48, UFQFPN 48	39	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031C8	Cortex-M0+	56	64	12	LQFP 48, UFQFPN 48	39	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031R6	Cortex-M0+	56	32	12	LQFP 64, UFBGA 64	53	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U031R8	Cortex-M0+	56	64	12	LQFP 64, UFBGA 64	53	1.65-3.6	8	1	1	2	1	16	1	1	1	2	3	4	2	0	No	YES	85
STM32U073K8	Cortex-M0+	56	64	40	UFQFPN 32	27	1.65-3.6	8	1	1	3	1	10	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073H8	Cortex-M0+	56	64	40	WLCSP 42	33	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073C8	Cortex-M0+	56	64	40	LQFP 48, UFQFPN 48	39	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073R8	Cortex-M0+	56	64	40	LQFP 64, UFBGA 64	53	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073M8	Cortex-M0+	56	64	40	LQFP 80, UFBGA 81	68/69	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073KB	Cortex-M0+	56	128	40	UFQFPN 32	27	1.65-3.6	8	1	1	3	1	10	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073HB	Cortex-M0+	56	128	40	WLCSP 42	33	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073CB	Cortex-M0+	56	128	40	LQFP 48, UFQFPN 48	39	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073RB	Cortex-M0+	56	128	40	LQFP 64, UFBGA 64	53	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85

STM32 U0系列 - Arm® Cortex®-M0+ 新一代超低功耗入门级MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	OPAMP	SPI	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	T° Max (°C)
STM32U073MB	Cortex-M0+	56	128	40	LQFP 80, UFBGA 81	68/69	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073KC	Cortex-M0+	56	256	40	UFQFPN 32	27	1.65-3.6	8	1	1	3	1	10	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073HC	Cortex-M0+	56	256	40	WLCSP 42	33	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073CC	Cortex-M0+	56	256	40	LQFP 48, UFQFPN 48	39	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073RC	Cortex-M0+	56	256	40	LQFP 64, UFBGA 64	53	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U073MC	Cortex-M0+	56	256	40	LQFP 80, UFBGA 81	68/69	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U083KC	Cortex-M0+	56	256	40	UFQFPN 32	27	1.65-3.6	8	1	1	3	1	10	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U083HC	Cortex-M0+	56	256	40	WLCSP 42	33	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U083CC	Cortex-M0+	56	256	40	LQFP 48, UFQFPN 48	39	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U083RC	Cortex-M0+	56	256	40	LQFP 64, UFBGA 64	53	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85
STM32U083MC	Cortex-M0+	56	256	40	LQFP 80, UFBGA 81	68/69	1.65-3.6	8	1	1	3	1	16	1	2	1	3	4	4	3	1	Yes	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer		4-bit U	ADC 14-bit Channels	DAC 12-bit Channels	COMP	OBAMB	SDI SDI	M-SPI	ìzc	U(S)ART	I PIIART	CAN	SDIO	E/S/MC	IICR Davice	USB HS OTG	JPEG Codec	GPU	3D GPU	TFT LCD	MIPI_DSI	SAI	DESDM	IVIALII ACCEIEI ALUI	irust'zone	TRNG	OTFUEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32U575CGTx	Cortex-M33	160	1024	786	LQFP48	36	1.71- 3.6	5	4	2	4	1 1	1 1	11	2	2 2	2 3	3 0	2[OCTO]	4	4	1	1[FD]	0	0 (0 1	0	N	Υ	N	0	0	1	0 (י כ	ſΥ	′ Y	' N	N	N	Υ	125
STM32U575CITx	Cortex-M33	160	2048	786	LQFP48	36	1.71- 3.6	5	4	2	4	1 1	1 1	11	2	2 2	2 3	3 0	2[0CT0]	4	4	1	1[FD]	0	0 (0 1	0	N	Υ	N	0	0	1	0 () \	ſΥ	′ Y	' N	N	N	Υ	125
STM32U575CGUx	Cortex-M33	160	1024	786	UFQFPN48	36	1.71- 3.6	5	4	2	4	1 1	1 1	11	2	2 2	2 3	3 0	2[0CT0]	4	4	1	1[FD]	0	0	0 1	0	N	Υ	N	0	0	1	0 (י כ	ſΥ	′ Y	' N	N	N	Υ	125
STM32U575ClUx	Cortex-M33	160	2048	786	UFQFPN48	36	1.71- 3.6	5	4	2	4	1 1	1 1	11	2	2 2	2 3	3 0	2[OCTO]	4	4	1	1[FD]	0	0 (0 1	0	N	Υ	N	0	0	1	0 (י כ	Υ	′ Y	' N	N	N	Υ	125
STM32U575RGTx	Cortex-M33	160	1024	786	LQFP64	50	1.71- 3.6	5	4	2	4	1 1	7 1	17	2	2 2	2 3	3 0	2[OCTO]	4	5	1	1[FD]	2	0 (0 1	0	N	Υ	N	0	0	1	0 -	1 '	Υ	′ Y	' N	N	N	Υ	125
STM32U575RITx	Cortex-M33	160	2048	786	LQFP64	50	1.71- 3.6	5	4	2	4	1 1	7 1	17	2	2 2	2 3	3 0	2[0CT0]	4	5	1	1[FD]	2	0	0 1	0	N	Υ	N	0	0	1	0 -	1 '	Υ	′ Y	' N	N	N	Υ	125
STM32U5750GYx	Cortex-M33	160	1024	786	WLCSP90	69	1.71- 3.6	5	4	2	4	1 1	6 1	16	2	2 2	2 3	3 0	2[0CT0]	4	5	1	1[FD]	2	1 (0 1	0	N	Υ	N	0	0	1	0	1 '	Υ	′ Y	' N	N	N	Υ	125
STM32U5750IYx	Cortex-M33	160	2048	786	WLCSP90	69	1.71- 3.6	5	4	2	4	1 1	6 1	16	2	2 2	2 3	3 0	2[0CT0]	4	5	1	1[FD]	2	1 (0 1	0	N	Υ	N	0	0	1	0	1 \	ſΥ	′ Y	' N	N	N	Υ	125
STM32U575VGTx	Cortex-M33	160	1024	786	LQFP100	82	1.71- 3.6	5	4	2	4	1 2	0 1	20	2	2 2	2 3	3 0	2[0CT0]	4	5	1	1[FD]	2	1 (0 1	0	N	Υ	N	0	0	2	0 -	1 \	/ Y	′ Y	' N	N	N	Υ	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	LPlimer	ADC 12-bit Units	ADC 12-bit Channels	ADC 14-bit Units	ADC 14-bit Channels	DAC 19-bit Channels	OPAMP	SPI	I2S	M-SPI	ÎŻC	U(S)ART		CAN	SDIO SDIO	USB Device	USB FS HOST/OTG	USB HS OTG	ard	CD	3D GPU	TFT LCD	MIPI DSI	SAL	DGMI	Math Accelerator	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	T° Max (°C)
STM32U575VITx	Cortex-M33	160	2048	786	LQFP100	82	1.71- 3.6	5	4 2	2 4	1	20	1	20	2 2	2 2	3	0	2[OCTO]	4	5 1	1	I [FD]	2 1	0	1	0 N	,	Y	N	0	0 :	2 () 1	Υ	Υ	Υ	N	N	1 1	N 125
STM32U575QGlx	Cortex-M33	160	1024	786	UFBGA132	109	1.71- 3.6	5	4 2	2 4	1	24	1	24	2 2	2 2	3	0	2[0CT0]	4	5 1	1	I [FD]	2 1	0	1	0 N	,	Y	N	0	0 :	2 () 1	Υ	Υ	Υ	N	N	1 1	N 125
STM32U575QIIx	Cortex-M33	160	2048	786	UFBGA132	109	1.71- 3.6	5	4 2	2 4	1	24	1	24	2 2	2 2	3	0	2[0CT0]	4	5 1	1	I[FD]	2 1	0	1	0 N	,	Y	N	0	0	2 () 1	Υ	Υ	Υ	N	N	1 1	N 125
STM32U575ZGTx	Cortex-M33	160	1024	786	LQFP144	113	1.71- 3.6	5	4 2	2 4	1	24	1	24	2 2	2 2	3	0	2[0CT0]	4	5 1	1	I [FD]	2 1	0	1	0 N	,	Y	N	0	0	2 () 1	Υ	Υ	Υ	N	N	1 1	N 125
STM32U575ZITx	Cortex-M33	160	2048	786	LQFP144	113	1.71- 3.6	5	4 2	2 4	1	24	1	24	2 2	2 2	3	0	2[0CT0]	4	5 1	1 1	1 [FD]	2 1	0	1	0 N	,	Y	N	0	0	2 () 1	Υ	Υ	Υ	N	N	1 1	N 125
STM32U575AGIx	Cortex-M33	160	1024	786	UFBGA169	137	1.71- 3.6	5	4 2	2 4	1	24	1	24	2 2	2 2	3	0	2[0CT0]	4	5 1	1	I [FD]	2 1	0	1	0 N	,	Y	N	0	0	2 () 1	Υ	Υ	Υ	N	N	N I	N 125
STM32U575Allx	Cortex-M33	160	2048	786	UFBGA169	137	1.71- 3.6	5	4 2	2 4	1	24	1	24	2 2	2 2	3	0	2[0CT0]	4	5 1	l 1	1 [FD]	2 1	0	1	0 N	,	Y	N	0	0	2 () 1	Υ	Υ	Υ	N	N	N I	N 125
STM32U585CITx	Cortex-M33	160	2048	786	LQFP48	36	1.71- 3.6	5	4 2	2 4	1	11	1	11	2 2	2 2	3	0	2[OCTO]	4	4 1	1	I [FD]	0 (0	1	0 N	,	Y	N	0	0	1 (0	Υ	Υ	Υ	Υ	Υ	Ϋ́	Y 125
STM32U585CIUx	Cortex-M33	160	2048	786	UFQFPN48	36	1.71- 3.6	5	4 2	2 4	1	11	1	11	2 2	2 2	3	0	2[0CT0]	4	4 1	1 1	I [FD]	0 0	0	1	0 N	,	Y	N	0	0	1 (0	Υ	Υ	Υ	Υ	Υ	Ϋ́	Y 125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	AUC 12-DIT UNITS	ADC 12-bit Channels	ADC 14-bit Units	ADC 14-bit Channels	DAC 19-bit Channels	OPAMP	SPI	12S	M-SPI	izc	U(S)ART	LPUART	CAN	SDIO	F(S)MC	IISB Device	USB HS OTG	JPEG Codec	GPU	3D GPU	TFT LCD	MIPI_DSI	SAI	DESDM	DCMI DCMI	Moth Apploator	IRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32U585RITx	Cortex-M33	160	2048	786	LQFP64	50	1.71- 3.6	5	4	2	4 1	17	1	17	2 2	2 2	3	0	2[0CT0]	4	5	1	1[FD]	2	0	0	0	N	Υ	N	0	0	1	0	1 \	ΥY	′ Y	′ Y	Υ	Υ	Υ	125
STM32U5850IYx	Cortex-M33	160	2048	786	WLCSP90	69	1.71- 3.6	5	4	2	4 1	16	6 1	16	2 2	2 2	3	0	2[0CT0]	4	5	1	1[FD]	2	1	0	1 0	N	Υ	N	0	0	1	0	1 \	ΥY	′ Y	′ Y	Υ	Υ	Υ	125
STM32U585VITx	Cortex-M33	160	2048	786	LQFP100	82	1.71- 3.6	5	4	2	4 1	20) 1	20	2 2	2 2	3	0	2[0CT0]	4	5	1	1[FD]	2	1	0	1 0	N	Υ	N	0	0	2	0	1 '	ΥY	′ Y	′ Y	Υ	Υ	Υ	125
STM32U585QIIx	Cortex-M33	160	2048	786	UFBGA132	109	1.71- 3.6	5	4	2	4 1	1 24	1	24	2 2	2 2	3	0	2[0CT0]	4	5	1	1[FD]	2	1	0	1 0	N	Υ	N	0	0	2	0	1 '	ΥY	′ Y	′ Y	Υ	Υ	Υ	125
STM32U585ZITx	Cortex-M33	160	2048	786	LQFP144	113	1.71- 3.6	5	4	2	4 1	1 24	1	24	2 2	2 2	3	0	2[0CT0]	4	5	1	1[FD]	2	1	0	1 0	N	Υ	N	0	0	2	0	1 '	ΥY	′ Y	′ Y	Υ	Υ	Υ	125
STM32U585Allx	Cortex-M33	160	2048	786	UFBGA169	137	1.71- 3.6	5	4	2	4 1	1 24	1	24	2 2	2 2	3	0	2[0CT0]	4	5	1	1[FD]	2	1	0 -	1 0	N	Υ	N	0	0	2	0	1 '	ΥY	′ Y	′ Y	Y	Υ	Υ	125
STM32U535CE	Cortex-M33	160	512	274	LQFP48, UFQFP48	37	1.71- 3.6	5	2	2	4 1	1 11	1	11	2 1	1	3	0	0	4	2	2	1[FD]	0	0	1 (0 0	N	N	N	0	0	0	0	0 '	ΥY	′ Y	' N	N	N	N	125
STM32U535JE	Cortex-M33	160	512	274	WLCSP72	50	1.71- 3.6	5	2	2	4 1	1 12	2 1	12	2 1	1	3	0	0	4	2	2	1[FD]	0	0	1 (0	N	N	N	0	0	0	0	י ס	ΥY	′ Y	' N	N	N	N	125
STM32U535NE	Cortex-M33	160	512	274	WLCSP56	39	1.71- 3.6	5	2	2	4 1	11	1	11 :	2 1	1	3	0	0	4	2	2	1[FD]	0	0	1 (0	N	N	N	0	0	0	0	י ס	ΥY	′ Y	' N	N	N	N	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	AUG 12-DICOLLIS	ADC 12-bit Channels	ADC 14-bit Units	ADC 14-bit Channels	DAC 12-bit Channels	COMB	SPI	128	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	USB HS UTG	JPEG Codec	GPU	3D GPU	TFT LCD	MIPI_DSI	SAI	DESDM	Matti Accelerator	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32U535RE	Cortex-M33	160	512	274	LQFP64, UFBGA64	51	1.71- 3.6	5	2	2	4 1	1 17	7 1	17	2	1 1	3	0	0	4	4	2	1[FD]	1	0	1	0 0	N	N	N	0	0	0	0 1	י ס	ſΥ	′ Y	' N	N	N	N	125
STM32U535VE	Cortex-M33	160	512	274	LQFP100, UFBGA100	82	1.71- 3.6	5	2	2	4 1	1 20	0 1	20	2	1 1	3	0	0	4	4	2	1[FD]	1	0	1	0 0	N	N	N	0	0	0	0	1 \	ſΥ	′ Y	' N	N	N	N	125
STM32U545CE	Cortex-M33	160	512	274	LQFP48, UFQFPN48	37	1.71- 3.6	5	2	2	4 1	1 1 ⁻	1 1	11	2	1 1	3	0	0	4	2	2	1[FD]	0	0	1	0 0	N	N	N	0	0	0	0 (۱ 0	ſΥ	′ Y	' Y	Υ	Υ	Υ	125
STM32U545JE	Cortex-M33	160	512	274	WLCSP72	50	1.71- 3.6	5	2	2	4 1	1 12	2 1	12	2	1 1	3	0	0	4	2	2	1[FD]	0	0	1	0 0	N	N	N	0	0	0	0 1	۱ 0	ſΥ	′ Y	' Y	Υ	Υ	Υ	125
STM32U545NE	Cortex-M33	160	512	274	WLCSP56	39	1.71- 3.6	5	2	2	4 1	1 1 ⁻	1 1	11	2	1 1	3	0	0	4	2	2	1[FD]	0	0	1	0 0	N	N	N	0	0	0	0	۱ 0	ſΥ	′ Y	' Y	Υ	Υ	Υ	125
STM32U545RE	Cortex-M33	160	512	274	LQFP64, UFBGA64	51	1.71- 3.6	5	2	2	4 1	1 17	7 1	17	2	1 1	3	0	0	4	4	2	1[FD]	1	0	1	0 0	N	N	N	0	0	0	0	۱ 0	ſΥ	′ Y	' Y	Υ	Υ	Υ	125
STM32U545VE	Cortex-M33	160	512	274	LQFP100, UFBGA100	82	1.71- 3.6	5	2	2	4 1	1 20	0 1	20	2	1 1	3	0	0	4	4	2	1[FD]	1	0	1	0 0	N	N	N	0	0	0	0	1 '	ſΥ	′ Y	′ Y	Υ	Υ	Υ	125
STM32U595VJ	Cortex-M33	160	4096	2514	LQFP100	82	1.71- 3.6	7	4	2	4 1	1 20) 2	20	2 2	2 2	2 3	0	5	6	6	1	1[FD]	2	Υ	0	Y 0	N	Υ	N	0	0	1	Υ ,	1 Y	I Y	′ Y	' N	N	N	N	85
STM32U595ZJ	Cortex-M33	160	4096	2514	LQFP144	114	1.71- 3.6	7	4	2	4 1	1 24	4 2	24	2 2	2 2	2 3	0	5	6	6	1	1[FD]	2	Υ	0	Y 0	N	Υ	N	0	0	2	Υ,	1 Y	I Y	′ Y	' N	N	N	N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	3 E	ADC 14-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	261	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	IISB Device	USB HS OTG	JPEG Codec	GPU	3D GPU	TFT LCD	MIPI_DSI	SAI	DFSDM	DCMI	Math Accelerator	Trust'Zone	TRNG	PRA	AES/DES	SHA/HMAC	T° Max (°C)
STM32U5A5VJ	Cortex-M33	160	4096	2514	LQFP100	82	1.71- 3.6	7	4	2	4	1 2	20 2	2 20	2	2	2	3 (0	5	6	6	1	1[FD]	2	Υ	0 \	0	N	Υ	N	0	0	2	Υ	Υ	N	Ϋ́	ΥY	Υ	Y	Υ	85
STM32U5A5ZJ	Cortex-M33	160	4096	2514	LQFP144	115	1.71- 3.6	7	4	2	4	1 2	24 2	2 24	2	2	2	3 (0	5	6	6	1	1[FD]	2	Υ	0 \	0	N	Υ	N	0	0	2	Υ	Υ	N	Ϋ́	ΥY	ſΥ	Y	Υ	85
STM32U595QJ	Cortex-M33	160	4096	2514	UFBGA132	110	1.71- 3.6	7	4	2	4	1 2	24 2	2 24	2	2	2	3 (0	5	6	6	1	1[FD]	2	Υ	0 \	0	N	Υ	N	0	0	2	Υ	Υ	N	Ϋ́	1 Y	J N	J N	N	85
STM32U595RJ	Cortex-M33	160	4096	2514	LQFP64	51	1.71- 3.6	7	4	2	4	1 1	7 2	2 27	2	2	2	3 (0	5	6	6	1	1[FD]	2	N	0 \	0	N	Υ	N	0	0	2	Υ	Υ	N	Ϋ́	Y I	J N	J N	N	85
STM32U5A5QJ	Cortex-M33	160	4096	2514	UFBGA132	110	1.71- 3.6	7	4	2	4	1 2	24 2	2 24	2	2	2	3 (0	5	6	6	1	1[FD]	2	Υ	0 \	/ 0	N	Υ	N	0	0	2	Υ	Υ	N	γ,	ΥY	/ Y	' Y	Υ	85
STM32U5A5RJ	Cortex-M33	160	4096	2514	LQFP64	51	1.71- 3.6	7	4	2	4	1 1	7 2	2 27	2	2	2	3 (0	5	6	6	1	1[FD]	2	N	0 \	/ 0	N	Υ	N	0	0	2	Υ	Υ	N	γ,	Ϋ́	Υ	' Y	Υ	85
STM32U595AJ	Cortex-M33	160	4096	2514	TFBGA 7X7X1.1 169L P 0.5 MM,TFBGA 7X7X1.1- 169-F13 P.5-B.25	136	1.71- 3.6	7	4	2	4	1 2	24 2	2 24	2	2	2	3	0	5	6	6	1	1[FD]	2	Υ	0 \	(0	N	Υ	N	0	0	2	Υ	Υ	N	γ ,	1 Y	N N	I N	N	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Advanced Timer (15-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	ADC 14-bit Channels	DAC 12-bit Channels	COMP	ODAMB OTA	I2S	M-SPI	ĺŹC	LPUAKI U(S)ART	GAN	SDIO	F(S)MC	USB Device	USB ES HOST/OTG	JPEG Codec	GPU	3D GPU	TFT LCD	MIPI DSI	SAI	DCMI	Math Accelerator	Trust'Zone	TRNG	OTENEC	AES/DES	SHA/HMAC	T° Max (°C)
STM32U5A5AJ	Cortex-M33	160	4096	2514	TFBGA 7X7X1.1 169L P 0.5 MM,TFBGA 7X7X1.1- 169-F13 P.5-B.25	136	1.71- 3.6	7	4 2	4	1	24	2 24	1 2	2 2	2 3	0	5	6	6 1	1[F	D] 2	Υ	0	Y) N	Υ	N	0	0	2 Y	′ Y	N	Υ	Υ	Y	ΥY	Υ	85
								STN	132U	599	_5/	19_5	F9_	5 G 9	_5F7	_5	G7 .	Advance	ed G	rapt	ic L	ine																	
STM32U599BJ	Cortex-M33	160	4096	2514	WLCSP208	145	1.71- 3.6	7	4 2	4	1	24	2 24	1 2	2 2	2 3	0	7	6	6 1	1[F	D] 2	Υ	0	0 \	/ N	Υ	Υ	Υ	Υ	2 Y	′ Y	N	Υ	Υ	N I	N N	N	85
STM32U599NI	Cortex-M33	160	2048	2514	TFBGA216	156	1.71- 3.6	7	4 2	4	1	24	2 24	1 2	2 2	2 3	0	7	6	6 1	1[F	D] 2	Υ	0	0 \	/ N	Υ	Υ	Υ	Υ	2 Y	′ Y	N	Υ	Υ	N I	N N	N	85
STM32U599NJ	Cortex-M33	160	4096	2514	TFBGA216	156	1.71- 3.6	7	4 2	2 4	1	24	2 24	1 2	2 2	2 3	3 0	7	6	6 1	1[F	D] 2	Υ	0	0 1	/ N	Υ	Υ	Υ	Υ	2 Y	′ Y	N	Υ	Υ	N I	N N	N	85
STM32U599VJ	Cortex-M33	160	4096	2514	LQFP100	82	1.71- 3.6	7	4 2	. 4	1	20	2 20	2	2 2	2 3	3 0	5	6	6 1	1[F	D] 2	Υ	0	0 1	/ N	Υ	Υ	Υ	N	2 Y	′ Y	N	Υ	Υ	N 1	N N	N	85
STM32U599ZJ	Cortex-M33	160	4096	2514	LQFP144, WLCSP150	108	1.71- 3.6	7	4 2	2 4	1	24	2 24	1 2	2 2	2 3	3 0	5	6	6 1	1[F	D] 2	Υ	0	0 \	/ N	Υ	Υ	Υ	N	2 \	′ Y	N	Υ	Υ	N 1	N N	N	85
STM32U5A9BJ	Cortey_M33	160	4096	2514	WLCSP208	1/15	1.71- 3.6	7	4 2	4	1	24	2 24	1 2	2 2	2	3 0	7	6	6 1	1 [F	מו 2	٧	0	0 \	/ N	Υ	v	٧	٧	2 \	, _v	N	γ	٧	v v	/ Y	γ	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	LY IIMer	ADC 12-bit Units	ADC 12-bit Channels	ADC 14-bit Units	ADC 14-bit Channels	DAC 12-hit Channels	OPAMP	SPI	128	M-SPI	l2C	U(S)ART	TOMICO I	CAN	F(S)MC	USB Device	USB FS HOST/OTG	JPEG Codec USB HS OTG	GPU		3D GPU	MIPI USI	SAI	DFSDM	DCMI	Math Accelerator	Trust'Zone	TRNG	OTEDEC	AES/DES	SHA/HMAC	T° Max (°C)
STM32U5A9NJ	Cortex-M33	160	4096	2514	TFBGA216	156	1.71- 3.6	7	4 2	2 4	. 1	24	2	24	2 2	2 2	3	0	7	6	6	1 1	[FD]	2 Y	0	0	YN	Υ		Y	ſΥ	2	Υ	Υ	N	Υ	Υ	Υ	ΥY	′ Y	85
STM32U5A9VJ	Cortex-M33	160	4096	2514	LQFP100	82	1.71- 3.6	7	4 2	2 4	. 1	20	2	20	2 2	2 2	3	0	5	6	6	1 1	[FD]	2 Y	0	0	Y N	Υ		Y	/ N	1 2	Υ	Υ	N	Υ	Υ	Ϋ́	ΥY	′ Y	85
STM32U5A9ZJ	Cortex-M33	160	4096	2514	LQFP144, WLCSP150	108	1.71- 3.6	7	4 2	2 4	. 1	24	2	24	2 2	2 2	3	0	5	6	6	1 1	[FD]	2 Y	0	0	Y N	Υ		Y	/ N	1 2	Υ	Υ	N	Υ	Υ	Y	ΥY	′ Y	85
STM32U5F7VJ	Cortex-M33	160	4096	3024	LQFP 100	63	1.71- 3.6	5	4 2	2 4	. 1	20	2	20	2 2	2 2	3	2	3	6	6	1 1	[FD]	2 1	0	0	1 Y	GPU	2D	N ·	1 0	2	0	1	Υ	Υ	Υ	N I	N N	ΙY	85
STM32U5G7VJ	Cortex-M33	160	4096	3024	LQFP 100	63	1.71- 3.6	5	4 2	2 4	. 1	20	2	20	2 2	2 2	3	2	3	6	6	1 1	[FD]	2 1	0	0	1 Y	GPU	2D	N ·	1 0	2	0	1	Υ	Υ	Υ	Ϋ́	ΥY	' Y	85
STM32U5F9BJ	Cortex-M33	160	4096	3024	WLCSP 208	145	1.71- 3.6	5	4 2	2 4	1	24	2	24	2 2	2 2	3	2	3	6	6	1	0	2 1	0	0	1 Y	GPU	2D	N ·	1 1	2	0	1	Υ	Υ	Υ	N I	N N	ΙY	85
STM32U5F9NJ	Cortex-M33	160	4096	3024	TFBGA 216	156	1.71- 3.6	5	4 2	2 4	. 1	24	2	24	2 2	2 2	3	2	3	6	6	1 1	[FD]	2 1	0	0	1 Y	GPU	2D	N ·	1 1	2	0	1	Υ	Υ	Υ	N I	N N	ΙY	85
STM32U5F9VJ	Cortex-M33	160	4096	3024	LQFP 100	63	1.71- 3.6	5	4 2	2 4	. 1	17	1	17	2 2	2 2	3	2	3	6	6	1 1	[FD]	1 (0	0	1 Y	GPU	2D	N ·	1 1	2	0	1	Υ	Υ	Υ	N I	N N	ΙY	85
STM32U5F9ZJ	Cortex-M33	160	4096	3024	LQFP 144 , UFBGA 144	101	1.71- 3.6	5	4 2	2 4	. 1	22	2	22	2 2	2 2	3	2	3	6	6	1 1	[FD]	2 1	0	0	1 Y	GPU	2D	N ·	1 1	2	0	1	Υ	Υ	Υ	N I	N N	ΙY	85

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Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM	Package	10	VDD	Timer (16-bit)	er (32-b	Advanced Timer (16-bit)	I PTimer	ADC 12-bit linits	ADC 13 hit Chample	ADC 14-bit Channels	DAC 12-bit Channels	COMP	OBAMB	I2S	M-SPI	IZU	U(S)AKI	LPUART	CAN	SDIO	F(S)MC	اچ	USB ES HOST/OTG	JPEG Codec	GPU	3D GPU	TFT LCD	MIPI DSI	SAI	DECINI	Math Accelerator	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32U5G9BJ	Cortex-M33	160	4096	3024	WLCSP 208	145	1.71- 3.6	5	4	2	4	1 2	4 2	24	2	2 2	2 3	3 2	3	6	6	5 1	0	2	1	0	0 1	Υ	GPU2D	N	1	1	2 () 1	Y	′ Y	Υ	Υ	Υ	Υ	Y	85
STM32U5G9NJ	Cortex-M33	160	4096	3024	TFBGA 216	156	1.71- 3.6	5	4	2	4	1 2	4 2	24	2	2 2	2 3	3 2	3	6	6	5 1	1[FD]	2	1	0	0 1	Υ	GPU2D	N	1	1	2 () 1	Y	′ Y	Υ	Υ	Υ	Υ	Y	85
STM32U5G9VJ	Cortex-M33	160	4096	3024	LQFP 100	63	1.71- 3.6	5	4	2	4	1 1	7 1	17	2	2	2 3	3 2	3	6	6	5 1	1[FD]	1	0	0	0 1	Υ	GPU2D	N	1	1	2 () 1	Y	′ Y	Υ	Υ	Υ	Υ	Y	85
STM32U5G9ZJ	Cortex-M33	160	4096	3024	LQFP 144, UFBGA 144	101	1.71- 3.6	5	4	2	4	1 2	2 2	22	2	2	2 3	3 2	3	6	6	3 1	1[FD]	2	1	0	0 1	Υ	GPU2D	N	1	1	2 () 1	Y	′ Y	Υ	Υ	Υ	Υ	Y	85

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能高安全MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	SAI	DFSDM	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
									S	ГМЗ	2L55	2 US	SB D	evic	e +	CAN	-FD	Line																
STM32L552CCTx	Cortex-M33	110	256	256	LQFP48	38	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	5	1	1[FD]	0	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552CCUx	Cortex-M33	110	256	256	UFQFN48	38	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	5	1	1[FD]	0	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552CETx	Cortex-M33	110	512	256	LQFP48	38	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	5	1	1[FD]	0	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552CEUx	Cortex-M33	110	512	256	UFQFN48	38	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	5	1	1[FD]	0	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552CETxP	Cortex-M33	110	512	256	LQFP48	36	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	4	1	1[FD]	0	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552CEUxP	Cortex-M33	110	512	256	UFQFN48	36	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	4	1	1[FD]	0	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552RCTx	Cortex-M33	110	256	256	LQFP64	52	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552RETx	Cortex-M33	110	512	256	LQFP64	52	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552RETxP	Cortex-M33	110	512	256	LQFP64	50	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	0	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552RETxQ	Cortex-M33	110	512	256	LQFP64	47	1.71-3.6	9	2	2	3	2	15	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552MEYxP	Cortex-M33	110	512	256	WLCSP81	54	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552MEYxQ	Cortex-M33	110	512	256	WLCSP81	51	1.71-3.6	9	2	2	3	2	15	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552VETx	Cortex-M33	110	512	256	LQFP100	83	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552VCTxQ	Cortex-M33	110	512	256	LQFP100	79	1.71-3.6	9	2	2	3	2	14	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552VETxQ	Cortex-M33	110	512	256	LQFP100	79	1.71-3.6	9	2	2	3	2	14	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能高安全MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	SAI	DFSDM	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32L552QElxP	Cortex-M33	110	512	256	UFBGA132	108	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552QClxQ	Cortex-M33	110	256	256	UFBGA132	105	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552QEIxQ	Cortex-M33	110	512	256	UFBGA132	105	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552ZETx	Cortex-M33	110	512	256	LQFP144	115	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552ZETxQ	Cortex-M33	110	512	256	LQFP144	111	1.71-3.6	9	2	2	3	2	14	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
STM32L552ZCTxQ	Cortex-M33	110	256	256	LQFP144	111	1.71-3.6	9	2	2	3	2	14	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	N/A	N/A	N/A	YES	125
								S	TMS	32L5	62 L	JSB	Devi	ce +	CA	N-FD	+ A	ES Line																
STM32L562CETx	Cortex-M33	110	512	256	LQFP48	38	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	5	1	1[FD]	0	0	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562CEUx	Cortex-M33	110	512	256	UFQFN48	38	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[OCTO]	4	5	1	1[FD]	0	0	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562CETxP	Cortex-M33	110	512	256	LQFP48	36	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	4	1	1[FD]	0	0	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562CEUxP	Cortex-M33	110	512	256	UFQFN48	36	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	4	1	1[FD]	0	0	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562RETx	Cortex-M33	110	512	256	LQFP64	52	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562RETxP	Cortex-M33	110	512	256	LQFP64	50	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[OCTO]	4	5	1	1[FD]	0	0	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562RETxQ	Cortex-M33	110	512	256	LQFP64	47	1.71-3.6	9	2	2	3	2	9	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562MEYxP	Cortex-M33	110	512	256	WLCSP81	54	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562MEYxQ	Cortex-M33	110	512	256	WLCSP81	51	1.71-3.6	9	2	2	3	2	15	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	0	1	2	1	YES	YES	YES	YES	YES	YES	125

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能高安全MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	12C	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	SAI	DFSDM	Trust'Zone	TRNG	OTFDEC	РКА	AES/DES	SHA/HMAC	T° Max (°C)
STM32L562VETx	Cortex-M33	110	512	256	LQFP100	83	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562VETxQ	Cortex-M33	110	512	256	LQFP100	79	1.71-3.6	9	2	2	3	2	14	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562QEIx	Cortex-M33	110	512	256	UFBGA132	110	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562QEIxP	Cortex-M33	110	512	256	UFBGA132	108	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562QElxQ	Cortex-M33	110	512	256	UFBGA132	105	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562ZETx	Cortex-M33	110	512	256	LQFP144	115	1.71-3.6	9	2	2	3	2	16	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125
STM32L562ZETxQ	Cortex-M33	110	512	256	LQFP144	111	1.71-3.6	9	2	2	3	2	14	2	2	2	3	1[0CT0]	4	5	1	1[FD]	1	1	1	2	1	YES	YES	YES	YES	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO		USB FS HOST/OTG	GPII	MIPI_DSI	SAI	DFSDM	DGMI	PSSI	TRNG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
											5	STM	32L	4x5	USE	3 OT	G L	ine																		
STM32L4P5CGTx	Cortex-M4	120	1024	320	LQFP48	38	1.71-3.6	9	2	2	2	2	10	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	0	N/A	YES	N/A	N/A	YES	125
STM32L4P5CGTxP	Cortex-M4	120	1024	320	LQFP48	36	1.71-3.6	9	2	2	2	2	10	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	0	N/A	YES	N/A	N/A	YES	125
STM32L4P5CGUx	Cortex-M4	120	1024	320	UFQFPN48	38	1.71-3.6	9	2	2	2	2	10	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	0	N/A	YES	N/A	N/A	YES	125
STM32L4P5CGUxP	Cortex-M4	120	1024	320	UFQFPN48	36	1.71-3.6	9	2	2	2	2	10	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	0	N/A	YES	N/A	N/A	YES	125
STM32L4P5CETx	Cortex-M4	120	512	320	LQFP48	38	1.71-3.6	9	2	2	2	2	10	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	0	N/A	YES	N/A	N/A	YES	125
STM32L4P5CEUx	Cortex-M4	120	512	320	UFQFPN48	38	1.71-3.6	9	2	2	2	2	10	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	0	N/A	YES	N/A	N/A	YES	125
STM32L4P5RGTx	Cortex-M4	120	1024	320	LQFP64	52	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5RGTxP	Cortex-M4	120	1024	320	LQFP64	50	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5RETx	Cortex-M4	120	512	320	LQFP64	52	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5VGTx	Cortex-M4	120	1024	320	LQFP100	83	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5VGTxP	Cortex-M4	120	1024	320	LQFP100	81	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5VGYx	Cortex-M4	120	1024	320	WLCSP100	81	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5VGYxP	Cortex-M4	120	1024	320	WLCSP100	79	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5VETx	Cortex-M4	120	512	320	LQFP100	83	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5VEYx	Cortex-M4	120	512	320	WLCSP100	81	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5QGIx	Cortex-M4	120	1024	320	UFBGA132	110	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5QGlxP	Cortex-M4	120	1024	320	UFBGA132	108	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5QEIx	Cortex-M4	120	512	320	UFBGA132	110	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5ZGTx	Cortex-M4	120	1024	320	LQFP144	115	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5ZGTxP	Cortex-M4	120	1024	320	LQFP144	113	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1 Y	ES 1	0	2	1	1	YES	YES	N/A	N/A	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	M-SPI	12C	U(S)ART	LPUART	CAN	SDI0	F(S)MC	USB FS HOST/OTG	GPU	TFT LCD	MIPI_DSI	SAI	DFSDM	DCMI	PSSI	TRNG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32L4P5ZETx	Cortex-M4	120	512	320	LQFP144	115	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5AGIx	Cortex-M4	120	1024	320	UFBGA169	136	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5AGIxP	Cortex-M4	120	1024	320	UFBGA169	134	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4P5AEIx	Cortex-M4	120	512	320	UFBGA169	136	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	N/A	N/A	YES	125
STM32L4Q5CGTx	Cortex-M4	120	1024	320	LQFP48	38	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1	YES	1	0	2	1	0	N/A	YES	YES	YES	YES	125
STM32L4Q5CGTxP	Cortex-M4	120	1024	320	LQFP48	38	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1	YES	1	0	2	1	0	N/A	YES	YES	YES	YES	125
STM32L4Q5CGUx	Cortex-M4	120	1024	320	UFQFPN48	38	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1	YES	1	0	2	1	0	N/A	YES	YES	YES	YES	125
STM32L4Q5CGUxP	Cortex-M4	120	1024	320	UFQFPN48	38	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1	YES	1	0	2	1	0	N/A	YES	YES	YES	YES	125
STM32L4Q5RGTx	Cortex-M4	120	1024	320	LQFP64	52	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5RGTxP	Cortex-M4	120	1024	320	LQFP64	52	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	0	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5VGTx	Cortex-M4	120	1024	320	LQFP100	83	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5VGTxP	Cortex-M4	120	1024	320	LQFP100	83	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5VGYx	Cortex-M4	120	1024	320	WLCSP100	81	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5VGYxP	Cortex-M4	120	1024	320	WLCSP100	81	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5QGlx	Cortex-M4	120	1024	320	UFBGA132	110	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5QGlxP	Cortex-M4	120	1024	320	UFBGA132	110	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5ZGTx	Cortex-M4	120	1024	320	LQFP144	115	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5ZGTxP	Cortex-M4	120	1024	320	LQFP144	115	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5AGIx	Cortex-M4	120	1024	320	UFBGA169	136	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4Q5AGIxP	Cortex-M4	120	1024	320	UFBGA169	136	1.71-3.6	9	2	2	2	2	16	2	2	2	3	2[0CT0]	4	5	1	1	2	1	1	YES	1	0	2	1	1	YES	YES	YES	YES	YES	125
STM32L4R5VGTx	Cortex-M4	120	1024	640	LQFP100	83	1.71-3.6	9	2	2	2	1	16	2	2	2	3	1[0CT0]	4	5	1	1	1	1	1	YES	0	0	2	1	1	N/A	YES	N/A	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	12C	U(S)ART	LPUART	CAN	SDI0		USB FS HOST/OTG	GPU	MILL DO	SAI	DFSDM	DCMI	PSSI	TRNG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32L4R5VITx	Cortex-M4	120	2048	640	LQFP100	83	1.71-3.6	9	2	2	2	1	16	2	2	2	3	1[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5QGlx	Cortex-M4	120	1024	640	UFBGA132	110	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5QIIx	Cortex-M4	120	2048	640	UFBGA132	110	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5ZGTx	Cortex-M4	120	1024	640	LQFP144	115	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5ZITx	Cortex-M4	120	2048	640	LQFP144	115	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5ZITxP	Cortex-M4	120	2048	640	LQFP144	113	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5ZGYx	Cortex-M4	120	1024	640	WLCSP144	115	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5ZIYx	Cortex-M4	120	2048	640	WLCSP144	115	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	. 1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5AGIx	Cortex-M4	120	1024	640	UFBGA169	140	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	. 1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R5Allx	Cortex-M4	120	2048	640	UFBGA169	140	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 Y	/ES) () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4S5VITx	Cortex-M4	120	2048	640	LQFP100	83	1.71-3.6	9	2	2	2	1	16	2	2	2	3	1[0CT0]	4	5	1	1	1	1	1 Y	/ES) () 2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S5QIIx	Cortex-M4	120	2048	640	UFBGA132	110	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S5ZITx	Cortex-M4	120	2048	640	LQFP144	115	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S5ZIYx	Cortex-M4	120	2048	640	WLCSP144	115	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S5Allx	Cortex-M4	120	2048	640	UFBGA169	140	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES) () 2	1	1	N/A	YES	N/A	YES	YES	125
											STM	32L	.4x7	US	В 0	TG -	+ TF	T Line																		
STM32L4R7VITx	Cortex-M4	120	2048	640	LQFP100	83	1.71-3.6	9	2	2	2	1	16	2	2	2	3	1[0CT0]	4	5	1	1	1	1	1 \	/ES	1 () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R7ZITx	Cortex-M4	120	2048	640	LQFP144	115	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES	1 () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R7Allx	Cortex-M4	120	2048	640	UFBGA169	140	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES	1 () 2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4S7VITx	Cortex-M4	120	2048	640	LQFP100	83	1.71-3.6	9	2	2	2	1	16	2	2	2	3	1[0CT0]	4	5	1	1	1	1	1 \	/ES	1 () 2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S7ZITx	Cortex-M4	120	2048	640	LQFP144	115	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1 \	/ES	1 () 2	1	1	N/A	YES	N/A	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB FS HOST/OTG	GPU	TET I CD	MIPI DSI	SAI	DFSDM	DCMI	PSSI	TRNG	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32L4S7Allx	Cortex-M4	120	2048	640	UFBGA169	140	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	0	2	1	1	N/A	YES	N/A	YES	YES	125
									S	ГМЗ	2L4	x9 L	JSB	OTG	+ T	FT -	+ N	IIPI-DSI L	ine																		
STM32L4R9VGTx	Cortex-M4	120	1024	640	LQFP100	77	1.71-3.6	9	2	2	2	1	14	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	0	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9VITx	Cortex-M4	120	2048	640	LQFP100	77	1.71-3.6	9	2	2	2	1	14	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	0	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9ZGTx	Cortex-M4	120	1024	640	LQFP144	110	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9ZITx	Cortex-M4	120	2048	640	LQFP144	110	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9ZGJx	Cortex-M4	120	1024	640	UFBGA144	112	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9ZIJx	Cortex-M4	120	2048	640	UFBGA144	112	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9ZGYx	Cortex-M4	120	1024	640	WLCSP144	110	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9ZIYx	Cortex-M4	120	2048	640	WLCSP144	110	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9ZIYxP	Cortex-M4	120	2048	640	WLCSP144	108	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9AGIx	Cortex-M4	120	1024	640	UFBGA169	131	1.71-3.6	9	2	2	2	1	14	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4R9Allx	Cortex-M4	120	2048	640	UFBGA169	131	1.71-3.6	9	2	2	2	1	14	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	N/A	N/A	125
STM32L4S9VITx	Cortex-M4	120	2048	640	LQFP100	77	1.71-3.6	9	2	2	2	1	14	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	0	1	2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S9ZITx	Cortex-M4	120	2048	640	LQFP144	112	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S9ZIJx	Cortex-M4	120	2048	640	UFBGA144	112	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S9ZIYx	Cortex-M4	120	2048	640	WLCSP144	112	1.71-3.6	9	2	2	2	1	16	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	YES	YES	125
STM32L4S9Allx	Cortex-M4	120	2048	640	UFBGA169	131	1.71-3.6	9	2	2	2	1	14	2	2	2	3	2[0CT0]	4	5	1	1	1	1	1	YES	1	1	2	1	1	N/A	YES	N/A	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
											;	STM	32L4	1x1	Acc	ess	Line																			
STM32L431KBUx	Cortex-M4	80	128	64	UFQFPN32	26	1.71-3.6	5	1	1	2	1	10	2	2	1	2	1[QUAD]	2	2	1	1	0	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431KCUx	Cortex-M4	80	256	64	UFQFPN32	26	1.71-3.6	5	1	1	2	1	10	2	2	1	2	1[QUAD]	2	2	1	1	0	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431CBTx	Cortex-M4	80	128	64	LQFP48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431CCTx	Cortex-M4	80	256	64	LQFP48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431CBUx	Cortex-M4	80	128	64	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431CCUx	Cortex-M4	80	256	64	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431CBYx	Cortex-M4	80	128	64	WLCSP49	39	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431CCYx	Cortex-M4	80	256	64	WLCSP49	39	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431RBTx	Cortex-M4	80	128	64	LQFP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431RCTx	Cortex-M4	80	256	64	LQFP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431RBlx	Cortex-M4	80	128	64	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431RClx	Cortex-M4	80	256	64	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431RBYx	Cortex-M4	80	128	64	WLCSP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431RCYx	Cortex-M4	80	256	64	WLCSP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431VCTx	Cortex-M4	80	256	64	LQFP100	83	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L431VClx	Cortex-M4	80	256	64	UFBGA100	83	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	0	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L451CCUx	Cortex-M4	80	256	160	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	1	2	1	3	1[QUAD]	4	4	1	1	0	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451CETx	Cortex-M4	80	512	160	LQFP48	38	1.71-3.6	5	1	1	2	1	10	1	2	1	3	1[QUAD]	4	4	1	1	0	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451CEUx	Cortex-M4	80	512	160	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	1	2	1	3	1[QUAD]	4	4	1	1	0	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451RCTx	Cortex-M4	80	256	160	LQFP64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L451RClx	Cortex-M4	80	256	160	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451REYx	Cortex-M4	80	512	160	WLCSP64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451REIx	Cortex-M4	80	512	160	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451RETx	Cortex-M4	80	512	160	LQFP64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451VCTx	Cortex-M4	80	256	160	LQFP100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451VClx	Cortex-M4	80	256	160	UFBGA100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451VETx	Cortex-M4	80	512	160	LQFP100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L451VElx	Cortex-M4	80	512	160	UFBGA100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	0	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L471RETx	Cortex-M4	80	512	128	LQFP64	51	1.71-3.6	9	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471RGTx	Cortex-M4	80	1024	128	LQFP64	51	1.71-3.6	9	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471VETx	Cortex-M4	80	512	128	LQFP100	82	1.71-3.6	9	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471VGTx	Cortex-M4	80	1024	128	LQFP100	82	1.71-3.6	9	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471QEIx	Cortex-M4	80	512	128	UFBGA132	109	1.71-3.6	9	2	2	2	3	19	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471QGIx	Cortex-M4	80	1024	128	UFBGA132	109	1.71-3.6	9	2	2	2	3	19	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471ZETx	Cortex-M4	80	512	128	LQFP144	114	1.71-3.6	9	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471ZEJx	Cortex-M4	80	512	128	UFBGA144	114	1.71-3.6	9	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471ZGTx	Cortex-M4	80	1024	128	LQFP144	114	1.71-3.6	9	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L471ZGJx	Cortex-M4	80	1024	128	UFBGA144	114	1.71-3.6	9	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	0	0	N/A	2	1	0	1	YES	N/A	N/A	125
											ST	M32	2L4x	2 US	B D	evic	e Li	ne																		
STM32L412K8Tx	Cortex-M4	80	64	40	LQFP32	26	1.71-3.6	4	1	1	2	2	10	0	1	1	1	0	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	M-SPI	12C	U(S)ART	LPUART	CAN	SDI0	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L412K8Ux	Cortex-M4	80	64	40	UFQFPN32	26	1.71-3.6	4	1	1	2	2	10	0	1	1	1	0	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412KBTx	Cortex-M4	80	128	40	LQFP32	26	1.71-3.6	4	1	1	2	2	10	0	1	1	1	1[QUAD]	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412KBUx	Cortex-M4	80	128	40	UFQFPN32	26	1.71-3.6	4	1	1	2	2	10	0	1	1	1	1[QUAD]	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412T8Yx	Cortex-M4	80	64	40	WLCSP36	30	1.71-3.6	4	1	1	2	2	10	0	1	1	1	1[QUAD]	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412TBYx	Cortex-M4	80	128	40	WLCSP36	30	1.71-3.6	4	1	1	2	2	10	0	1	1	1	1[QUAD]	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412TBYxP	Cortex-M4	80	128	40	WLCSP36	28	1.71-3.6	4	1	1	2	2	10	0	1	1	1	1[QUAD]	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412C8Tx	Cortex-M4	80	64	40	LQFP48	38	1.71-3.6	4	1	1	2	2	10	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412C8Ux	Cortex-M4	80	64	40	UFQFPN48	38	1.71-3.6	4	1	1	2	2	10	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412CBTx	Cortex-M4	80	128	40	LQFP48	38	1.71-3.6	4	1	1	2	2	10	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412CBTxP	Cortex-M4	80	128	40	LQFP48	36	1.71-3.6	4	1	1	2	2	10	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412CBUx	Cortex-M4	80	128	40	UFQFPN48	38	1.71-3.6	4	1	1	2	2	10	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412CBUxP	Cortex-M4	80	128	40	UFQFPN48	36	1.71-3.6	4	1	1	2	2	10	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412R8Tx	Cortex-M4	80	64	40	LQFP64	52	1.71-3.6	4	1	1	2	2	16	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412R8lx	Cortex-M4	80	64	40	UFBGA64	52	1.71-3.6	4	1	1	2	2	16	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412RBTx	Cortex-M4	80	128	40	LQFP64	52	1.71-3.6	4	1	1	2	2	16	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412RBTxP	Cortex-M4	80	128	40	LQFP64	50	1.71-3.6	4	1	1	2	2	16	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412RBlx	Cortex-M4	80	128	40	UFBGA64	52	1.71-3.6	4	1	1	2	2	16	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L412RBlxP	Cortex-M4	80	128	40	UFBGA64	50	1.71-3.6	4	1	1	2	2	16	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	N/A	N/A	125
STM32L422KBTx	Cortex-M4	80	128	40	LQFP32	26	1.71-3.6	4	1	1	2	2	10	0	1	1	1	1[QUAD]	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	YES	N/A	125
STM32L422KBUx	Cortex-M4	80	128	40	UFQFPN32	26	1.71-3.6	4	1	1	2	2	10	0	1	1	1	1[QUAD]	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	YES	N/A	125
STM32L422TBYx	Cortex-M4	80	128	40	WLCSP36	30	1.71-3.6	4	1	1	2	2	10	0	1	1	1	1[QUAD]	2	2	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDI0	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L422CBTx	Cortex-M4	80	128	40	LQFP48	26	1.71-3.6	4	1	1	2	2	10	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	YES	N/A	125
STM32L422CBUx	Cortex-M4	80	128	40	UFQFPN48	26	1.71-3.6	4	1	1	2	2	10	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	YES	N/A	125
STM32L422RBTx	Cortex-M4	80	128	40	LQFP64	26	1.71-3.6	4	1	1	2	2	16	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	YES	N/A	125
STM32L422RBIx	Cortex-M4	80	128	40	UFBGA64	26	1.71-3.6	4	1	1	2	2	16	0	1	1	2	1[QUAD]	3	3	1	0	0	0	1	0	0	N/A	0	0	0	0	YES	YES	N/A	125
STM32L432KBUx	Cortex-M4	80	128	64	UFQFPN32	26	1.71-3.6	5	1	1	2	1	10	2	2	1	2	1[QUAD]	2	2	1	1	0	0	1	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L432KCUx	Cortex-M4	80	256	64	UFQFPN32	26	1.71-3.6	5	1	1	2	1	10	2	2	1	2	1[QUAD]	2	2	1	1	0	0	1	0	0	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L442KCUx	Cortex-M4	80	256	64	UFQFPN32	26	1.71-3.6	5	1	1	2	1	10	2	2	1	2	1[QUAD]	2	2	1	1	0	0	1	0	0	N/A	1	0	0	1	YES	YES	N/A	125
STM32L452CCUx	Cortex-M4	80	256	160	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	1	2	1	3	1[QUAD]	4	4	1	1	0	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452CETx	Cortex-M4	80	512	160	LQFP48	38	1.71-3.6	5	1	1	2	1	10	1	2	1	3	1[QUAD]	4	4	1	1	0	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452CEUx	Cortex-M4	80	512	160	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	1	2	1	3	1[QUAD]	4	4	1	1	0	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452RCTx	Cortex-M4	80	256	160	LQFP64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452RCIx	Cortex-M4	80	256	160	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452RETx	Cortex-M4	80	512	160	LQFP64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452RETxP	Cortex-M4	80	512	160	LQFP64	50	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452REIx	Cortex-M4	80	512	160	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452REYx	Cortex-M4	80	512	160	WLCSP64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452VCTx	Cortex-M4	80	256	160	LQFP100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452VClx	Cortex-M4	80	256	160	UFBGA100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452VETx	Cortex-M4	80	512	160	LQFP100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L452VEIx	Cortex-M4	80	512	160	UFBGA100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	N/A	N/A	125
STM32L462CETx	Cortex-M4	80	512	160	LQFP48	38	1.71-3.6	5	1	1	2	1	10	1	2	1	3	1[QUAD]	4	4	1	1	0	0	1	0	0	N/A	1	1	0	0	YES	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDI0	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L462CEUx	Cortex-M4	80	512	160	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	1	2	1	3	1[QUAD]	4	4	1	1	0	0	1	0	0	N/A	1	1	0	0	YES	YES	N/A	125
STM32L462RETx	Cortex-M4	80	512	160	LQFP64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	YES	N/A	125
STM32L462REIx	Cortex-M4	80	512	160	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	YES	N/A	125
STM32L462REYx	Cortex-M4	80	512	160	WLCSP64	52	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	YES	N/A	125
STM32L462VETx	Cortex-M4	80	512	160	LQFP100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	YES	N/A	125
STM32L462VEIx	Cortex-M4	80	512	160	UFBGA100	83	1.71-3.6	5	1	1	2	1	16	1	2	1	3	1[QUAD]	4	4	1	1	1	0	1	0	0	N/A	1	1	0	0	YES	YES	N/A	125
					l				ST	M32	2L4x	3 US	B D	evic	e +	Seg	men	t LCD Lin	e																	
STM32L433CBTx	Cortex-M4	80	128	64	LQFP48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433CBUx	Cortex-M4	80	128	64	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19	N/A	1	0	0	1			N/A	
STM32L433CBYx	Cortex-M4	80	128	64	WLCSP49	39	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19	N/A	1	0	0	1			N/A	
STM32L433CCTx	Cortex-M4	80	256	64	LQFP48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19		1	0	0	1			N/A	
STM32L433CCUx	Cortex-M4	80	256	64	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19		1	0	0	1			N/A	
STM32L433CCYx	Cortex-M4	80	256	64	WLCSP49	39	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433RBTx	Cortex-M4	80	128	64	LQFP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433RBIx	Cortex-M4	80	128	64	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433RBYx	Cortex-M4	80	128	64	WLCSP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433RCTx	Cortex-M4	80	256	64	LQFP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433RCTxP	Cortex-M4	80	256	64	LQFP64	50	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L433RClx	Cortex-M4	80	256	64	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433RCYx	Cortex-M4	80	256	64	WLCSP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433VCTx	Cortex-M4	80	256	64	LQFP100	83	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x44/ 8x40	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L433VClx	Cortex-M4	80	256	64	UFBGA100	83	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x44/ 8x40	N/A	1	0	0	1	YES	N/A	N/A	125
STM32L443CCTx	Cortex-M4	80	256	64	LQFP48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19	N/A	1	0	0	1	YES	YES	N/A	125
STM32L443CCUx	Cortex-M4	80	256	64	UFQFPN48	38	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19	N/A	1	0	0	1	YES	YES	N/A	125
STM32L443CCYx	Cortex-M4	80	256	64	WLCSP49	39	1.71-3.6	5	1	1	2	1	10	2	2	1	3	1[QUAD]	3	3	1	1	0	0	1	0	4x19	N/A	1	0	0	1	YES	YES	N/A	125
STM32L443RCTx	Cortex-M4	80	256	64	LQFP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	YES	N/A	125
STM32L443RClx	Cortex-M4	80	256	64	UFBGA64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	YES	N/A	125
STM32L443RCYx	Cortex-M4	80	256	64	WLCSP64	52	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x32/ 8x28	N/A	1	0	0	1	YES	YES	N/A	125
STM32L443VCTx	Cortex-M4	80	256	64	LQFP100	83	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x44/ 8x40	N/A	1	0	0	1	YES	YES	N/A	125
STM32L443VClx	Cortex-M4	80	256	64	UFBGA100	83	1.71-3.6	5	1	1	2	1	16	2	2	1	3	1[QUAD]	3	3	1	1	1	0	1	0	4x44/ 8x40	N/A	1	0	0	1	YES	YES	N/A	125
											S	TMS	32L4	x5 U	ISB	OTG	Line	e																		
STM32L475RCTx	Cortex-M4	80	256	128	LQFP64	51	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L475RETx	Cortex-M4	80	512	128	LQFP64	51	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L475RGTx	Cortex-M4	80	1024	128	LQFP64	51	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L475VCTx	Cortex-M4	80	256	128	LQFP100	82	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	0	N/A	2	1	0	1	YES	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	12C	U(S)ART	LPUART	CAN	SDI0	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L475VETx	Cortex-M4	80	512	128	LQFP100	82	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	0	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L475VGTx	Cortex-M4	80	1024	128	LQFP100	82	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	0	N/A	2	1	0	1	YES	N/A	N/A	125
									S	ТМЗ	2L4)	ر6 U	SB C	TG .	+ Se	egm	ent	LCD Line	;																	
STM32L476RCTx	Cortex-M4	80	256	128	LQFP64	51	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x28/ 4x32	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476RETx	Cortex-M4	80	512	128	LQFP64	51	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x28/ 4x32	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476RGTx	Cortex-M4	80	1024	128	LQFP64	51	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x28/ 4x32	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476JEYx	Cortex-M4	80	512	128	WLCSP72	57	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x28/ 4x32	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476JGYx	Cortex-M4	80	1024	128	WLCSP72	57	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x28/ 4x32	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476JGYxP	Cortex-M4	80	1024	128	WLCSP72	55	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x28/ 4x32	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476MEYx	Cortex-M4	80	512	128	WLCSP81	65	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x30/ 4x32	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476MGYx	Cortex-M4	80	1024	128	WLCSP81	65	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x30/ 4x32	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476VCTx	Cortex-M4	80	256	128	LQFP100	82	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476VETx	Cortex-M4	80	512	128	LQFP100	82	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476VGTx	Cortex-M4	80	1024	128	LQFP100	82	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476QEIx	Cortex-M4	80	512	128	UFBGA132	109	1.71-3.6	11	2	2	2	3	19	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L476QGIx	Cortex-M4	80	1024	128	UFBGA132	109	1.71-3.6	11	2	2	2	3	19	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476ZETx	Cortex-M4	80	512	128	LQFP144	114	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476ZGTx	Cortex-M4	80	1024	128	LQFP144	114	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476ZGTxP	Cortex-M4	80	1024	128	LQFP144	112	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L476ZGJx	Cortex-M4	80	1024	128	UFBGA144	114	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	N/A	N/A	125
STM32L486RGTx	Cortex-M4	80	1024	128	LQFP64	51	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x28/ 4x32	N/A	2	1	0	1	YES	YES	N/A	125
STM32L486JGYx	Cortex-M4	80	1024	128	WLCSP72	57	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	0	0	1	8x28/ 4x32	N/A	2	1	0	1	YES	YES	N/A	125
STM32L486VGTx	Cortex-M4	80	1024	128	LQFP100	82	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	YES	N/A	125
STM32L486QGlx	Cortex-M4	80	1024	128	UFBGA132	109	1.71-3.6	11	2	2	2	3	19	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	YES	N/A	125
STM32L486ZGTx	Cortex-M4	80	1024	128	LQFP144	114	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	3	5	1	1	1	1	0	1	8x40/ 4x44	N/A	2	1	0	1	YES	YES	N/A	125
STM32L496RETx	Cortex-M4	80	512	320	LQFP64	52	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	0	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496RGTx	Cortex-M4	80	1024	320	LQFP64	52	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	0	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496RGTxP	Cortex-M4	80	1024	320	LQFP64	50	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	0	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496VETx	Cortex-M4	80	512	320	LQFP100	83	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDI0	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L496VGTx	Cortex-M4	80	1024	320	LQFP100	83	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496VGTxP	Cortex-M4	80	1024	320	LQFP100	81	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496VGYx	Cortex-M4	80	1024	320	WLCSP100	83	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496VGYxP	Cortex-M4	80	1024	320	WLCSP100	81	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496WEYx	Cortex-M4	80	512	320	WLCSP115	86	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496WGYxP	Cortex-M4	80	1024	320	WLCSP115	84	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496QElx	Cortex-M4	80	512	320	UFBGA132	110	1.71-3.6	11	2	2	2	3	19	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496QGIx	Cortex-M4	80	1024	320	UFBGA132	110	1.71-3.6	11	2	2	2	3	19	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496QGIxP	Cortex-M4	80	1024	320	UFBGA132	108	1.71-3.6	11	2	2	2	3	19	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496ZETx	Cortex-M4	80	512	320	LQFP144	115	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496ZGTx	Cortex-M4	80	1024	320	LQFP144	115	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496ZGTxP	Cortex-M4	80	1024	320	LQFP144	113	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496AEIx	Cortex-M4	80	512	320	UFBGA169	136	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L496AGIx	Cortex-M4	80	1024	320	UFBGA169	136	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	M-SPI	120	U(S)ART	LPUART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Segment LCD	GPU	SAI	DFSDM	DCMI	SWPMI	TRNG	AES/DES	SHA/HMAC	T° Max (°C)
STM32L496AGIxP	Cortex-M4	80	1024	320	UFBGA169	134	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	N/A	N/A	125
STM32L4A6RGTx	Cortex-M4	80	1024	320	LQFP64	52	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	0	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6RGTxP	Cortex-M4	80	1024	320	LQFP64	50	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	0	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6VGTx	Cortex-M4	80	1024	320	LQFP100	83	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6VGTxP	Cortex-M4	80	1024	320	LQFP100	81	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6VGYx	Cortex-M4	80	1024	320	WLCSP100	83	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6VGYxP	Cortex-M4	80	1024	320	WLCSP100	81	1.71-3.6	11	2	2	2	3	16	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6QGIx	Cortex-M4	80	1024	320	UFBGA132	110	1.71-3.6	11	2	2	2	3	19	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6QGIxP	Cortex-M4	80	1024	320	UFBGA132	108	1.71-3.6	11	2	2	2	3	19	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6ZGTx	Cortex-M4	80	1024	320	LQFP144	115	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6ZGTxP	Cortex-M4	80	1024	320	LQFP144	113	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6AGIx	Cortex-M4	80	1024	320	UFBGA169	136	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125
STM32L4A6AGIxP	Cortex-M4	80	1024	320	UFBGA169	136	1.71-3.6	11	2	2	2	3	24	2	2	2	3	1[QUAD]	4	5	1	2	1	1	0	1	8x40/ 4x44	YES	2	1	1	1	YES	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	128	120	U(S)ART	SDI0	F(S)MC	USB Device	Segment LCD	AES/DES	T° Max (°C)
									S	TM32L	.10x V	alue L	ine												
STM32L100C6U6A	Cortex-M3	32	32	4	2	UFQFPN48	37	1.8-3.6	8	0	1	14	2	2	0	2	0	0	0	0	0	0	4x16	N/A	85
STM32L100R8T6A	Cortex-M3	32	64	8	2	LQFP64	51	1.8-3.6	8	0	1	20	2	2	0	2	0	2	3	0	0	1	4x32/8x28	N/A	85
STM32L100RBT6A	Cortex-M3	32	128	16	2	LQFP64	51	1.8-3.6	8	0	1	20	2	2	0	2	0	2	3	0	0	1	4x32/8x28	N/A	85
STM32L100RCT6	Cortex-M3	32	256	16	4	LQFP64	51	1.8-3.6	8	0	1	20	2	2	0	3	2	2	3	0	0	1	4x32/8x28	N/A	85
									٤	STM32	L15x I	LCD Li	1e												
STM32L151C6T6A	Cortex-M3	32	32	16	4	LQFP48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151C6U6A	Cortex-M3	32	32	16	4	UFQFPN48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151C8T6A	Cortex-M3	32	64	32	4	LQFP48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151C8U6A	Cortex-M3	32	64	32	4	UFQFPN48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151CBT6A	Cortex-M3	32	128	32	4	LQFP48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151CBU6A	Cortex-M3	32	128	32	4	UFQFPN48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151R6T6A	Cortex-M3	32	32	16	4	LQFP64	51	1.65-3.6	8	0	1	20	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151R6H6A	Cortex-M3	32	32	16	4	TFBGA64	50	1.65-3.6	8	0	1	19	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151R8T6A	Cortex-M3	32	64	32	4	LQFP64	51	1.65-3.6	8	0	1	20	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151R8H6A	Cortex-M3	32	64	32	4	TFBGA64	50	1.65-3.6	8	0	1	19	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151RBTxA	Cortex-M3	32	128	32	4	LQFP64	51	1.65-3.6	8	0	1	20	2	2	0	2	0	2	3	0	0	1	0	N/A	105
STM32L151RBHxA	Cortex-M3	32	128	32	4	TFBGA64	50	1.65-3.6	8	0	1	19	2	2	0	2	0	2	3	0	0	1	0	N/A	105
STM32L151V8T6A	Cortex-M3	32	64	32	4	LQFP100	83	1.65-3.6	8	0	1	24	2	2	0	2	0	2	3	0	0	1	0	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	12S	120	U(S)ART	SDIO	F(S)MC	USB Device	Segment LCD	AES/DES	T° Max (°C)
STM32L151V8H6A	Cortex-M3	32	64	32	4	UFBGA100	83	1.65-3.6	8	0	1	24	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151VBT6A	Cortex-M3	32	128	32	4	LQFP100	83	1.65-3.6	8	0	1	24	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151VBH6A	Cortex-M3	32	128	32	4	UFBGA100	83	1.65-3.6	8	0	1	24	2	2	0	2	0	2	3	0	0	1	0	N/A	85
STM32L151CCTx	Cortex-M3	32	256	32	8	LQFP48	37	1.65-3.6	8	1	1	14	2	2	2	3	2	2	3	0	0	1	0	N/A	105
STM32L151CCUx	Cortex-M3	32	256	32	8	UFQFPN48	37	1.65-3.6	8	1	1	14	2	2	2	3	2	2	3	0	0	1	0	N/A	105
STM32L151UCYx	Cortex-M3	32	256	32	8	WLCSP63	51	1.65-3.6	8	1	1	21	2	2	2	3	2	2	3	0	0	1	0	N/A	105
STM32L151RCT6	Cortex-M3	32	256	32	8	LQFP64	51	1.65-3.6	8	1	1	21	2	2	2	3	2	2	3	0	0	1	0	N/A	85
STM32L151VCT6	Cortex-M3	32	256	32	8	LQFP100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	3	0	0	1	0	N/A	85
STM32L151VCH6	Cortex-M3	32	256	32	8	UFBGA100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	3	0	0	1	0	N/A	85
STM32L151QCH6	Cortex-M3	32	256	32	8	UFBGA132	109	1.65-3.6	8	1	1	40	2	2	2	3	2	2	3	0	0	1	0	N/A	85
STM32L151ZCT6	Cortex-M3	32	256	32	8	LQFP144	115	1.65-3.6	8	1	1	40	2	2	2	3	2	2	3	0	0	1	0	N/A	85
STM32L151RDTx	Cortex-M3	32	384	48	12	LQFP64	51	1.65-3.6	8	1	1	21	2	2	3	3	2	2	5	1	0	1	0	N/A	105
STM32L151RDYx	Cortex-M3	32	384	48	12	WLCSP64	51	1.65-3.6	8	1	1	21	2	2	3	3	2	2	5	1	0	1	0	N/A	105
STM32L151VDT6	Cortex-M3	32	384	48	12	LQFP100	83	1.65-3.6	8	1	1	25	2	2	3	3	2	2	5	1	1	1	0	N/A	85
STM32L151QDH6	Cortex-M3	32	384	48	12	UFBGA132	109	1.65-3.6	8	1	1	40	2	2	3	3	2	2	5	1	1	1	0	N/A	85
STM32L151ZDT6	Cortex-M3	32	384	48	12	LQFP144	115	1.65-3.6	8	1	1	40	2	2	3	3	2	2	5	1	1	1	0	N/A	85
STM32L151VDTxX	Cortex-M3	32	384	80	16	LQFP100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	1	1	1	0	N/A	105
STM32L151VDYxX	Cortex-M3	32	384	80	16	WLCSP104	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	1	1	1	0	N/A	105
STM32L151RET6	Cortex-M3	32	512	80	16	LQFP64	51	1.65-3.6	8	1	1	21	2	2	2	3	2	2	5	0	0	1	0	N/A	85
STM32L151VETx	Cortex-M3	32	512	80	16	LQFP100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	0	0	1	0	N/A	105

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	I2S	120	U(S)ART	SDIO	F(S)MC	USB Device	Segment LCD	AES/DES	T° Max (°C)
STM32L151VEYx	Cortex-M3	32	512	80	16	WLCSP104	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	0	0	1	0	N/A	105
STM32L151QEH6	Cortex-M3	32	512	80	16	UFBGA132	109	1.65-3.6	8	1	1	40	2	2	2	3	2	2	5	0	0	1	0	N/A	85
STM32L151ZET6	Cortex-M3	32	512	80	16	LQFP144	115	1.65-3.6	8	1	1	40	2	2	2	3	2	2	5	0	0	1	0	N/A	85
STM32L152C6T6A	Cortex-M3	32	32	16	4	LQFP48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	4x16	N/A	85
STM32L152C6U6A	Cortex-M3	32	32	16	4	UFQFPN48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	4x16	N/A	85
STM32L152C8T6A	Cortex-M3	32	64	32	4	LQFP48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	4x16	N/A	85
STM32L152C8U6A	Cortex-M3	32	64	32	4	UFQFPN48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	4x16	N/A	85
STM32L152CBT6A	Cortex-M3	32	128	32	4	LQFP48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	4x16	N/A	85
STM32L152CBU6A	Cortex-M3	32	128	32	4	UFQFPN48	37	1.65-3.6	8	0	1	14	2	2	0	2	0	2	3	0	0	1	4x16	N/A	85
STM32L152R6T6A	Cortex-M3	32	32	16	4	LQFP64	51	1.65-3.6	8	0	1	20	2	2	0	2	0	2	3	0	0	1	4x32/8x28	N/A	85
STM32L152R6H6A	Cortex-M3	32	32	16	4	TFBGA64	50	1.65-3.6	8	0	1	19	2	2	0	2	0	2	3	0	0	1	4x31/8x27	N/A	85
STM32L152R8T6A	Cortex-M3	32	64	32	4	LQFP64	51	1.65-3.6	8	0	1	20	2	2	0	2	0	2	3	0	0	1	4x32/8x28	N/A	85
STM32L152R8H6A	Cortex-M3	32	64	32	4	TFBGA64	50	1.65-3.6	8	0	1	19	2	2	0	2	0	2	3	0	0	1	4x31/8x27	N/A	85
STM32L152RBT6A	Cortex-M3	32	128	32	4	LQFP64	51	1.65-3.6	8	0	1	20	2	2	0	2	0	2	3	0	0	1	4x32/8x28	N/A	85
STM32L152RBH6A	Cortex-M3	32	128	32	4	TFBGA64	50	1.65-3.6	8	0	1	19	2	2	0	2	0	2	3	0	0	1	4x31/8x27	N/A	85
STM32L152V8T6A	Cortex-M3	32	64	32	4	LQFP100	83	1.65-3.6	8	0	1	24	2	2	0	2	0	2	3	0	0	1	4x44/8x40	N/A	85
STM32L152V8H6A	Cortex-M3	32	64	32	4	UFBGA100	83	1.65-3.6	8	0	1	24	2	2	0	2	0	2	3	0	0	1	4x44/8x40	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	I2S	120	U(S)ART	SDIO	F(S)MC	USB Device	Segment LCD	AES/DES	T° Max (°C)
STM32L152VBT6A	Cortex-M3	32	128	32	4	LQFP100	83	1.65-3.6	8	0	1	24	2	2	0	2	0	2	3	0	0	1	4x44/8x40	N/A	85
STM32L152VBH6A	Cortex-M3	32	128	32	4	UFBGA100	83	1.65-3.6	8	0	1	24	2	2	0	2	0	2	3	0	0	1	4x44/8x40	N/A	85
STM32L152CCT6	Cortex-M3	32	256	32	8	LQFP48	37	1.65-3.6	8	1	1	14	2	2	2	3	2	2	3	0	0	1	4x18	N/A	85
STM32L152CCU6	Cortex-M3	32	256	32	8	UFQFPN48	37	1.65-3.6	8	1	1	14	2	2	2	3	2	2	3	0	0	1	4x18	N/A	85
STM32L152UCY6	Cortex-M3	32	256	32	8	WLCSP63	51	1.65-3.6	8	1	1	21	2	2	2	3	2	2	3	0	0	1	4x32/8x28	N/A	85
STM32L152RCT6	Cortex-M3	32	256	32	8	LQFP64	51	1.65-3.6	8	1	1	21	2	2	2	3	2	2	3	0	0	1	4x32/8x28	N/A	85
STM32L152VCT6	Cortex-M3	32	256	32	8	LQFP100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	3	0	0	1	4x44/8x40	N/A	85
STM32L152VCH6	Cortex-M3	32	256	32	8	UFBGA100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	3	0	0	1	4x44/8x40	N/A	85
STM32L152ZCT6	Cortex-M3	32	256	32	8	LQFP144	115	1.65-3.6	8	1	1	40	2	2	2	3	2	2	3	0	0	1	4x44/8x40	N/A	85
STM32L152QCH6	Cortex-M3	32	256	32	8	UFBGA132	109	1.65-3.6	8	1	1	40	2	2	2	3	2	2	3	0	0	1	4x44/8x40	N/A	85
STM32L152RDT6	Cortex-M3	32	384	48	12	LQFP64	51	1.65-3.6	8	1	1	21	2	2	3	3	2	2	5	1	0	1	4x32/8x28	N/A	85
STM32L152RDY6	Cortex-M3	32	384	48	12	WLCSP64	51	1.65-3.6	8	1	1	21	2	2	3	3	2	2	5	1	0	1	4x32/8x28	N/A	85
STM32L152VDT6	Cortex-M3	32	384	48	12	LQFP100	83	1.65-3.6	8	1	1	25	2	2	3	3	2	2	5	1	1	1	4x44/8x40	N/A	85
STM32L152QDH6	Cortex-M3	32	384	48	12	UFBGA132	109	1.65-3.6	8	1	1	40	2	2	3	3	2	2	5	1	1	1	4x44/8x40	N/A	85
STM32L152ZDT6	Cortex-M3	32	384	48	12	LQFP144	115	1.65-3.6	8	1	1	40	2	2	3	3	2	2	5	1	1	1	4x44/8x40	N/A	85
STM32L152VDT6X	Cortex-M3	32	384	80	16	LQFP100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	1	1	1	4x44/8x40	N/A	85
STM32L152VDY6X	Cortex-M3	32	384	80	16	WLCSP104	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	1	1	1	4x44/8x40	N/A	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	OPAMP	SPI	128	120	U(S)ART	SDIO	F(S)MC	USB Device	Segment LCD	AES/DES	T° Max (°C)
STM32L152RET6	Cortex-M3	32	512	80	16	LQFP64	51	1.65-3.6	8	1	1	21	2	2	2	3	2	2	5	0	0	1	4x32/8x28	N/A	85
STM32L152VET6	Cortex-M3	32	512	80	16	LQFP100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	0	0	1	4x44/8x40	N/A	85
STM32L152VEY6	Cortex-M3	32	512	80	16	WLCSP104	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	0	0	1	4x44/8x40	N/A	85
STM32L152QEH6	Cortex-M3	32	512	80	16	UFBGA132	109	1.65-3.6	8	1	1	40	2	2	2	3	2	2	5	0	0	1	4x44/8x40	N/A	85
STM32L152ZET6	Cortex-M3	32	512	80	16	LQFP144	115	1.65-3.6	8	1	1	40	2	2	2	3	2	2	5	0	0	1	4x44/8x40	N/A	85
									STN	32L16	x LCD	+ AES	Line												
STM32L162RCT6	Cortex-M3	32	256	32	8	LQFP64	51	1.65-3.6	8	1	1	21	2	2	2	3	2	2	3	0	0	1	4x32/8x28	YES	85
STM32L162VCT6	Cortex-M3	32	256	32	8	LQFP100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	3	0	0	1	4x44/8x40	YES	85
STM32L162VCH6	Cortex-M3	32	256	32	8	UFBGA100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	3	0	0	1	4x44/8x40	YES	85
STM32L162QCH6	Cortex-M3	32	256	32	8	UFBGA132	109	1.65-3.6	8	1	1	40	2	2	2	3	2	2	3	0	0	1	4x44/8x40	YES	85
STM32L162ZCT6	Cortex-M3	32	256	32	8	LQFP144	115	1.65-3.6	8	1	1	40	2	2	2	3	2	2	3	0	0	1	4x44/8x40	YES	85
STM32L162RDT6	Cortex-M3	32	384	48	12	LQFP64	51	1.65-3.6	8	1	1	21	2	2	3	3	2	2	5	1	0	1	4x32/8x28	YES	85
STM32L162RDY6	Cortex-M3	32	384	48	12	WLCSP64	51	1.65-3.6	8	1	1	21	2	2	3	3	2	2	5	1	0	1	4x32/8x28	YES	85
STM32L162VDT6	Cortex-M3	32	384	48	12	LQFP100	83	1.65-3.6	8	1	1	25	2	2	3	3	2	2	5	1	1	1	4x44/8x40	YES	85
STM32L162QDH6	Cortex-M3	32	384	48	12	UFBGA132	109	1.65-3.6	8	1	1	40	2	2	3	3	2	2	5	1	1	1	4x44/8x40	YES	85
STM32L162ZDT6	Cortex-M3	32	384	48	12	LQFP144	115	1.65-3.6	8	1	1	40	2	2	3	3	2	2	5	1	1	1	4x44/8x40	YES	85
STM32L162VDY6X	Cortex-M3	32	384	80	16	WLCSP104	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	0	0	1	4x44/8x40	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	ОРАМР	SPI	12S	120	U(S)ART	SDI0	F(S)MC	USB Device	Segment LCD	AES/DES	T° Max (°C)
STM32L162RET6	Cortex-M3	32	512	80	16	LQFP64	51	1.65-3.6	8	1	1	21	2	2	2	3	2	2	5	0	0	1	4x32/8x28	YES	85
STM32L162VET6	Cortex-M3	32	512	80	16	LQFP100	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	0	0	1	4x44/8x40	YES	85
STM32L162VEY6	Cortex-M3	32	512	80	16	WLCSP104	83	1.65-3.6	8	1	1	25	2	2	2	3	2	2	5	0	0	1	4x44/8x40	YES	85
STM32L162ZET6	Cortex-M3	32	512	80	16	LQFP144	115	1.65-3.6	8	1	1	40	2	2	2	3	2	2	5	0	0	1	4x44/8x40	YES	85

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	128	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
								Sī	「M32L	OxO Va	alue Li	ne												
STM32L010F4P6	Cortex-M0+	32	16	2	128B	TSS0P20	16	1.8-3.6	2	1	1	7	0	0	1	0	1	1	1	0	0	N/A	N/A	85
STM32L010K4T6	Cortex-M0+	32	16	2	128B	LQFP32	26	1.8-3.6	2	1	1	10	0	0	1	0	1	1	1	0	0	N/A	N/A	85
STM32L010K8T6	Cortex-M0+	32	64	8	128B	LQFP32	25	1.8-3.6	2	1	1	10	0	0	1	0	1	1	0	0	0	N/A	N/A	85
STM32L010C6T6	Cortex-M0+	32	32	8	128B	LQFP48	38	1.8-3.6	2	1	1	10	0	0	1	0	1	1	1	0	0	N/A	N/A	85
STM32L010R8T6	Cortex-M0+	32	64	8	128B	LQFP64	51	1.8-3.6	2	1	1	16	0	0	1	1	1	1	1	0	0	N/A	N/A	85
STM32L010RBT6	Cortex-M0+	32	128	20	128B	LQFP64	51	1.8-3.6	3	1	1	16	0	0	1	1	1	1	1	0	0	N/A	N/A	85
								ST	M32L0)x1 Ac	cess L	ine												
STM32L011D3Px	Cortex-M0+	32	8	2	512B	TSS0P14	11	1.65-3.6	2	1	1	4	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011D4Px	Cortex-M0+	32	16	2	512B	TSS0P14	11	1.65-3.6	2	1	1	4	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011F3Px	Cortex-M0+	32	8	2	512B	TSS0P20	16	1.65-3.6	2	1	1	9	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011F3Ux	Cortex-M0+	32	8	2	512B	UFQFPN20	16	1.65-3.6	2	1	1	7	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011F4Px	Cortex-M0+	32	16	2	512B	TSS0P20	16	1.65-3.6	2	1	1	9	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011F4Ux	Cortex-M0+	32	16	2	512B	UFQFPN20	16	1.65-3.6	2	1	1	7	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011E3Yx	Cortex-M0+	32	8	2	512B	WLCSP25	21	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011E4Yx	Cortex-M0+	32	16	2	512B	WLCSP25	21	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011G3Ux	Cortex-M0+	32	8	2	512B	UFQFPN28	24	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011G4Ux	Cortex-M0+	32	16	2	512B	UFQFPN28	24	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011K3Tx	Cortex-M0+	32	8	2	512B	LQFP32	26	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011K3Ux	Cortex-M0+	32	8	2	512B	UFQFPN32	28	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	128	12C	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
STM32L011K4Tx	Cortex-M0+	32	16	2	512B	LQFP32	26	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L011K4Ux	Cortex-M0+	32	16	2	512B	UFQFPN32	28	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L021D4Px	Cortex-M0+	32	16	2	512B	TSS0P14	11	1.65-3.6	2	1	1	4	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L021F4Px	Cortex-M0+	32	16	2	512B	TSS0P20	16	1.65-3.6	2	1	1	9	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L021F4Ux	Cortex-M0+	32	16	2	512B	UFQFPN20	16	1.65-3.6	2	1	1	7	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L021G4Ux	Cortex-M0+	32	16	2	512B	UFQFPN28	24	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L021K4Tx	Cortex-M0+	32	16	2	512B	LQFP32	26	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L021K4Ux	Cortex-M0+	32	16	2	512B	UFQFPN32	28	1.65-3.6	2	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L031F4Px	Cortex-M0+	32	16	8	1	TSS0P20	15	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031F6Px	Cortex-M0+	32	32	8	1	TSS0P20	15	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031E4Yx	Cortex-M0+	32	16	8	1	WLCSP25	20	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031E6Yx	Cortex-M0+	32	32	8	1	WLCSP25	20	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031G4Ux	Cortex-M0+	32	16	8	1	UFQFPN28	21	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031G6Ux	Cortex-M0+	32	32	8	1	UFQFPN28	21	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031K4Tx	Cortex-M0+	32	16	8	1	LQFP32	25	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031K4Ux	Cortex-M0+	32	16	8	1	UFQFPN32	27	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031K6Tx	Cortex-M0+	32	32	8	1	LQFP32	25	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031K6Ux	Cortex-M0+	32	32	8	1	UFQFPN32	27	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031C4Tx	Cortex-M0+	32	16	8	1	LQFP48	38	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125
STM32L031C6Tx	Cortex-M0+	32	32	8	1	LQFP48	38	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	СОМР	SPI	12S	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
STM32L041E6Yx	Cortex-M0+	32	32	8	1	WLCSP25	20	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L041F6Px	Cortex-M0+	32	32	8	1	TSS0P20	15	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L041G6Ux	Cortex-M0+	32	32	8	1	UFQFPN28	21	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L041K6Tx	Cortex-M0+	32	32	8	1	LQFP32	25	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L041K6Ux	Cortex-M0+	32	32	8	1	UFQFPN32	27	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L041C6Tx	Cortex-M0+	32	32	8	1	LQFP48	38	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L041C6Ux	Cortex-M0+	32	32	8	1	UFQFPN48	38	1.65-3.6	3	1	1	10	0	2	1	0	1	1	1	0	0	N/A	YES	125
STM32L051K6Tx	Cortex-M0+	32	32	8	2	LQFP32	25	1.65-3.6	4	1	1	10	0	2	1	0	1	2	0	0	0	N/A	N/A	125
STM32L051K6Ux	Cortex-M0+	32	32	8	2	UFQFPN32	27	1.65-3.6	4	1	1	10	0	2	1	0	1	2	0	0	0	N/A	N/A	125
STM32L051K8Tx	Cortex-M0+	32	64	8	2	LQFP32	25	1.65-3.6	4	1	1	10	0	2	1	0	1	2	0	0	0	N/A	N/A	125
STM32L051K8Ux	Cortex-M0+	32	64	8	2	UFQFPN32	27	1.65-3.6	4	1	1	10	0	2	1	0	1	2	0	0	0	N/A	N/A	125
STM32L051T6Yx	Cortex-M0+	32	32	8	2	WLCSP36	29	1.65-3.6	4	1	1	10	0	2	1	0	2	2	1	0	0	N/A	N/A	125
STM32L051T8Yx	Cortex-M0+	32	64	8	2	WLCSP36	29	1.65-3.6	4	1	1	10	0	2	1	0	2	2	1	0	0	N/A	N/A	125
STM32L051C6Tx	Cortex-M0+	32	32	8	2	LQFP48	37	1.65-3.6	4	1	1	10	0	2	2	1	2	2	1	0	0	N/A	N/A	125
STM32L051C6Ux	Cortex-M0+	32	32	8	2	UFQFPN48	37	1.65-3.6	4	1	1	10	0	2	2	1	2	2	1	0	0	N/A	N/A	125
STM32L051C8Tx	Cortex-M0+	32	64	8	2	LQFP48	37	1.65-3.6	4	1	1	10	0	2	2	1	2	2	1	0	0	N/A	N/A	125
STM32L051C8Ux	Cortex-M0+	32	64	8	2	UFQFPN48	37	1.65-3.6	4	1	1	10	0	2	2	1	2	2	1	0	0	N/A	N/A	125
STM32L051R6Tx	Cortex-M0+	32	32	8	2	LQFP64	51	1.65-3.6	4	1	1	16	0	2	2	1	2	2	1	0	0	N/A	N/A	125
STM32L051R6Hx	Cortex-M0+	32	32	8	2	TFBGA64	51	1.65-3.6	4	1	1	16	0	2	2	1	2	2	1	0	0	N/A	N/A	125
STM32L051R8Tx	Cortex-M0+	32	64	8	2	LQFP64	51	1.65-3.6	4	1	1	16	0	2	2	1	2	2	1	0	0	N/A	N/A	125
STM32L051R8Hx	Cortex-M0+	32	64	8	2	TFBGA64	51	1.65-3.6	4	1	1	16	0	2	2	1	2	2	1	0	0	N/A	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
STM32L071K8Ux	Cortex-M0+	32	64	20	3	UFQFPN32	23	1.65-3.6	6	1	1	10	0	2	1	0	2	3	1	0	0	N/A	N/A	125
STM32L071KBTx	Cortex-M0+	32	128	20	6	LQFP32	25	1.65-3.6	6	1	1	10	0	2	1	0	2	3	1	0	0	N/A	N/A	125
STM32L071KBUx	Cortex-M0+	32	128	20	6	UFQFPN32	23	1.65-3.6	6	1	1	10	0	2	1	0	2	3	1	0	0	N/A	N/A	125
STM32L071KZTx	Cortex-M0+	32	192	20	6	LQFP32	25	1.65-3.6	6	1	1	10	0	2	1	0	2	3	1	0	0	N/A	N/A	125
STM32L071KZUx	Cortex-M0+	32	192	20	6	UFQFPN32	23	1.65-3.6	6	1	1	10	0	2	1	0	2	3	1	0	0	N/A	N/A	125
STM32L071C8Tx	Cortex-M0+	32	64	20	3	LQFP48	37	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071CBTx	Cortex-M0+	32	128	20	6	LQFP48	37	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071CBYx	Cortex-M0+	32	128	20	6	WLCSP49	40	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071CZTx	Cortex-M0+	32	192	20	6	LQFP48	37	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071CZYx	Cortex-M0+	32	192	20	6	WLCSP49	40	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071RBTx	Cortex-M0+	32	128	20	6	LQFP64	51	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071RBHx	Cortex-M0+	32	128	20	6	TFBGA64	51	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071RZTx	Cortex-M0+	32	192	20	6	LQFP64	51	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071RZHx	Cortex-M0+	32	192	20	6	TFBGA64	51	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071V8Tx	Cortex-M0+	32	64	20	3	LQFP100	84	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071V8lx	Cortex-M0+	32	64	20	3	UFBGA100	84	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071VBTx	Cortex-M0+	32	128	20	6	LQFP100	84	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071VBlx	Cortex-M0+	32	128	20	6	UFBGA100	84	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071VZTx	Cortex-M0+	32	192	20	6	LQFP100	84	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L071VZlx	Cortex-M0+	32	192	20	6	UFBGA100	84	1.65-3.6	6	1	1	16	0	2	2	1	3	4	1	0	0	N/A	N/A	125
STM32L081KZTx	Cortex-M0+	32	192	20	6	LQFP32	25	1.65-3.6	6	1	1	10	0	2	1	0	2	3	1	0	0	N/A	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
STM32L081KZUx	Cortex-M0+	32	192	20	6	UFQFPN32	23	1.65-3.6	6	1	1	10	0	2	1	0	2	3	1	0	0	N/A	YES	125
STM32L081CBTx	Cortex-M0+	32	128	20	6	LQFP48	40	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	YES	125
STM32L081CBUx	Cortex-M0+	32	128	20	6	UFQFPN48	40	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	YES	125
STM32L081CZTx	Cortex-M0+	32	192	20	6	LQFP48	40	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	YES	125
STM32L081CZUx	Cortex-M0+	32	192	20	6	UFQFPN48	40	1.65-3.6	6	1	1	13	0	2	2	1	3	4	1	0	0	N/A	YES	125
							S	TM32L0x2	USB c	rystal	-less p	oroduc	t Line											
STM32L052K6Tx	Cortex-M0+	32	32	8	2	LQFP32	25	1.65-3.6	4	1	1	10	1	2	1	0	1	2	0	1	0	YES	N/A	125
STM32L052K6Ux	Cortex-M0+	32	32	8	2	UFQFPN32	27	1.65-3.6	4	1	1	10	1	2	1	0	1	2	0	1	0	YES	N/A	125
STM32L052K8Tx	Cortex-M0+	32	64	8	2	LQFP32	25	1.65-3.6	4	1	1	10	1	2	1	0	1	2	0	1	0	YES	N/A	125
STM32L052K8Ux	Cortex-M0+	32	64	8	2	UFQFPN32	27	1.65-3.6	4	1	1	10	1	2	1	0	1	2	0	1	0	YES	N/A	125
STM32L052T6Yx	Cortex-M0+	32	32	8	2	WLCSP36	29	1.65-3.6	4	1	1	10	1	2	1	0	2	2	1	1	0	YES	N/A	125
STM32L052T8Yx	Cortex-M0+	32	64	8	2	WLCSP36	29	1.65-3.6	4	1	1	10	1	2	1	1	2	2	1	1	0	YES	N/A	125
STM32L052C6Tx	Cortex-M0+	32	32	8	2	LQFP48	37	1.65-3.6	4	1	1	10	1	2	2	1	2	2	1	1	0	YES	N/A	125
STM32L052C8Tx	Cortex-M0+	32	64	8	2	LQFP48	37	1.65-3.6	4	1	1	10	1	2	2	1	2	2	1	1	0	YES	N/A	125
STM32L052R6Tx	Cortex-M0+	32	32	8	2	LQFP64	51	1.65-3.6	4	1	1	16	1	2	2	1	2	2	1	1	0	YES	N/A	125
STM32L052R6Hx	Cortex-M0+	32	32	8	2	TFBGA64	50	1.65-3.6	4	1	1	15	1	2	2	1	2	2	1	1	0	YES	N/A	125
STM32L052R8Tx	Cortex-M0+	32	64	8	2	LQFP64	51	1.65-3.6	4	1	1	16	1	2	2	1	2	2	1	1	0	YES	N/A	125
STM32L052R8Hx	Cortex-M0+	32	64	8	2	TFBGA64	50	1.65-3.6	4	1	1	15	1	2	2	1	2	2	1	1	0	YES	N/A	125
STM32L062K8Tx	Cortex-M0+	32	64	8	2	LQFP32	25	1.65-3.6	4	1	1	10	1	2	1	0	1	2	0	1	0	YES	YES	125
STM32L062K8Ux	Cortex-M0+	32	64	8	2	UFQFPN32	27	1.65-3.6	4	1	1	10	1	2	1	0	1	2	0	1	0	YES	YES	125
STM32L062T8Yx	Cortex-M0+	32	64	8	2	WLCSP36	29	1.65-3.6	4	1	1	10	1	2	1	0	2	2	1	1	0	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	I2S	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
STM32L062C8Ux	Cortex-M0+	32	64	8	2	UFQFPN48	37	1.65-3.6	4	1	1	10	1	2	1	0	2	2	1	1	0	YES	YES	125
STM32L072KBTx	Cortex-M0+	32	128	20	6	LQFP32	25	1.65-3.6	6	1	1	10	2	2	1	0	2	3	1	1	0	YES	N/A	125
STM32L072KBUx	Cortex-M0+	32	128	20	6	UFQFPN32	23	1.65-3.6	6	1	1	10	2	2	1	0	2	3	1	1	0	YES	N/A	125
STM32L072KZTx	Cortex-M0+	32	192	20	6	LQFP32	25	1.65-3.6	6	1	1	10	2	2	1	0	2	3	1	1	0	YES	N/A	125
STM32L072KZUx	Cortex-M0+	32	192	20	6	UFQFPN32	23	1.65-3.6	6	1	1	10	2	2	1	0	2	3	1	1	0	YES	N/A	125
STM32L072CBTx	Cortex-M0+	32	128	20	6	LQFP48	37	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072CBYx	Cortex-M0+	32	128	20	6	WLCSP49	40	1.65-3.6	6	1	1	13	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072CBUx	Cortex-M0+	32	128	20	6	UFQFPN48	40	1.65-3.6	6	1	1	13	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072CZTx	Cortex-M0+	32	192	20	6	LQFP48	37	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072CZYx	Cortex-M0+	32	192	20	6	WLCSP49	40	1.65-3.6	6	1	1	13	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072CZUx	Cortex-M0+	32	192	20	6	UFQFPN48	40	1.65-3.6	6	1	1	13	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072RBTx	Cortex-M0+	32	128	20	6	LQFP64	51	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072RBHx	Cortex-M0+	32	128	20	6	TFBGA64	50	1.65-3.6	6	1	1	15	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072RZTx	Cortex-M0+	32	192	20	6	LQFP64	51	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072RZHx	Cortex-M0+	32	192	20	6	TFBGA64	50	1.65-3.6	6	1	1	15	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072RZIx	Cortex-M0+	32	192	20	6	UFBGA64	51	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072V8Tx	Cortex-M0+	32	64	20	3	LQFP100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072V8lx	Cortex-M0+	32	64	20	3	UFBGA100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072VBTx	Cortex-M0+	32	128	20	6	LQFP100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072VBlx	Cortex-M0+	32	128	20	6	UFBGA100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L072VZTx	Cortex-M0+	32	192	20	6	LQFP100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
STM32L072VZIx	Cortex-M0+	32	192	20	6	UFBGA100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	0	YES	N/A	125
STM32L082KBTx	Cortex-M0+	32	128	20	6	LQFP32	25	1.65-3.6	6	1	1	10	2	2	1	0	2	3	1	1	0	YES	YES	125
STM32L082KBUx	Cortex-M0+	32	128	20	6	UFQFPN32	23	1.65-3.6	6	1	1	10	2	2	1	0	2	3	1	1	0	YES	YES	125
STM32L082KZTx	Cortex-M0+	32	192	20	6	LQFP32	25	1.65-3.6	6	1	1	10	2	2	1	0	2	3	1	1	0	YES	YES	125
STM32L082KZUx	Cortex-M0+	32	192	20	6	UFQFPN32	23	1.65-3.6	6	1	1	10	2	2	1	0	2	3	1	1	0	YES	YES	125
STM32L082CZYx	Cortex-M0+	32	192	20	6	WLCSP49	40	1.65-3.6	6	1	1	13	2	2	2	1	3	4	1	1	0	YES	YES	125
STM32L082CZUx	Cortex-M0+	32	192	20	6	UFQFPN48	40	1.65-3.6	6	1	1	13	2	2	2	1	3	4	1	1	0	YES	YES	125
								STM	32L0x	3 USB	+ LCD	Line		1	ı	ı	ı							
STM32L053C6Tx	Cortex-M0+	32	32	8	2	LQFP48	37	1.65-3.6	4	1	1	10	1	2	2	1	2	2	1	1	4x18	YES	N/A	125
STM32L053C6Ux	Cortex-M0+	32	32	8	2	UFQFPN48	37	1.65-3.6	4	1	1	10	1	2	2	1	2	2	1	1	4x18	YES	N/A	125
STM32L053C8Tx	Cortex-M0+	32	64	8	2	LQFP48	37	1.65-3.6	4	1	1	10	1	2	2	1	2	2	1	1	4x18	YES	N/A	125
STM32L053C8Ux	Cortex-M0+	32	64	8	2	UFQFPN48	37	1.65-3.6	4	1	1	10	1	2	2	1	2	2	1	1	4x18	YES	N/A	125
STM32L053R6Tx	Cortex-M0+	32	32	8	2	LQFP64	51	1.65-3.6	4	1	1	16	1	2	2	1	2	2	1	1	8x28/4x32	YES	N/A	125
STM32L053R6Hx	Cortex-M0+	32	32	8	2	TFBGA64	50	1.65-3.6	4	1	1	15	1	2	2	1	2	2	1	1	8x28/4x32	YES	N/A	125
STM32L053R8Tx	Cortex-M0+	32	64	8	2	LQFP64	51	1.65-3.6	4	1	1	16	1	2	2	1	2	2	1	1	8x28/4x32	YES	N/A	125
STM32L053R8Hx	Cortex-M0+	32	64	8	2	TFBGA64	50	1.65-3.6	4	1	1	15	1	2	2	1	2	2	1	1	8x28/4x32	YES	N/A	125
STM32L063C8Tx	Cortex-M0+	32	64	8	2	LQFP48	37	1.65-3.6	4	1	1	10	1	2	2	1	2	2	1	1	4x18	YES	YES	125
STM32L063C8Ux	Cortex-M0+	32	64	8	2	UFQFPN48	37	1.65-3.6	4	1	1	10	1	2	2	1	2	2	1	1	4x18	YES	YES	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	I2S	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
STM32L063R8Tx	Cortex-M0+	32	64	8	2	LQFP64	51	1.65-3.6	4	1	1	16	1	2	2	1	2	2	1	1	8x28/4x32	YES	YES	125
STM32L073CBTx	Cortex-M0+	32	128	20	6	LQFP48	37	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	N/A	125
STM32L073CBUx	Cortex-M0+	32	128	20	6	UFQFPN48	37	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	N/A	125
STM32L073CZTx	Cortex-M0+	32	192	20	6	LQFP48	37	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	N/A	125
STM32L073CZUx	Cortex-M0+	32	192	20	6	UFQFPN48	37	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	N/A	125
STM32L073CZYx	Cortex-M0+	32	192	20	6	WLCSP49	37	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	N/A	125
STM32L073RBTx	Cortex-M0+	32	128	20	6	LQFP64	51	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x28/4x32	YES	N/A	125
STM32L073RBHx	Cortex-M0+	32	128	20	6	TFBGA64	50	1.65-3.6	6	1	1	15	2	2	2	1	3	4	1	1	8x28/4x32	YES	N/A	125
STM32L073RZTx	Cortex-M0+	32	192	20	6	LQFP64	51	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x28/4x32	YES	N/A	125
STM32L073RZHx	Cortex-M0+	32	192	20	6	TFBGA64	50	1.65-3.6	6	1	1	15	2	2	2	1	3	4	1	1	8x28/4x32	YES	N/A	125
STM32L073V8Tx	Cortex-M0+	32	64	20	3	LQFP100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	N/A	125
STM32L073V8lx	Cortex-M0+	32	64	20	3	UFBGA100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	N/A	125
STM32L073VBTx	Cortex-M0+	32	128	20	6	LQFP100	84	1.65-3.6	6	1	1	15	2	2	2	1	3	4	1	1	8x48/4x52	YES	N/A	125
STM32L073VBlx	Cortex-M0+	32	128	20	6	UFBGA100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	N/A	125
STM32L073VZTx	Cortex-M0+	32	192	20	6	LQFP100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	N/A	125
STM32L073VZIx	Cortex-M0+	32	192	20	6	UFBGA100	84	1.65-3.6	6	1	1	15	2	2	2	1	3	4	1	1	8x48/4x52	YES	N/A	125

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	12S	120	U(S)ART	LPUART	USB Device	Segment LCD	TRNG	AES/DES	T° Max (°C)
STM32L083CBTx	Cortex-M0+	32	128	20	6	LQFP48	40	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	YES	125
STM32L083CBUx	Cortex-M0+	32	128	20	6	UFQFPN48	40	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	YES	125
STM32L083CZTx	Cortex-M0+	32	192	20	6	LQFP48	40	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	YES	125
STM32L083CZUx	Cortex-M0+	32	192	20	6	UFQFPN48	40	1.65-3.6	6	1	1	10	2	2	2	1	3	4	1	1	4x18	YES	YES	125
STM32L083RBTx	Cortex-M0+	32	128	20	6	LQFP64	51	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x28/4x32	YES	YES	125
STM32L083RZTx	Cortex-M0+	32	192	20	6	LQFP64	51	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x28/4x32	YES	YES	125
STM32L083RZHx	Cortex-M0+	32	192	20	6	TFBGA64	51	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x28/4x32	YES	YES	125
STM32L083V8Tx	Cortex-M0+	32	64	20	3	LQFP100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	YES	125
STM32L083V8lx	Cortex-M0+	32	64	20	3	UFBGA100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	YES	125
STM32L083VBTx	Cortex-M0+	32	128	20	6	LQFP100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	YES	125
STM32L083VBlx	Cortex-M0+	32	128	20	6	UFBGA100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	YES	125
STM32L083VZTx	Cortex-M0+	32	192	20	6	LQFP100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	YES	125
STM32L083VZIx	Cortex-M0+	32	192	20	6	UFBGA100	84	1.65-3.6	6	1	1	16	2	2	2	1	3	4	1	1	8x48/4x52	YES	YES	125

STM32 WB0系列- Arm® Cortex®-M0+ 紧凑、节能设计的2.4GHz BLE 5.4 无线MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	SPI	I2S	120	U(S)ART	LPUART	RF	TRNG	PKA	AES/DES	T° Max (°C)	Remarks	PDM (digital microphone interface)	BLE
							ı			Network	Co-F	roce	ssor												
STM32WB05KNV6	Cortex-M0+	64	192	24	VFQFPN32	20	1.7-3.6	-	-	-	-	-	-	-	-	-	-	2.4G	YES	YES	YES	85	Network Co-Processor		BLE5.4
STM32WB05KNV7	Cortex-M0+	64	192	24	VFQFPN32	20	1.7-3.6	-	-	-	-	-	-	-	-	-	-	2.4G	YES	YES	YES	105	Network Co-Processor		BLE5.4
STM32WB05TNF6	Cortex-M0+	64	192	24	WLCSP36	20	1.7-3.6	-	-	-	-	-	-	-	-	-	-	2.4G	YES	YES	YES	85	Network Co-Processor		BLE5.4
STM32WB05TNF7	Cortex-M0+	64	192	24	WLCSP36	20	1.7-3.6	-	-	-	-	-	-	-	-	-	-	2.4G	YES	YES	YES	105	Network Co-Processor		BLE5.4
										Applicat	ion P	roces	sor												
STM32WB05KZV6	Cortex-M0+	64	192	24	VFQFPN32	20	1.7-3.6	3	0	0	1	8	1	1	1	1	1	2.4G	YES	YES	YES	85			BLE5.4
STM32WB05KZV7	Cortex-M0+	64	192	24	VFQFPN32	20	1.7-3.6	3	0	0	1	8	1	1	1	1	1	2.4G	YES	YES	YES	105			BLE5.4
STM32WB05TZF6	Cortex-M0+	64	192	24	WLCSP36	20	1.7-3.6	3	0	0	1	8	1	1	1	1	1	2.4G	YES	YES	YES	85			BLE5.4
STM32WB05TZF7	Cortex-M0+	64	192	24	WLCSP36	20	1.7-3.6	3	0	0	1	8	1	1	1	1	1	2.4G	YES	YES	YES	105			BLE5.4
STM32WB06CCF6	Cortex-M0+	64	256	32	WLCSP49	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	85		1	BLE5.4
STM32WB06CCF7	Cortex-M0+	64	256	32	WLCSP49	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	105		1	BLE5.4
STM32WB06CCV6	Cortex-M0+	64	256	32	VFQFPN48	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	85		1	BLE5.4
STM32WB06CCV7	Cortex-M0+	64	256	32	VFQFPN48	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	105		1	BLE5.4
STM32WB06KCV6	Cortex-M0+	64	256	32	VFQFPN32	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	85		1	BLE5.4
STM32WB06KCV7	Cortex-M0+	64	256	32	VFQFPN32	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	105		1	BLE5.4
STM32WB07CCF6	Cortex-M0+	64	256	64	WLCSP49	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	85		1	BLE5.4

STM32 WB0系列- Arm® Cortex®-M0+ 紧凑、节能设计的2.4GHz BLE 5.4 无线MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Advanced Timer (16-bit)	High Resolution Timer	ADC 12-bit Units	ADC 12-bit Channels	SPI	12S	120	U(S)ART	LPUART	RF	TRNG	PKA	AES/DES	T° Max (°C)	Remarks	PDM (digital microphone interface)	BLE
STM32WB07CCF7	Cortex-M0+	64	256	64	WLCSP49	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	105		1	BLE5.4
STM32WB07CCV6	Cortex-M0+	64	256	64	VFQFPN48	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	85		1	BLE5.4
STM32WB07CCV7	Cortex-M0+	64	256	64	VFQFPN48	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	105		1	BLE5.4
STM32WB07KCV6	Cortex-M0+	64	256	64	VFQFPN32	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	85		1	BLE5.4
STM32WB07KCV7	Cortex-M0+	64	256	64	VFQFPN32	32	1.7-3.6	3	1	SysTick	1	8	2	2	2	1	1	2.4G	YES	YES	YES	105		1	BLE5.4
STM32WB09KEV6	Cortex-M0+	64	512	64	VFQFPN32	20	1.7-3.6	3	0	0	1	8	1	1	1	1	1	2.4G	YES	YES	YES	85			BLE5.4
STM32WB09KEV7	Cortex-M0+	64	512	64	VFQFPN32	20	1.7-3.6	3	0	0	1	8	1	1	1	1	1	2.4G	YES	YES	YES	105			BLE5.4
STM32WB09TEF6	Cortex-M0+	64	512	64	WLCSP36	20	1.7-3.6	3	0	0	1	8	1	1	1	1	1	2.4G	YES	YES	YES	85			BLE5.4
STM32WB09TEF7	Cortex-M0+	64	512	64	WLCSP36	20	1.7-3.6	3	0	0	1	8	1	1	1	1	1	2.4G	YES	YES	YES	105			BLE5.4

STM32 WB系列 – Arm® Cortex®-M4和Cortex®-M0+双核2.4G无线MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	COMP	CDI	M-SPI	120	U(S)ART	LPUART	USB Device	Segment LCD	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	RF	Protocol	RF Power	TRNG	PKA	AES/DES	T° Max (°C)
<u> </u>													e1	rM2	211/5	Pv5	Stand	ard	Line																
	M4 &	2.1				=0=5																								21.55					
STM32WB15CCUx	M0+	64	320	48	0	UFQFPN48	30	1.71-3.6	3	1	1	2	1	13	1	1	0	1	1	1	0	0	0	0	0	0	0	N/A	2.4G	BLE5.3	5.5 dBm	YES	YES	YES	105
STM32WB15CCUxE	M4 & M0+	64	320	48	0	UFQFPN48	37	1.71-3.6	3	1	1	2	1	13	1	1	0	1	1	1	0	0	0	0	0	0	0	N/A	2.4G	BLE5.3	5.5 dBm	YES	YES	YES	105
STM32WB35CCUxA	M4 & M0+	64	256	96	0	UFQFPN48	30	1.71-3.6	3	1	1	2	1	13	2	1	1 [QUAD]	2	1	1	1	0	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB35CEUxA	M4 & M0+	64	512	96	0	UFQFPN48	30	1.71-3.6	3	1	1	2	1	13	2	1	1 [QUAD]	2	1	1	1	0	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55CCUx	M4 & M0+	64	256	128	0	UFQFPN48	30	1.71-3.6	3	1	1	2	1	13	2	1	1 [QUAD]	2	1	1	1	4x13	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55CEUx	M4 & M0+	64	512	256	0	UFQFPN48	30	1.71-3.6	3	1	1	2	1	13	2	1	1 [QUAD]	2	1	1	1	4x13	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55CGUx	M4 & M0+	64	1024	256	0	UFQFPN48	30	1.71-3.6	3	1	1	2	1	13	2	1	1 [QUAD]	2	1	1	1	4x13	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55RCVx	M4 & M0+	64	256	128	0	VFQFPN68	49	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	4x28	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55RGVx	M4 & M0+	64	512	256	6 0	VFQFPN68	49	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	4x28	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105

STM32 WB系列 – Arm® Cortex®-M4和Cortex®-M0+双核2.4G无线MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	COMP	SPI	M-SPI	12C	U(S)ART	LPUART	USB Device	Segment LCD	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	RF	Protocol	RF Power	TRNG	PKA	AES/DES	T° Max (°C)
STM32WB55REVx	M4 & M0+	64	1024	256	0	VFQFPN68	49	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	4x28	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55VCQx	M4 & M0+	64	256	128	0	UFBGA129	72	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	8x40/ 4x44	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55VCYx	M4 & M0+	64	256	128	0	WLCSP100	72	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	8x40/ 4x44	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55VEQx	M4 & M0+	64	512	256	0	UFBGA129	72	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	8x40/ 4x44	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55VEYx	M4 & M0+	64	512	256	0	WLCSP100	72	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	8x40/ 4x44	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55VGQx	M4 & M0+	64	1024	256	0	UFBGA129	72	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	8x40/ 4x44	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55VGYx	M4 & M0+	64	1024	256	0	WLCSP100	72	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	8x40/ 4x44	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	105
STM32WB55VYY6	M4 & M0+	64	640	256	0	WLCSP100	72	1.71-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	8x40/ 4x44	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	YES	85

STM32 WB系列 – Arm® Cortex®-M4和Cortex®-M0+双核2.4G无线MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	E2PROM (KBytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	COMP	SPI	M-SPI	I2C	U(S)ART	LPUART	USB Device	Segment LCD	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	Math Accelerator	Ŗ	Protocol	RF Power	TRNG	PKA	AES/UES	T° Max (°C)
														ST	M32	WB	x0 Valu	ıe Li	ine																
STM32WB10CCU6	M4 & M0+	64	320	48	0	UFQFPN48	30	2-3.6	3	1	1	2	1	13	0	1	0	1	1	0	0	0	0	0	0	0	0	N/A	2.4G	BLE5.3	4dBm	YES	YES	S YE	S 85
STM32WB30CEU6A	M4 & M0+	64	512	96	0	UFQFPN48	30	2-3.6	3	1	1	2	1	13	0	1	0	1	1	0	0	0	0	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	4dBm	YES	YES	S YE	S 85
STM32WB50CGU6	M4 & M0+	64	1024	128	0	UFQFPN48	30	2-3.6	3	1	1	2	1	13	0	1	0	1	1	0	0	0	0	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	4dBm	YES	YES	S YE	ES 85
													5	STM	132V	/B5	M Mod	ule	Line	,															
STM32WB5MMGH6	M4 & M0+	64	1024	256	0	SiP-LGA 86	68	1.8-3.6	3	1	1	2	1	19	2	2	1 [QUAD]	2	1	1	1	8x40	1	0	0	0	0	N/A	2.4G	BLE5.3 Zigbee 3.0 OpenThread 802.15.4 proprietary	6 dBm	YES	YES	S YE	ES 85

STM32 WBA系列 – Arm® Cortex®-M33超低功耗, 高性能安全2.4GHz BLE 5.4 无线MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	SPI	120	U(S)ART	LPUART	쮸	Trust'Zone	TRNG	PKA	AES/DES	T° Max (°C)
							Ac	cess	Line					l									
STM32WBA52CGUx	Cortex-M33	100	1024	128	UFQFPN48	35	1.71-3.6	3	1	1	2	1	9	2	2	2	1	2.4G	YES	YES	YES	YES	85
STM32WBA52CEUx	Cortex-M33	100	512	96	UFQFPN48	35	1.71-3.6	3	1	1	2	1	9	2	2	2	1	2.4G	YES	YES	YES	YES	85
							St	retch	Line														
STM32WBA54CEU6	Cortex-M33	100	512	96	UFQFPN48	35	1.71-3.6	3	1	1	2	1	9	2	2	2	1	2.4G	YES	YES	YES	YES	85
STM32WBA54CEU7	Cortex-M33	100	512	96	UFQFPN48	35	1.71-3.6	3	1	1	2	1	9	2	2	2	1	2.4G	YES	YES	YES	YES	105
STM32WBA54CGU6	Cortex-M33	100	1024	1024	UFQFPN48	35	1.71-3.6	3	1	1	2	1	9	2	2	2	1	2.4G	YES	YES	YES	YES	85
STM32WBA54CGU7	Cortex-M33	100	1024	1024	UFQFPN48	35	1.71-3.6	3	1	1	2	1	9	2	2	2	1	2.4G	YES	YES	YES	YES	105
STM32WBA54KEU6	Cortex-M33	100	512	96	UFQFPN32	20	1.71-3.6	3	1	1	2	1	8	2	2	2	1	2.4G	YES	YES	YES	YES	85
STM32WBA54KEU7	Cortex-M33	100	512	96	UFQFPN32	20	1.71-3.6	3	1	1	2	1	8	2	2	2	1	2.4G	YES	YES	YES	YES	105
STM32WBA54KGU6	Cortex-M33	100	1024	1024	UFQFPN32	20	1.71-3.6	3	1	1	2	1	8	2	2	2	1	2.4G	YES	YES	YES	YES	85
STM32WBA54KGU7	Cortex-M33	100	1024	1024	UFQFPN32	20	1.71-3.6	3	1	1	2	1	8	2	2	2	1	2.4G	YES	YES	YES	YES	105
STM32WBA55CEU6	Cortex-M33	100	512	96	UFQFPN48	31	1.71-3.6	3	1	1	2	1	8	2	2	2	1	2.4G	YES	YES	YES	YES	85
STM32WBA55CEU7	Cortex-M33	100	512	96	UFQFPN48	31	1.71-3.6	3	1	1	2	1	8	2	2	2	1	2.4G	YES	YES	YES	YES	105
STM32WBA55CGU6	Cortex-M33	100	1024	1024	UFQFPN48	31	1.71-3.6	3	1	1	2	1	8	2	2	2	1	2.4G	YES	YES	YES	YES	85
STM32WBA55CGU7	Cortex-M33	100	1024	1024	UFQFPN48	31	1.71-3.6	3	1	1	2	1	8	2	2	2	1	2.4G	YES	YES	YES	YES	105
STM32WBA55UEI6	Cortex-M33	100	512	96	UFBGA59	35	1.71-3.6	3	1	1	2	1	10	2	2	2	1	2.4G	YES	YES	YES	YES	85

STM32 WBA系列 – Arm® Cortex®-M33超低功耗, 高性能安全2.4GHz BLE 5.4 无线MCU

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	SPI	12C	U(S)ART	LPUART	꼮	Trust'Zone	TRNG	PKA	AES/DES	T° Max (°C)
STM32WBA55UEI7	Cortex-M33	100	512	96	UFBGA59	35	1.71-3.6	3	1	1	2	1	10	2	2	2	1	2.4G	YES	YES	YES	YES	105
STM32WBA55UGI6	Cortex-M33	100	1024	1024	UFBGA59	35	1.71-3.6	3	1	1	2	1	10	2	2	2	1	2.4G	YES	YES	YES	YES	85
STM32WBA55UGI7	Cortex-M33	100	1024	1024	UFBGA59	35	1.71-3.6	3	1	1	2	1	10	2	2	2	1	2.4G	YES	YES	YES	YES	105

STM32 WL系列 – Arm® Cortex®-M4 / – Arm® Cortex®和Cortex®-M0+长距离无线SoC

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	I2S	12C	U(S)ART	LPUART	짞	Radio Freq. Range (MHz)	PA	Modulations	Standard protocol	TRNG	PKA	AES/DES	T° Max (°C)
							STM	32 W	VL5×	c Du	al-c	ore l	Line	ARN	Л® C	orte	x® I	V14 +	- Co	rtex	®-M0+								
STM32WL54CCUx	M4 & M0+	48 & 48	256	64	UFQFPN48	29	1.8-3.6	3	1	1	3	1	13	1	2	2	1	3	2	1	Sub-G	150 - 960	HPA:22dBm LPA:15dBm	(G)FSK (G)MSK BPSK	sigfox M-Bus MIOTY ZETA	YES	YES	YES	105
STM32WL54JClx	M4 & M0+	48 & 48	256	64	UFBGA73	43	1.8-3.6	3	1	1	3	1	16	1	2	2	1	3	2	1	Sub-G	150 - 960	HPA:22dBm LPA:15dBm	(G)FSK (G)MSK BPSK	sigfox M-Bus MIOTY ZETA	YES	YES	YES	105
STM32WL55CCUx	M4 & M0+	48 & 48	256	64	UFQFPN48	29	1.8-3.6	3	1	1	3	1	13	1	2	2	1	3	2	1	LoRa	150 - 960	HPA:22dBm LPA:15dBm	LoRa® (G)FSK (G)MSK BPSK	LoRaWAN® sigfox M-Bus MIOTY ZETA	YES	YES	YES	105
STM32WL55JClx	M4 & M0+	48 & 48	256	64	UFBGA73	43	1.8-3.6	3	1	1	3	1	16	1	2	2	1	3	2	1	LoRa	150 - 960	HPA:22dBm LPA:15dBm	LoRa® (G)FSK (G)MSK BPSK	LoRaWAN® sigfox M-Bus MIOTY ZETA	YES	YES	YES	105
								S	TM3	32WI	Ex S	Sign	le-c	ore l	Line	AR	M® (Corte	ex®-	M4									
STM32WLE4CCUx	Cortex-M4	48	256	64	UFQFPN48	29	1.8-3.6	3	1	1	3	1	13	1	2	2	1	3	2	1	Sub-G	150 - 960	HPA:22dBm LPA:15dBm	(G)FSK (G)MSK BPSK	sigfox M-Bus MIOTY ZETA	YES	YES	YES	105
STM32WLE4JClx	Cortex-M4	48	256	64	UFBGA73	43	1.8-3.6	3	1	1	3	1	16	1	2	2	1	3	2	1	Sub-G	150 - 960	HPA:22dBm LPA:15dBm	(G)FSK (G)MSK BPSK	sigfox M-Bus MIOTY ZETA	YES	YES	YES	105
STM32WLE5C8U6	Cortex-M4	48	64	20	UFQFPN48	29	1.8-3.6	3	1	1	3	1	13	1	2	2	1	3	2	1	LoRa	150 - 960	HPA:22dBm LPA:15dBm	LoRa® (G)FSK (G)MSK BPSK	LoRaWAN® sigfox M-Bus MIOTY ZETA	YES	YES	YES	85

STM32 WL系列 – Arm® Cortex®-M4 / – Arm® Cortex®和Cortex®-M0+长距离无线SoC

Commercial Product Code	Core	Frequency (MHz)	Flash (Kbytes)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 12-bit Channels	DAC 12-bit Channels	COMP	SPI	128	120	U(S)ART	LPUART	RF	Radio Freq. Range (MHz)	PA	Modulations	Standard protocol	TRNG	PKA	AES/DES	T° Max (°C)
STM32WLE5CBU6	Cortex-M4	48	128	48	UFQFPN48	29	1.8-3.6	3	1	1	3	1	13	1	2	2	1	3	2	1	LoRa	150 - 960	HPA:22dBm LPA:15dBm	LoRa® (G)FSK (G)MSK BPSK	LoRaWAN® sigfox M-Bus MIOTY ZETA	YES	YES	YES	85
STM32WLE5CCUx	Cortex-M4	48	256	64	UFQFPN48	29	1.8-3.6	3	1	1	3	1	13	1	2	2	1	3	2	1	LoRa	150 - 960	HPA:22dBm LPA:15dBm	LoRa® (G)FSK (G)MSK BPSK	LoRaWAN® sigfox M-Bus MIOTY ZETA	YES	YES	YES	105
STM32WLE5J8I6	Cortex-M4	48	64	20	UFBGA73	43	1.8-3.6	3	1	1	3	1	16	1	2	2	1	3	2	1	LoRa	150 - 960	HPA:22dBm LPA:15dBm	LoRa® (G)FSK (G)MSK BPSK	LoRaWAN® sigfox M-Bus MIOTY ZETA	YES	YES	YES	85
STM32WLE5JBI6	Cortex-M4	48	128	48	UFBGA73	43	1.8-3.6	3	1	1	3	1	16	1	2	2	1	3	2	1	LoRa	150 - 960	HPA:22dBm LPA:15dBm	LoRa® (G)FSK (G)MSK BPSK	LoRaWAN® sigfox M-Bus MIOTY ZETA	YES	YES	YES	85
STM32WLE5JClx	Cortex-M4	48	256	64	UFBGA73	43	1.8-3.6	3	1	1	3	1	16	1	2	2	1	3	2	1	LoRa	150 - 960	HPA:22dBm LPA:15dBm	LoRa® (G)FSK (G)MSK BPSK	LoRaWAN® sigfox M-Bus MIOTY ZETA	YES	YES	YES	105

Note:

- 1. STM32WL support LoRa® ,(G)FSK, (G)MSK and BPSK
- 2. STM32WL5x = dual core; STM32WLEx = single core
- 3. STM32WLx5 = all modulations available ; STM32WLx4 = all modulations available except LoRa $^{\circ}$

SPIRIT系列 – Sub1G Hz 射频收发器

Commercial Product Code	Туре	Frequency bands(MHz)	Modulation	Tx Power(Max)(dBm)	Sensitivity(best)(dBm)	Supply Current	Protocols	Temp Range(°C)	FIFO (Bytes)	Package	10	VDD	SPI
SPIRIT1QTR	Transceiver	150-174 300-348 387-470 779-956	2-FSK, GFSK, MSK, GMSK, OOK, ASK	16	-120	RX:9.2mA TX: 19.5mA@+11dBm Sleep: 850nA Standby: 600nA	WMBUS, Proprietary, 6LoWPAN	-40 ~105	RX: 96 TX: 96	QFN20	4	1.8-3.6	1
S2-LPQTR	Transceiver	413-479 826-958	2(G)FSK, 4(G)FSK, 00K, ASK	16	-130	RX:7mA TX: 10mA@+10dBm Sleep: 700nA Standby: 500nA	WMBUS,Proprietary, 6LoWPAN, WiSUN, KNX, SigFox	-40 ~105	RX: 128 TX: 128	QFN24	4	1.8-3.6	1
S2-LPCBQTR	Transceiver	452-527 904-1055	2(G)FSK, 4(G)FSK, 00K, ASK	16	-130	RX:7mA TX: 10mA@+10dBm Sleep: 700nA Standby: 500nA	WMBUS,Proprietary, 6LoWPAN, WiSUN, KNX, SigFox	-40 ~105	RX: 128 TX: 128	QFN24	4	1.8-3.6	1

STM32 MP1系列 – Arm® Cortex®-A7/Cortex®-A7 + M4 高性价比工业级MPU

Commercial Product Code	Core	Frequency (MHz)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	I PTimer	ADC 12-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	SPI	l2S	M-SPI	ÎZC	U(S)ART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	MDIO	3D GPU	TFT LCD	MIPI_DSI	SAI	SPOIFRY	DESDM	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
									STN	1321	MP1	51	AR	M®	Cor	tex	® -A7 +	Co	rtex	(® -N	14 <i>l</i>	Acc	ess	Line	;														
STM32MP151AAD3	A7 & M4	650 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5 (0	2	17	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP151AAB3	A7 & M4	650 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5 (0	2	17	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP151AAC3	A7 & M4	650 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5 (0	2	22	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1 [GI	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP151AAA3	A7 & M4	650 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5 (0	2	22	2 2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1 [GI	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	N/A		125 [Tj]
STM32MP151CAD3	A7 & M4	650 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5 (0	2	17	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	YES	YES	125 [Tj]
STM32MP151CAB3	A7 & M4	650 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5 (0	2	17	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	YES	YES	125 [Tj]
STM32MP151CAC3	A7 & M4	650 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5 (0	2	22	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1 [Gl	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	YES	YES	125 [Tj]
STM32MP151CAA3	A7 & M4	650 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5 0	0	2	22	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1 [GI) [1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	YES	YES	125 [Tj]
STM32MP151DAD1	A7 & M4	800 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5 (0	2	17	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP151DAB1	A7 & M4	800 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5 (0	2	17	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1	1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP151DAC1	A7 & M4	800 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5 (0	2	22	2	6	3	2 [QUAD]	6	8	0	3	1	0	2	1 1 [GI)] 1	N/A	1	0	4	4	8 1	N/A	YES	N/A	N/A	N/A		105 [Tj]

STM32 MP1系列 – Arm® Cortex®-A7/Cortex®-A7 + M4 高性价比工业级MPU

Commercial Product Code	Core	Frequency (MHz)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 19-bit Channels	ADC 16-bit Units	ADC 16-bit Channels	SPI	128	M-SPI	I2C	U(S)ART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	MDIO	3D GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DFSDM	DCMI	Trust'Zone	TRNG	OTFDEC	РКА	AES/DES	SHA/HMAC	T° Max (°C)
STM32MP151DAA1	A7 & M4	800 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0	0	2 2	22 2	2 6	3	2 [QUAD]	6	8	0	3	1	0	2	1 [GI) [N/A	. 1	0	4	4	8	1	N/A	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP151FAD1	A7 & M4	800 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0	0	2	17 2	2 6	3	2 [QUAD]	6	8	0	3	1	0	2	1	1	N/A	. 1	0	4	4	8	1	N/A	YES	N/A	N/A	YES	YES	105 [Tj]
STM32MP151FAB1	A7 & M4	800 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0	0	2	17 2	2 6	3	2 [QUAD]	6	8	0	3	1	0	2	1	1	N/A	. 1	0	4	4	8	1	N/A	YES	N/A	N/A	YES	YES	105 [Tj]
STM32MP151FAC1	A7 & M4	800 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0	0 2	2 2	22 2	2 6	3	2 [QUAD]	6	8	0	3	1	0	2 1	1 [GI	1	N/A	1	0	4	4	8	1	N/A	YES	N/A	N/A	YES	YES	105 [Tj]
STM32MP151FAA1	A7 & M4	800 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0	0 2	2 2	22 2	2 6	3	2 [QUAD]	6	8	0	3	1	0	2 1	1 [Gl)] 1	N/A	1	0	4	4	8	1	N/A	YES	N/A	N/A	YES	YES	105 [Tj]
						STM	1321	/IP1	53	Per	forn	nan	ice	Line	Du	al A	RM® C	orte	X®	-A7 -	+ Co	orte	X® ·	-M4	+ 2x	CAN	FD													
STM32MP153AAD3	Dual A7 & M4	650 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0	0	2	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	2	2	1	1	N/A	. 1	0	4	4	8	1	N/A	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP153AAB3	Dual A7 & M4	650 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0	0	2	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	2	2	1	1	N/A	. 1	0	4	4	8	1	N/A	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP153AAC3	Dual A7 & M4	650 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0	0	2 2	22 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	2	2	1 [GI) [N/A	. 1	0	4	4	8	1	N/A	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP153AAA3	Dual A7 & M4	650 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0	0	2 2	22 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	2	2	1 [GI	1 1	N/A	. 1	0	4	4	8	1	N/A	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP153CAD3	Dual A7 & M4	650 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0	0	2	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	2	2	1	1	N/A	1	0	4	4	8	1	N/A	YES	N/A	N/A	YES	YES	125 [Tj]
STM32MP153CAB3	Dual A7 & M4	650 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0	0	2	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	2	2	1	1	N/A	. 1	0	4	4	8	1	N/A	YES	N/A	N/A	YES	YES	125 [Tj]

STM32 MP1系列 – Arm® Cortex®-A7/Cortex®-A7 + M4 高性价比工业级MPU

Commercial Product Code	Core	Frequency (MHz)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 19-bit Channels	ADC 16-hit Ilnite	ADC 16-bit Channels	SPI	128	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC		USB FS HOST/OTG	IISB HS OTG	Ethernet	MDIO	3D GPU	TFT LCD	MIPI_DSI	SAI	SPUIERY	DESDM	DOM!	TenetiZene	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32MP153CAC3	Dual A7 & M4	650 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0	0	2 2	22 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	2	2	1	1 Gb]	1	N/A	1	0	4	4	8 -	l N	/A Y	ES	N/A	N/A	YES	YES	125 [Tj]
STM32MP153CAA3	Dual A7 & M4	650 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0	0 :	2 2	22 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	2	2	1	1 Gb]	1	N/A	1	0	4	4	8 1	l N	/A Y	ES	N/A	N/A	YES	YES	125 [Tj]
STM32MP153DAD1	Dual A7 & M4	800 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0	0 :	2 1	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1	1	1	N/A	1	0	4	4	8 1	N,	/A Y	ES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP153DAB1	Dual A7 & M4	800 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0	0 :	2 1	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1	1	1	N/A	1	0	4	4	8 1	N.	/A Y	ES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP153DAC1	Dual A7 & M4	800 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0	0 :	2 2	22 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 [(1 Gb]	1	N/A	1	0	4	4	8 1	N,	′A Y	ES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP153DAA1	Dual A7 & M4	800 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0	0 :	2 2	22 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 [(1 Gb]	1	N/A	1	0	4	4	8 1	N,	′A Y	ES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP153FAD1	Dual A7 & M4	800 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0	0 :	2 1	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1	1	1	N/A	1	0	4	4	8 1	N.	′A Y	ES	N/A	N/A	YES	YES	105 [Tj]
STM32MP153FAB1	Dual A7 & M4	800 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0	0 2	2 1	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1	1	1	N/A	1	0	4	4	8 -	l N	/A Y	ES	N/A	N/A	YES	YES	105 [Tj]
STM32MP153FAC1	Dual A7 & M4	800 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0	0	2 2	22 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1	1 Gb]	1	N/A	1	0	4	4	8	I N	/A Y	ES	N/A	N/A	YES	YES	105 [Tj]
STM32MP153FAA1	Dual A7 & M4	800 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0	0 :	2 2	22 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1	1 Gb]	1	N/A	1	0	4	4	8 1	N	/A Y	ES	N/A	N/A	YES	YES	105 [Tj]
				1		STM3	2MI	P15	7 D	ual	ARI	M®	Cor	tex [©]	-A7	7 +	Cortex®) -M	4 +	2xC	AN	FD	+ 3	D GI	PU +	- MI	PI-D	SI													
STM32MP157AAD3	Dual A7 & M4	650 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0	0	2 1	17 2	2 6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1	1	1	YES	1	1	4	4	8	I N	/A Y	ES	N/A	N/A	N/A	YES	125 [Tj]

Commercial Product Code	Core	Frequency (MHz)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 10-bit Changes	ADC 16_hit linite	ADC 16-bit Channels	SPI	128	M-SPI	12C	U(S)ART	CAN	SDIO SDIO	F(S)MC	SB	USB FS HOST/OTG	Ethernet	MDIO	3D GPU	TFT LCD	MIPI_DSI	SAI	SPNIFRX	DESDM	Irust.70ue	TRNG	טורטבני	ראא	PKA PKA	AFC/DEC	CHV/HWVC	T° Max (°C)
STM32MP157AAB3	Dual A7 & M4	650 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0 (0 2	2 1	17 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 1	1	YES	1	1	4	4	8 1	l N/	A YE	S N/	/A N	/A N	/A YE		125 [Tj]
STM32MP157AAC3	Dual A7 & M4	650 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0 () 2	2 2	22 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 [Gb]	1	YES	1	1	4	4	8 1	N/	A YE	S N/	/A N	/A N	/A YE		125 [Tj]
STM32MP157AAA3	Dual A7 & M4	650 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0 () 2	2 2	22 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 [Gb]	1	YES	1	1	4	4	8 1	N/	A YE	S N/	/A N	/A N	/A YE		125 [Tj]
STM32MP157CAD3	Dual A7 & M4	650 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0 () 2	2 1	17 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 1	1	YES	1	1	4	4	8 1	N/	A YE	S N/	/A N	/A YI	ES YE		125 [Tj]
STM32MP157CAB3	Dual A7 & M4	650 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0 () 2	2 1	17 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 1	1	YES	1	1	4	4	8 1	N/	A YE	S N/	/A N	/A YI	ES YE		25 [Tj]
STM32MP157CAC3	Dual A7 & M4	650 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0 () 2	2 2	22 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 [Gb]	1	YES	1	1	4	4	8 1	N/	4 YE	S N/	/A N	/A YI	ES YE	_S ¹	25 [Tj]
STM32MP157CAA3	Dual A7 & M4	650 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0 () 2	2 2	22 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 [Gb]	1	YES	1	1	4	4	8 1	N/	4 YE	S N/	/A N	/A YI	ES YE	ES 1 [25 [Tj]
STM32MP157DAD1	Dual A7 & M4	800 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0 () 2	2 1	7 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 1	1	YES	1	1	4	4	8 -	I N/	A YE	S N/	/A N	/A N	/A YE		105 [Tj]
STM32MP157DAB1	Dual A7 & M4	800 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0 () 2	2 1	17 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 1	1	YES	1	1	4	4	8	I N/	A YE	S N/	/A N	/A N	/A YE		105 [Tj]
STM32MP157DAC1	Dual A7 & M4	800 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0 () 2	2 2	22 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 1 [Gb]	1	YES	1	1	4	4	8 1	N/	A YE	S N/	/A N	/A N.	/A YE	->-	105 [Tj]
STM32MP157DAA1	Dual A7 & M4	800 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0 () 2	2 2	22 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2	1 [Gb]	1	YES	1	1	4	4	8 1	N/	A YE	S N/	/A N	/A N.	/A YE		105 [Tj]

Commercial Product Code	Core	Frequency (MHz)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	SPI	I2S	M-SPI	I2C	U(S)ART	CAN	SDIO SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Ethernet	MDIO	3D GPU	TFT LCD	MIPI_DSI	SAI	CONTRACTOR	DECOM	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32MP157FAD1	Dual A7 & M4	800 & 209	708	TFBGA257	98	1.7-3.6	17	2	2	5	0 0) 2	17	7 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2 1	1	1	YES	1	1	4 4	1 8	8 1	N/A	YES	N/A	N/A	YES	YES	105 [Tj]
STM32MP157FAB1	Dual A7 & M4	800 & 209	708	LFBGA354	98	1.7-3.6	17	2	2	5	0 0) 2	17	7 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2 1	1	1	YES	1	1	4 4	ļ {	3 1	N/A	YES	N/A	N/A	YES	YES	105 [Tj]
STM32MP157FAC1	Dual A7 & M4	800 & 209	708	TFBGA361	148	1.7-3.6	17	2	2	5	0 0) 2	22	2 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2 1	1 [Gb]	1	YES	1	1	4 4	1 8	3 1	N/A	YES	N/A	N/A	YES	YES	105 [Tj]
STM32MP157FAA1	Dual A7 & M4	800 & 209	708	LFBGA448	176	1.7-3.6	17	2	2	5	0 0) 2	22	2 2	6	3	2 [QUAD]	6	8	2 [FD]	3	1	0	2 1	1 [Gb]	1	YES	1	1	4 4	. 8	3 1	N/A	YES	N/A	N/A	YES	YES	105 [Tj]
									S	TM	32M	P13	1 A	RM	[®] Co	rte	x® -A7	+ 1	x Et	hern	et -	+ 1x	AE	C															
STM32MP131DAE7	Cortex -A7	1000	168	LFBGA289	135	1.71- 3.6	17	2	2	5	1 1	8 0	0	0	5	4	2 [QUAD]	5	8	0	2	1	0	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ /	4 0	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP131DAF7	Cortex -A7	1000	168	TFBGA320	135	1.71- 3.6	17	2	2	5	1 1	8 0	0	0	5	4	2 [QUAD]	5	8	0	2	1	0	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP131DAG7	Cortex -A7	1000	168	TFBGA289	135	1.71- 3.6	17	2	2	5	1 1	8 0	0	0	5	4	2 [QUAD]	5	8	0	2	1	0	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP131FAE7	Cortex -A7	1000	168	LFBGA289	135	1.71- 3.6	17	2	2	5	1 1	8 0	0	0	5	4	2 [QUAD]	5	8	0	2	1	0	1 1	1 [Gb]	0	N/A	0	0	2 4	1 4	4 0	YES	YES	YES	YES	YES	YES	105 [Tj]
STM32MP131FAF7	Cortex -A7	1000	168	TFBGA320	135	1.71- 3.6	17	2	2	5	1 1	8 0	0	0	5	4	2 [QUAD]	5	8	0	2	1	0	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	YES	S YES	YES	YES	105 [Tj]
STM32MP131FAG7	Cortex -A7	1000	168	TFBGA289	135	1.71- 3.6	17	2	2	5	1 1	8 0	0	0	5	4	2 [QUAD]	5	8	0	2	1	0	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	YES	YES	YES	YES	105 [Tj]
STM32MP131AAF3	Cortex -A7	650	168	TFBGA320	135	1.71- 3.6	17	2	2	5	1 1	8 0	0	0	5	4	2 [QUAD]	5	8	0	2	1	0	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ. 4	4 0	YES	YES	N/A	N/A	N/A	YES	125 [Tj]

Commercial Product Code	Core	Frequency (MHz)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Units	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	SPI	12S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC		USB HS OTG	Ethernet	MDIO	3D GPU	TFT LCD	MIPI_DSI	SAI	CONTRACTOR	DEMI	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32MP131AAG3	Cortex -A7	650	168	TFBGA289	135	1.71- 3.6	17	2	2	5	1 18	3 0	0	0	5	4	2 [QUAD]	5	8	0	2	1 ()	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP131CAE3	Cortex -A7	650	168	LFBGA289	135	1.71- 3.6	17	2	2	5	1 18	3 0	0	0	5	4	2 [QUAD]	5	8	0	2	1 ()	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	YES	YES	YES	YES	125 [Tj]
STM32MP131CAF3	Cortex -A7	650	168	TFBGA320	135	1.71- 3.6	17	2	2	5	1 18	3 0	0	0	5	4	2 [QUAD]	5	8	0	2	1 ()	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	YES	YES	YES	YES	125 [Tj]
STM32MP131CAG3	Cortex -A7	650	168	TFBGA289	135	1.71- 3.6	17	2	2	5	1 18	3 0	0	0	5	4	2 [QUAD]	5	8	0	2	1 ()	1 1	1 [Gb]	0	N/A	0	0	2 4	ļ. Z	4 0	YES	YES	YES	YES	YES	YES	125 [Tj]
								STIV	1321	MP1	133 A	RM	® C	orte	K® -	A7	+ 2x Et	ther	net	+ 2)	(AE)C +	2 x	CAN	I FD														
STM32MP133DAE7	Cortex -A7	1000	168	LFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 ()	1 1	2 [Gb]	0	N/A	0	0	2 4	ļ /	4 0	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP133DAF7	Cortex -A7	1000	168	TFBGA320	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 ()	1 1	2 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP133DAG7	Cortex -A7	1000	168	TFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 ()	1 1	2 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP133FAE7	Cortex -A7	1000	168	LFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 ()	1 1	2 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	YES	YES	YES	YES	105 [Tj]
STM32MP133FAF7	Cortex -A7	1000	168	TFBGA320	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 ()	1 1	2 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	YES	YES	YES	YES	105 [Tj]
STM32MP133FAG7	Cortex -A7	1000	168	TFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 ()	1 1	2 [Gb]	0	N/A	0	0	2 4	ļ 4	4 0	YES	YES	YES	YES	YES	YES	105 [Tj]
STM32MP133AAE3	Cortex -A7	650	168	LFBGA289	135	1.71- 3.6	17	2	2	5	2 37	0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 ()	1 1	2 [Gb]	0	N/A	0	0	2 4	ļ. 4	4 0	YES	YES	N/A	N/A	N/A	YES	125 [Tj]

Commercial Product Code	Core	Frequency (MHz)	RAM (Kbytes)	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Channels ADC 12-bit Units	ADC 16-bit Units	ADC 16-bit Channels	DAC 12-bit Channels	SPI	2S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	IICR Device	USB ES HOST/OTG	Ethernet	MDIO	3D GPU	TFT LCD	MIPI_DSI	SPUIFKX	DESDM	DCMI	Trust'Zone	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32MP133AAF3	Cortex -A7	650	168	TFBGA320	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	0	0 2	2 4	4	0	YES	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP133AAG3	Cortex -A7	650	168	TFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	0	0 2	2 4	4	0	YES	YES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP133CAE3	Cortex -A7	650	168	LFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	0	0 2	2 4	4	0	YES	YES	YES	YES	YES	YES	125 [Tj]
STM32MP133CAF3	Cortex -A7	650	168	TFBGA320	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 (0	1 1	2 [Gb]	0	N/A	0	0 2	2 4	4	0	YES	YES	YES	YES	YES	YES	125 [Tj]
STM32MP133CAG3	Cortex -A7	650	168	TFBGA289	135	1.71- 3.6	17	2	2	5	2 37	0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1 (0	1 1	2 [Gb]	0	N/A	0	0 2	2 4	4	0	YES	YES	YES	YES	YES	YES	125 [Tj]
					S	TM32N	MP1	35 <i>i</i>	ARN	И® (Corte	K® -	A7	+ 2>	Ett	ieri	net + 2	хΑ	DC -	+ 2x	CAI	N FD	+	LCD.	·TFT 4	- Ca	mera												
STM32MP135DAE7	Cortex -A7	1000	168	LFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	1	0 2	2 4	4	1	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP135DAF7	Cortex -A7	1000	168	TFBGA320	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	1	0 2	2 4	4	1	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP135DAG7	Cortex -A7	1000	168	TFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	1	0 2	2 4	4	1	YES	YES	N/A	N/A	N/A	YES	105 [Tj]
STM32MP135FAE7	Cortex -A7	1000	168	LFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	1	0 2	2 4	4	1	YES	YES	YES	YES	YES	YES	105 [Tj]
STM32MP135FAF7	Cortex -A7	1000	168	TFBGA320	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	1	0 2	2 4	4	1	YES	YES	YES	YES	YES	YES	105 [Tj]
STM32MP135FAG7	Cortex -A7	1000	168	TFBGA289	135	1.71- 3.6	17	2	2	5	2 37	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [Gb]	0	N/A	1	0 2	2 4	4	1	YES	YES	YES	YES	YES	YES	105 [Tj]

Commercial Product Code	Core	Frequency (MHz)	RAM (Kbytes)	Package	0 I	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	mer	AUG 12-bit Unanneis	AUC 16-bit Units	ADC 16 hit libits	DAC 12-bit Channels	SPI	I2S	M-SPI	120	U(S)ART	CAN	SDIO	F(S)MC	USB Device	USB FS HOST/OTG	Emeller	MDIO	3D GPU	TFT LCD	MIPI_DSI	SAI	SPDIFRX	DESDM	IIUSEZONE	Tero+17ono	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	T° Max (°C)
STM32MP135AAE3	Cortex -A7	650	168	LFBGA289	135	1.71- 3.6	17	2	2	5	2 3	7 0	0 0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [G		N/A	1	0	2	4	4 1	I YE	ES Y	ES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP135AAF3	Cortex -A7	650	168	TFBGA320	135	1.71- 3.6	17	2	2	5	2 3	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	[G		N/A	1	0	2	4	4 1	YE	ES Y	ES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP135AAG3	Cortex -A7	650	168	TFBGA289	135	1.71- 3.6	17	2	2	5	2 3	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [G		N/A	1	0	2	4	4 1	YE	S Y	ES	N/A	N/A	N/A	YES	125 [Tj]
STM32MP135CAE3	Cortex -A7	650	168	LFBGA289	135	1.71- 3.6	17	2	2	5	2 3	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	2 [G		N/A	1	0	2	4	4 1	YE	S Y	ES '	YES	YES	YES	YES	125 [Tj]
STM32MP135CAF3	Cortex -A7	650	168	TFBGA320	135	1.71- 3.6	17	2	2	5	2 3	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	[G		N/A	1	0	2	4	4 1	YE	S Y	ES \	YES	YES	YES	YES	125 [Tj]
STM32MP135CAG3	Cortex -A7	650	168	TFBGA289	135	1.71- 3.6	17	2	2	5	2 3	7 0	0	0	5	4	2 [QUAD]	5	8	2 [FD]	2	1	0	1 1	[G		N/A	1	0	2	4	4 1	YE	ES Y	ES \	YES	YES	YES	YES	125 [Tj]

STM32 MP2系列 -Arm® Cortex®-A35 + M33/Cortex®-A35 + M33 + NPU 64位工业级边缘AI MPU

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Commercial Product Code	Core	Frequency (MHz)	RAM	Package	10	VDD	Timer (16-bit)	Timer (29-hit)	Advanced Times (16 bit)	ADC 12-bit Units	ADC 12-bit Channels	SPI	I2S	M-SPI	120	UART	U(S)ART	CAN	SDIO	F(S)MC	IISB 2 0 HS HOST	PCle 2.0	Ethernet	JPEG Codec	HW_Video_Codec	NPU	3D GPU	TFT LCD	MIPI_DSI	LVDS	MIPI OSI	SAI	SPDIFRX	Trust'Zone	TRNG	OTFDEC	RSA/ECC	SHA/HMAC AES/DES	T° Max (°C)
								STI	VI32	MP2	251 /	ARN	N® (Cortex	® -A	35	+ C	orte	χ®	-M3	3 +	1x	Ether	net															
STM32MP251AAI3	A35 & M33	1200 & 400	808	TFBGA436	172	1.71~ 3.6	10			5 3			2	2 [0CT0]			4	0			1 1		1[Gb &TSN]	N	N	N	1	0	N	1 -	1 4	4	Υ	Υ	N	N	N Y	125 [Tj]
STM32MP251AAK3	A35 & M33	1200 & 400	808	VFBGA424	144	1.71~ 3.6	10	4 ;	3 5	5 3	21	8	3	2 [0CT0]	8	6	4	0	3	1	1 1	1 1	1[Gb &TSN] N	N	N	N	1	0	N	1 -	1 4	4	Υ	Υ	N	N	N Y	125 [Tj]
STM32MP251AAL3	A35 & M33	1200 & 400	808	VFBGA361	144	1.71~ 3.6	10	4 :	3 5	5 3	23	8	3	2 [0CT0]	8	6	4	0	3	1	1 1	1	1[Gb &TSN] N	N	N	N	1	0	N	1 -	1 4	4	Υ	Υ	N	N	N Y	125 [Tj]
STM32MP251CAI3	A35 & M33	1200 & 400	808	TFBGA436	172	1.71~ 3.6	10	4 :	3 5	5 3	23	8	3	2 [OCTO]	8	6	4	0	3	1	1 1	1 1	1[Gb &TSI		N	N	N	1	0	N	1	1 4	4	Υ	Υ	Υ	Υ	Y	125 [Tj]
STM32MP251CAK3	A35 & M33	1200 & 400	808	VFBGA424	144	1.71~ 3.6	10	4 ;	3 5	5 3	21	8	3	2 [0CT0]	8	6	4	0	3	1	1 1	1 1	1[Gb &TSI		N	N	N	1	0	N	1	1 4	4	Υ	Υ	Υ	Υ	Y	125 [Tj]
STM32MP251CAL3	A35 & M33	1200 & 400	808	VFBGA361	144	1.71~ 3.6	10	4 :	3 5	5 3	23	8	3	2 [OCTO]	8	6	4	0	3	1	1 1	1 1	1[Gb &TSI] N	N	N	N	1	0	N	1	1 4	4	Υ	Υ	Υ	Υ	Y	125 [Tj]
STM32MP251DAI3	A35 & M33	1500 & 400	808	TFBGA436	172	1.71~ 3.6	10	4 :	3 5	5 3	23	8	3	2 [OCTO]	8	6	4	0	3	1	1 1	1 1	1[Gb &TSI] N	N	N	N	1	0	N	1	1 4	4	Υ	Υ	N	N	N Y	125 [Tj]
STM32MP251DAK3	A35 & M33	1500 & 400	808	VFBGA424	144	1.71~ 3.6	10	4 ;	3 5	5 3	21	8	3	2 [OCTO]	8	6	4	0	3	1	1 1	1	1[Gb &TSN		N	N	N	1	0	N	1	1 4	4	Υ	Υ	N	N	N Y	125 [Tj]
STM32MP251DAL3	A35 & M33	1500 & 400	808	VFBGA361	144	1.71~ 3.6	10	4 ;	3 5	5 3	23	8	3	2 [0CT0]	8	6	4	0	3	1	1 1	1	1[Gb &TSN		N	N	N	1	0	N	1	1 4	4	Υ	Υ	N	N	N Y	125 [Tj]
STM32MP251FAI3	A35 & M33	1500 & 400	808	TFBGA436	172	1.71~ 3.6	10	4 ;	3 5	5 3	23	8	3	2 [OCTO]	8	6	4	0	3	1	1 1	1	1[Gb &TSN] N	N	N	N	1	0	N	1 -	1 4	4	Υ	Υ	Υ	Υ	YY	125 [Tj]
STM32MP251FAK3	A35 & M33	1500 & 400	808	VFBGA424	144	1.71~ 3.6	10	4 ;	3 5	5 3	21	8	3	2 [OCTO]	8	6	4	0	3	1	1 1	l 1	1[Gb &TSN	I I V	N	N	N	1	0	N	1	1 4	4	Υ	Υ	Υ	Υ	Y	125 [Tj]

STM32 MP2系列 -Arm® Cortex®-A35 + M33/Cortex®-A35 + M33 + NPU 64位工业级边缘AI MPU

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Commercial Product Code	Core	Frequency (MHz)	RAM	Package	I0	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	LPTimer	ADC 12-bit Channels ADC 12-bit Units	SPI	128	M-SPI	12C	ÜÄRT	U(S)ART	CAN	SDI0	F(S)MC	USB 2.0 HS HOST	USB 2.0/3.0 OTG/DRD	BCIS 2.0	Ethernet	JPEG Codec	HW_Video_Codec	NPU	3D GPU	TFT LCD	MIPI_DSI	LVDS	MIPI_CSI	DCMI	SAI	SPDIFRX	Trust'Zone	TRNG	OTEDEC	AES/DES	SHA/HMAC	T° Max (°C)
STM32MP251FAL3	A35 & M33	1500 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [0CT0]	8	6	4	0	3	1	1	1 1	1 8	1[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Ϋ́	Υ	ΥY	ΥY	Υ	125 [Tj]
						STM	32 V	IP2	53 I	Dua	I ARI	M® (Cor	tex® -A	35	+ C	orte	ex® ·	-M3	33 +	- 2>	c Eth	eri	net + :	3x (CAN FI	0														
STM32MP253AAI3	Dual A35 & M33	1200 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [0CT0]	8	6	4	3 [FD]	3	1	1	1 1	1 2	2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Υ,	Υ	N N	N N	Υ	125 [Tj]
STM32MP253AAK3	Dual A35 & M33	1200 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3	5	3 21	8	3	2 [OCTO]	8	6	4	3 [FD]	3	1	1	1 1	1	2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Υ,	Υ	N N	N N	Υ	125 [Tj]
STM32MP253AAL3	Dual A35 & M33	1200 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [0CT0]	8	6	4	3 [FD]	3	1	1	1 1		2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Υ,	Υ	N N	N N	Υ	125 [Tj]
STM32MP253CAI3	Dual A35 & M33	1200 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [0CT0]	8	6	4	3 [FD]	3	1	1	1 1		2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Ϋ́	Υ	ΥY	γY	Y	125 [Tj]
STM32MP253CAK3	Dual A35 & M33	1200 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3	5	3 21	8	3	2 [0CT0]	8	6	4	3 [FD]	3	1	1	1 1		2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	γ,	Υ	Y	ΥY	Υ	125 [Tj]
STM32MP253CAL3	Dual A35 & M33	1200 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [OCTO]	8	6	4	3 [FD]	3	1	1	1 1	1	2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Ϋ́	Υ	Y۱	ΥY	Υ	125 [Tj]
STM32MP253DAI3	Dual A35 & M33	1500 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [OCTO]	8	6	4	3 [FD]	3	1	1	1 1		2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Υ,	Υ	N I	N N	Υ	125 [Tj]
STM32MP253DAK3	Dual A35 & M33	1500 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3	5	3 21	8	3	2 [0CT0]	8	6	4	3 [FD]	3	1	1	1 1		2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Υ,	Υ	N I	N N	Υ	125 [Tj]
STM32MP253DAL3	Dual A35 & M33	1500 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [0CT0]	8	6	4	3 [FD]	3	1	1	1 1	1	2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Υ,	Υ	N N	N N	Υ	125 [Tj]
STM32MP253FAI3	Dual A35 & M33	1500 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [0CT0]	8	6	4	3 [FD]	3	1	1	1 1		2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4	Υ,	Υ	Y۱	YY	Υ	125 [Tj]

STM32 MP2系列 -Arm® Cortex®-A35 + M33/Cortex®-A35 + M33 + NPU 64位工业级边缘AI MPU

STIVISZ IVIFZ	1473 1	711111	UU	II LEX -A	JJ	T IVIO	J/ C	וטי	lG/	\ _/	700	Ţ	. 14	เงง +	14	ı	0-	+ 177	_	-111-		, 100	53KAI	IVI	1 0														
Commercial Product Code	Core	Frequency (MHz)	RAM	Package	I0	VDD	Timer (16-bit)	Timer (32-hit)	Advanced Timer (16-bit)	ADC 12-bit Units	ADC 12-bit Channels	SPI	I2S	M-SPI	I2C	UART	U(S)ART	CAN	SDIO	E/S/MC		PCle 2.0	Ethernet	JPEG Codec	HW_Video_Codec	NPU	3D GPU	TFT LCD	MIPI_DSI	LVDS	MIPI CSI	DCMI	SAI	SPDIERY Irust'Zone	TRNG	OTFDEC	RSA/ECC	AES/DES	T° Max (°C)
STM32MP253FAK3	Dual A35 & M33	1500 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3	5 3	21	8	3	2 [0CT0]	8	6	4 [3 [FD]	3	1 1	1 1	1	2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4 Y	ſΥ	Y	Υ	Υ	Y 125 [Tj]
STM32MP253FAL3	Dual A35 & M33	1500 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5 3	23	8	3	2 [0CT0]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	N	N	N	N	1	0	N	1	1	4	4 Y	Υ	Y	Υ	Υ	Y 125 [Tj]
				STM32MP	255	Dual AF	RM®	Cor	tex	® -A	35 +	- Cc	orte	x® -M3	3 +	2x	Eth	erne	et +	3x (CAN	FD	+ H.26	64 +	3D GP	U + AI	/ NN ·	+LV	DS										
STM32MP255AAI3	Dual A35 & M33	1200 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3	5 3	23	8	3	2 [0CT0]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1	1	4	4 Y	! Y	N	N	N	Y 125 [Tj]
STM32MP255AAK3	Dual A35 & M33	1200 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3 !	5 3	21	8	3	2 [0CT0]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1	1	4	4 Y	′ Y	N	N	N	Y 125 [Tj]
STM32MP255AAL3	Dual A35 & M33	1200 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5 3	23	8	3	2 [0CT0]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1	1	4	4 Y	Υ	N	N	N	Y 125 [Tj]
STM32MP255CAI3	Dual A35 & M33	1200 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3	5 3	23	8	3	2 [OCTO]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1	1	4	4 Y	Υ	Y	Υ	Υ	Y 125 [Tj]
STM32MP255CAK3	Dual A35 & M33	1200 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3	5 3	21	8	3	2 [0CT0]	8	6	4 [3 [FD]	3	1 1	1 1	1	2[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1	1	4	4 Y	Υ	Y	Υ	Υ	Y 125 [Tj]
STM32MP255CAL3	Dual A35 & M33	1200 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5 3	23	8	3	2 [0CT0]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1	1	4	4 Y	′ Y	Υ	Υ	Υ	Y 125 [Tj]
STM32MP255DAI3	Dual A35 & M33	1500 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3	5 3	23	8	3	2 [OCTO]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1	1	4	4 Y	′ Y	N	N	N	Y 125 [Tj]
STM32MP255DAK3	Dual A35 & M33	1500 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3	5 3	21	8	3	2 [OCTO]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1	1	4	4 Y	′ Y	N	N	N	Y 125 [Tj]
STM32MP255DAL3	Dual A35 & M33	1500 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5 3	23	8	3	2 [0CT0]	8	6	4 [3 [FD]	3	1 1	1	1	2[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1	1	4	4 Y	ſ Y	N	N	N	Y 125 [Tj]

STM32 MP2系列 -Arm® Cortex®-A35 + M33/Cortex®-A35 + M33 + NPU 64位工业级边缘AI MPU

																	-																					
Commercial Product Code	Core	Frequency (MHz)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-hit)	Advanced Timer (16-hit)	ADC 12-bit Units	ADC 12-bit Channels	SPI	M-SPI	120	UART	U(S)ART	CAN	SDIO SDIO	F/SIMC	USB 2.0/3.0 UI G/UKU	PCle 2.0	Ethernet	JPEG Codec	HW_Video_Codec	NPU	3D GPU	TFT LCD	MIPI_DSI	LVDS	MIPI CSI	SAI	SPDIFRX	Trust'Zone	TRNG	OTENEC:	AES/DES	SHA/HMAC	T° Max (°C)
STM32MP255FAI3	Dual A35 & M33	1500 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3 5	3	23	8	2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	2[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	ΥY	Υ		125 [Tj]
STM32MP255FAK3	Dual A35 & M33	1500 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3 5	3	21	8	3 2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	2[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	Y۱	Υ	· •	125 [Tj]
STM32MP255FAL3	Dual A35 & M33	1500 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3 5	3	23	8	2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	2[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	ΥY	Υ	1 Y I	125 [Tj]
		ST	M32	MP257 Du	al AF	RM® Co	rtex	B -	\35 -	+ Co	rtex	® -N	133 + 3)	Eth	err	iet (2	2+1	sw	itch) +	3x C	AN FC) +H	.264 +	3D GPI	J + A	I / N	IN 4	- LV	DS								
STM32MP257AAI3	Dual A35 & M33	1200 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3 5	3	23	8	2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	3[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	N N	N N	1 Y I	125 [Tj]
STM32MP257AAK3	Dual A35 & M33	1200 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3 5	3	21	8	2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	3[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	N N	N N	Y	125 [Tj]
STM32MP257AAL3	Dual A35 & M33	1200 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3 5	3	23	8	3 2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	3[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	N N	N N		125 [Tj]
STM32MP257CAI3	Dual A35 & M33	1200 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3 5	3	23	8	3 2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	3[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	ΥY	Υ		125 [Tj]
STM32MP257CAK3	Dual A35 & M33	1200 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3 5	3	21	8	3 2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	3[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	ΥY	Υ	Y	125 [Tj]
STM32MP257CAL3	Dual A35 & M33	1200 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3 5	3	23	8	2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	3[Gb] &TSN	Υ	H264/ VP8	1.2 TOPS	YES	1	1	1	1 1	4	. 4	Υ	Υ	ΥY	Υ		125 [Tj]
STM32MP257DAI3	Dual A35 & M33	1500 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3 5	3	23	8	2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	3[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	. 4	Υ	Υ	N N	N N	· v	125 [Tj]
STM32MP257DAK3	Dual A35 & M33	1500 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3 5	3	21	8	3 2 [OCTO	8	6	4	3 [FD]	3	1 1	1 1	1	3[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	N N	N N		125 [Tj]

STM32 MP2系列 -Arm® Cortex®-A35 + M33/Cortex®-A35 + M33 + NPU 64位工业级边缘AI MPU

Commercial Product Code	Core	Frequency (MHz)	RAM	Package	10	VDD	Timer (16-bit)	Timer (32-bit)	Advanced Timer (16-bit)	I PTimer	ADC 12-bit Channels	SPI	I2S	M-SPI	120	UART	U(S)ART	CON	SDIO SDIO	USB 2.0 HS HOST	USB 2.0/3.0 OTG/DRD	PCle 2.0	Ethernet	JPEG Codec	HW_Video_Codec	NPU	3D GPU	TFT LCD	MIPI_DSI		DCMI	SAI	SPDIFRX	Trust'Zone	TRNG	NTEDEC OTEDEC	AES/DES	SHA/HMAC	To May (of)
STM32MP257DAL3	Dual A35 & M33	1500 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [0CT0]	8	6		3 [D]	3 1	1 1	1		3[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	4	Υ	ΥI	N N	I N	Y	25 Tj]
STM32MP257FAI3	Dual A35 & M33	1500 & 400	808	TFBGA436	172	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [OCTO]	8	6	/	3 [D]	3 1	1 1	1		3[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	4	Υ	γ,	ΥΥ	Y	Υ	25 Tj]
STM32MP257FAK3	Dual A35 & M33	1500 & 400	808	VFBGA424	144	1.71~ 3.6	10	4	3	5	3 21	8	3	2 [OCTO]	8	6	/	3 :D]	3 1	1 1	1		3[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	4	Υ	γ,	YY	Υ		25 Tj]
STM32MP257FAL3	Dual A35 & M33	1500 & 400	808	VFBGA361	144	1.71~ 3.6	10	4	3	5	3 23	8	3	2 [OCTO]	8	6	4 [F	3 [D]	3 1	1 1	1	1	3[Gb] &TSN	Υ	H264/ VP8	1.35 TOPS	YES	1	1	1	1 1	4	4	Υ	Υ	YY	Y	γ 1 [25 Tj]

*Y=Yes, N=N/A

缩写和封装

缩写

ADC : Analog-to-digital converter

ART : ART Accelerator

ATAPI : AT attachment packet interface

AWU : Auto wake-up from halt

BLPD : Byte level protocol decoder

BOD : Brown-out detector

CAN : Controller area network

CAPCOM: Capture compare

CSS : Clock security system

DALI : Digital addressable lighting interface

DDC : Data display channel

DiSEqC : Digital satellite equipment control

DMA : Direct memory access
DSC : Dual supply control

DTC : Data transfer coprocessor

ETM : Embedded trace macrocell

EMI : External memory interface

HDLC : High-level data link control IAP : In-application programming

IC/OC : Input capture/output compare

ICP : In-circuit programming

IR : Infrared

IrDA : Infrared data association

ISP : In-system programming

 I^2C : Inter-integrated circuit

I²S : Inter-IC sound

LCD : Liquid crystal display

LIN : Local interconnect network

LVD : Low voltage detection

MAC : Media access control

MC : Motor control

MFT : Multifunction timer

MMC : MultiMediaCard

NMI : Non-maskable interrupt

OSG : Oscillator safeguard

PCA : Programmable counter array

PDR : Power-down reset

PHW : Programmable halt wake-up

PEC : Peripheral event controller

PLD : Programmable logic device

PLL : Phase locked loop

POR : Power-on reset

PVD : Programmable voltage detector

PVR : Programmable voltage regulator

PWM : Pulse width modulation

ROP : Readout protection

RTC : Real-time clock timer

SAI : Serial Audio Interface

SC : Smartcard

SCI : Serial communication interface

SCR : Smartcard reader

SDIO : Secure digital input output

SDMMC : Secure Digital / Multi Media Card

SMI : Serial memory interface

SPI : Serial peripheral interface

SSC : Single-cycle switching support

SSP : Synchronous serial port

TBU : Time base unit

TLI : Top level interrupt

UART : Universal asynchronous receiver transmitter

USART : Universal sync/async receiver transmitter

USB : Universal Serial Bus

WDG : Watchdog timer

WWDG : Window watchdog timer

封装

SO : plastic Small Outline package

TSSOP : Thin Small Outline Package

LFBGA : Low Profile Fine Pitch Ball Grid Array

UFBGA : Ultra Thin Profile Fine Pitch Ball Grid Array

TFBGA : Thin Profile Fine Pitch Ball Grid Array

SiP-LGA: System in Package Land Grid Array

LQFP : Low Profile Quad Flat Package

UFQFN : Ultra thin Fine pitch Quad Flat No-lead

UFQFPN: Ultra thin Fine pitch Quad Flat Packages No-lead

VFQFPN: Very thin Fine pitch Quad Flat Packages No-lead

WLCSP : Wafer-Level Chip-Scale Package

STM32 产品型号(仅适用于MCU)



STM32MPU产品型号



SPIRIT 产品型号

订购代码	封装	包装
SPIRIT1QTR	QFN20	卷带包装
S2-LPQTR	QFN24 4x4x1	卷带包装
S2-LPCBQTR	QFN24 4x4x1	卷带包装

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ST大学计划联络邮箱: edu.china@st.com

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