

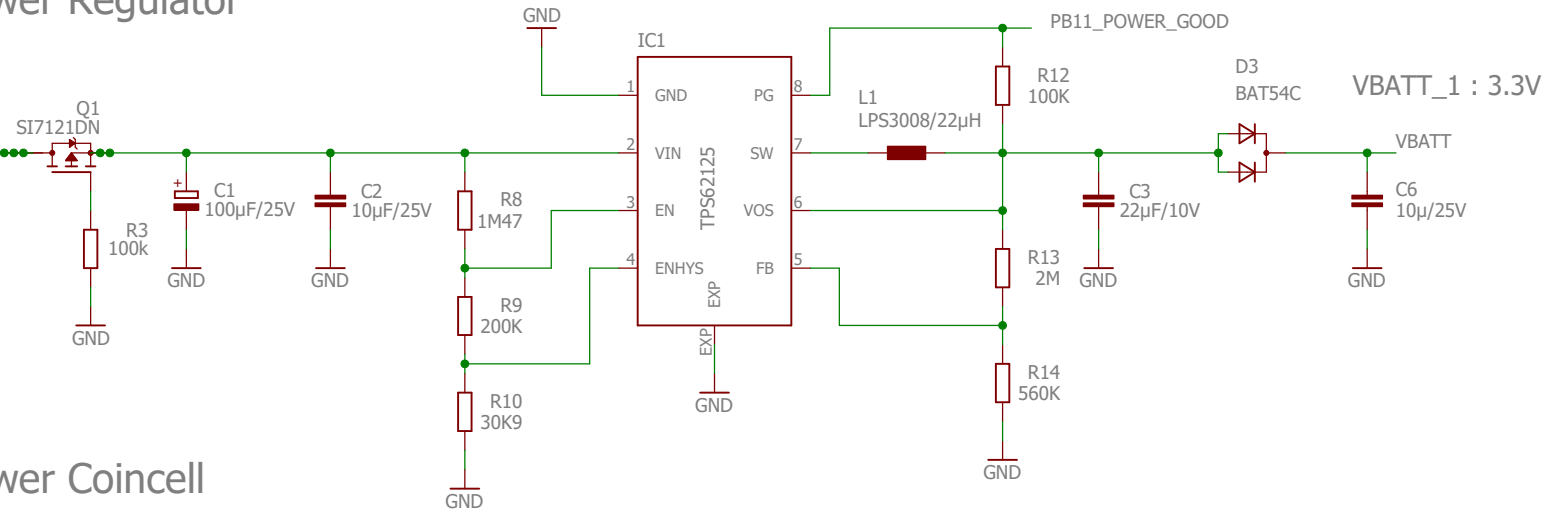
EFM32 Power Regulator

braun

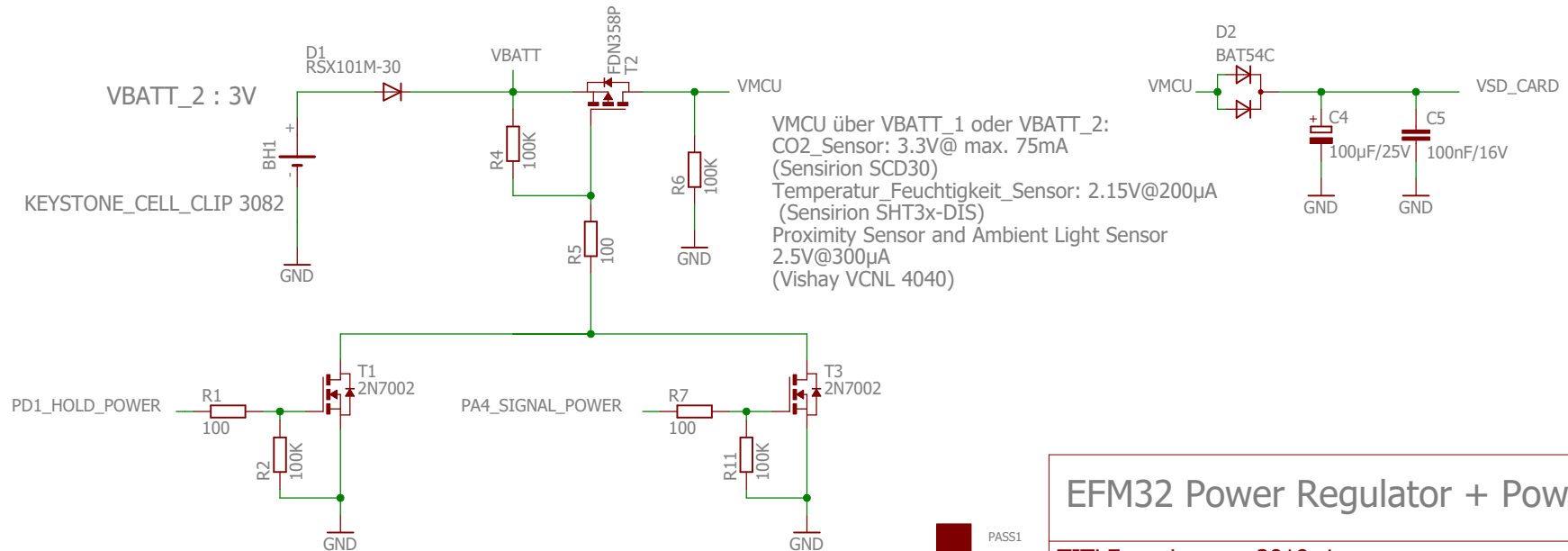
BATT_1_GND X1-1 1

BATT_1_INPUT X1-2 2
MKDS1/2-3,81

rot



EFM32 Power Coincell



EFM32 Power Regulator + Power Coincell

TITLE: Logger_2019_1

Document Number:

REV:

Date: 28.03.2019 15:02

Sheet: 1/6

EFM32 I/O + POWER

PA0_SENSOR_I2C_SDA	1	EFM32G230F128	PA0/I2C0_SDA#0/TIM0_CC0#0,1
PA1_SENSOR_I2C_SCL	2		PA1/CMU_CLK1#0/I2C0_SCL#0/TIM0_CC1#0,1
PA2_POWER_LED	3		PA2/CMU_CLK0#0/TIM0_CC2#0,1
PA3_KEY_START_STOP	4		PA3/TIM0_CDTI0#0
PA4_SIGNAL_POWER	5		PA4/TIM0_CDTI1#0
PA5_LOG_FLUSH_LED	6		PA5/LEU1_TX#1/TIM0_CDTI2#0
	7		PA6/LEU1_RX#1
	17		PA8/TIM2_CC0#0
	18		PA9/TIM2_CC1#0
	19		PA10/TIM2_CC2#0

IC2A

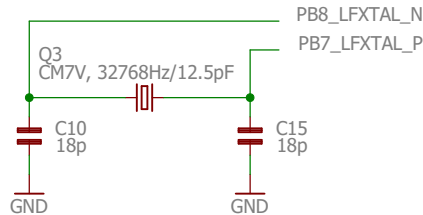
PB7_LFXTAL_P	15	EFM32G230F128	PB7/LFXTAL_P/US1_CLK#0
PB8_LFXTAL_N	16		PB8/LFXTAL_N/US1_CS#0
PB11_POWER_GOOD	21		PB11/DAC0_OUT0/OPAMP_OUT0/LETIM0_OUT0#1
	22		PB12/DAC0_OUT1/OPAMP_OUT1/LETIM0_OUT1#1
PB13_HFXTAL_P	24		PB13/HFXTAL_P/LEU0_TX#1
PB14_HFXTAL_N	25		PB14/HFXTAL_N/LEU0_RX#1

IC2B

PE9_SENSOR1_MOSFET	56	EFM32G230F128	PE8/PCNT2_S0IN#1
PE10_US0_TX	57		PE9/PCNT2_S1IN#1
PE11_US0_RX	58		PE10/TIM1_CC0#1/US0_TX#0
PE12_SENSOR1_ADDR	59		PE11/TIM1_CC1#1/US0_RX#0
PE13_SENSOR1_ALERT	60		PE12/TIM1_CC2#1/US0_CLK#0
PE14_SENSOR1_RESET	61		PE13/ACMP0_O/US0_CS#0
PE15_SENSOR1_RESERVE	62		PE14/LEU0_TX#2
	63		PE15/LEU0_RX#2

IC2E

CL f. 32KHz crystal = 22pF as in STK3600



PC9_SENSOR2_MOSFET	41
PC10_SENSOR2_ADDR	42
PC11_SENSOR2_ALERT	43
PC12_SD_PULLUP	44
PC13_SENSOR2_RESET	45
PC14_SENSOR2_RESERVE	46

PC0_US1_TX	9
PC1_US1_RX	10
PC2_SD_SDO	11
PC3_SD_SDI	12
PC4_SD_CLK	13
PC5_SD_CS	14
PC6_SD_CD	37
PC7_SD_WP	38

EFM32G230F128

PC0/ACMP0_CH0/PCNT0_S0IN#2/US1_TX#0	41
PC1/ACMP0_CH1/PCNT0_S1IN#2/US1_RX#0	42
PC2/ACMP0_CH2/US2_TX	43
PC3/ACMP0_CH3/US2_RX	44
PC4/ACMP0_CH4/LETIM0_OUT0#3/PCNT1_S0IN/US2_CLK	45
PC5/ACMP0_CH5/LETIM0_OUT1#3/PCNT1_S1IN/US2_CS	46
PC6/ACMP0_CH6/I2C0_SDA#2/LEU1_TX#0	47
PC7/ACMP0_CH7/I2C0_SCL#2/LEU1_RX#0	48
PC8/ACMP1_CH0/TIM2_CC0#2/US0_CS#2	
PC9/ACMP1_CH1/TIM2_CC1#2/US0_CLK#2	
PC10/ACMP1_CH2/TIM2_CC2#2/US0_RX#2	
PC11/ACMP1_CH3/US0_TX#2	
PC12/ACMP1_CH4/CMU_CLK0#1	
PC13/ACMP1_CH5/PCNT0_S0IN#0/TIM0_CDTI0#1,3/TIM1_CC0#0	
PC14/ACMP1_CH6/PCNT0_S1IN#0/TIM0_CDTI1#1,3/TIM1_CC1#0	
PC15/ACMP1_CH7/DBG_SWO#1/TIM0_CDTI2#1,3/TIM1_CC2#0	

IC2C

PD0_SD_POWER	28
PD1_HOLD_POWER	29
PD2_PULS1_COUNTER	30
PD3_PULS2_COUNTER	31
PD4_LEUART_TX	32
PD5_LEUART_RX	33
PD6_SENSOR_I2C_SDA	34
PD7_SENSOR_I2C_SCL	35

EFM32G230F128

PD0/ADC0_CH0/PCNT2_S0IN#0/US1_TX#1	36
PD1/ADC0_CH1/PCNT2_S1IN#0/TIM0_CC0#3/US1_RX#1	
PD2/ADC0_CH2/TIM0_CC1#3/US1_CLK#1	
PD3/ADC0_CH3/TIM0_CC2#3/US1_CS#1	
PD4/ADC0_CH4/LEU0_TX#0	
PD5/ADC0_CH5/LEU0_RX#0	
PD6/ADC0_CH6/I2C0_SDA#1/LETIM0_OUT0#0	
PD7/ADC0_CH7/I2C0_SCL#1/LETIM0_OUT1#0	
PD8/CMU_CLK1#1	

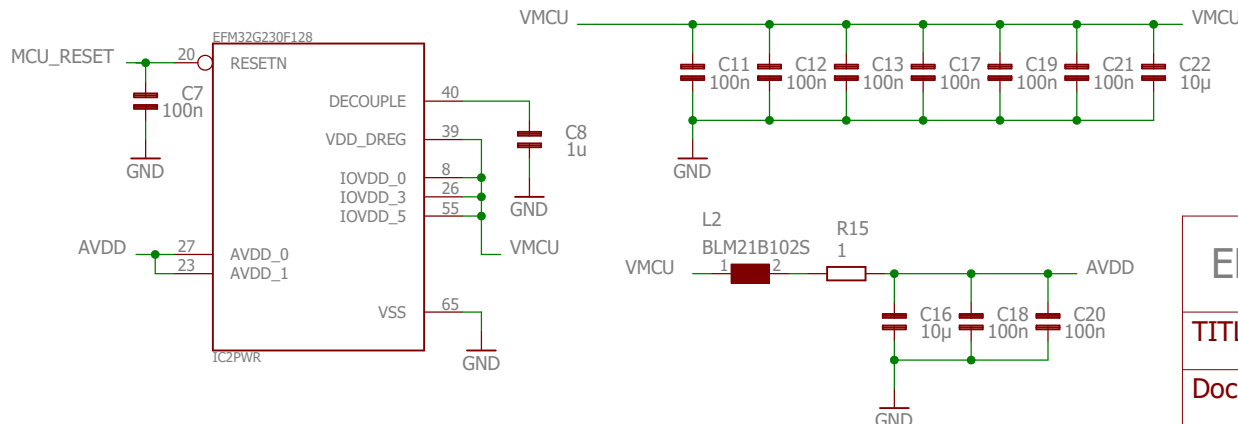
IC2D

PF0_DBG_SWCLK	49
PF1_DBG_SWDIO	50
PF2_DBG_SWO	51

EFM32G230F128

PF0/DBG_SWCLK#0,1/LETIM0_OUT0#2	52
PF1/DBG_SWDIO#0,1/LETIM0_OUT1#2	53
PF2/ACMP1_O/DBG_SWO#0	54
PF3/TIM0_CDTI0#2	
PF4/TIM0_CDTI1#2	
PF5/TIM0_CDTI2#2	

IC2F



EFM32 I/O + POWER

TITLE: Logger_2019_1

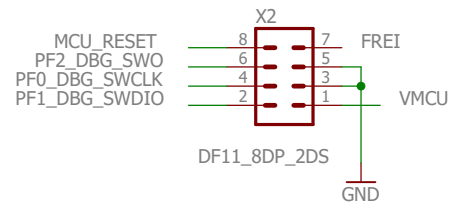
Document Number:

REV:

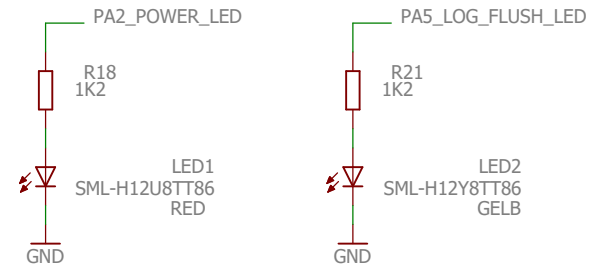
Date: 28.03.2019 15:02

Sheet: 2/6

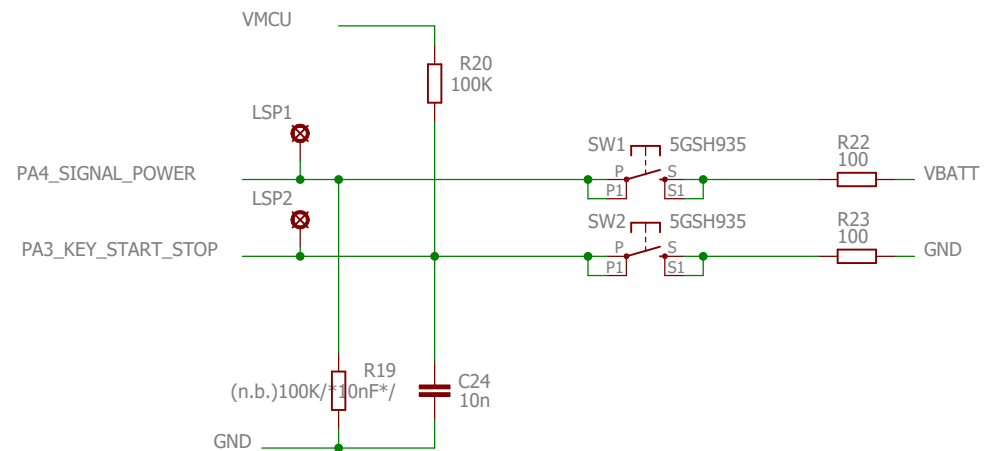
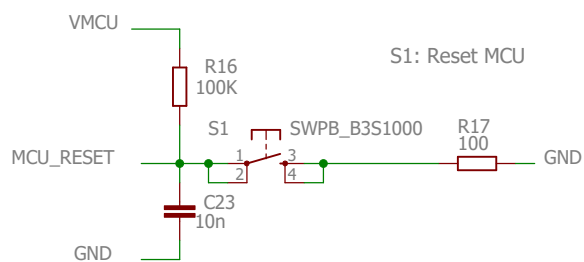
DEBUG INTERFACE



LED's



PUSHBUTTONS



DEBUG INTERFACE + LED's + PUSHBUTTONS

TITLE: Logger_2019_1

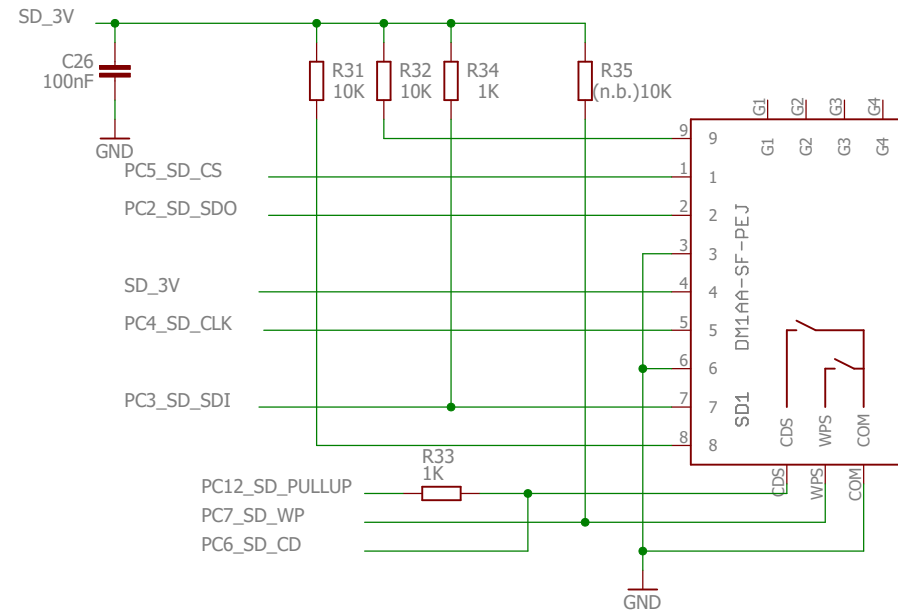
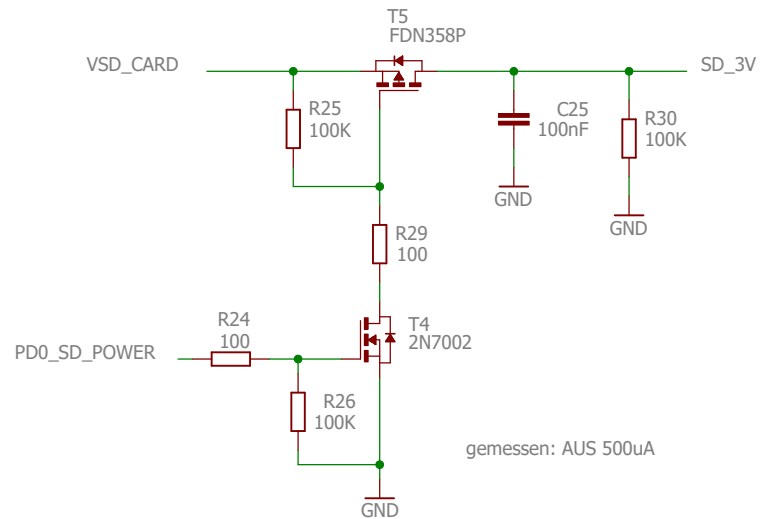
Document Number:

REV:

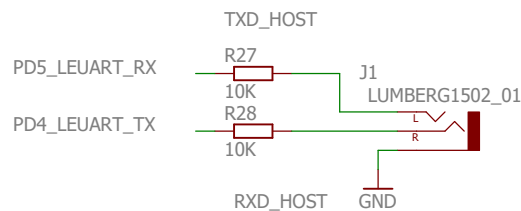
Date: 28.03.2019 15:02

Sheet: 3/6

SD SOCKET

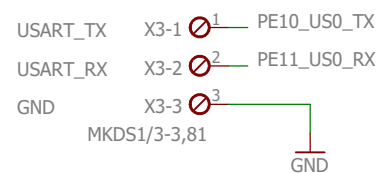


LEUART



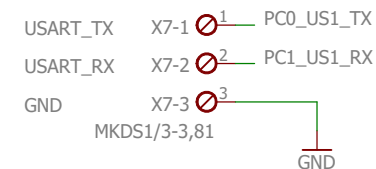
Standard - Ausgabe für Hyperterminal

USART0



Ausgabe für MOMO - Hauptplatine

USART1



SD SOCKET + LEUART + USART0 + USART1

TITLE: Logger_2019_1

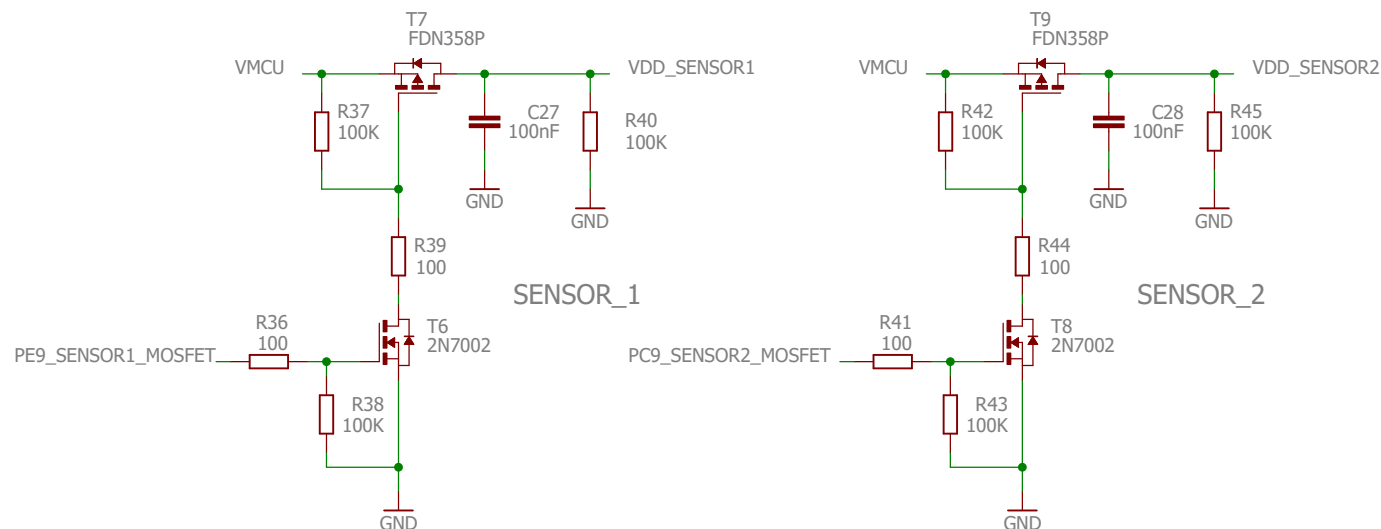
Document Number:

REV:

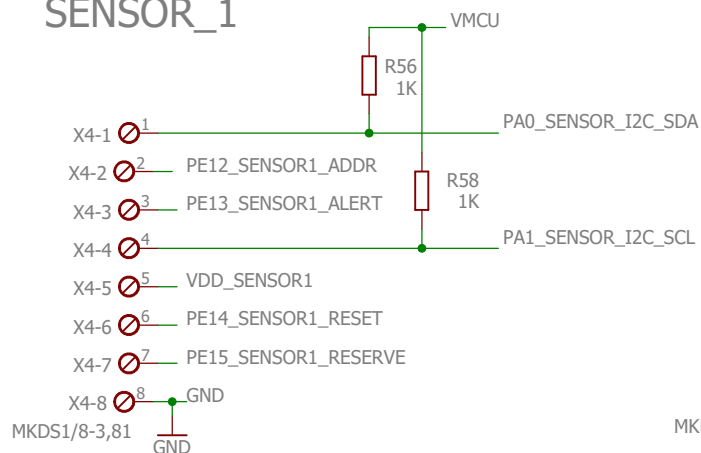
Date: 28.03.2019 15:02

Sheet: 4/6

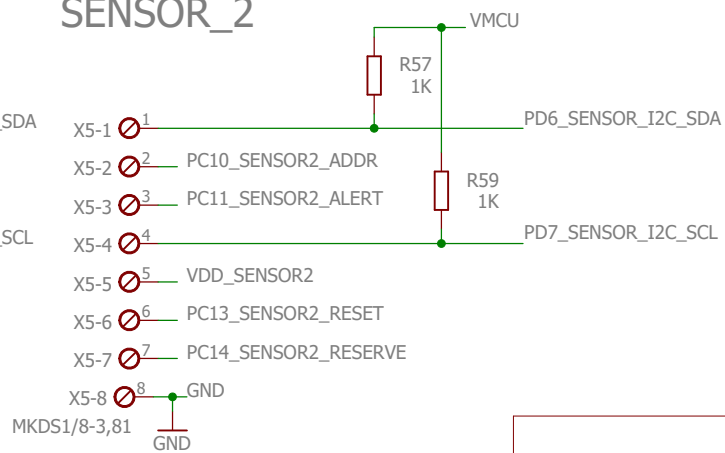
SENSOR_ON_OFF



SENSOR_1



SENSOR_2



SENSOR_ON_OFF + SENSOR_1 + SENSOR_2

TITLE: Logger_2019_1

Document Number:

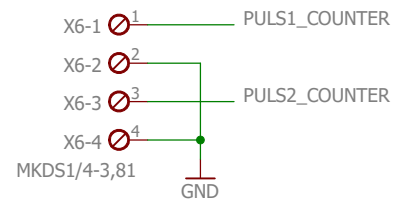
REV:

Date: 28.03.2019 15:02

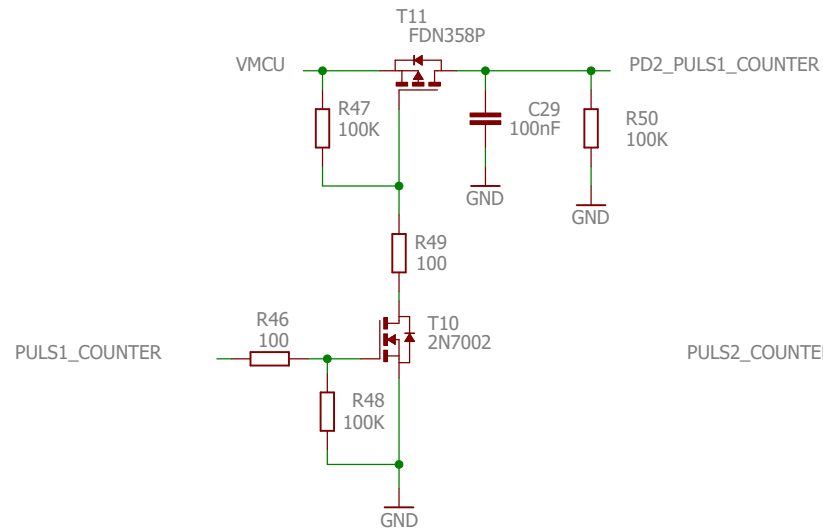
Sheet: 5/6

PULS_COUNTER

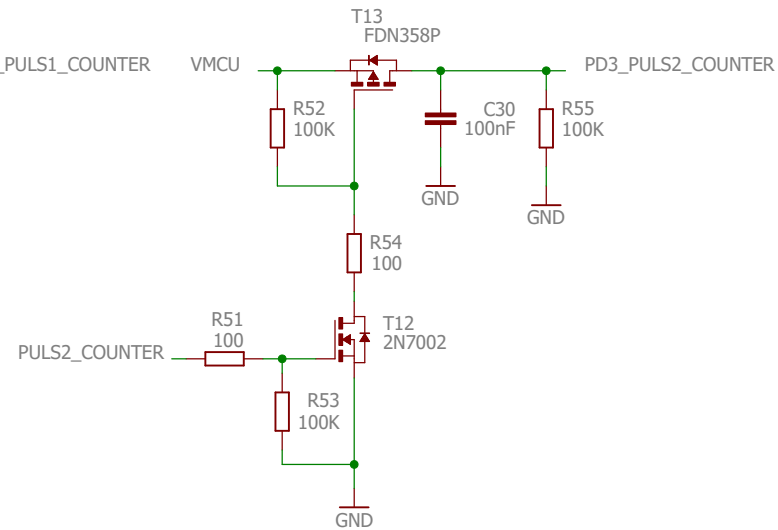
Ue = 0V...60V



COUNTER_1



COUNTER_2



PULS_COUNTER + COUNTER_1 + COUNTER_2

TITLE: Logger_2019_1

Document Number:

REV:

Date: 28.03.2019 15:02

Sheet: 6/6