

IAR_SYSTEMS_J_LINK PINOUT FOR SWD Serial Wire Debug

Diagram illustrating the pinout for the IAR_SYSTEMS_J_LINK SWD Serial Wire Debug interface. The diagram shows two headers, X2 and SV1, connected by green lines.

Header X2 (Left):

- Pin 10: not provided / SWD
- Pin 8: not provided / SWD
- Pin 6: not provided / SWD
- Pin 4: not provided / SWD
- Pin 2: not provided / SWD
- Pin 9: not used
- Pin 7: not used
- Pin 5: not used
- Pin 3: GND
- Pin 1: not used

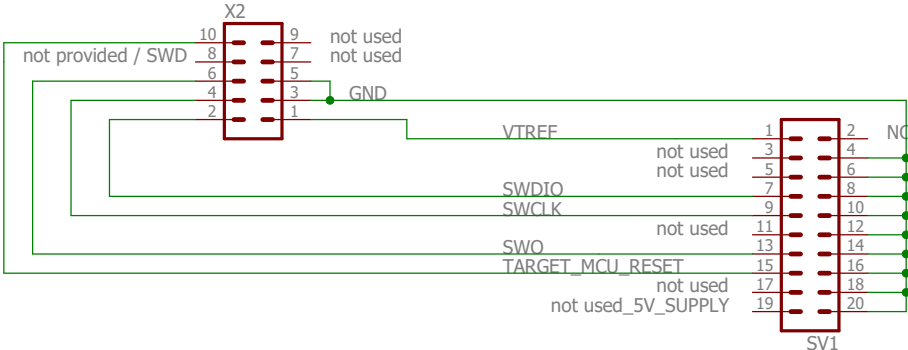
Header SV1 (Right):

- Pin 1: not used
- Pin 3: not used
- Pin 5: not used
- Pin 7: not used
- Pin 9: not used
- Pin 11: not used
- Pin 13: not used
- Pin 15: not used
- Pin 17: not used
- Pin 19: not used
- Pin 2: NC
- Pin 4: not used
- Pin 6: not used
- Pin 8: not used
- Pin 10: not used
- Pin 12: not used
- Pin 14: not used
- Pin 16: not used
- Pin 18: not used
- Pin 20: not used

Connections:

- SV1 Pin 2 (NC) connects to X2 Pin 10 (not provided / SWD)
- SV1 Pin 4 connects to X2 Pin 8 (not provided / SWD)
- SV1 Pin 6 connects to X2 Pin 6 (not provided / SWD)
- SV1 Pin 8 connects to X2 Pin 4 (not provided / SWD)
- SV1 Pin 10 connects to X2 Pin 2 (not provided / SWD)
- SV1 Pin 12 connects to X2 Pin 9 (not used)
- SV1 Pin 14 connects to X2 Pin 7 (not used)
- SV1 Pin 16 connects to X2 Pin 5 (not used)
- SV1 Pin 18 connects to X2 Pin 3 (GND)
- SV1 Pin 20 connects to X2 Pin 1 (not used)

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