



```
Memory 1
Address: 0x00404000
0x00404000
   *....
   0x00404020
                                  *.....
0 \times 00404040
   *.....
0x00404060
   *.....
0x00404080
   *.....
0x004040A0
   0x004040C0
                                *..........
0x004040E0
   Memory 1 Registers Watch 1
peter_ruszel_260_assign10.asm → ×
  : this can be changed to any value between 2 - 16
  : the correct V pattern should be "drawn" in memory in all cases
  ROWS = 9
```

```
Memory 1
Address: 0x00404000
  *....
  *....
  *.....
  20 20 20 20 20 20 2a 20 20 20 20 20 20 20 20 20 20 20 2a 00 00 00 00 00 00 00 00
                            * . . . . . . . . . . . . . . .
  Memory 1 Registers Watch 1
peter_ruszel_260_assign10.asm + X
 ; this can be changed to any value between 2 - 16
 ; the correct V pattern should be "drawn" in memory in all cases
 ROWS = 13
```