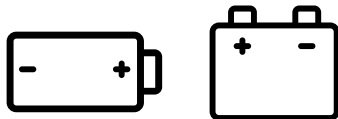
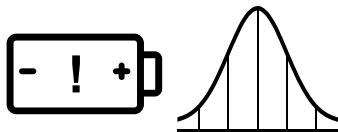


### Cell design



### Cell quality (defects and conformance)



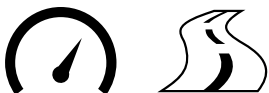
### Module/pack design



### Cell operating limits



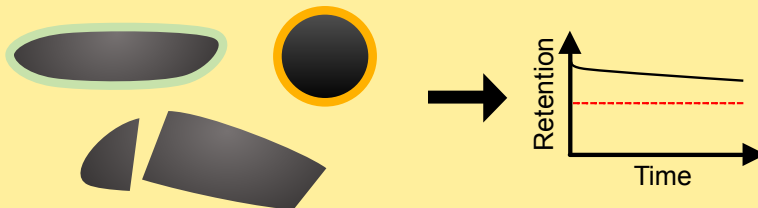
### End-user behavior



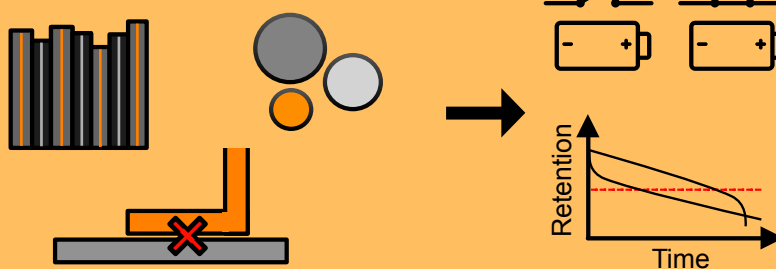
### External environment



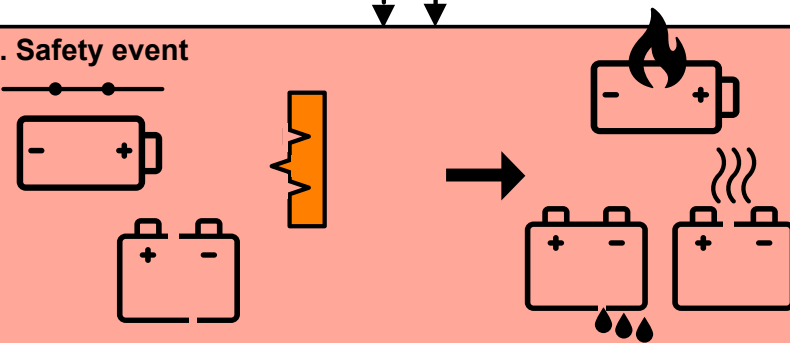
## 1. Performance degradation



## 2. Functional failure



## 3. Safety event



Severity

Likelihood